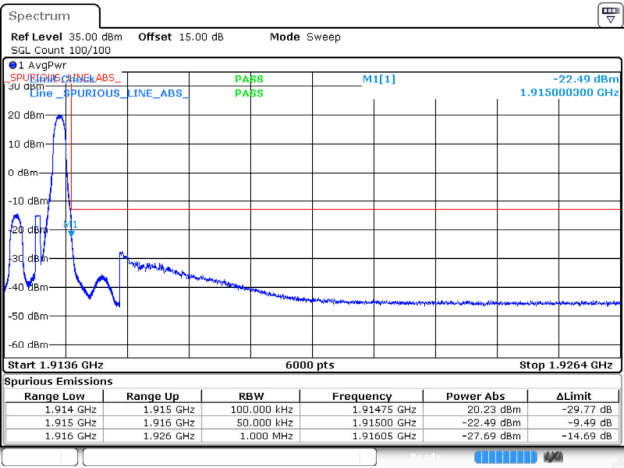
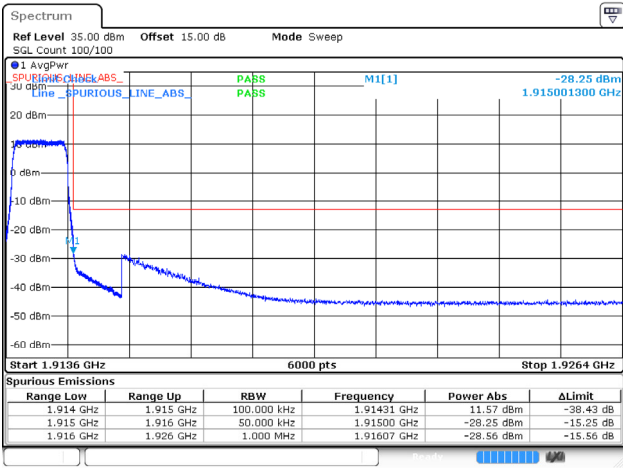


1.4MHz_High_16QAM_1@5



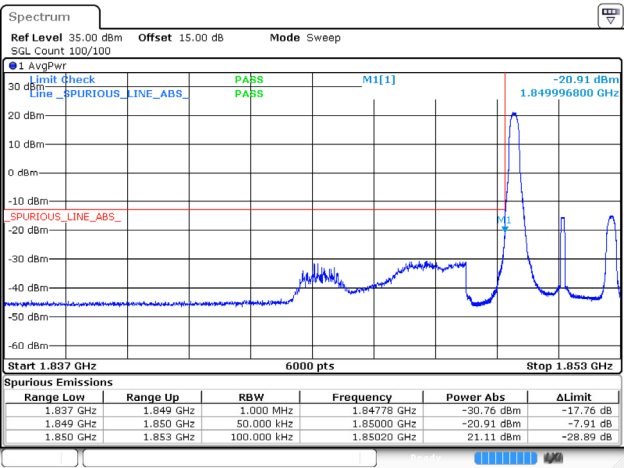
ProjectNo.:RKSA250928001 Tester:Hugh Wu
Date: 9 OCT.2025 13:58:15

1.4MHz_High_16QAM_6@0



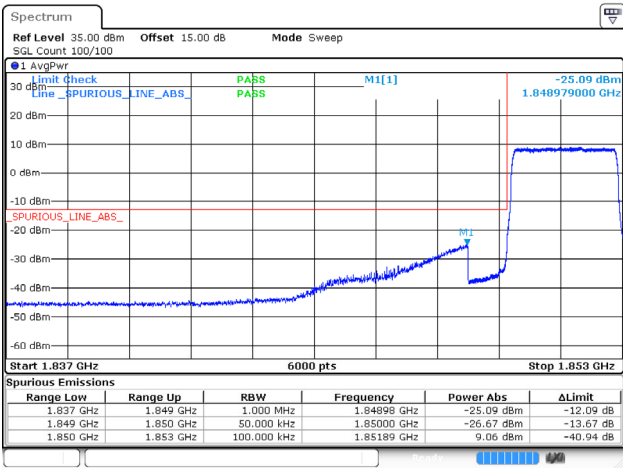
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Date: 9 OCT.2025 13:58:40

3MHz_Low_QPSK_1@0



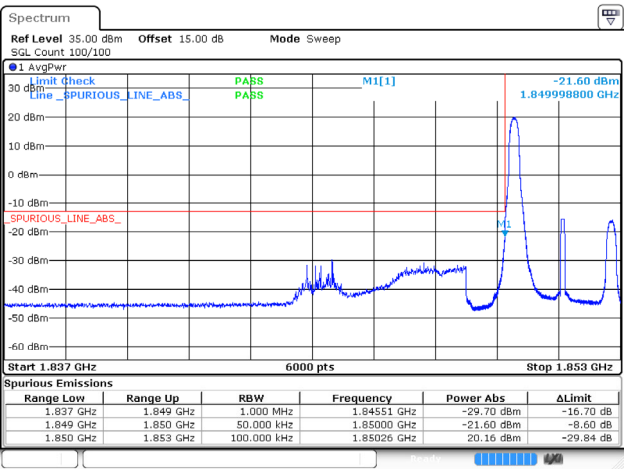
ProjectNo.:RKSA250928001 Tester:Hugh Wu
Date: 9 OCT.2025 13:59:34

3MHz_Low_QPSK_15@0



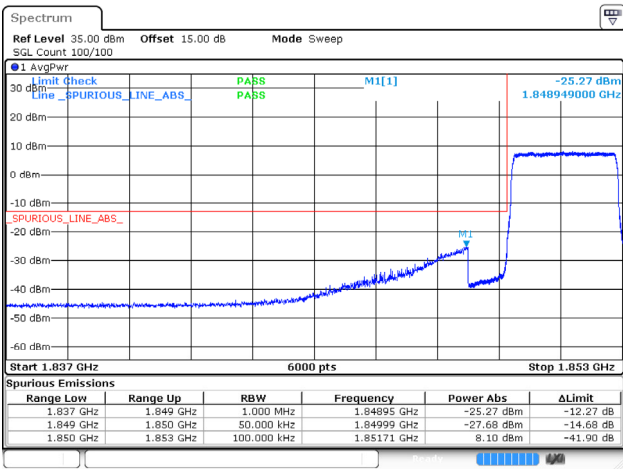
ProjectNo.:RKSA250928001 Tester:Hugh Wu
Date: 9 OCT.2025 14:00:03

3MHz_Low_16QAM_1@0



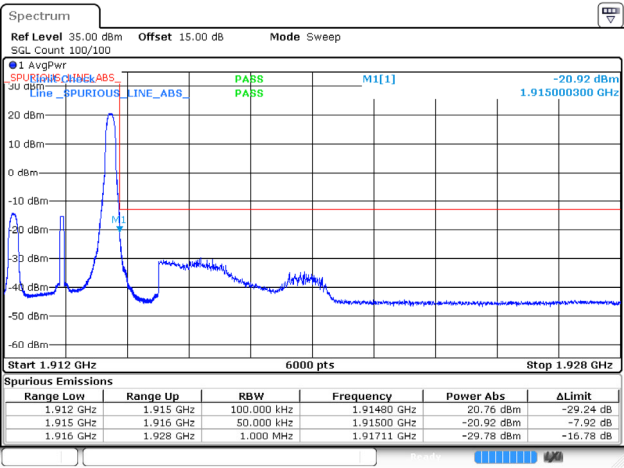
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Date: 9 OCT.2025 14:00:33

3MHz_Low_16QAM_15@0



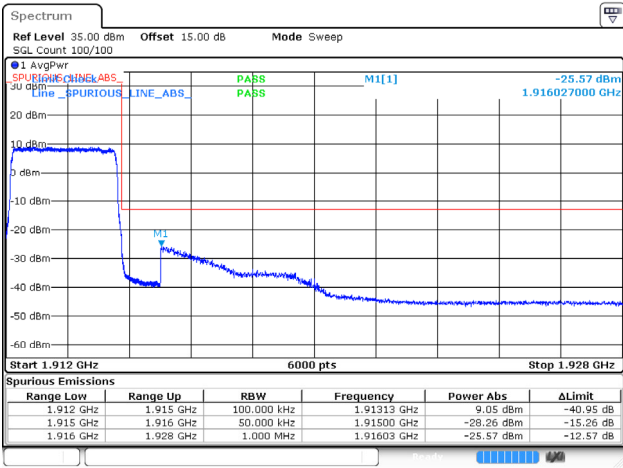
ProjectNo.:RKSA250928001 Tester:Hugh Wu
Date: 9 OCT.2025 14:01:01

3MHz_High_QPSK_1@14



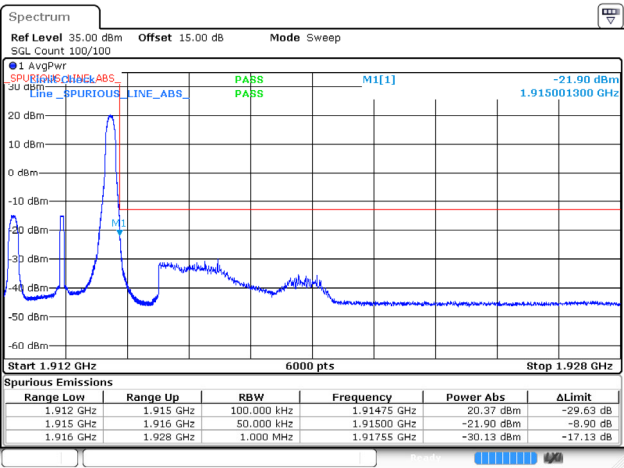
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Date: 9 OCT.2025 14:01:30

3MHz_High_QPSK_15@0



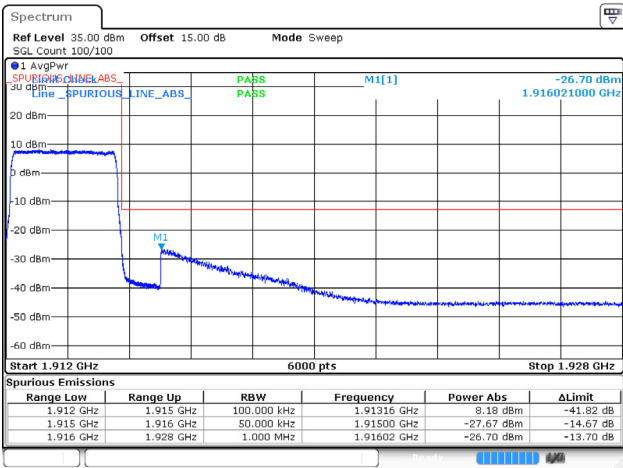
ProjectNo.:RKSA250928001 Tester:Hugh Wu
Date: 9 OCT.2025 14:01:58

3MHz_High_16QAM_1@14



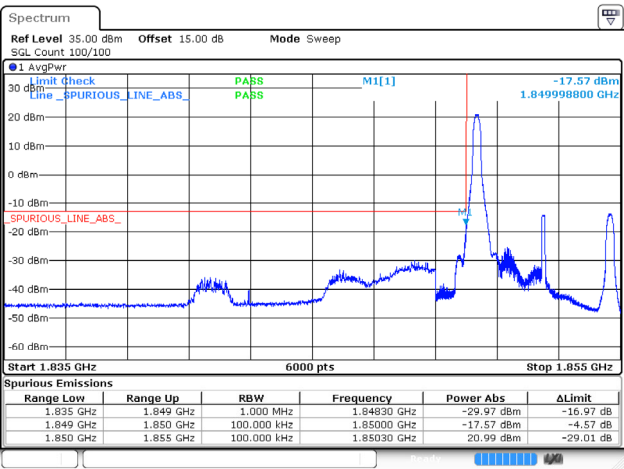
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Date: 9 OCT.2025 14:02:27

3MHz_High_16QAM_15@0



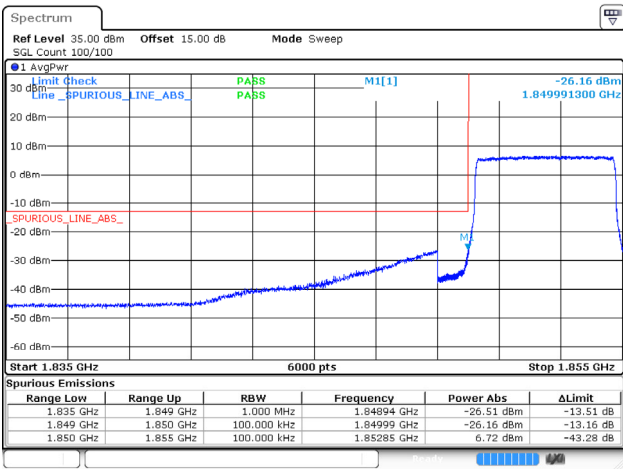
ProjectNo.:RKSA250928001 Tester:Hugh Wu
Date: 9 OCT.2025 14:02:55

5MHz_Low_QPSK_1@0



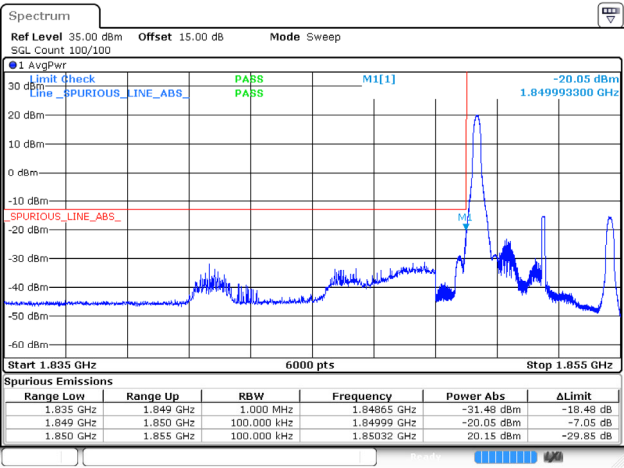
ProjectNo.:RKSA250928001 Tester:Hugh Wu
Date: 9 OCT.2025 14:03:49

5MHz_Low_QPSK_25@0



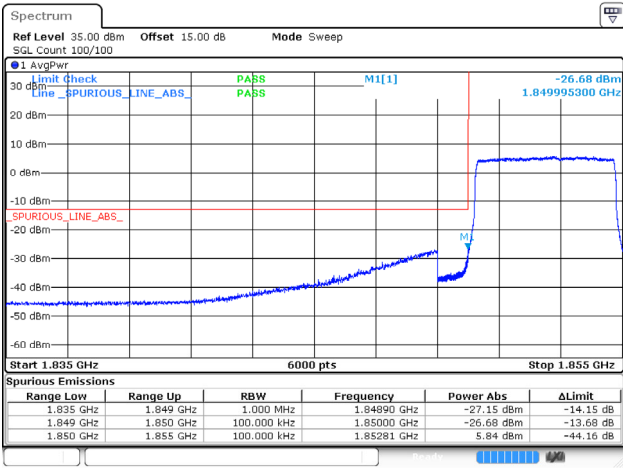
ProjectNo.:RKSA250928001 Tester:Hugh Wu
Date: 9 OCT.2025 14:04:17

5MHz_Low_16QAM_1@0



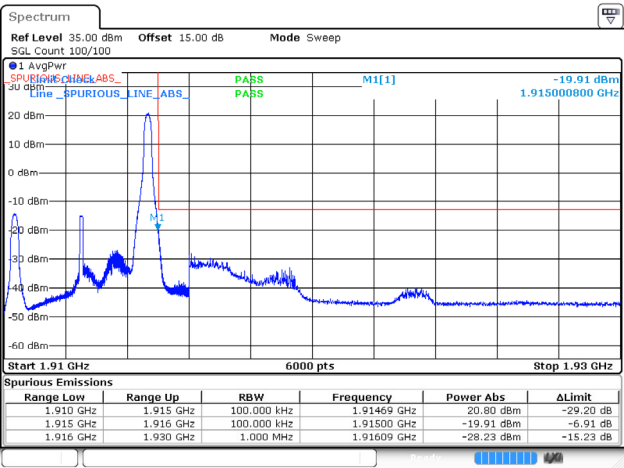
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Date: 9 OCT.2025 14:04:47

5MHz_Low_16QAM_25@0



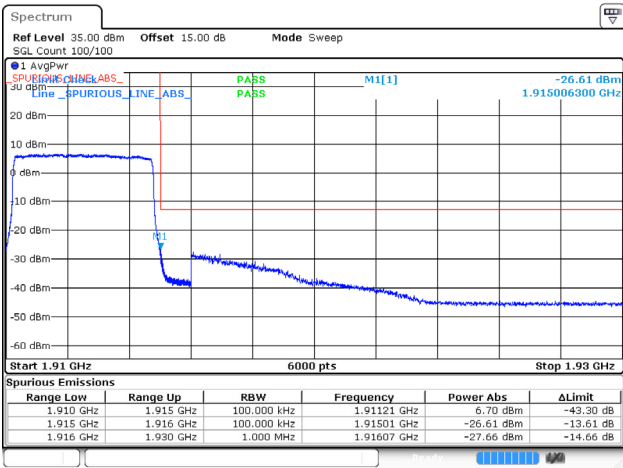
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Date: 9 OCT.2025 14:05:14

5MHz_High_QPSK_1@24



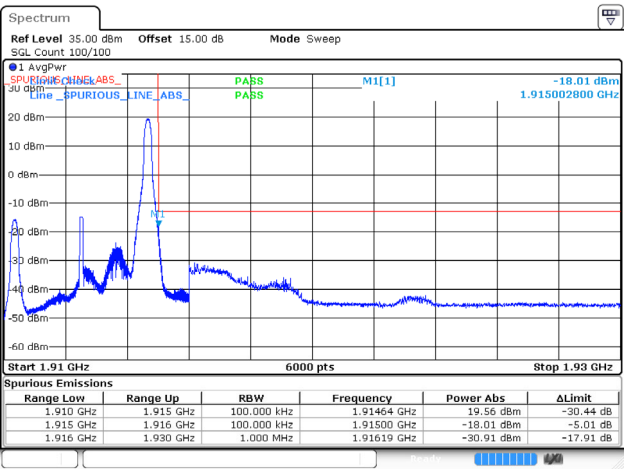
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5MHz_High_QPSK_25@0



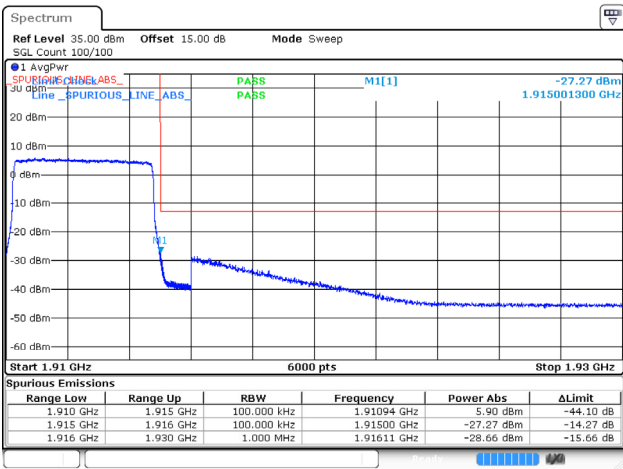
ProjectNo.:RKSA250928001 Tester:Hugh Wu
Date: 9 OCT.2025 14:06:06

5MHz_High_16QAM_1@24



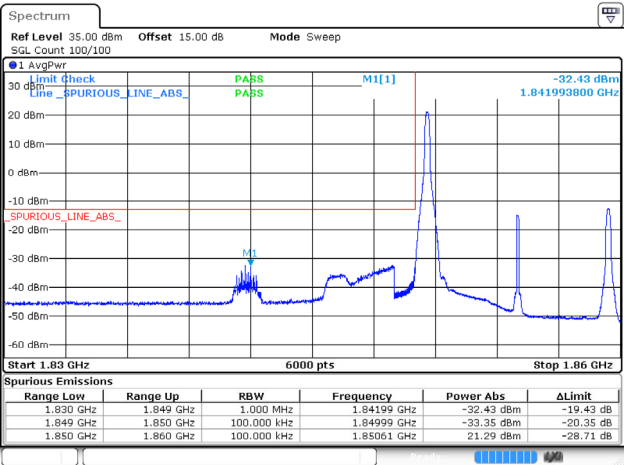
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Date: 9 OCT.2025 14:06:33

5MHz_High_16QAM_25@0



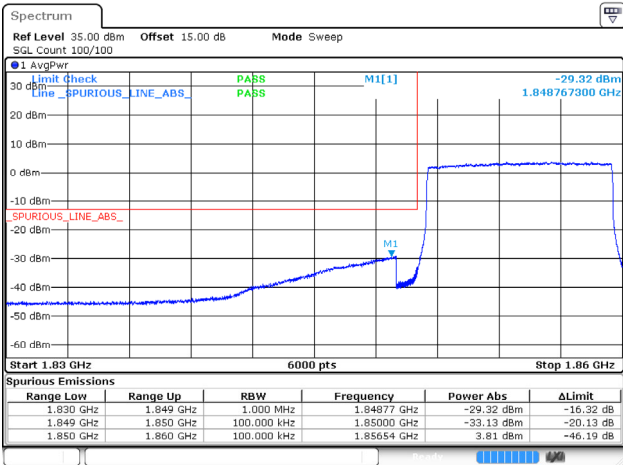
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10MHz_Low_QPSK_1@0



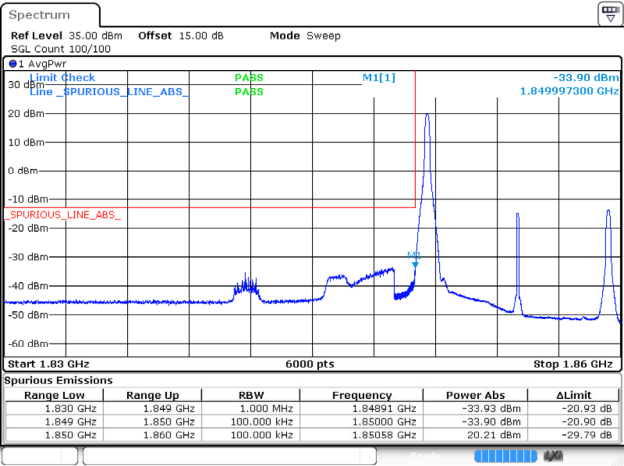
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Date: 9 OCT.2025 14:08:16

10MHz_Low_QPSK_50@0



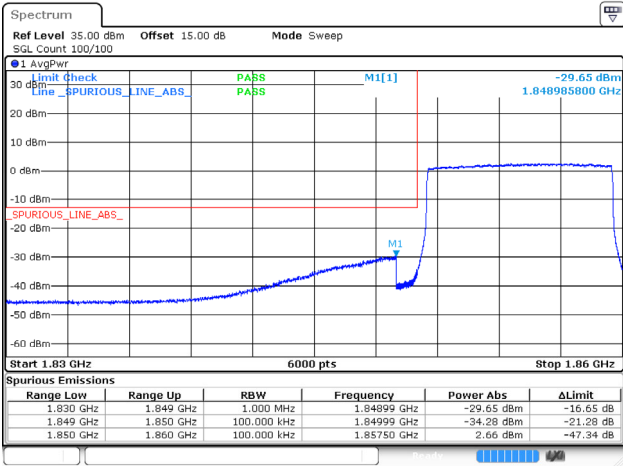
ProjectNo.:RKSA250928001 Tester:Hugh Wu
Date: 9 OCT.2025 14:08:52

10MHz_Low_16QAM_1@0



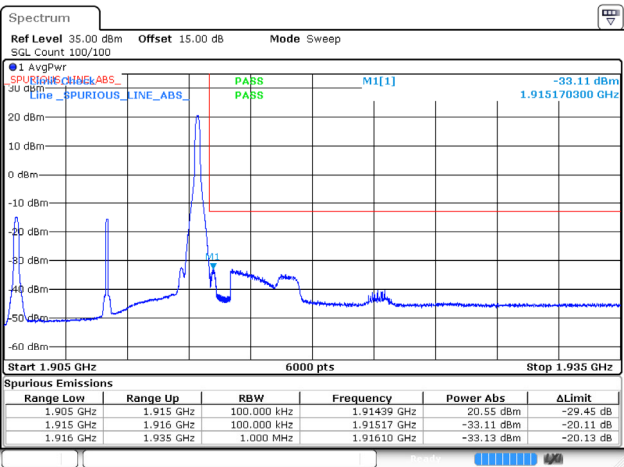
ProjectNo.:RKSA250928001 Tester:Hugh Wu
Date: 9 OCT.2025 14:09:31

10MHz_Low_16QAM_50@0



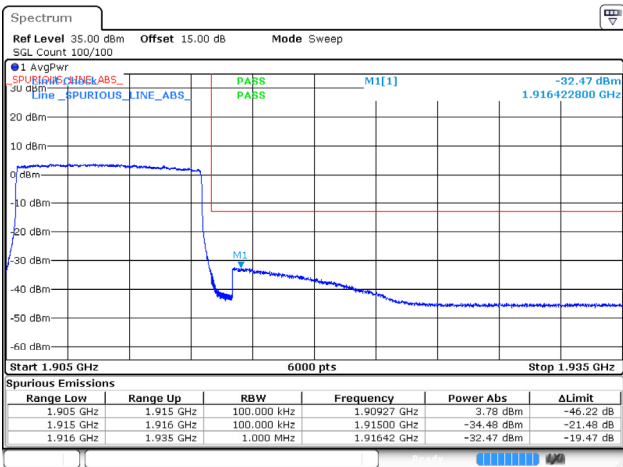
ProjectNo.:RKSA250928001 Tester:Hugh Wu
Date: 9 OCT.2025 14:10:08

10MHz_High_QPSK_1@49



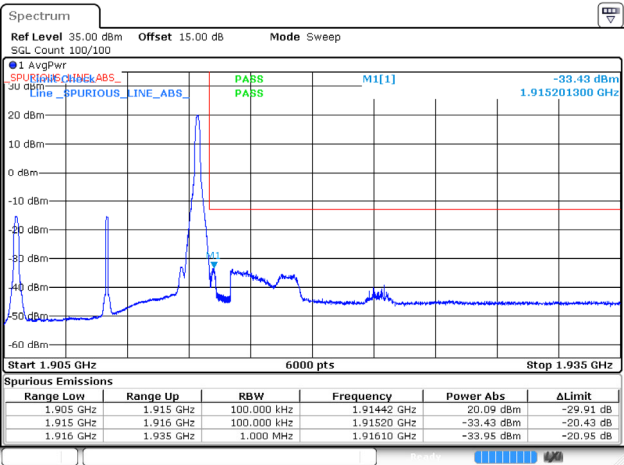
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Date: 9 OCT.2025 14:10:44

10MHz_High_QPSK_50@0



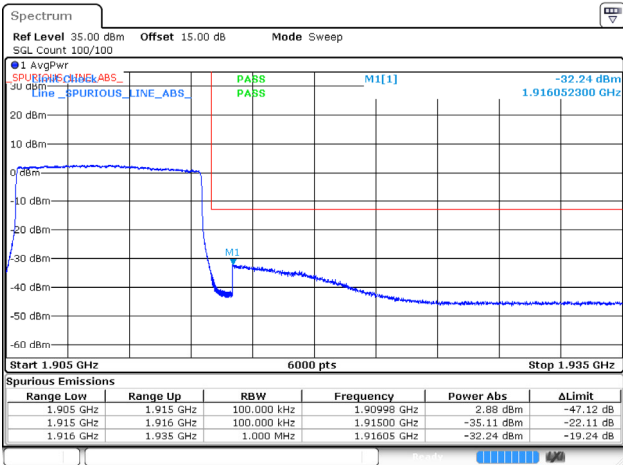
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Date: 9 OCT.2025 14:11:21

10MHz_High_16QAM_1@49



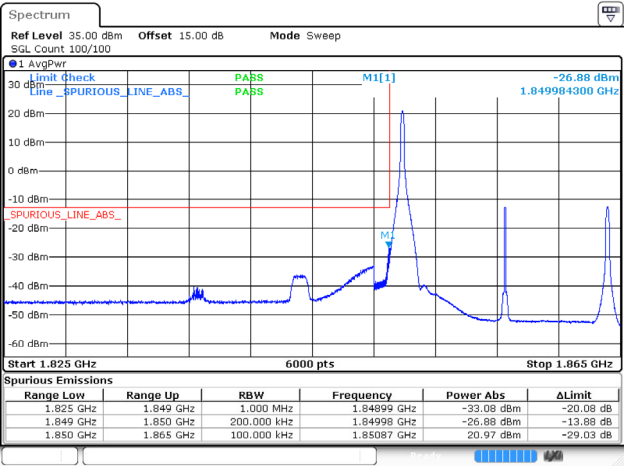
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Date: 9 OCT.2025 14:11:58

10MHz_High_16QAM_50@0



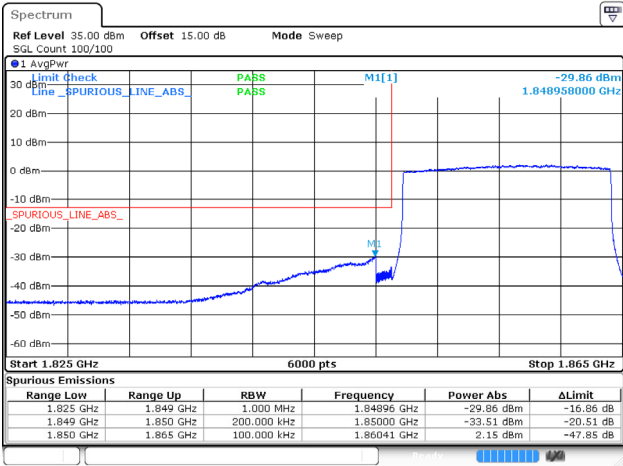
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Date: 9 OCT.2025 14:12:36

15MHz_Low_QPSK_1@0



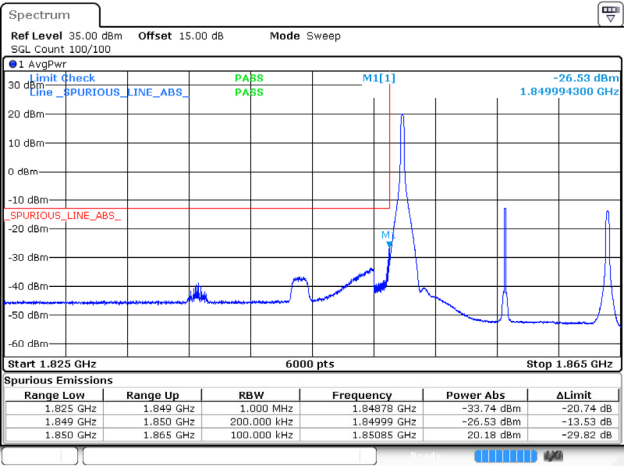
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15MHz_Low_QPSK_75@0



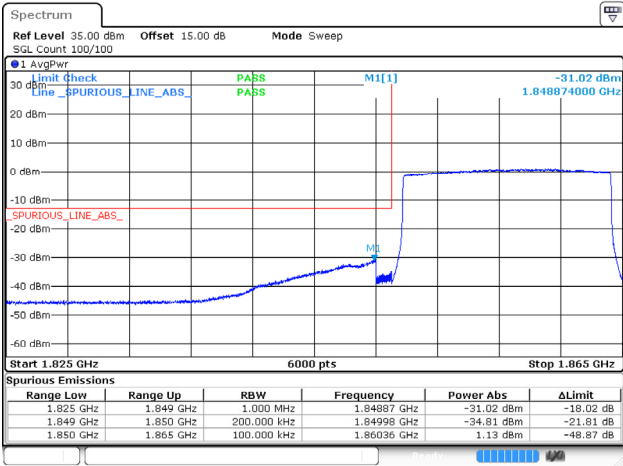
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15MHz_Low_16QAM_1@0



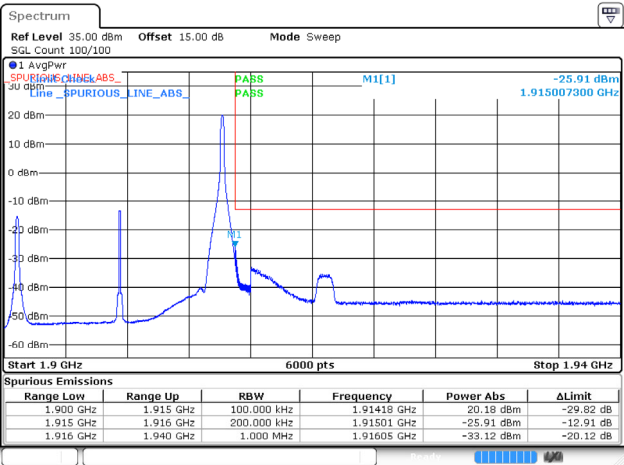
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15MHz_Low_16QAM_75@0



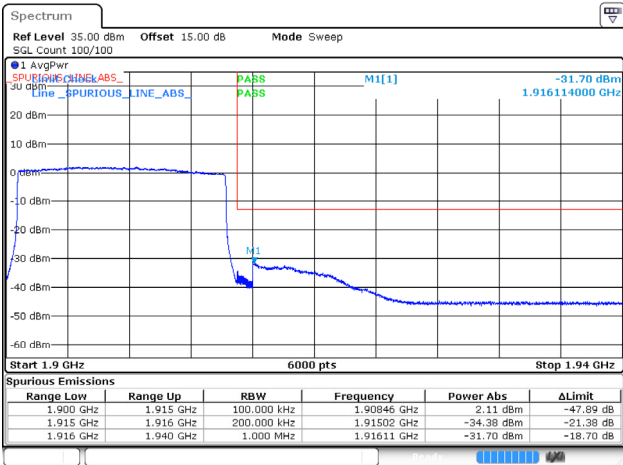
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Date: 9 OCT.2025 14:44:42

15MHz_High_QPSK_1@74



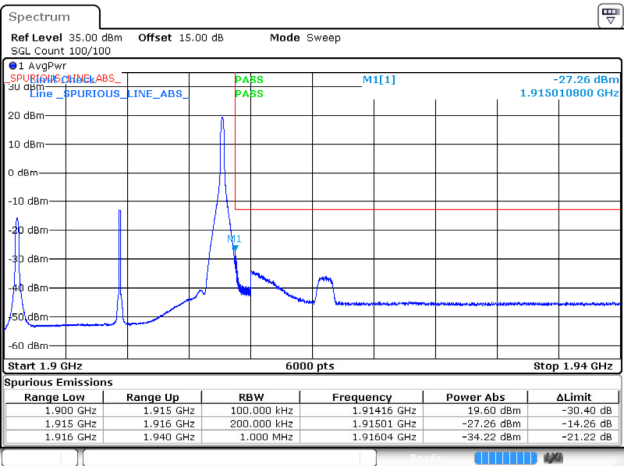
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Date: 9 OCT.2025 14:45:28

15MHz_High_QPSK_75@0



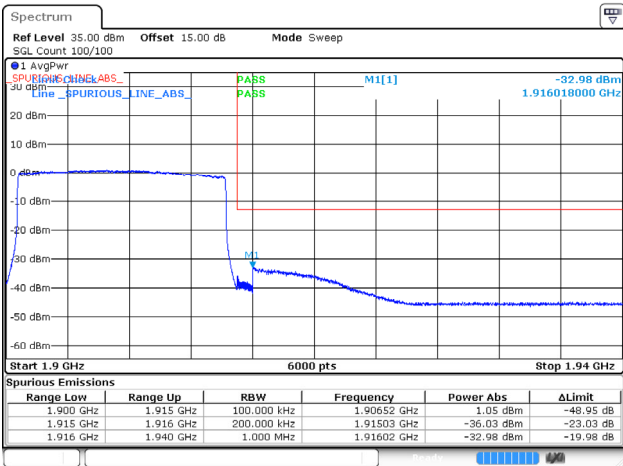
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15MHz_High_16QAM_1@74



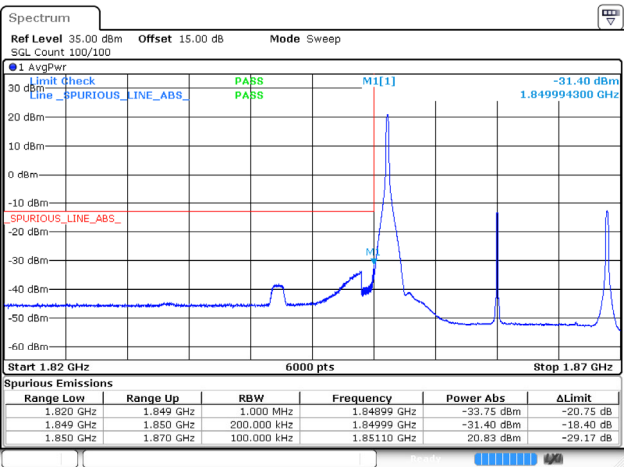
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Date: 9 OCT.2025 14:47:03

15MHz_High_16QAM_75@0



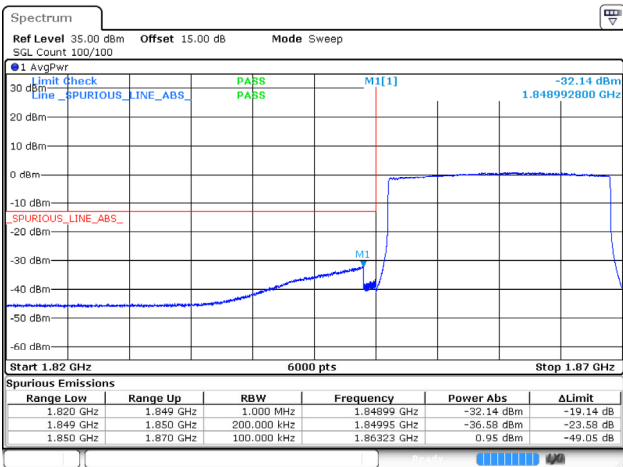
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Date: 9 OCT.2025 14:47:50

20MHz_Low_QPSK_1@0



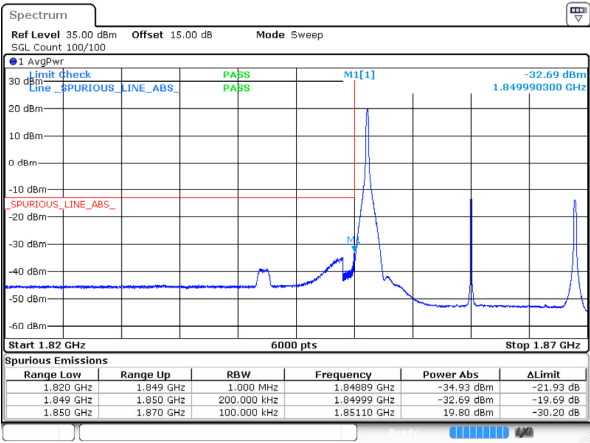
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Date: 9 OCT.2025 14:49:37

20MHz_Low_QPSK_100@0



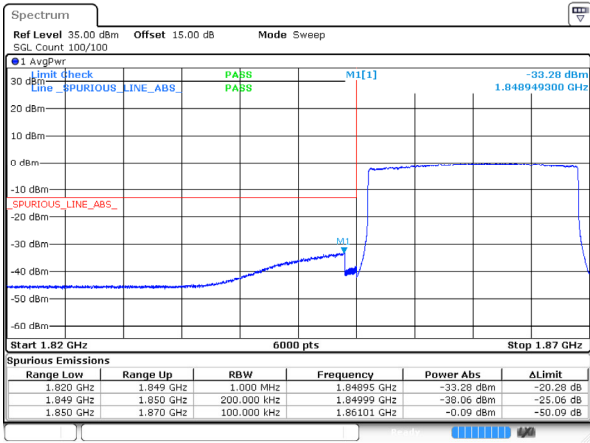
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Date: 9 OCT.2025 14:50:36

20MHz_Low_16QAM_1@0



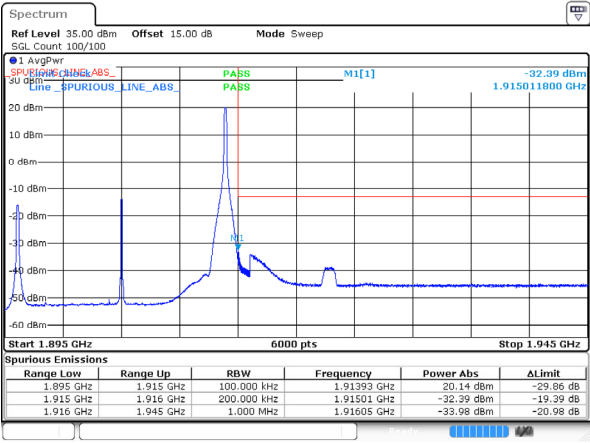
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Date: 9.OCT.2025 14:51:34

20MHz_Low_16QAM_100@0



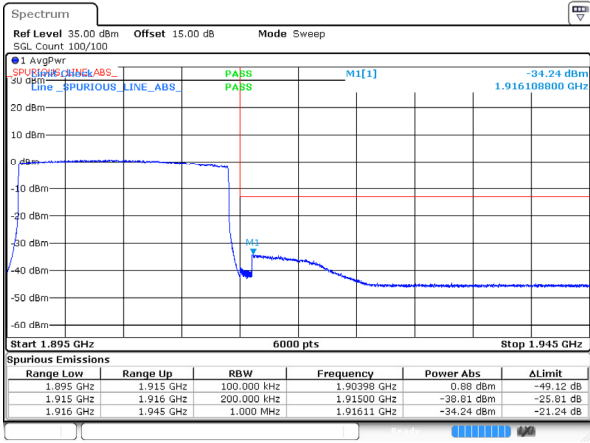
ProjectNo.:RKSA250928001 Tester:Hugh Wu
Date: 9.OCT.2025 14:52:31

20MHz_High_QPSK_1@99



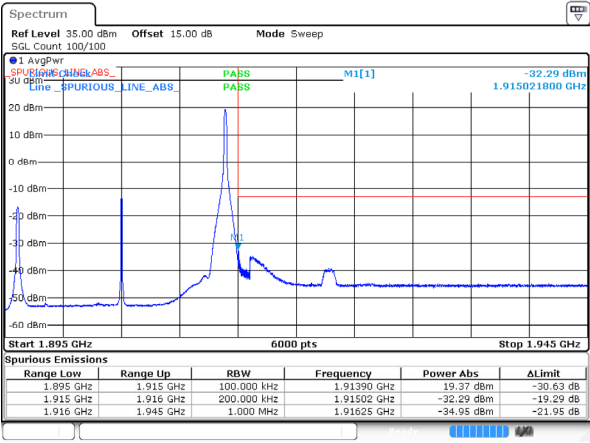
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20MHz_High_QPSK_100@0



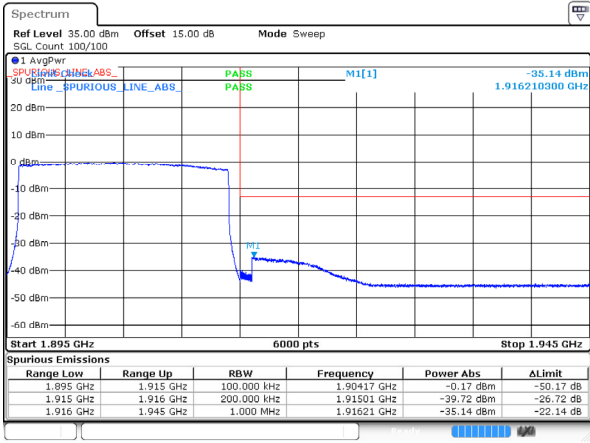
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Date: 9.OCT.2025 14:54:26

20MHz_High_16QAM_1@99



ProjectNo.:RKSA250928001 Tester:Hugh Wu
Date: 9.OCT.2025 14:55:23

20MHz_High_16QAM_100@0



ProjectNo.:RKSA250928001 Tester:Hugh Wu
Date: 9.OCT.2025 14:56:19

***** END *****