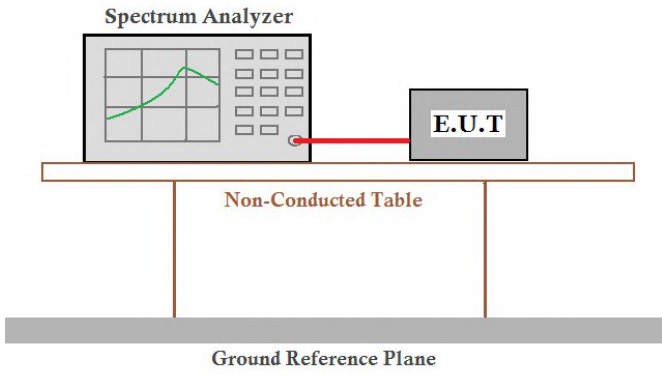


4.6 Hopping Channel Number

Test Requirement:	47CRF Part 15.247 (a)(1)(iii)	
Test Method:	ANSI C63.10-2020 + Cor. 1-2023+C63.10a-2024 + Errata to C63.10a-2024 §7.8.3	
Receiver setup:	RBW=100kHz, VBW=300kHz, Frequency range=2400MHz-2483.5MHz, Detector=Peak	
Limit:	15 channels	
Test setup:	 The diagram illustrates the test setup. A Spectrum Analyzer is connected via a red cable to an E.U.T. (Equipment Under Test). Both are placed on a Non-Conducted Table. Below the table is a Ground Reference Plane.	
Test Instruments:	Refer to section 3.0 for details	
Test mode:	Refer to section 2.2 for details	
Test environment:	Temp.: 23.2°C	Humid.: 41%RH
Test voltage:	DC 7.7V from battery	
Test results:	Pass	

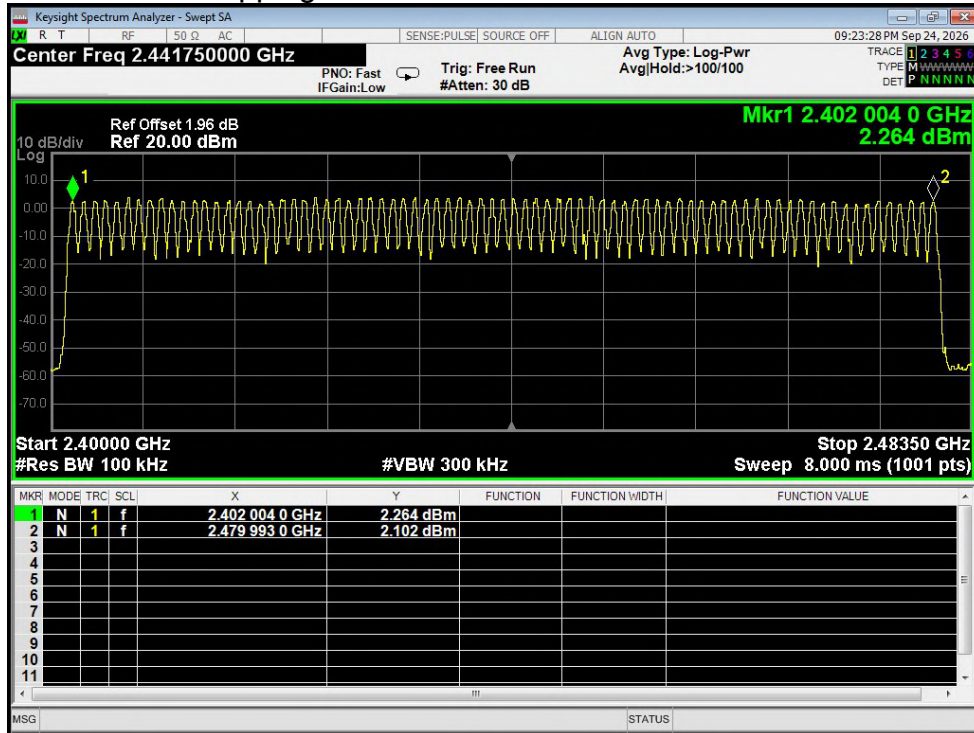
Measurement Data:

Mode	Hopping channel numbers	Limit	Result
GFSK	79	15	Pass
$\pi/4$ -DQPSK	79	15	Pass
8DPSK	79	15	Pass

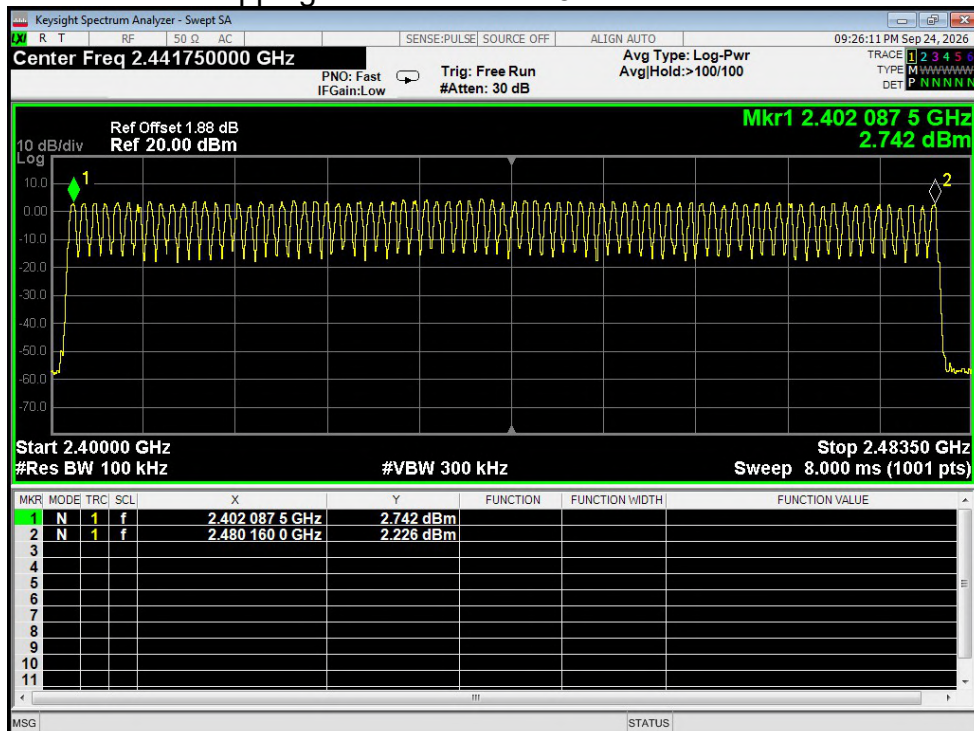


Test plot as follows:

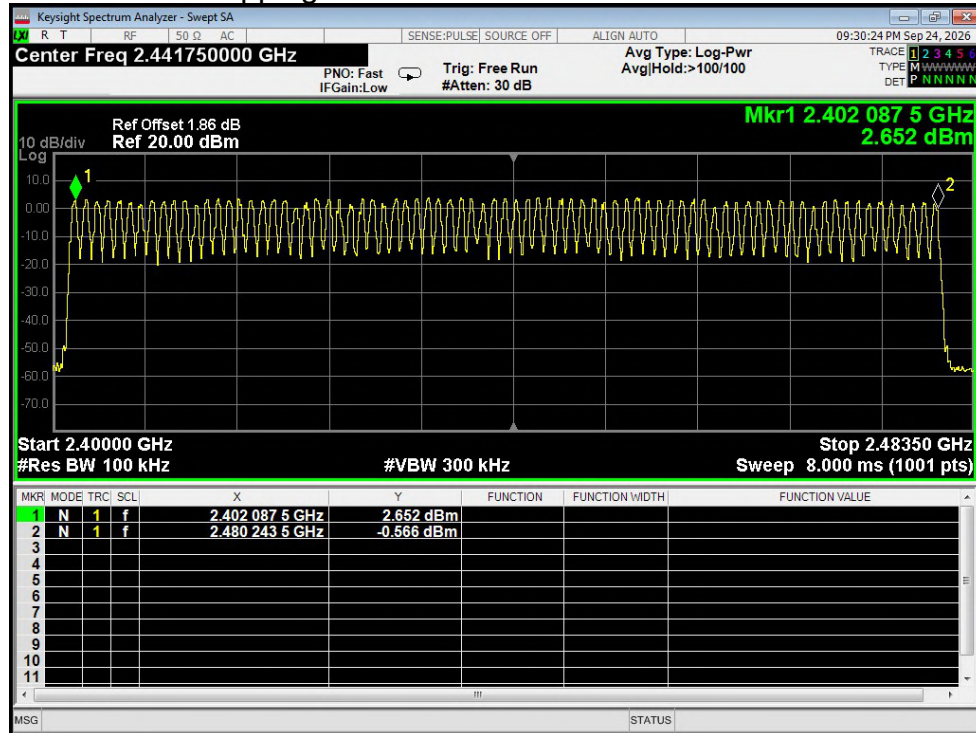
Hopping No. NVNT 1-DH5 2402MHz Ant1



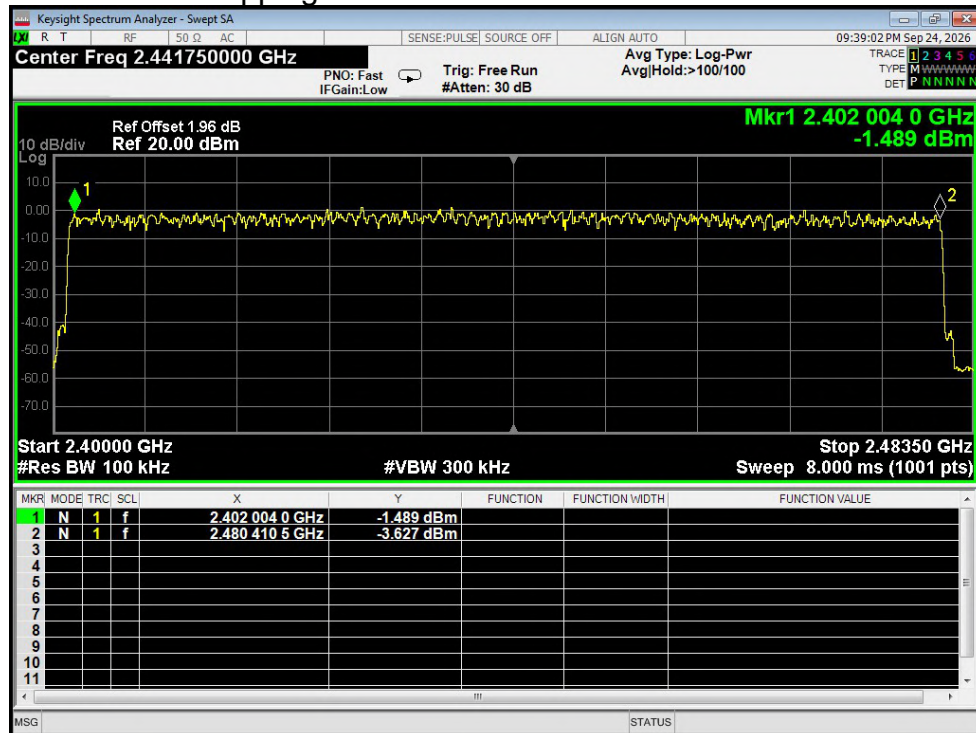
Hopping No. NVNT 1-DH5 2441MHz Ant1



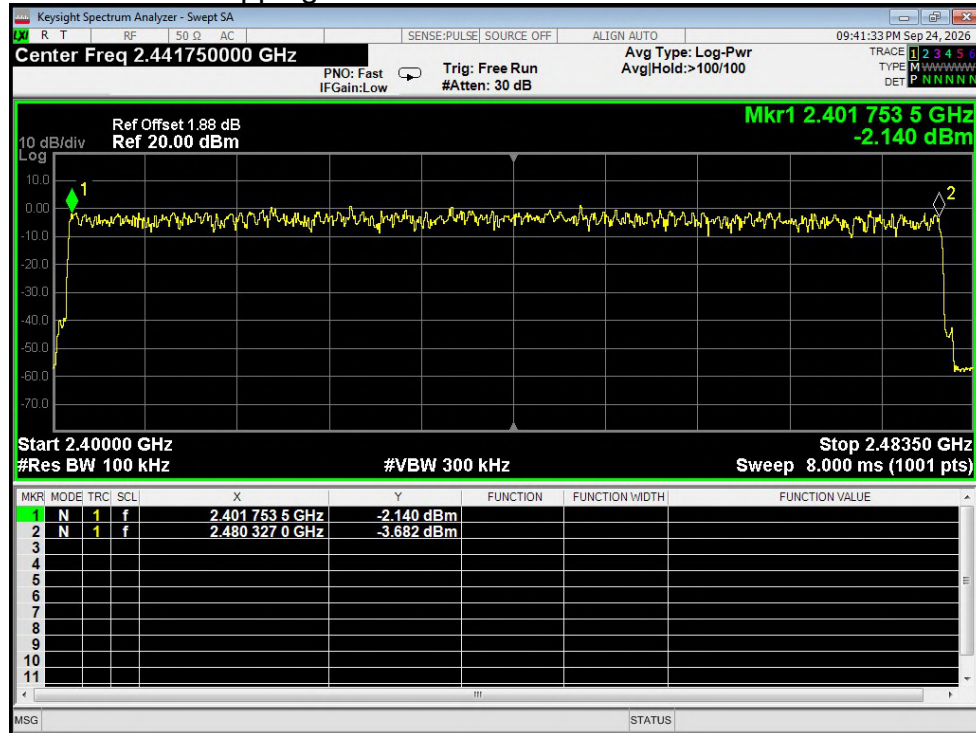
Hopping No. NVNT 1-DH5 2480MHz Ant1



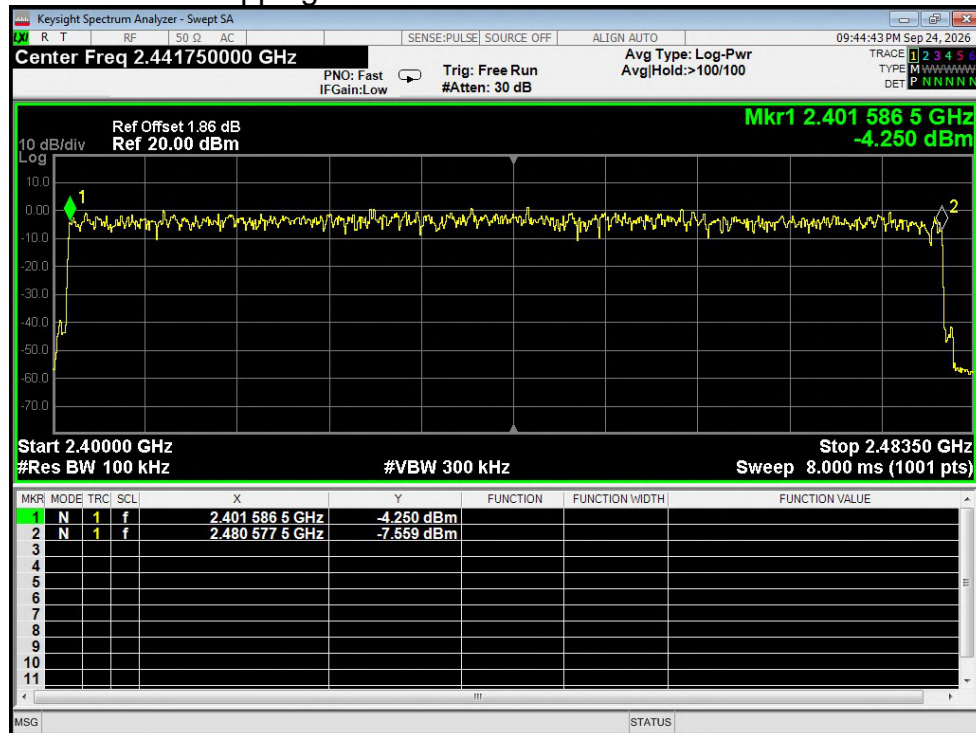
Hopping No. NVNT 2-DH5 2402MHz Ant1



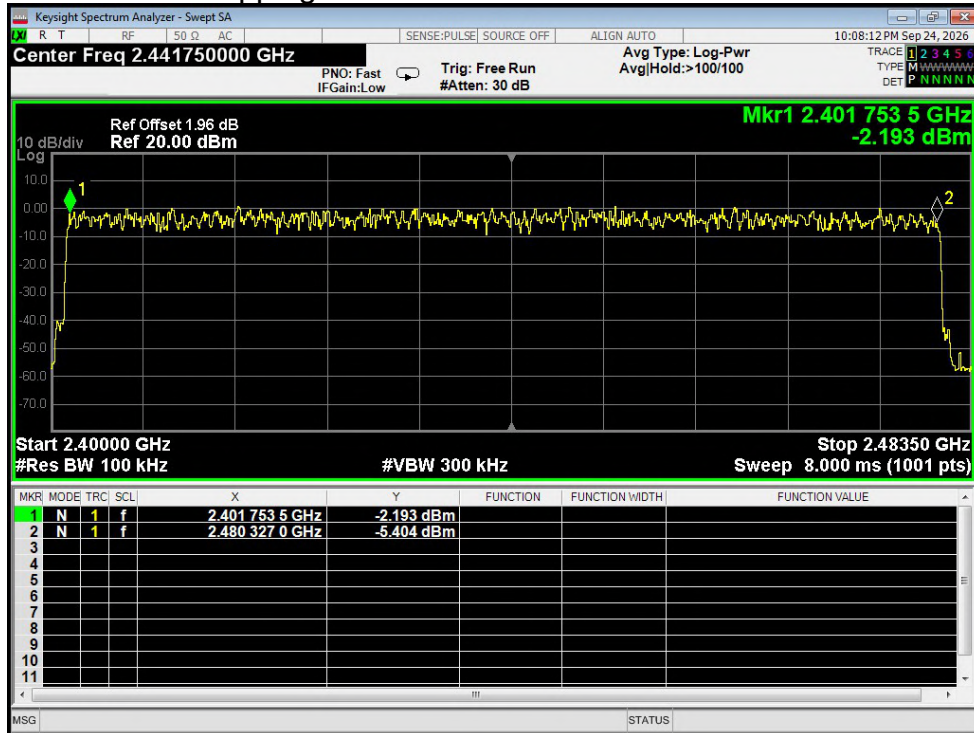
Hopping No. NVNT 2-DH5 2441MHz Ant1



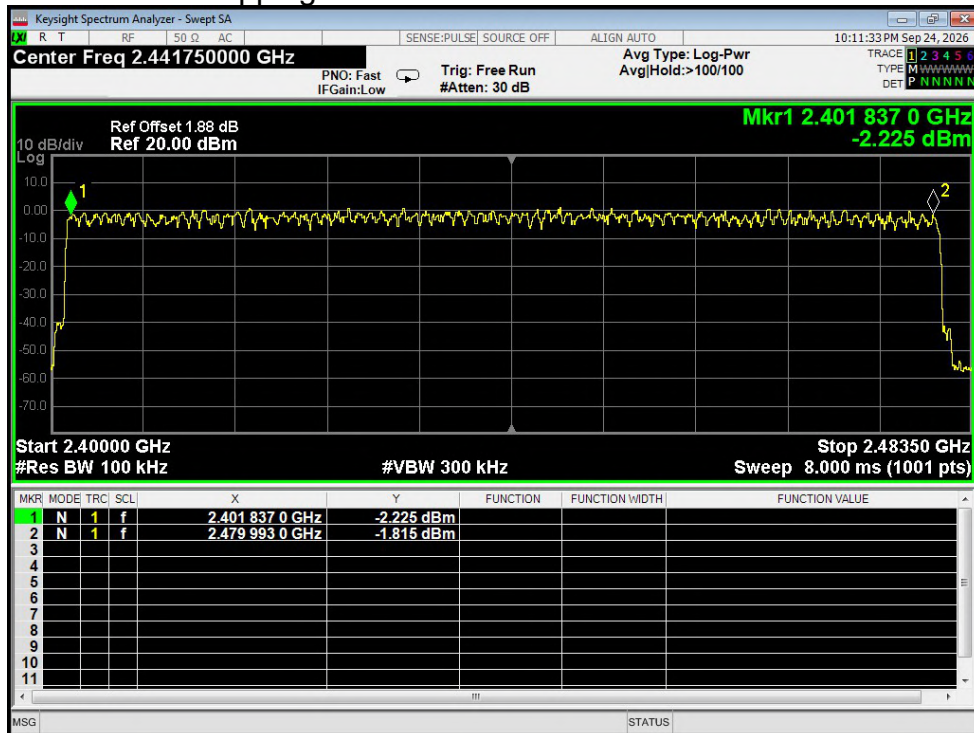
Hopping No. NVNT 2-DH5 2480MHz Ant1



Hopping No. NVNT 3-DH5 2402MHz Ant1

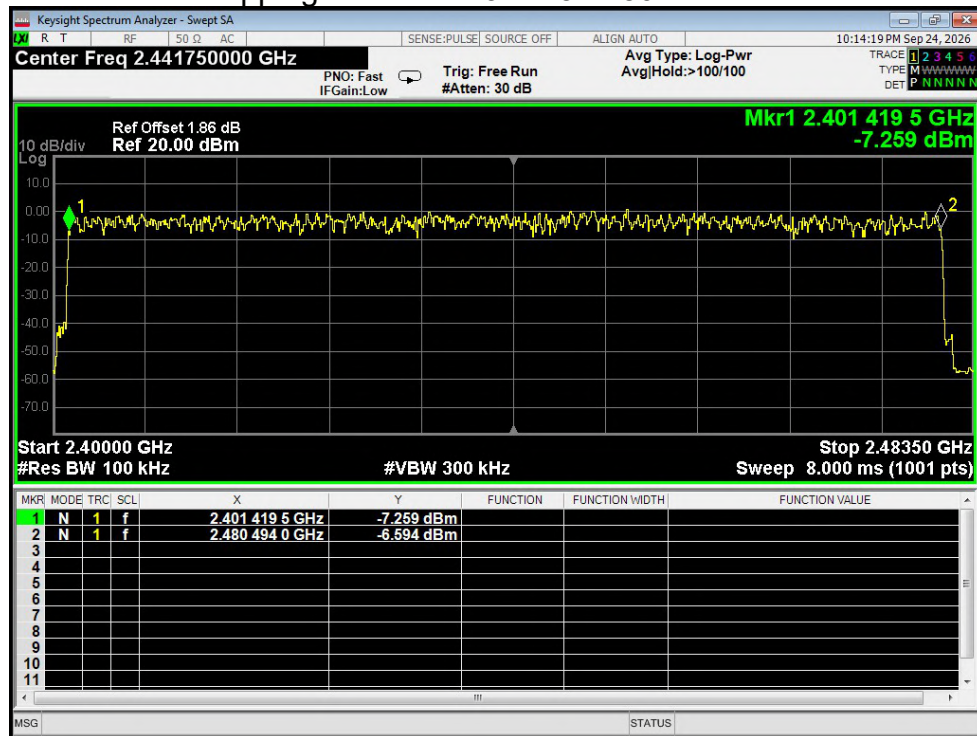


Hopping No. NVNT 3-DH5 2441MHz Ant1

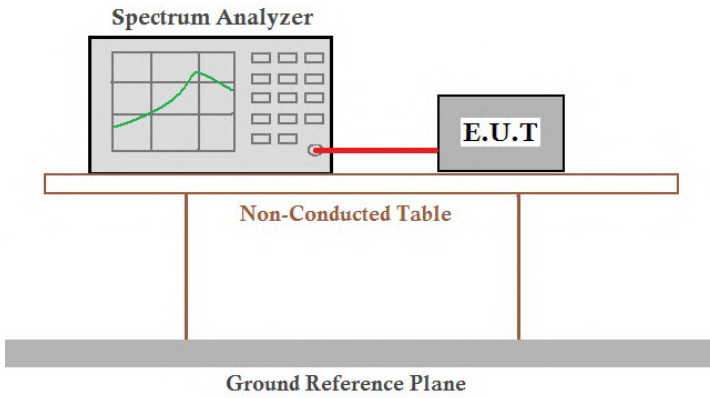




Hopping No. NVNT 3-DH5 2480MHz Ant1



4.7 Dwell Time

Test Requirement:	47CRF Part 15.247 (a)(1)(iii)	
Test Method:	ANSI C63.10-2020 + Cor. 1-2023+C63.10a-2024 + Errata to C63.10a-2024 §7.8.4	
Receiver setup:	RBW=1MHz, VBW=3MHz, Span=0Hz, Detector=Peak	
Limit:	0.4 Second	
Test setup:	 The diagram illustrates the test setup. A Spectrum Analyzer is connected to an E.U.T. (Equipment Under Test) via a red cable. Both the Spectrum Analyzer and the E.U.T. are placed on a Non-Conducted Table. The table is supported by a Ground Reference Plane.	
Test Instruments:	Refer to section 3.0 for details	
Test mode:	Refer to section 2.2 for details	
Test environment:	Temp.: 23.2°C	Humid.: 41%RH
Test voltage:	DC 7.7V from battery	
Test results:	Pass	

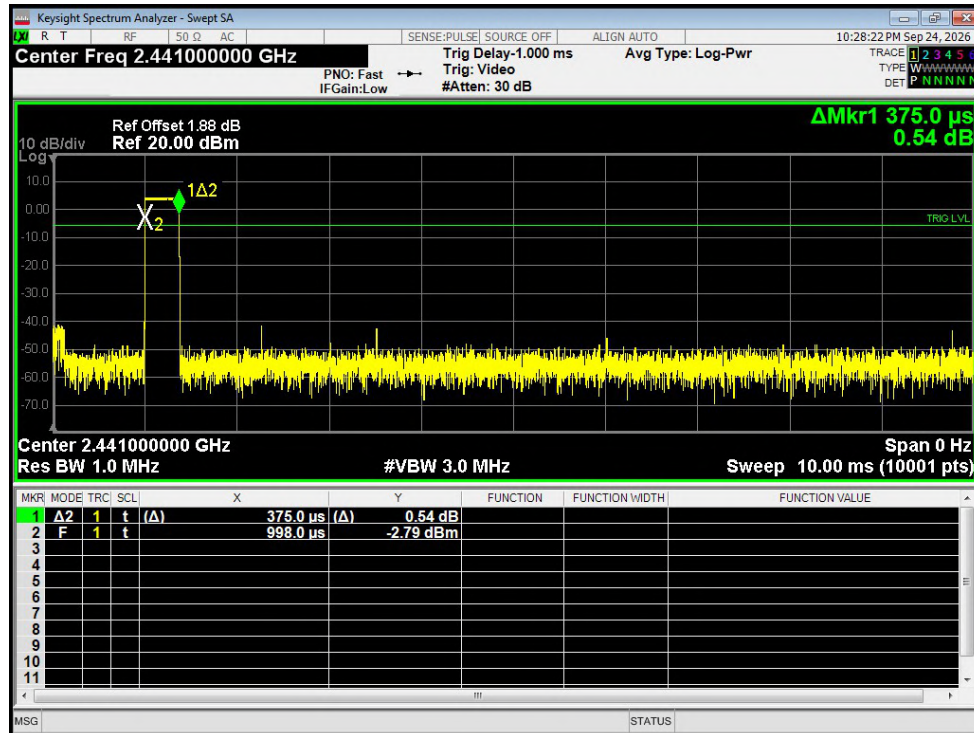


Measurement Result

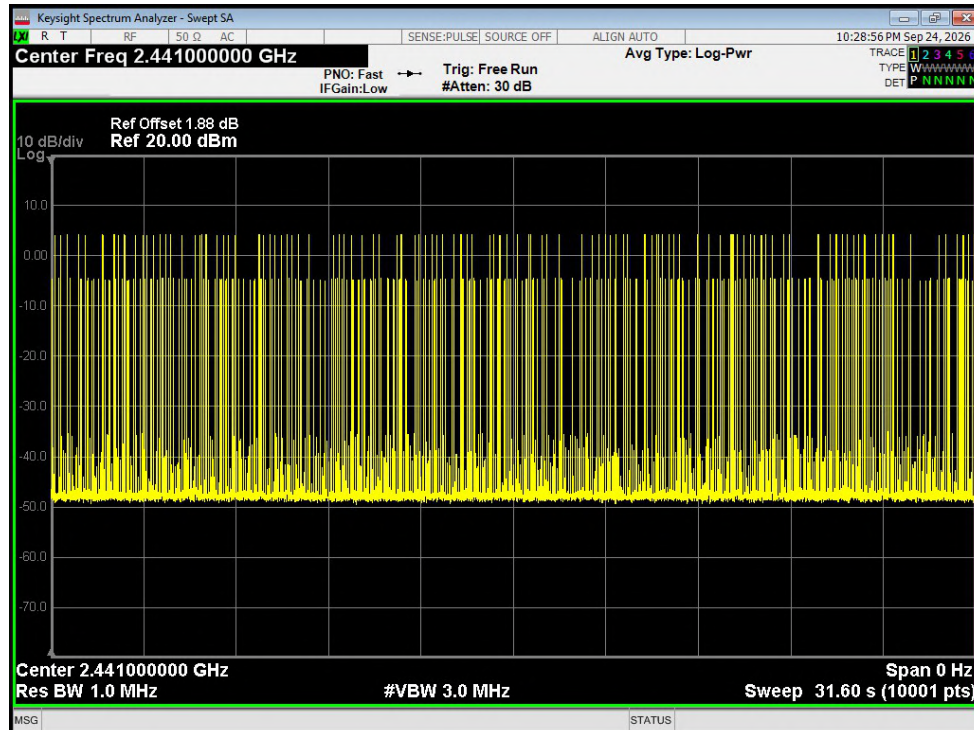
Mode	Frequency (MHz)	Pulse Time (ms)	Total Dwell Time (ms)	Burst Count	Period Time (ms)	Limit (ms)	Result
1-DH1	2441	0.375	44.25	118	31600	400	Pass
1-DH3	2441	1.631	76.657	47	31600	400	Pass
1-DH5	2441	2.879	106.523	37	31600	400	Pass
2-DH1	2441	0.381	1.143	3	31600	400	Pass
2-DH3	2441	1.635	104.64	64	31600	400	Pass
2-DH5	2441	2.882	126.808	44	31600	400	Pass
3-DH1	2441	0.382	41.638	109	31600	400	Pass
3-DH3	2441	1.633	97.98	60	31600	400	Pass
3-DH5	2441	2.883	8.649	3	31600	400	Pass

Test plot as follows:

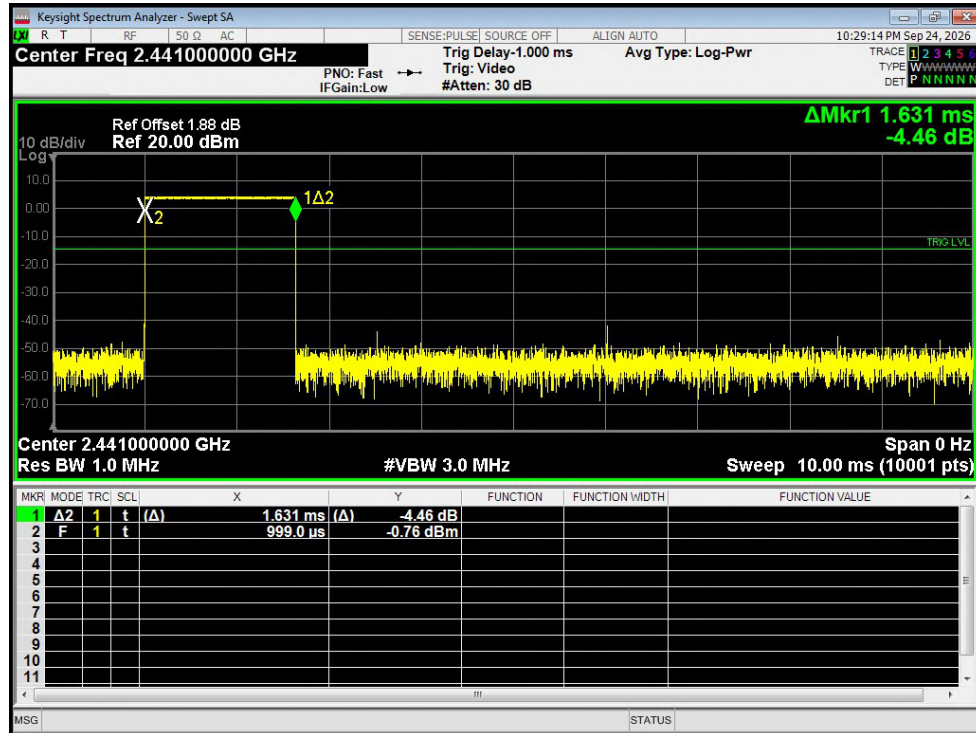
Dwell NVNT 1-DH1 2441MHz Ant1 One Burst



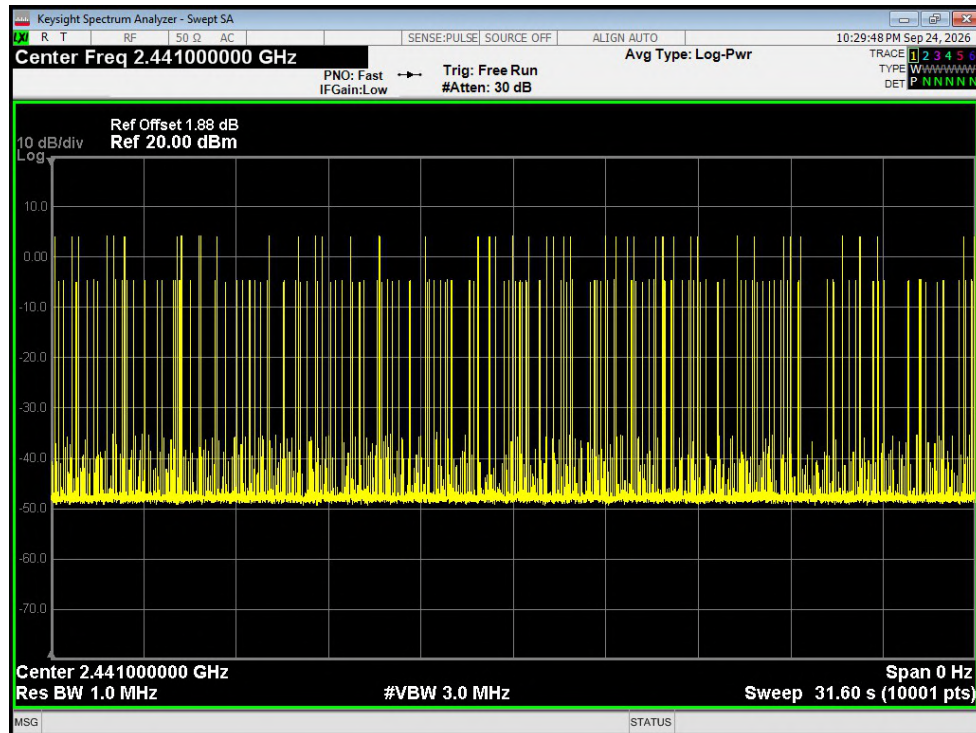
Dwell NVNT 1-DH1 2441MHz Ant1 Accumulated



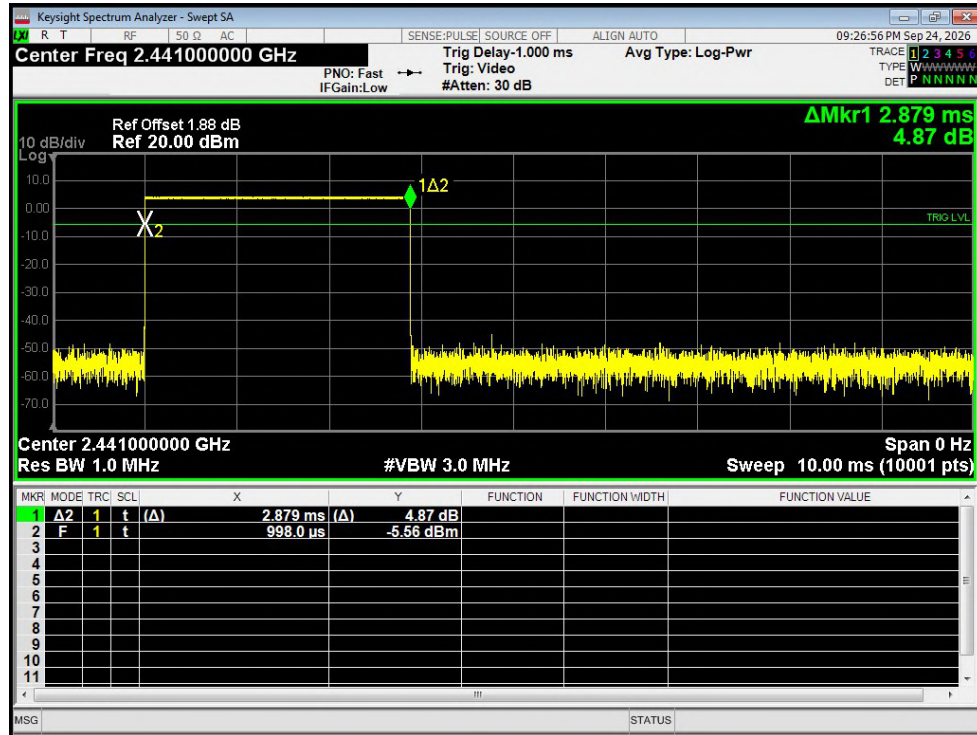
Dwell NVNT 1-DH3 2441MHz Ant1 One Burst



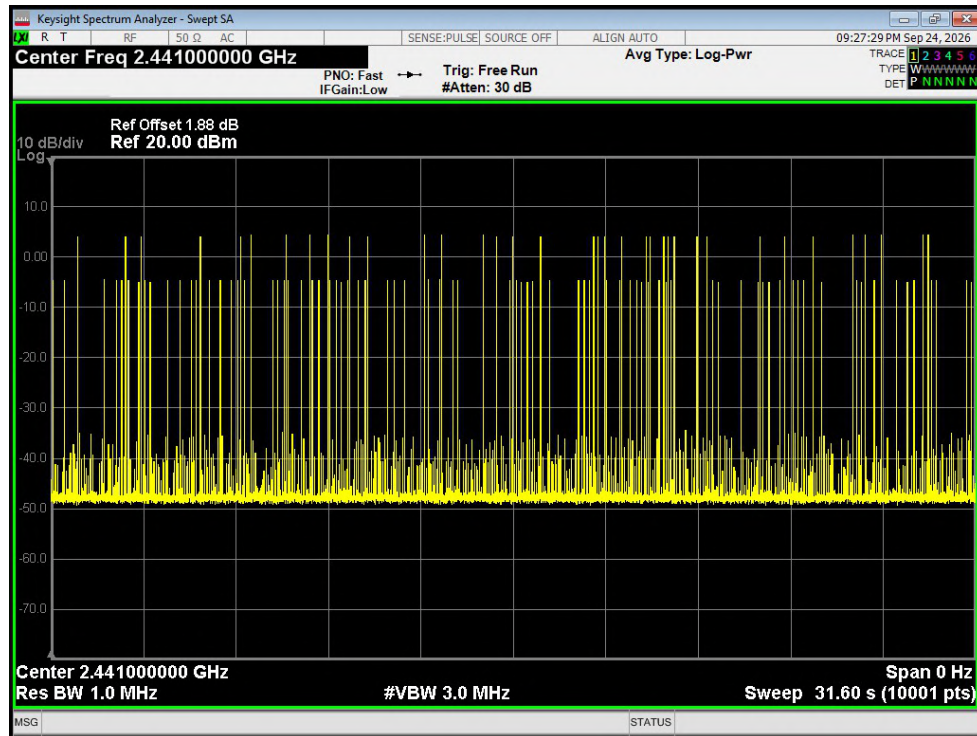
Dwell NVNT 1-DH3 2441MHz Ant1 Accumulated



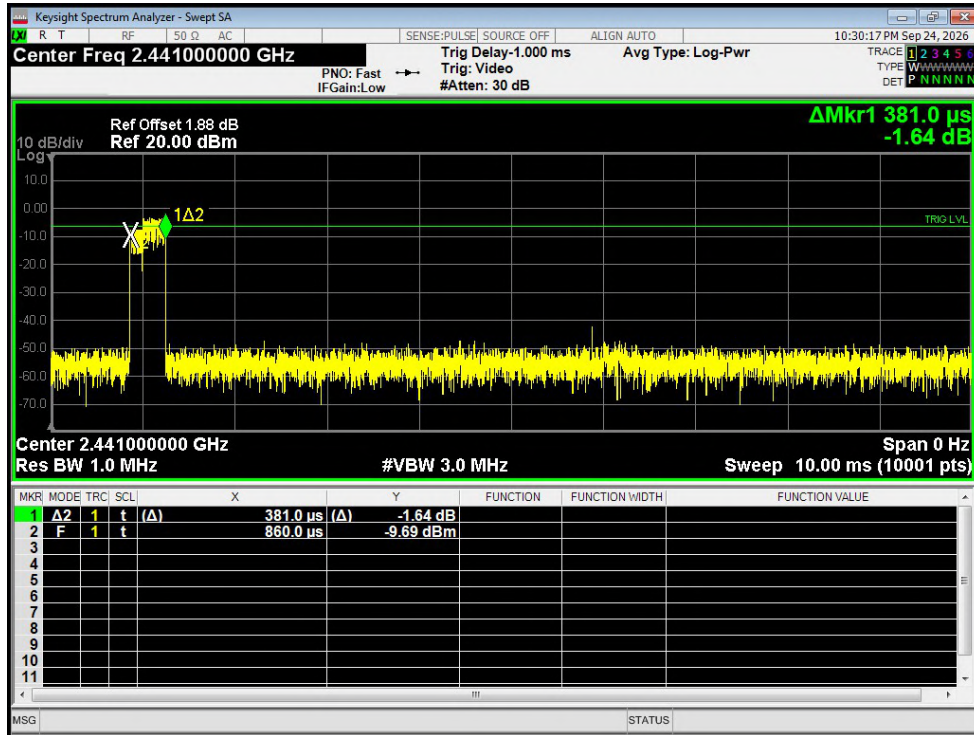
Dwell NVNT 1-DH5 2441MHz Ant1 One Burst



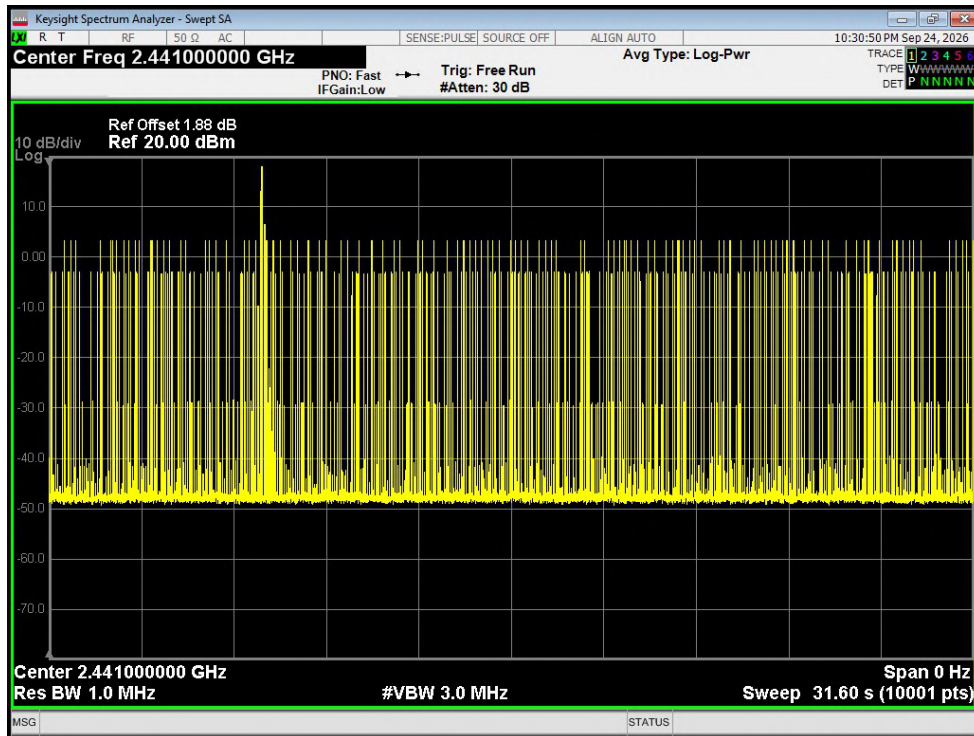
Dwell NVNT 1-DH5 2441MHz Ant1 Accumulated



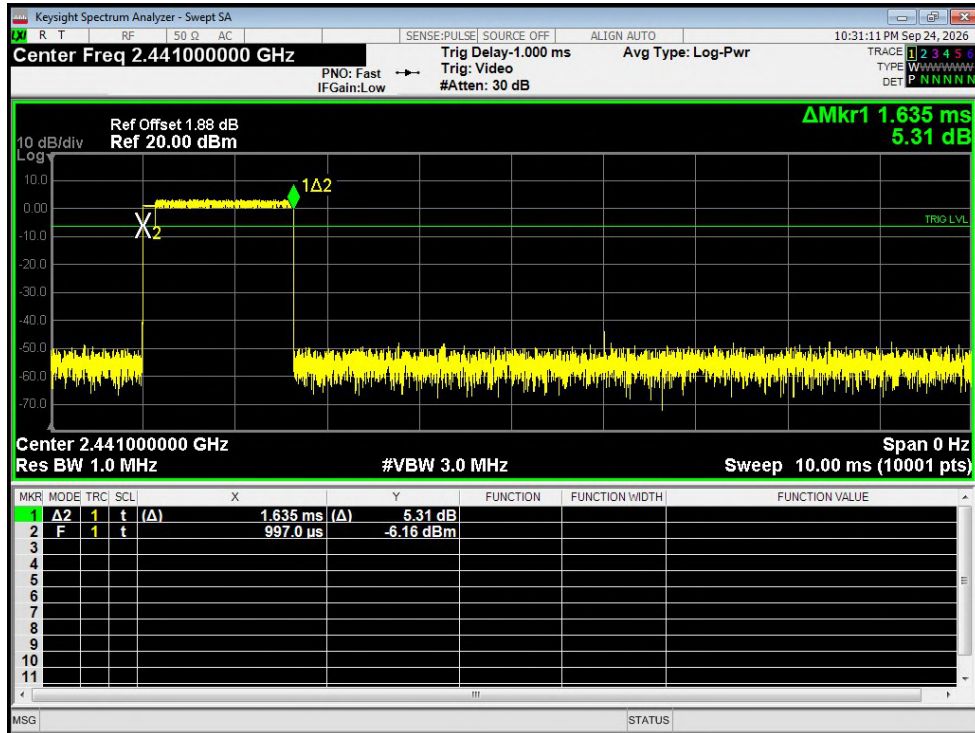
Dwell NVNT 2-DH1 2441MHz Ant1 One Burst



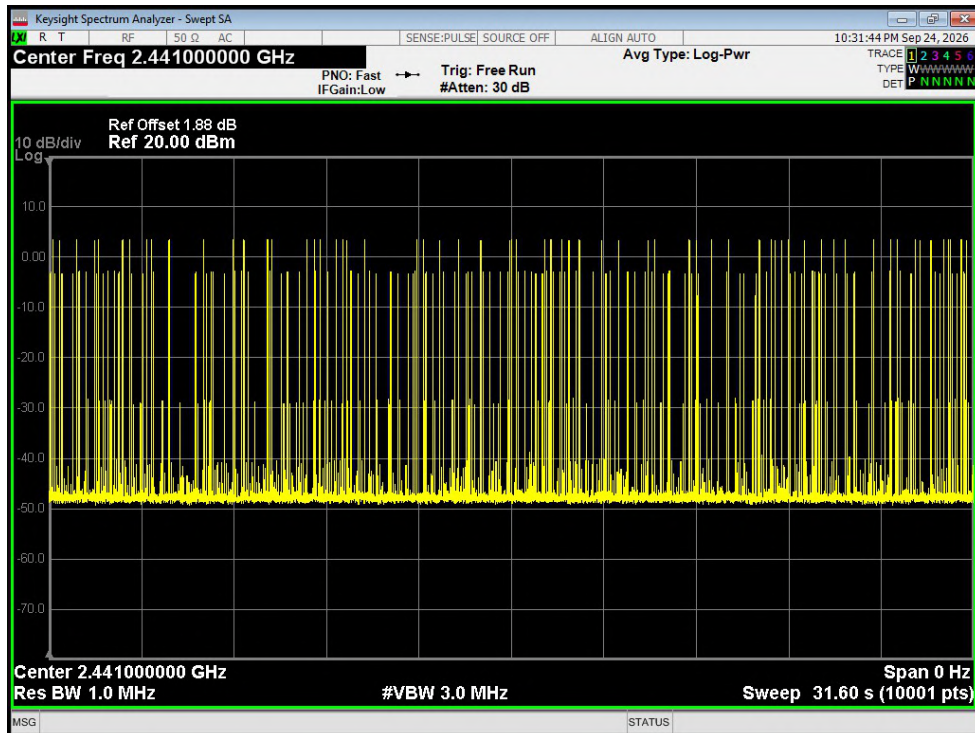
Dwell NVNT 2-DH1 2441MHz Ant1 Accumulated



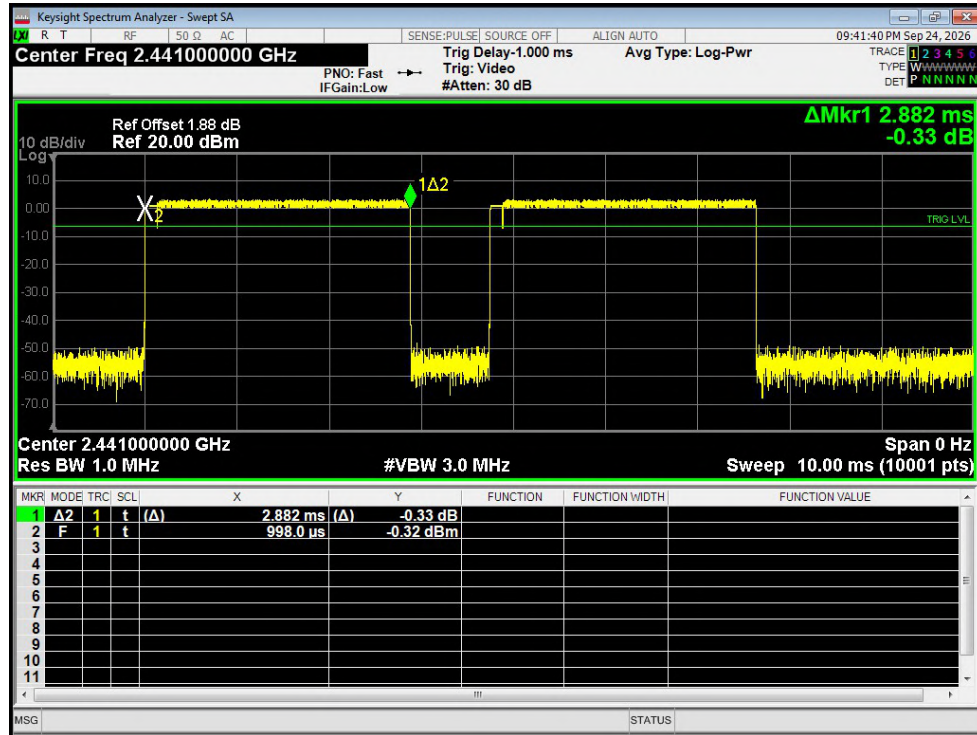
Dwell NVNT 2-DH3 2441MHz Ant1 One Burst



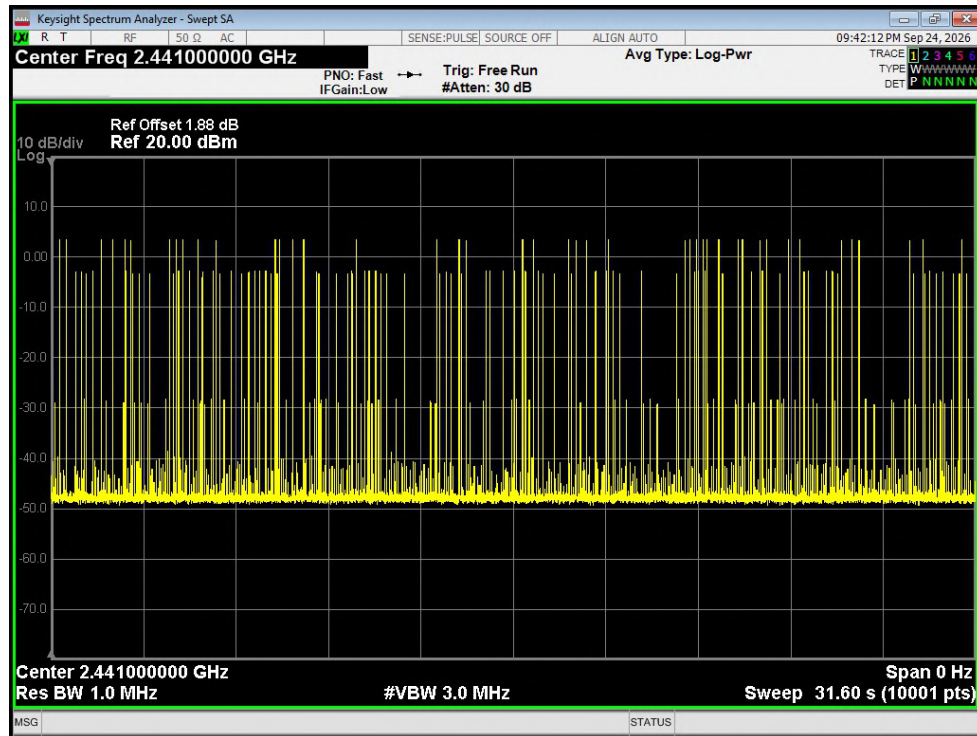
Dwell NVNT 2-DH3 2441MHz Ant1 Accumulated



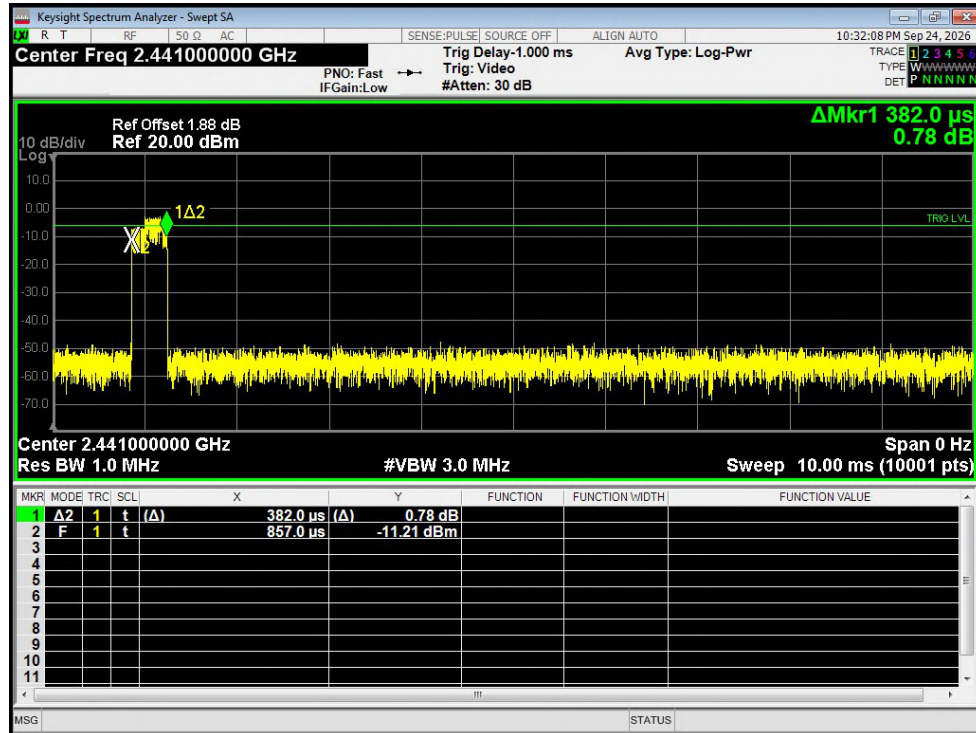
Dwell NVNT 2-DH5 2441MHz Ant1 One Burst



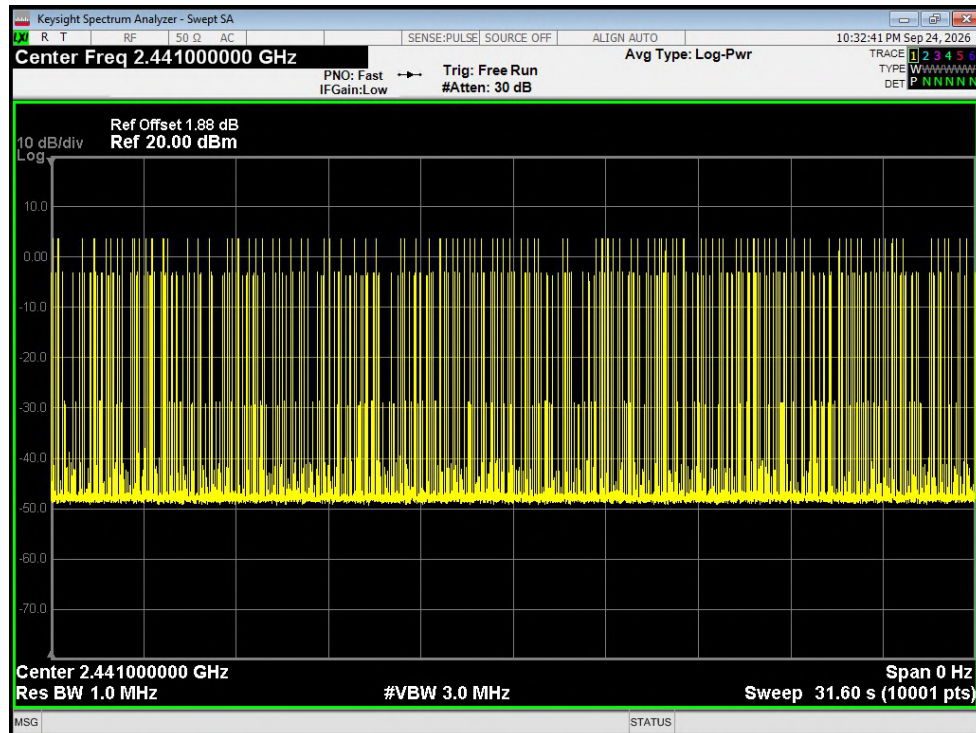
Dwell NVNT 2-DH5 2441MHz Ant1 Accumulated



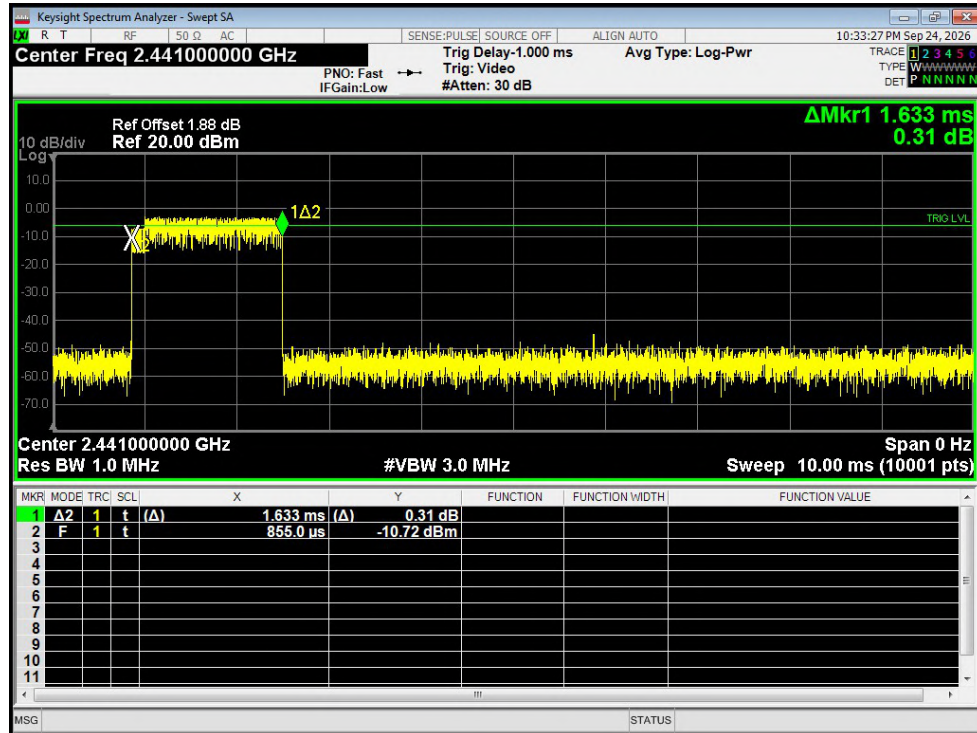
Dwell NVNT 3-DH1 2441MHz Ant1 One Burst



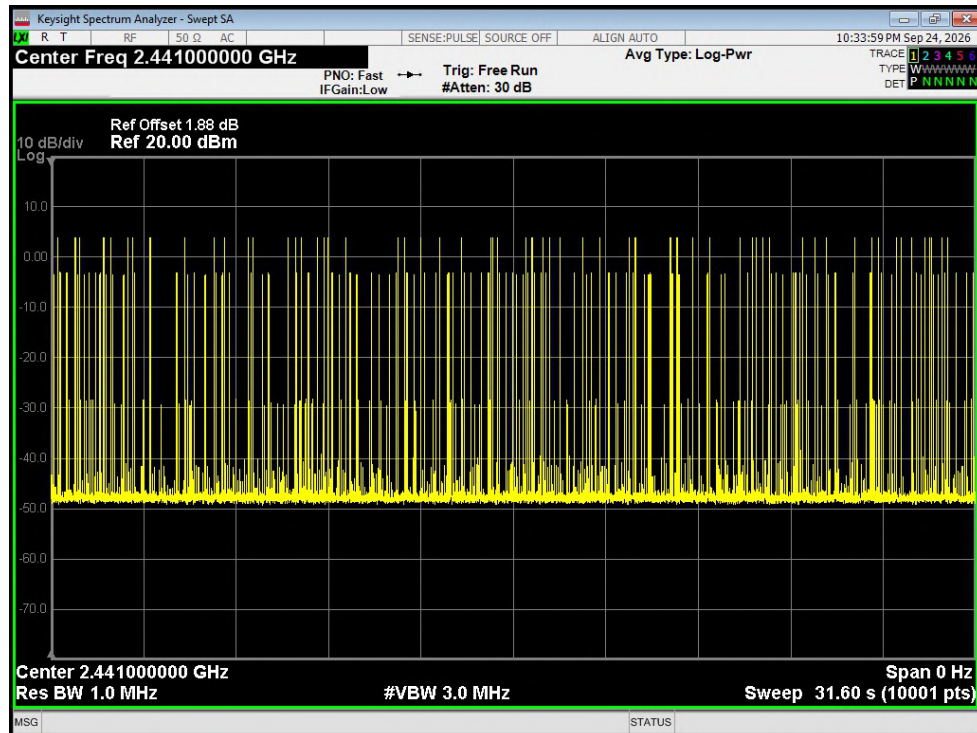
Dwell NVNT 3-DH1 2441MHz Ant1 Accumulated



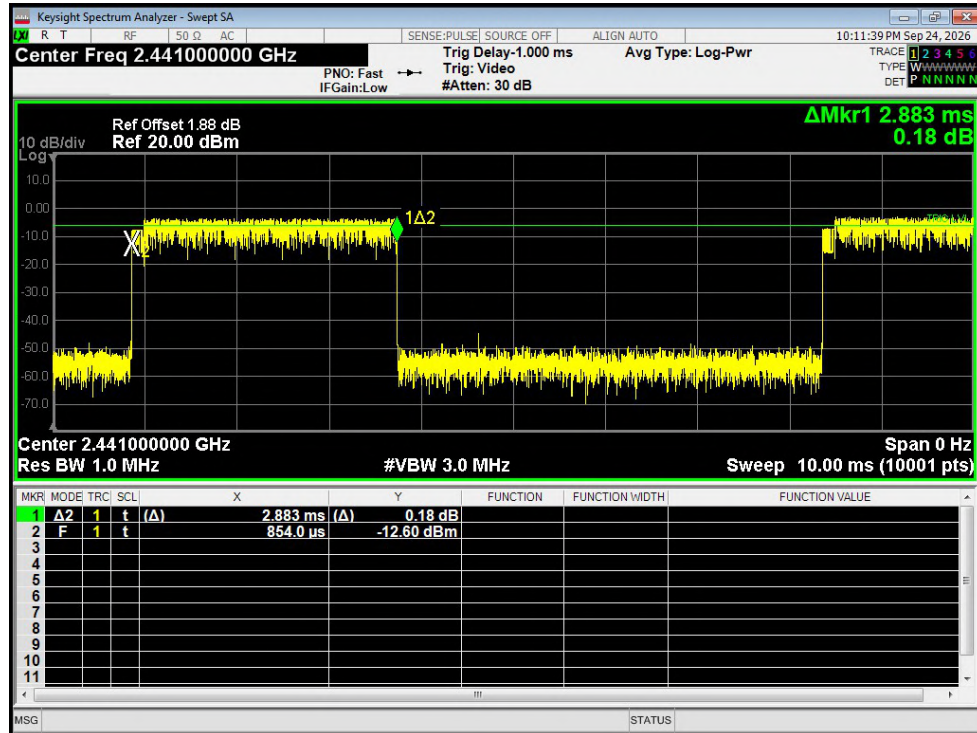
Dwell NVNT 3-DH3 2441MHz Ant1 One Burst



Dwell NVNT 3-DH3 2441MHz Ant1 Accumulated



Dwell NVNT 3-DH5 2441MHz Ant1 One Burst



Dwell NVNT 3-DH5 2441MHz Ant1 Accumulated

