



Appendix A: Effective (Isotropic) Radiated Power Output Data

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	EIRP (dBm)	Power Class	Verdict
N41	30	20	DFT-PI2BPSK	L	Edge_1RB_Left	19.86	19.76	PC3	PASS
N41	30	20	DFT-PI2BPSK	L	Edge_1RB_Right	20.06	19.96	PC3	PASS
N41	30	20	DFT-PI2BPSK	L	Outer_Full	19.12	19.02	PC3	PASS
N41	30	20	DFT-PI2BPSK	L	Inner_Full	18.68	18.58	PC3	PASS
N41	30	20	DFT-QPSK	L	Edge_1RB_Left	19.99	19.89	PC3	PASS
N41	30	20	DFT-QPSK	L	Edge_1RB_Right	20.10	20	PC3	PASS
N41	30	20	DFT-QPSK	L	Outer_Full	19.13	19.03	PC3	PASS
N41	30	20	DFT-QPSK	L	Inner_Full	18.71	18.61	PC3	PASS
N41	30	20	CP-QPSK	L	Edge_1RB_Left	9.98	9.88	PC3	PASS
N41	30	20	CP-QPSK	L	Edge_1RB_Right	10.01	9.91	PC3	PASS
N41	30	20	CP-QPSK	L	Outer_Full	10.03	9.93	PC3	PASS
N41	30	20	CP-QPSK	L	Inner_Full	10.04	9.94	PC3	PASS
N41	30	20	CP-16QAM	L	Edge_1RB_Left	10.05	9.95	PC3	PASS
N41	30	20	CP-16QAM	L	Edge_1RB_Right	10.13	10.03	PC3	PASS
N41	30	20	CP-16QAM	L	Outer_Full	10.06	9.96	PC3	PASS
N41	30	20	CP-16QAM	L	Inner_Full	10.06	9.96	PC3	PASS
N41	30	20	CP-64QAM	L	Edge_1RB_Left	10.06	9.96	PC3	PASS
N41	30	20	CP-64QAM	L	Edge_1RB_Right	10.14	10.04	PC3	PASS
N41	30	20	CP-64QAM	L	Outer_Full	10.15	10.05	PC3	PASS
N41	30	20	CP-64QAM	L	Inner_Full	10.16	10.06	PC3	PASS
N41	30	20	CP-256QAM	L	Edge_1RB_Left	20.04	19.94	PC3	PASS
N41	30	20	CP-256QAM	L	Edge_1RB_Right	20.22	20.12	PC3	PASS
N41	30	20	CP-256QAM	L	Outer_Full	19.31	19.21	PC3	PASS
N41	30	20	CP-256QAM	L	Inner_Full	19.32	19.22	PC3	PASS
N41	30	20	DFT-PI2BPSK	M	Edge_1RB_Left	22.94	22.84	PC3	PASS
N41	30	20	DFT-PI2BPSK	M	Edge_1RB_Right	19.89	19.79	PC3	PASS
N41	30	20	DFT-PI2BPSK	M	Outer_Full	18.76	18.66	PC3	PASS
N41	30	20	DFT-PI2BPSK	M	Inner_Full	18.31	18.21	PC3	PASS
N41	30	20	DFT-QPSK	M	Edge_1RB_Left	19.39	19.29	PC3	PASS
N41	30	20	DFT-QPSK	M	Edge_1RB_Right	19.76	19.66	PC3	PASS
N41	30	20	DFT-QPSK	M	Outer_Full	18.68	18.58	PC3	PASS
N41	30	20	DFT-QPSK	M	Inner_Full	18.25	18.15	PC3	PASS



N41	30	20	CP-QPSK	M	Edge_1RB_Left	19.14	19.04	PC3	PASS
N41	30	20	CP-QPSK	M	Edge_1RB_Right	19.69	19.59	PC3	PASS
N41	30	20	CP-QPSK	M	Outer_Full	18.80	18.7	PC3	PASS
N41	30	20	CP-QPSK	M	Inner_Full	18.72	18.62	PC3	PASS
N41	30	20	CP-16QAM	M	Edge_1RB_Left	19.15	19.05	PC3	PASS
N41	30	20	CP-16QAM	M	Edge_1RB_Right	19.69	19.59	PC3	PASS
N41	30	20	CP-16QAM	M	Outer_Full	18.72	18.62	PC3	PASS
N41	30	20	CP-16QAM	M	Inner_Full	18.65	18.55	PC3	PASS
N41	30	20	CP-64QAM	M	Edge_1RB_Left	19.35	19.25	PC3	PASS
N41	30	20	CP-64QAM	M	Edge_1RB_Right	19.92	19.82	PC3	PASS
N41	30	20	CP-64QAM	M	Outer_Full	18.68	18.58	PC3	PASS
N41	30	20	CP-64QAM	M	Inner_Full	18.64	18.54	PC3	PASS
N41	30	20	CP-256QAM	M	Edge_1RB_Left	19.22	19.12	PC3	PASS
N41	30	20	CP-256QAM	M	Edge_1RB_Right	19.72	19.62	PC3	PASS
N41	30	20	CP-256QAM	M	Outer_Full	18.65	18.55	PC3	PASS
N41	30	20	CP-256QAM	M	Inner_Full	18.57	18.47	PC3	PASS
N41	30	20	DFT-PI2BPSK	H	Edge_1RB_Left	23.24	23.14	PC3	PASS
N41	30	20	DFT-PI2BPSK	H	Edge_1RB_Right	20.32	20.22	PC3	PASS
N41	30	20	DFT-PI2BPSK	H	Outer_Full	19.05	18.95	PC3	PASS
N41	30	20	DFT-PI2BPSK	H	Inner_Full	18.57	18.47	PC3	PASS
N41	30	20	DFT-QPSK	H	Edge_1RB_Left	19.65	19.55	PC3	PASS
N41	30	20	DFT-QPSK	H	Edge_1RB_Right	20.20	20.1	PC3	PASS
N41	30	20	DFT-QPSK	H	Outer_Full	18.96	18.86	PC3	PASS
N41	30	20	DFT-QPSK	H	Inner_Full	18.49	18.39	PC3	PASS
N41	30	20	CP-QPSK	H	Edge_1RB_Left	19.43	19.33	PC3	PASS
N41	30	20	CP-QPSK	H	Edge_1RB_Right	20.22	20.12	PC3	PASS
N41	30	20	CP-QPSK	H	Outer_Full	19.19	19.09	PC3	PASS
N41	30	20	CP-QPSK	H	Inner_Full	19.06	18.96	PC3	PASS
N41	30	20	CP-16QAM	H	Edge_1RB_Left	19.57	19.47	PC3	PASS
N41	30	20	CP-16QAM	H	Edge_1RB_Right	20.23	20.13	PC3	PASS
N41	30	20	CP-16QAM	H	Outer_Full	19.10	19	PC3	PASS
N41	30	20	CP-16QAM	H	Inner_Full	18.97	18.87	PC3	PASS
N41	30	20	CP-64QAM	H	Edge_1RB_Left	19.73	19.63	PC3	PASS
N41	30	20	CP-64QAM	H	Edge_1RB_Right	20.38	20.28	PC3	PASS
N41	30	20	CP-64QAM	H	Outer_Full	19.06	18.96	PC3	PASS
N41	30	20	CP-64QAM	H	Inner_Full	18.96	18.86	PC3	PASS
N41	30	20	CP-256QAM	H	Edge_1RB_Left	19.68	19.58	PC3	PASS
N41	30	20	CP-256QAM	H	Edge_1RB_Right	20.23	20.13	PC3	PASS
N41	30	20	CP-256QAM	H	Outer_Full	19.05	18.95	PC3	PASS
N41	30	20	CP-256QAM	H	Inner_Full	18.91	18.81	PC3	PASS



N41	30	30	DFT-PI2BPSK	L	Edge_1RB_Left	19.95	19.85	PC3	PASS
N41	30	30	DFT-PI2BPSK	L	Edge_1RB_Right	20.08	19.98	PC3	PASS
N41	30	30	DFT-PI2BPSK	L	Outer_Full	19.06	18.96	PC3	PASS
N41	30	30	DFT-PI2BPSK	L	Inner_Full	18.60	18.5	PC3	PASS
N41	30	30	DFT-QPSK	L	Edge_1RB_Left	20.09	19.99	PC3	PASS
N41	30	30	DFT-QPSK	L	Edge_1RB_Right	20.05	19.95	PC3	PASS
N41	30	30	DFT-QPSK	L	Outer_Full	19.08	18.98	PC3	PASS
N41	30	30	DFT-QPSK	L	Inner_Full	18.62	18.52	PC3	PASS
N41	30	30	CP-QPSK	L	Edge_1RB_Left	20.03	19.93	PC3	PASS
N41	30	30	CP-QPSK	L	Edge_1RB_Right	20.09	19.99	PC3	PASS
N41	30	30	CP-QPSK	L	Outer_Full	19.20	19.1	PC3	PASS
N41	30	30	CP-QPSK	L	Inner_Full	18.76	18.66	PC3	PASS
N41	30	30	CP-16QAM	L	Edge_1RB_Left	20.08	19.98	PC3	PASS
N41	30	30	CP-16QAM	L	Edge_1RB_Right	20.12	20.02	PC3	PASS
N41	30	30	CP-16QAM	L	Outer_Full	19.18	19.08	PC3	PASS
N41	30	30	CP-16QAM	L	Inner_Full	18.72	18.62	PC3	PASS
N41	30	30	CP-64QAM	L	Edge_1RB_Left	20.28	20.18	PC3	PASS
N41	30	30	CP-64QAM	L	Edge_1RB_Right	20.30	20.2	PC3	PASS
N41	30	30	CP-64QAM	L	Outer_Full	19.14	19.04	PC3	PASS
N41	30	30	CP-64QAM	L	Inner_Full	18.72	18.62	PC3	PASS
N41	30	30	CP-256QAM	L	Edge_1RB_Left	20.14	20.04	PC3	PASS
N41	30	30	CP-256QAM	L	Edge_1RB_Right	20.11	20.01	PC3	PASS
N41	30	30	CP-256QAM	L	Outer_Full	19.13	19.03	PC3	PASS
N41	30	30	CP-256QAM	L	Inner_Full	18.68	18.58	PC3	PASS
N41	30	30	DFT-PI2BPSK	M	Edge_1RB_Left	23.12	23.02	PC3	PASS
N41	30	30	DFT-PI2BPSK	M	Edge_1RB_Right	19.98	19.88	PC3	PASS
N41	30	30	DFT-PI2BPSK	M	Outer_Full	18.80	18.7	PC3	PASS
N41	30	30	DFT-PI2BPSK	M	Inner_Full	18.38	18.28	PC3	PASS
N41	30	30	DFT-QPSK	M	Edge_1RB_Left	19.65	19.55	PC3	PASS
N41	30	30	DFT-QPSK	M	Edge_1RB_Right	19.90	19.8	PC3	PASS
N41	30	30	DFT-QPSK	M	Outer_Full	18.71	18.61	PC3	PASS
N41	30	30	DFT-QPSK	M	Inner_Full	18.29	18.19	PC3	PASS
N41	30	30	CP-QPSK	M	Edge_1RB_Left	19.48	19.38	PC3	PASS
N41	30	30	CP-QPSK	M	Edge_1RB_Right	19.83	19.73	PC3	PASS
N41	30	30	CP-QPSK	M	Outer_Full	18.83	18.73	PC3	PASS
N41	30	30	CP-QPSK	M	Inner_Full	18.44	18.34	PC3	PASS
N41	30	30	CP-16QAM	M	Edge_1RB_Left	19.49	19.39	PC3	PASS
N41	30	30	CP-16QAM	M	Edge_1RB_Right	19.82	19.72	PC3	PASS
N41	30	30	CP-16QAM	M	Outer_Full	18.77	18.67	PC3	PASS
N41	30	30	CP-16QAM	M	Inner_Full	18.36	18.26	PC3	PASS



N41	30	30	CP-64QAM	M	Edge_1RB_Left	19.64	19.54	PC3	PASS
N41	30	30	CP-64QAM	M	Edge_1RB_Right	19.97	19.87	PC3	PASS
N41	30	30	CP-64QAM	M	Outer_Full	18.71	18.61	PC3	PASS
N41	30	30	CP-64QAM	M	Inner_Full	18.35	18.25	PC3	PASS
N41	30	30	CP-256QAM	M	Edge_1RB_Left	19.49	19.39	PC3	PASS
N41	30	30	CP-256QAM	M	Edge_1RB_Right	19.75	19.65	PC3	PASS
N41	30	30	CP-256QAM	M	Outer_Full	18.69	18.59	PC3	PASS
N41	30	30	CP-256QAM	M	Inner_Full	18.28	18.18	PC3	PASS
N41	30	30	DFT-PI2BPSK	H	Edge_1RB_Left	23.35	23.25	PC3	PASS
N41	30	30	DFT-PI2BPSK	H	Edge_1RB_Right	20.07	19.97	PC3	PASS
N41	30	30	DFT-PI2BPSK	H	Outer_Full	18.87	18.77	PC3	PASS
N41	30	30	DFT-PI2BPSK	H	Inner_Full	18.37	18.27	PC3	PASS
N41	30	30	DFT-QPSK	H	Edge_1RB_Left	19.85	19.75	PC3	PASS
N41	30	30	DFT-QPSK	H	Edge_1RB_Right	20.02	19.92	PC3	PASS
N41	30	30	DFT-QPSK	H	Outer_Full	18.77	18.67	PC3	PASS
N41	30	30	DFT-QPSK	H	Inner_Full	18.29	18.19	PC3	PASS
N41	30	30	CP-QPSK	H	Edge_1RB_Left	19.60	19.5	PC3	PASS
N41	30	30	CP-QPSK	H	Edge_1RB_Right	19.93	19.83	PC3	PASS
N41	30	30	CP-QPSK	H	Outer_Full	19.04	18.94	PC3	PASS
N41	30	30	CP-QPSK	H	Inner_Full	18.59	18.49	PC3	PASS
N41	30	30	CP-16QAM	H	Edge_1RB_Left	19.73	19.63	PC3	PASS
N41	30	30	CP-16QAM	H	Edge_1RB_Right	20.06	19.96	PC3	PASS
N41	30	30	CP-16QAM	H	Outer_Full	18.97	18.87	PC3	PASS
N41	30	30	CP-16QAM	H	Inner_Full	18.50	18.4	PC3	PASS
N41	30	30	CP-64QAM	H	Edge_1RB_Left	19.87	19.77	PC3	PASS
N41	30	30	CP-64QAM	H	Edge_1RB_Right	20.21	20.11	PC3	PASS
N41	30	30	CP-64QAM	H	Outer_Full	18.90	18.8	PC3	PASS
N41	30	30	CP-64QAM	H	Inner_Full	18.47	18.37	PC3	PASS
N41	30	30	CP-256QAM	H	Edge_1RB_Left	19.71	19.61	PC3	PASS
N41	30	30	CP-256QAM	H	Edge_1RB_Right	19.98	19.88	PC3	PASS
N41	30	30	CP-256QAM	H	Outer_Full	18.85	18.75	PC3	PASS
N41	30	30	CP-256QAM	H	Inner_Full	18.38	18.28	PC3	PASS
N41	30	40	DFT-PI2BPSK	L	Edge_1RB_Left	20.99	20.89	PC3	PASS
N41	30	40	DFT-PI2BPSK	L	Edge_1RB_Right	20.82	20.72	PC3	PASS
N41	30	40	DFT-PI2BPSK	L	Outer_Full	19.62	19.52	PC3	PASS
N41	30	40	DFT-PI2BPSK	L	Inner_Full	18.94	18.84	PC3	PASS
N41	30	40	DFT-QPSK	L	Edge_1RB_Left	21.00	20.9	PC3	PASS
N41	30	40	DFT-QPSK	L	Edge_1RB_Right	20.77	20.67	PC3	PASS
N41	30	40	DFT-QPSK	L	Outer_Full	19.63	19.53	PC3	PASS
N41	30	40	DFT-QPSK	L	Inner_Full	18.96	18.86	PC3	PASS



N41	30	40	CP-QPSK	L	Edge_1RB_Left	10.87	10.77	PC3	PASS
N41	30	40	CP-QPSK	L	Edge_1RB_Right	10.98	10.88	PC3	PASS
N41	30	40	CP-QPSK	L	Outer_Full	10.91	10.81	PC3	PASS
N41	30	40	CP-QPSK	L	Inner_Full	10.92	10.82	PC3	PASS
N41	30	40	CP-16QAM	L	Edge_1RB_Left	11.01	10.91	PC3	PASS
N41	30	40	CP-16QAM	L	Edge_1RB_Right	10.95	10.85	PC3	PASS
N41	30	40	CP-16QAM	L	Outer_Full	10.94	10.84	PC3	PASS
N41	30	40	CP-16QAM	L	Inner_Full	11.02	10.92	PC3	PASS
N41	30	40	CP-64QAM	L	Edge_1RB_Left	11.03	10.93	PC3	PASS
N41	30	40	CP-64QAM	L	Edge_1RB_Right	10.95	10.85	PC3	PASS
N41	30	40	CP-64QAM	L	Outer_Full	10.87	10.77	PC3	PASS
N41	30	40	CP-64QAM	L	Inner_Full	10.95	10.85	PC3	PASS
N41	30	40	CP-256QAM	L	Edge_1RB_Left	21.54	21.44	PC3	PASS
N41	30	40	CP-256QAM	L	Edge_1RB_Right	21.07	20.97	PC3	PASS
N41	30	40	CP-256QAM	L	Outer_Full	20.05	19.95	PC3	PASS
N41	30	40	CP-256QAM	L	Inner_Full	19.30	19.2	PC3	PASS
N41	30	40	DFT-PI2BPSK	M	Edge_1RB_Left	20.22	20.12	PC3	PASS
N41	30	40	DFT-PI2BPSK	M	Edge_1RB_Right	20.48	20.38	PC3	PASS
N41	30	40	DFT-PI2BPSK	M	Outer_Full	19.06	18.96	PC3	PASS
N41	30	40	DFT-PI2BPSK	M	Inner_Full	18.46	18.36	PC3	PASS
N41	30	40	DFT-QPSK	M	Edge_1RB_Left	20.32	20.22	PC3	PASS
N41	30	40	DFT-QPSK	M	Edge_1RB_Right	20.51	20.41	PC3	PASS
N41	30	40	DFT-QPSK	M	Outer_Full	19.02	18.92	PC3	PASS
N41	30	40	DFT-QPSK	M	Inner_Full	18.40	18.3	PC3	PASS
N41	30	40	CP-QPSK	M	Edge_1RB_Left	20.03	19.93	PC3	PASS
N41	30	40	CP-QPSK	M	Edge_1RB_Right	20.36	20.26	PC3	PASS
N41	30	40	CP-QPSK	M	Outer_Full	19.21	19.11	PC3	PASS
N41	30	40	CP-QPSK	M	Inner_Full	18.55	18.45	PC3	PASS
N41	30	40	CP-16QAM	M	Edge_1RB_Left	20.04	19.94	PC3	PASS
N41	30	40	CP-16QAM	M	Edge_1RB_Right	20.37	20.27	PC3	PASS
N41	30	40	CP-16QAM	M	Outer_Full	19.14	19.04	PC3	PASS
N41	30	40	CP-16QAM	M	Inner_Full	18.48	18.38	PC3	PASS
N41	30	40	CP-64QAM	M	Edge_1RB_Left	20.18	20.08	PC3	PASS
N41	30	40	CP-64QAM	M	Edge_1RB_Right	20.54	20.44	PC3	PASS
N41	30	40	CP-64QAM	M	Outer_Full	19.10	19	PC3	PASS
N41	30	40	CP-64QAM	M	Inner_Full	18.42	18.32	PC3	PASS
N41	30	40	CP-256QAM	M	Edge_1RB_Left	20.09	19.99	PC3	PASS
N41	30	40	CP-256QAM	M	Edge_1RB_Right	20.39	20.29	PC3	PASS
N41	30	40	CP-256QAM	M	Outer_Full	19.11	19.01	PC3	PASS
N41	30	40	CP-256QAM	M	Inner_Full	18.45	18.35	PC3	PASS



N41	30	40	DFT-PI2BPSK	H	Edge_1RB_Left	24.04	23.94	PC3	PASS
N41	30	40	DFT-PI2BPSK	H	Edge_1RB_Right	20.83	20.73	PC3	PASS
N41	30	40	DFT-PI2BPSK	H	Outer_Full	19.40	19.3	PC3	PASS
N41	30	40	DFT-PI2BPSK	H	Inner_Full	18.72	18.62	PC3	PASS
N41	30	40	DFT-QPSK	H	Edge_1RB_Left	20.54	20.44	PC3	PASS
N41	30	40	DFT-QPSK	H	Edge_1RB_Right	20.68	20.58	PC3	PASS
N41	30	40	DFT-QPSK	H	Outer_Full	19.29	19.19	PC3	PASS
N41	30	40	DFT-QPSK	H	Inner_Full	18.63	18.53	PC3	PASS
N41	30	40	CP-QPSK	H	Edge_1RB_Left	20.33	20.23	PC3	PASS
N41	30	40	CP-QPSK	H	Edge_1RB_Right	20.58	20.48	PC3	PASS
N41	30	40	CP-QPSK	H	Outer_Full	19.53	19.43	PC3	PASS
N41	30	40	CP-QPSK	H	Inner_Full	18.86	18.76	PC3	PASS
N41	30	40	CP-16QAM	H	Edge_1RB_Left	20.42	20.32	PC3	PASS
N41	30	40	CP-16QAM	H	Edge_1RB_Right	20.69	20.59	PC3	PASS
N41	30	40	CP-16QAM	H	Outer_Full	19.47	19.37	PC3	PASS
N41	30	40	CP-16QAM	H	Inner_Full	18.77	18.67	PC3	PASS
N41	30	40	CP-64QAM	H	Edge_1RB_Left	20.57	20.47	PC3	PASS
N41	30	40	CP-64QAM	H	Edge_1RB_Right	20.75	20.65	PC3	PASS
N41	30	40	CP-64QAM	H	Outer_Full	19.42	19.32	PC3	PASS
N41	30	40	CP-64QAM	H	Inner_Full	18.71	18.61	PC3	PASS
N41	30	40	CP-256QAM	H	Edge_1RB_Left	20.42	20.32	PC3	PASS
N41	30	40	CP-256QAM	H	Edge_1RB_Right	20.63	20.53	PC3	PASS
N41	30	40	CP-256QAM	H	Outer_Full	19.39	19.29	PC3	PASS
N41	30	40	CP-256QAM	H	Inner_Full	18.71	18.61	PC3	PASS
N41	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	23.22	23.12	PC3	PASS
N41	30	50	DFT-PI2BPSK	L	Edge_1RB_Right	19.72	19.62	PC3	PASS
N41	30	50	DFT-PI2BPSK	L	Outer_Full	19.92	19.82	PC3	PASS
N41	30	50	DFT-PI2BPSK	L	Inner_Full	19.20	19.1	PC3	PASS
N41	30	50	DFT-QPSK	L	Edge_1RB_Left	19.83	19.73	PC3	PASS
N41	30	50	DFT-QPSK	L	Edge_1RB_Right	19.57	19.47	PC3	PASS
N41	30	50	DFT-QPSK	L	Outer_Full	19.82	19.72	PC3	PASS
N41	30	50	DFT-QPSK	L	Inner_Full	19.12	19.02	PC3	PASS
N41	30	50	CP-QPSK	L	Edge_1RB_Left	19.68	19.58	PC3	PASS
N41	30	50	CP-QPSK	L	Edge_1RB_Right	19.67	19.57	PC3	PASS
N41	30	50	CP-QPSK	L	Outer_Full	19.79	19.69	PC3	PASS
N41	30	50	CP-QPSK	L	Inner_Full	19.12	19.02	PC3	PASS
N41	30	50	CP-16QAM	L	Edge_1RB_Left	19.60	19.5	PC3	PASS
N41	30	50	CP-16QAM	L	Edge_1RB_Right	19.68	19.58	PC3	PASS
N41	30	50	CP-16QAM	L	Outer_Full	19.77	19.67	PC3	PASS
N41	30	50	CP-16QAM	L	Inner_Full	19.12	19.02	PC3	PASS



N41	30	50	CP-64QAM	L	Edge_1RB_Left	19.84	19.74	PC3	PASS
N41	30	50	CP-64QAM	L	Edge_1RB_Right	19.85	19.75	PC3	PASS
N41	30	50	CP-64QAM	L	Outer_Full	19.71	19.61	PC3	PASS
N41	30	50	CP-64QAM	L	Inner_Full	19.07	18.97	PC3	PASS
N41	30	50	CP-256QAM	L	Edge_1RB_Left	19.67	19.57	PC3	PASS
N41	30	50	CP-256QAM	L	Edge_1RB_Right	19.60	19.5	PC3	PASS
N41	30	50	CP-256QAM	L	Outer_Full	19.70	19.6	PC3	PASS
N41	30	50	CP-256QAM	L	Inner_Full	19.04	18.94	PC3	PASS
N41	30	50	DFT-PI2BPSK	M	Edge_1RB_Left	22.23	22.13	PC3	PASS
N41	30	50	DFT-PI2BPSK	M	Edge_1RB_Right	19.64	19.54	PC3	PASS
N41	30	50	DFT-PI2BPSK	M	Outer_Full	19.39	19.29	PC3	PASS
N41	30	50	DFT-PI2BPSK	M	Inner_Full	18.68	18.58	PC3	PASS
N41	30	50	DFT-QPSK	M	Edge_1RB_Left	18.87	18.77	PC3	PASS
N41	30	50	DFT-QPSK	M	Edge_1RB_Right	19.66	19.56	PC3	PASS
N41	30	50	DFT-QPSK	M	Outer_Full	19.30	19.2	PC3	PASS
N41	30	50	DFT-QPSK	M	Inner_Full	18.62	18.52	PC3	PASS
N41	30	50	CP-QPSK	M	Edge_1RB_Left	18.83	18.73	PC3	PASS
N41	30	50	CP-QPSK	M	Edge_1RB_Right	19.63	19.53	PC3	PASS
N41	30	50	CP-QPSK	M	Outer_Full	19.32	19.22	PC3	PASS
N41	30	50	CP-QPSK	M	Inner_Full	18.70	18.6	PC3	PASS
N41	30	50	CP-16QAM	M	Edge_1RB_Left	18.78	18.68	PC3	PASS
N41	30	50	CP-16QAM	M	Edge_1RB_Right	19.67	19.57	PC3	PASS
N41	30	50	CP-16QAM	M	Outer_Full	19.30	19.2	PC3	PASS
N41	30	50	CP-16QAM	M	Inner_Full	18.64	18.54	PC3	PASS
N41	30	50	CP-64QAM	M	Edge_1RB_Left	19.00	18.9	PC3	PASS
N41	30	50	CP-64QAM	M	Edge_1RB_Right	19.83	19.73	PC3	PASS
N41	30	50	CP-64QAM	M	Outer_Full	19.23	19.13	PC3	PASS
N41	30	50	CP-64QAM	M	Inner_Full	18.58	18.48	PC3	PASS
N41	30	50	CP-256QAM	M	Edge_1RB_Left	18.78	18.68	PC3	PASS
N41	30	50	CP-256QAM	M	Edge_1RB_Right	19.69	19.59	PC3	PASS
N41	30	50	CP-256QAM	M	Outer_Full	19.24	19.14	PC3	PASS
N41	30	50	CP-256QAM	M	Inner_Full	18.59	18.49	PC3	PASS
N41	30	50	DFT-PI2BPSK	H	Edge_1RB_Left	22.38	22.28	PC3	PASS
N41	30	50	DFT-PI2BPSK	H	Edge_1RB_Right	19.88	19.78	PC3	PASS
N41	30	50	DFT-PI2BPSK	H	Outer_Full	19.57	19.47	PC3	PASS
N41	30	50	DFT-PI2BPSK	H	Inner_Full	18.88	18.78	PC3	PASS
N41	30	50	DFT-QPSK	H	Edge_1RB_Left	18.97	18.87	PC3	PASS
N41	30	50	DFT-QPSK	H	Edge_1RB_Right	19.81	19.71	PC3	PASS
N41	30	50	DFT-QPSK	H	Outer_Full	19.48	19.38	PC3	PASS
N41	30	50	DFT-QPSK	H	Inner_Full	18.77	18.67	PC3	PASS



N41	30	50	CP-QPSK	H	Edge_1RB_Left	18.89	18.79	PC3	PASS
N41	30	50	CP-QPSK	H	Edge_1RB_Right	19.78	19.68	PC3	PASS
N41	30	50	CP-QPSK	H	Outer_Full	19.46	19.36	PC3	PASS
N41	30	50	CP-QPSK	H	Inner_Full	18.84	18.74	PC3	PASS
N41	30	50	CP-16QAM	H	Edge_1RB_Left	19.21	19.11	PC3	PASS
N41	30	50	CP-16QAM	H	Edge_1RB_Right	20.05	19.95	PC3	PASS
N41	30	50	CP-16QAM	H	Outer_Full	19.72	19.62	PC3	PASS
N41	30	50	CP-16QAM	H	Inner_Full	19.07	18.97	PC3	PASS
N41	30	50	CP-64QAM	H	Edge_1RB_Left	19.14	19.04	PC3	PASS
N41	30	50	CP-64QAM	H	Edge_1RB_Right	20.00	19.9	PC3	PASS
N41	30	50	CP-64QAM	H	Outer_Full	19.65	19.55	PC3	PASS
N41	30	50	CP-64QAM	H	Inner_Full	19.01	18.91	PC3	PASS
N41	30	50	CP-256QAM	H	Edge_1RB_Left	19.19	19.09	PC3	PASS
N41	30	50	CP-256QAM	H	Edge_1RB_Right	19.90	19.8	PC3	PASS
N41	30	50	CP-256QAM	H	Outer_Full	19.59	19.49	PC3	PASS
N41	30	50	CP-256QAM	H	Inner_Full	18.98	18.88	PC3	PASS
N41	30	60	DFT-PI2BPSK	L	Edge_1RB_Left	19.45	19.35	PC3	PASS
N41	30	60	DFT-PI2BPSK	L	Edge_1RB_Right	19.09	18.99	PC3	PASS
N41	30	60	DFT-PI2BPSK	L	Outer_Full	18.87	18.77	PC3	PASS
N41	30	60	DFT-PI2BPSK	L	Inner_Full	18.80	18.7	PC3	PASS
N41	30	60	DFT-QPSK	L	Edge_1RB_Left	19.44	19.34	PC3	PASS
N41	30	60	DFT-QPSK	L	Edge_1RB_Right	19.07	18.97	PC3	PASS
N41	30	60	DFT-QPSK	L	Outer_Full	18.99	18.89	PC3	PASS
N41	30	60	DFT-QPSK	L	Inner_Full	18.83	18.73	PC3	PASS
N41	30	60	CP-QPSK	L	Edge_1RB_Left	19.45	19.35	PC3	PASS
N41	30	60	CP-QPSK	L	Edge_1RB_Right	19.18	19.08	PC3	PASS
N41	30	60	CP-QPSK	L	Outer_Full	19.04	18.94	PC3	PASS
N41	30	60	CP-QPSK	L	Inner_Full	18.93	18.83	PC3	PASS
N41	30	60	CP-16QAM	L	Edge_1RB_Left	19.48	19.38	PC3	PASS
N41	30	60	CP-16QAM	L	Edge_1RB_Right	19.22	19.12	PC3	PASS
N41	30	60	CP-16QAM	L	Outer_Full	19.00	18.9	PC3	PASS
N41	30	60	CP-16QAM	L	Inner_Full	18.89	18.79	PC3	PASS
N41	30	60	CP-64QAM	L	Edge_1RB_Left	19.67	19.57	PC3	PASS
N41	30	60	CP-64QAM	L	Edge_1RB_Right	19.38	19.28	PC3	PASS
N41	30	60	CP-64QAM	L	Outer_Full	18.97	18.87	PC3	PASS
N41	30	60	CP-64QAM	L	Inner_Full	18.86	18.76	PC3	PASS
N41	30	60	CP-256QAM	L	Edge_1RB_Left	19.63	19.53	PC3	PASS
N41	30	60	CP-256QAM	L	Edge_1RB_Right	19.20	19.1	PC3	PASS
N41	30	60	CP-256QAM	L	Outer_Full	18.99	18.89	PC3	PASS
N41	30	60	CP-256QAM	L	Inner_Full	18.85	18.75	PC3	PASS



N41	30	60	DFT-PI2BPSK	M	Edge_1RB_Left	22.16	22.06	PC3	PASS
N41	30	60	DFT-PI2BPSK	M	Edge_1RB_Right	19.31	19.21	PC3	PASS
N41	30	60	DFT-PI2BPSK	M	Outer_Full	18.66	18.56	PC3	PASS
N41	30	60	DFT-PI2BPSK	M	Inner_Full	18.47	18.37	PC3	PASS
N41	30	60	DFT-QPSK	M	Edge_1RB_Left	18.78	18.68	PC3	PASS
N41	30	60	DFT-QPSK	M	Edge_1RB_Right	19.26	19.16	PC3	PASS
N41	30	60	DFT-QPSK	M	Outer_Full	18.57	18.47	PC3	PASS
N41	30	60	DFT-QPSK	M	Inner_Full	18.39	18.29	PC3	PASS
N41	30	60	CP-QPSK	M	Edge_1RB_Left	18.62	18.52	PC3	PASS
N41	30	60	CP-QPSK	M	Edge_1RB_Right	19.22	19.12	PC3	PASS
N41	30	60	CP-QPSK	M	Outer_Full	18.61	18.51	PC3	PASS
N41	30	60	CP-QPSK	M	Inner_Full	18.46	18.36	PC3	PASS
N41	30	60	CP-16QAM	M	Edge_1RB_Left	18.63	18.53	PC3	PASS
N41	30	60	CP-16QAM	M	Edge_1RB_Right	19.19	19.09	PC3	PASS
N41	30	60	CP-16QAM	M	Outer_Full	18.54	18.44	PC3	PASS
N41	30	60	CP-16QAM	M	Inner_Full	18.40	18.3	PC3	PASS
N41	30	60	CP-64QAM	M	Edge_1RB_Left	18.69	18.59	PC3	PASS
N41	30	60	CP-64QAM	M	Edge_1RB_Right	19.33	19.23	PC3	PASS
N41	30	60	CP-64QAM	M	Outer_Full	18.49	18.39	PC3	PASS
N41	30	60	CP-64QAM	M	Inner_Full	18.36	18.26	PC3	PASS
N41	30	60	CP-256QAM	M	Edge_1RB_Left	18.62	18.52	PC3	PASS
N41	30	60	CP-256QAM	M	Edge_1RB_Right	19.10	19	PC3	PASS
N41	30	60	CP-256QAM	M	Outer_Full	18.47	18.37	PC3	PASS
N41	30	60	CP-256QAM	M	Inner_Full	18.33	18.23	PC3	PASS
N41	30	60	DFT-PI2BPSK	H	Edge_1RB_Left	22.39	22.29	PC3	PASS
N41	30	60	DFT-PI2BPSK	H	Edge_1RB_Right	19.56	19.46	PC3	PASS
N41	30	60	DFT-PI2BPSK	H	Outer_Full	18.84	18.74	PC3	PASS
N41	30	60	DFT-PI2BPSK	H	Inner_Full	18.65	18.55	PC3	PASS
N41	30	60	DFT-QPSK	H	Edge_1RB_Left	19.00	18.9	PC3	PASS
N41	30	60	DFT-QPSK	H	Edge_1RB_Right	19.44	19.34	PC3	PASS
N41	30	60	DFT-QPSK	H	Outer_Full	18.74	18.64	PC3	PASS
N41	30	60	DFT-QPSK	H	Inner_Full	18.56	18.46	PC3	PASS
N41	30	60	CP-QPSK	H	Edge_1RB_Left	18.83	18.73	PC3	PASS
N41	30	60	CP-QPSK	H	Edge_1RB_Right	19.34	19.24	PC3	PASS
N41	30	60	CP-QPSK	H	Outer_Full	18.77	18.67	PC3	PASS
N41	30	60	CP-QPSK	H	Inner_Full	18.74	18.64	PC3	PASS
N41	30	60	CP-16QAM	H	Edge_1RB_Left	18.86	18.76	PC3	PASS
N41	30	60	CP-16QAM	H	Edge_1RB_Right	19.40	19.3	PC3	PASS
N41	30	60	CP-16QAM	H	Outer_Full	18.81	18.71	PC3	PASS
N41	30	60	CP-16QAM	H	Inner_Full	18.68	18.58	PC3	PASS



N41	30	60	CP-64QAM	H	Edge_1RB_Left	19.10	19	PC3	PASS
N41	30	60	CP-64QAM	H	Edge_1RB_Right	19.66	19.56	PC3	PASS
N41	30	60	CP-64QAM	H	Outer_Full	18.75	18.65	PC3	PASS
N41	30	60	CP-64QAM	H	Inner_Full	18.63	18.53	PC3	PASS
N41	30	60	CP-256QAM	H	Edge_1RB_Left	18.87	18.77	PC3	PASS
N41	30	60	CP-256QAM	H	Edge_1RB_Right	19.39	19.29	PC3	PASS
N41	30	60	CP-256QAM	H	Outer_Full	18.72	18.62	PC3	PASS
N41	30	60	CP-256QAM	H	Inner_Full	18.59	18.49	PC3	PASS
N41	30	80	DFT-PI2BPSK	L	Edge_1RB_Left	19.68	19.58	PC3	PASS
N41	30	80	DFT-PI2BPSK	L	Edge_1RB_Right	19.43	19.33	PC3	PASS
N41	30	80	DFT-PI2BPSK	L	Outer_Full	19.20	19.1	PC3	PASS
N41	30	80	DFT-PI2BPSK	L	Inner_Full	18.87	18.77	PC3	PASS
N41	30	80	DFT-QPSK	L	Edge_1RB_Left	19.81	19.71	PC3	PASS
N41	30	80	DFT-QPSK	L	Edge_1RB_Right	19.58	19.48	PC3	PASS
N41	30	80	DFT-QPSK	L	Outer_Full	19.17	19.07	PC3	PASS
N41	30	80	DFT-QPSK	L	Inner_Full	18.89	18.79	PC3	PASS
N41	30	80	CP-QPSK	L	Edge_1RB_Left	19.64	19.54	PC3	PASS
N41	30	80	CP-QPSK	L	Edge_1RB_Right	19.44	19.34	PC3	PASS
N41	30	80	CP-QPSK	L	Outer_Full	19.23	19.13	PC3	PASS
N41	30	80	CP-QPSK	L	Inner_Full	18.97	18.87	PC3	PASS
N41	30	80	CP-16QAM	L	Edge_1RB_Left	19.60	19.5	PC3	PASS
N41	30	80	CP-16QAM	L	Edge_1RB_Right	19.54	19.44	PC3	PASS
N41	30	80	CP-16QAM	L	Outer_Full	19.17	19.07	PC3	PASS
N41	30	80	CP-16QAM	L	Inner_Full	18.94	18.84	PC3	PASS
N41	30	80	CP-64QAM	L	Edge_1RB_Left	19.87	19.77	PC3	PASS
N41	30	80	CP-64QAM	L	Edge_1RB_Right	19.75	19.65	PC3	PASS
N41	30	80	CP-64QAM	L	Outer_Full	19.15	19.05	PC3	PASS
N41	30	80	CP-64QAM	L	Inner_Full	18.92	18.82	PC3	PASS
N41	30	80	CP-256QAM	L	Edge_1RB_Left	19.75	19.65	PC3	PASS
N41	30	80	CP-256QAM	L	Edge_1RB_Right	19.51	19.41	PC3	PASS
N41	30	80	CP-256QAM	L	Outer_Full	19.15	19.05	PC3	PASS
N41	30	80	CP-256QAM	L	Inner_Full	19.15	19.05	PC3	PASS
N41	30	80	DFT-PI2BPSK	M	Edge_1RB_Left	18.80	18.7	PC3	PASS
N41	30	80	DFT-PI2BPSK	M	Edge_1RB_Right	19.05	18.95	PC3	PASS
N41	30	80	DFT-PI2BPSK	M	Outer_Full	18.65	18.55	PC3	PASS
N41	30	80	DFT-PI2BPSK	M	Inner_Full	18.37	18.27	PC3	PASS
N41	30	80	DFT-QPSK	M	Edge_1RB_Left	22.52	22.42	PC3	PASS
N41	30	80	DFT-QPSK	M	Edge_1RB_Right	19.58	19.48	PC3	PASS
N41	30	80	DFT-QPSK	M	Outer_Full	18.99	18.89	PC3	PASS
N41	30	80	DFT-QPSK	M	Inner_Full	18.72	18.62	PC3	PASS



N41	30	80	CP-QPSK	M	Edge_1RB_Left	18.92	18.82	PC3	PASS
N41	30	80	CP-QPSK	M	Edge_1RB_Right	19.42	19.32	PC3	PASS
N41	30	80	CP-QPSK	M	Outer_Full	18.97	18.87	PC3	PASS
N41	30	80	CP-QPSK	M	Inner_Full	18.72	18.62	PC3	PASS
N41	30	80	CP-16QAM	M	Edge_1RB_Left	18.88	18.78	PC3	PASS
N41	30	80	CP-16QAM	M	Edge_1RB_Right	19.38	19.28	PC3	PASS
N41	30	80	CP-16QAM	M	Outer_Full	18.86	18.76	PC3	PASS
N41	30	80	CP-16QAM	M	Inner_Full	18.63	18.53	PC3	PASS
N41	30	80	CP-64QAM	M	Edge_1RB_Left	19.01	18.91	PC3	PASS
N41	30	80	CP-64QAM	M	Edge_1RB_Right	19.52	19.42	PC3	PASS
N41	30	80	CP-64QAM	M	Outer_Full	18.78	18.68	PC3	PASS
N41	30	80	CP-64QAM	M	Inner_Full	18.58	18.48	PC3	PASS
N41	30	80	CP-256QAM	M	Edge_1RB_Left	18.84	18.74	PC3	PASS
N41	30	80	CP-256QAM	M	Edge_1RB_Right	19.24	19.14	PC3	PASS
N41	30	80	CP-256QAM	M	Outer_Full	18.76	18.66	PC3	PASS
N41	30	80	CP-256QAM	M	Inner_Full	18.53	18.43	PC3	PASS
N41	30	80	DFT-PI2BPSK	H	Edge_1RB_Left	22.82	22.72	PC3	PASS
N41	30	80	DFT-PI2BPSK	H	Edge_1RB_Right	20.13	20.03	PC3	PASS
N41	30	80	DFT-PI2BPSK	H	Outer_Full	19.36	19.26	PC3	PASS
N41	30	80	DFT-PI2BPSK	H	Inner_Full	19.02	18.92	PC3	PASS
N41	30	80	DFT-QPSK	H	Edge_1RB_Left	19.52	19.42	PC3	PASS
N41	30	80	DFT-QPSK	H	Edge_1RB_Right	20.01	19.91	PC3	PASS
N41	30	80	DFT-QPSK	H	Outer_Full	19.24	19.14	PC3	PASS
N41	30	80	DFT-QPSK	H	Inner_Full	18.93	18.83	PC3	PASS
N41	30	80	CP-QPSK	H	Edge_1RB_Left	19.29	19.19	PC3	PASS
N41	30	80	CP-QPSK	H	Edge_1RB_Right	19.89	19.79	PC3	PASS
N41	30	80	CP-QPSK	H	Outer_Full	19.44	19.34	PC3	PASS
N41	30	80	CP-QPSK	H	Inner_Full	19.14	19.04	PC3	PASS
N41	30	80	CP-16QAM	H	Edge_1RB_Left	19.45	19.35	PC3	PASS
N41	30	80	CP-16QAM	H	Edge_1RB_Right	20.07	19.97	PC3	PASS
N41	30	80	CP-16QAM	H	Outer_Full	19.34	19.24	PC3	PASS
N41	30	80	CP-16QAM	H	Inner_Full	19.07	18.97	PC3	PASS
N41	30	80	CP-64QAM	H	Edge_1RB_Left	19.60	19.5	PC3	PASS
N41	30	80	CP-64QAM	H	Edge_1RB_Right	20.22	20.12	PC3	PASS
N41	30	80	CP-64QAM	H	Outer_Full	19.28	19.18	PC3	PASS
N41	30	80	CP-64QAM	H	Inner_Full	19.02	18.92	PC3	PASS
N41	30	80	CP-256QAM	H	Edge_1RB_Left	19.47	19.37	PC3	PASS
N41	30	80	CP-256QAM	H	Edge_1RB_Right	20.02	19.92	PC3	PASS
N41	30	80	CP-256QAM	H	Outer_Full	19.27	19.17	PC3	PASS
N41	30	80	CP-256QAM	H	Inner_Full	18.99	18.89	PC3	PASS



N41	30	90	DFT-PI2BPSK	L	Edge_1RB_Left	19.24	19.14	PC3	PASS
N41	30	90	DFT-PI2BPSK	L	Edge_1RB_Right	19.21	19.11	PC3	PASS
N41	30	90	DFT-PI2BPSK	L	Outer_Full	19.20	19.1	PC3	PASS
N41	30	90	DFT-PI2BPSK	L	Inner_Full	18.83	18.73	PC3	PASS
N41	30	90	DFT-QPSK	L	Edge_1RB_Left	19.25	19.15	PC3	PASS
N41	30	90	DFT-QPSK	L	Edge_1RB_Right	19.18	19.08	PC3	PASS
N41	30	90	DFT-QPSK	L	Outer_Full	19.21	19.11	PC3	PASS
N41	30	90	DFT-QPSK	L	Inner_Full	18.86	18.76	PC3	PASS
N41	30	90	CP-QPSK	L	Edge_1RB_Left	19.25	19.15	PC3	PASS
N41	30	90	CP-QPSK	L	Edge_1RB_Right	19.37	19.27	PC3	PASS
N41	30	90	CP-QPSK	L	Outer_Full	19.22	19.12	PC3	PASS
N41	30	90	CP-QPSK	L	Inner_Full	18.94	18.84	PC3	PASS
N41	30	90	CP-16QAM	L	Edge_1RB_Left	19.21	19.11	PC3	PASS
N41	30	90	CP-16QAM	L	Edge_1RB_Right	19.36	19.26	PC3	PASS
N41	30	90	CP-16QAM	L	Outer_Full	19.16	19.06	PC3	PASS
N41	30	90	CP-16QAM	L	Inner_Full	18.90	18.8	PC3	PASS
N41	30	90	CP-64QAM	L	Edge_1RB_Left	19.47	19.37	PC3	PASS
N41	30	90	CP-64QAM	L	Edge_1RB_Right	19.42	19.32	PC3	PASS
N41	30	90	CP-64QAM	L	Outer_Full	19.15	19.05	PC3	PASS
N41	30	90	CP-64QAM	L	Inner_Full	18.87	18.77	PC3	PASS
N41	30	90	CP-256QAM	L	Edge_1RB_Left	19.30	19.2	PC3	PASS
N41	30	90	CP-256QAM	L	Edge_1RB_Right	19.30	19.2	PC3	PASS
N41	30	90	CP-256QAM	L	Outer_Full	19.13	19.03	PC3	PASS
N41	30	90	CP-256QAM	L	Inner_Full	18.86	18.76	PC3	PASS
N41	30	90	DFT-PI2BPSK	M	Edge_1RB_Left	21.71	21.61	PC3	PASS
N41	30	90	DFT-PI2BPSK	M	Edge_1RB_Right	19.08	18.98	PC3	PASS
N41	30	90	DFT-PI2BPSK	M	Outer_Full	18.85	18.75	PC3	PASS
N41	30	90	DFT-PI2BPSK	M	Inner_Full	18.52	18.42	PC3	PASS
N41	30	90	DFT-QPSK	M	Edge_1RB_Left	18.42	18.32	PC3	PASS
N41	30	90	DFT-QPSK	M	Edge_1RB_Right	19.02	18.92	PC3	PASS
N41	30	90	DFT-QPSK	M	Outer_Full	18.77	18.67	PC3	PASS
N41	30	90	DFT-QPSK	M	Inner_Full	18.46	18.36	PC3	PASS
N41	30	90	CP-QPSK	M	Edge_1RB_Left	18.30	18.2	PC3	PASS
N41	30	90	CP-QPSK	M	Edge_1RB_Right	19.02	18.92	PC3	PASS
N41	30	90	CP-QPSK	M	Outer_Full	18.78	18.68	PC3	PASS
N41	30	90	CP-QPSK	M	Inner_Full	18.54	18.44	PC3	PASS
N41	30	90	CP-16QAM	M	Edge_1RB_Left	18.30	18.2	PC3	PASS
N41	30	90	CP-16QAM	M	Edge_1RB_Right	18.98	18.88	PC3	PASS
N41	30	90	CP-16QAM	M	Outer_Full	18.70	18.6	PC3	PASS
N41	30	90	CP-16QAM	M	Inner_Full	18.46	18.36	PC3	PASS



N41	30	90	CP-64QAM	M	Edge_1RB_Left	18.33	18.23	PC3	PASS
N41	30	90	CP-64QAM	M	Edge_1RB_Right	19.00	18.9	PC3	PASS
N41	30	90	CP-64QAM	M	Outer_Full	18.66	18.56	PC3	PASS
N41	30	90	CP-64QAM	M	Inner_Full	18.42	18.32	PC3	PASS
N41	30	90	CP-256QAM	M	Edge_1RB_Left	18.26	18.16	PC3	PASS
N41	30	90	CP-256QAM	M	Edge_1RB_Right	18.88	18.78	PC3	PASS
N41	30	90	CP-256QAM	M	Outer_Full	18.63	18.53	PC3	PASS
N41	30	90	CP-256QAM	M	Inner_Full	18.40	18.3	PC3	PASS
N41	30	90	DFT-PI2BPSK	H	Edge_1RB_Left	22.00	21.9	PC3	PASS
N41	30	90	DFT-PI2BPSK	H	Edge_1RB_Right	19.57	19.47	PC3	PASS
N41	30	90	DFT-PI2BPSK	H	Outer_Full	19.06	18.96	PC3	PASS
N41	30	90	DFT-PI2BPSK	H	Inner_Full	18.69	18.59	PC3	PASS
N41	30	90	DFT-QPSK	H	Edge_1RB_Left	18.75	18.65	PC3	PASS
N41	30	90	DFT-QPSK	H	Edge_1RB_Right	19.45	19.35	PC3	PASS
N41	30	90	DFT-QPSK	H	Outer_Full	18.95	18.85	PC3	PASS
N41	30	90	DFT-QPSK	H	Inner_Full	18.60	18.5	PC3	PASS
N41	30	90	CP-QPSK	H	Edge_1RB_Left	18.56	18.46	PC3	PASS
N41	30	90	CP-QPSK	H	Edge_1RB_Right	19.41	19.31	PC3	PASS
N41	30	90	CP-QPSK	H	Outer_Full	18.97	18.87	PC3	PASS
N41	30	90	CP-QPSK	H	Inner_Full	18.69	18.59	PC3	PASS
N41	30	90	CP-16QAM	H	Edge_1RB_Left	18.54	18.44	PC3	PASS
N41	30	90	CP-16QAM	H	Edge_1RB_Right	19.42	19.32	PC3	PASS
N41	30	90	CP-16QAM	H	Outer_Full	18.88	18.78	PC3	PASS
N41	30	90	CP-16QAM	H	Inner_Full	18.61	18.51	PC3	PASS
N41	30	90	CP-64QAM	H	Edge_1RB_Left	18.71	18.61	PC3	PASS
N41	30	90	CP-64QAM	H	Edge_1RB_Right	19.46	19.36	PC3	PASS
N41	30	90	CP-64QAM	H	Outer_Full	18.84	18.74	PC3	PASS
N41	30	90	CP-64QAM	H	Inner_Full	18.57	18.47	PC3	PASS
N41	30	90	CP-256QAM	H	Edge_1RB_Left	18.51	18.41	PC3	PASS
N41	30	90	CP-256QAM	H	Edge_1RB_Right	19.32	19.22	PC3	PASS
N41	30	90	CP-256QAM	H	Outer_Full	18.81	18.71	PC3	PASS
N41	30	90	CP-256QAM	H	Inner_Full	18.54	18.44	PC3	PASS
N41	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	22.01	21.91	PC3	PASS
N41	30	100	DFT-PI2BPSK	L	Edge_1RB_Right	18.89	18.79	PC3	PASS
N41	30	100	DFT-PI2BPSK	L	Outer_Full	19.31	19.21	PC3	PASS
N41	30	100	DFT-PI2BPSK	L	Inner_Full	19.04	18.94	PC3	PASS
N41	30	100	DFT-QPSK	L	Edge_1RB_Left	18.58	18.48	PC3	PASS
N41	30	100	DFT-QPSK	L	Edge_1RB_Right	18.71	18.61	PC3	PASS
N41	30	100	DFT-QPSK	L	Outer_Full	19.21	19.11	PC3	PASS
N41	30	100	DFT-QPSK	L	Inner_Full	18.94	18.84	PC3	PASS



N41	30	100	CP-QPSK	L	Edge_1RB_Left	18.42	18.32	PC3	PASS
N41	30	100	CP-QPSK	L	Edge_1RB_Right	18.66	18.56	PC3	PASS
N41	30	100	CP-QPSK	L	Outer_Full	19.20	19.1	PC3	PASS
N41	30	100	CP-QPSK	L	Inner_Full	18.94	18.84	PC3	PASS
N41	30	100	CP-16QAM	L	Edge_1RB_Left	18.44	18.34	PC3	PASS
N41	30	100	CP-16QAM	L	Edge_1RB_Right	18.85	18.75	PC3	PASS
N41	30	100	CP-16QAM	L	Outer_Full	19.29	19.19	PC3	PASS
N41	30	100	CP-16QAM	L	Inner_Full	19.05	18.95	PC3	PASS
N41	30	100	CP-64QAM	L	Edge_1RB_Left	18.77	18.67	PC3	PASS
N41	30	100	CP-64QAM	L	Edge_1RB_Right	18.87	18.77	PC3	PASS
N41	30	100	CP-64QAM	L	Outer_Full	19.25	19.15	PC3	PASS
N41	30	100	CP-64QAM	L	Inner_Full	19.00	18.9	PC3	PASS
N41	30	100	CP-256QAM	L	Edge_1RB_Left	18.60	18.5	PC3	PASS
N41	30	100	CP-256QAM	L	Edge_1RB_Right	18.75	18.65	PC3	PASS
N41	30	100	CP-256QAM	L	Outer_Full	19.22	19.12	PC3	PASS
N41	30	100	CP-256QAM	L	Inner_Full	18.98	18.88	PC3	PASS
N41	30	100	DFT-PI2BPSK	M	Edge_1RB_Left	20.77	20.67	PC3	PASS
N41	30	100	DFT-PI2BPSK	M	Edge_1RB_Right	18.30	18.2	PC3	PASS
N41	30	100	DFT-PI2BPSK	M	Outer_Full	18.80	18.7	PC3	PASS
N41	30	100	DFT-PI2BPSK	M	Inner_Full	18.61	18.51	PC3	PASS
N41	30	100	DFT-QPSK	M	Edge_1RB_Left	17.53	17.43	PC3	PASS
N41	30	100	DFT-QPSK	M	Edge_1RB_Right	18.13	18.03	PC3	PASS
N41	30	100	DFT-QPSK	M	Outer_Full	18.70	18.6	PC3	PASS
N41	30	100	DFT-QPSK	M	Inner_Full	18.54	18.44	PC3	PASS
N41	30	100	CP-QPSK	M	Edge_1RB_Left	17.41	17.31	PC3	PASS
N41	30	100	CP-QPSK	M	Edge_1RB_Right	18.20	18.1	PC3	PASS
N41	30	100	CP-QPSK	M	Outer_Full	18.72	18.62	PC3	PASS
N41	30	100	CP-QPSK	M	Inner_Full	18.55	18.45	PC3	PASS
N41	30	100	CP-16QAM	M	Edge_1RB_Left	17.42	17.32	PC3	PASS
N41	30	100	CP-16QAM	M	Edge_1RB_Right	18.17	18.07	PC3	PASS
N41	30	100	CP-16QAM	M	Outer_Full	18.64	18.54	PC3	PASS
N41	30	100	CP-16QAM	M	Inner_Full	18.49	18.39	PC3	PASS
N41	30	100	CP-64QAM	M	Edge_1RB_Left	17.55	17.45	PC3	PASS
N41	30	100	CP-64QAM	M	Edge_1RB_Right	18.20	18.1	PC3	PASS
N41	30	100	CP-64QAM	M	Outer_Full	18.60	18.5	PC3	PASS
N41	30	100	CP-64QAM	M	Inner_Full	18.45	18.35	PC3	PASS
N41	30	100	CP-256QAM	M	Edge_1RB_Left	17.37	17.27	PC3	PASS
N41	30	100	CP-256QAM	M	Edge_1RB_Right	18.06	17.96	PC3	PASS
N41	30	100	CP-256QAM	M	Outer_Full	18.57	18.47	PC3	PASS
N41	30	100	CP-256QAM	M	Inner_Full	18.43	18.33	PC3	PASS



N41	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	17.56	17.46	PC3	PASS
N41	30	100	DFT-PI2BPSK	H	Edge_1RB_Right	18.49	18.39	PC3	PASS
N41	30	100	DFT-PI2BPSK	H	Outer_Full	18.77	18.67	PC3	PASS
N41	30	100	DFT-PI2BPSK	H	Inner_Full	18.58	18.48	PC3	PASS
N41	30	100	DFT-QPSK	H	Edge_1RB_Left	17.61	17.51	PC3	PASS
N41	30	100	DFT-QPSK	H	Edge_1RB_Right	18.49	18.39	PC3	PASS
N41	30	100	DFT-QPSK	H	Outer_Full	18.92	18.82	PC3	PASS
N41	30	100	DFT-QPSK	H	Inner_Full	18.67	18.57	PC3	PASS
N41	30	100	CP-QPSK	H	Edge_1RB_Left	17.58	17.48	PC3	PASS
N41	30	100	CP-QPSK	H	Edge_1RB_Right	18.61	18.51	PC3	PASS
N41	30	100	CP-QPSK	H	Outer_Full	18.92	18.82	PC3	PASS
N41	30	100	CP-QPSK	H	Inner_Full	18.70	18.6	PC3	PASS
N41	30	100	CP-16QAM	H	Edge_1RB_Left	17.67	17.57	PC3	PASS
N41	30	100	CP-16QAM	H	Edge_1RB_Right	18.60	18.5	PC3	PASS
N41	30	100	CP-16QAM	H	Outer_Full	18.82	18.72	PC3	PASS
N41	30	100	CP-16QAM	H	Inner_Full	18.63	18.53	PC3	PASS
N41	30	100	CP-64QAM	H	Edge_1RB_Left	17.76	17.66	PC3	PASS
N41	30	100	CP-64QAM	H	Edge_1RB_Right	18.64	18.54	PC3	PASS
N41	30	100	CP-64QAM	H	Outer_Full	18.78	18.68	PC3	PASS
N41	30	100	CP-64QAM	H	Inner_Full	18.59	18.49	PC3	PASS
N41	30	100	CP-256QAM	H	Edge_1RB_Left	17.61	17.51	PC3	PASS
N41	30	100	CP-256QAM	H	Edge_1RB_Right	18.51	18.41	PC3	PASS
N41	30	100	CP-256QAM	H	Outer_Full	18.76	18.66	PC3	PASS
N41	30	100	CP-256QAM	H	Inner_Full	18.57	18.47	PC3	PASS



Appendix B: Peak-to-Average Ratio

Peak-to-Average Ratio

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result(dB)	Limit(dB)	Verdict
N41	30	100	DFT-PI2BPSK	L	Outer_Full	10.72	≤13	PASS
N41	30	100	DFT-QPSK	L	Outer_Full	9.07	≤13	PASS
N41	30	100	DFT-PI2BPSK	M	Outer_Full	9.81	≤13	PASS
N41	30	100	DFT-QPSK	M	Outer_Full	10.19	≤13	PASS
N41	30	100	DFT-PI2BPSK	H	Outer_Full	10.58	≤13	PASS
N41	30	100	DFT-QPSK	H	Outer_Full	9.28	≤13	PASS



Appendix C: 26dB Bandwidth and Occupied Bandwidth

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	99% OBW (MHz)	26dB BW (MHz)	Verdict
N41	30	20	DFT-QPSK	L	Outer_Full	17.871	18.920	PASS
N41	30	20	DFT-PI2BPSK	L	Outer_Full	17.834	19.080	PASS
N41	30	20	CP-QPSK	L	Outer_Full	18.234	19.480	PASS
N41	30	20	CP-16QAM	L	Outer_Full	18.253	19.400	PASS
N41	30	20	CP-64QAM	L	Outer_Full	18.301	19.480	PASS
N41	30	20	CP-256QAM	L	Outer_Full	18.225	19.320	PASS
N41	30	20	DFT-QPSK	M	Outer_Full	17.85	19.200	PASS
N41	30	20	DFT-PI2BPSK	M	Outer_Full	17.931	19.240	PASS
N41	30	20	CP-QPSK	M	Outer_Full	18.282	19.480	PASS
N41	30	20	CP-16QAM	M	Outer_Full	18.258	19.240	PASS
N41	30	20	CP-64QAM	M	Outer_Full	18.246	19.320	PASS
N41	30	20	CP-256QAM	M	Outer_Full	18.274	19.120	PASS
N41	30	20	DFT-QPSK	H	Outer_Full	17.908	19.000	PASS
N41	30	20	DFT-PI2BPSK	H	Outer_Full	17.875	19.040	PASS
N41	30	20	CP-QPSK	H	Outer_Full	18.286	19.400	PASS
N41	30	20	CP-16QAM	H	Outer_Full	18.23	19.400	PASS
N41	30	20	CP-64QAM	H	Outer_Full	18.27	19.280	PASS
N41	30	20	CP-256QAM	H	Outer_Full	18.277	19.320	PASS
N41	30	30	DFT-QPSK	L	Outer_Full	26.848	28.140	PASS
N41	30	30	DFT-PI2BPSK	L	Outer_Full	26.866	28.380	PASS
N41	30	30	CP-QPSK	L	Outer_Full	27.952	28.860	PASS
N41	30	30	CP-16QAM	L	Outer_Full	27.93	29.520	PASS
N41	30	30	CP-64QAM	L	Outer_Full	27.899	29.280	PASS
N41	30	30	CP-256QAM	L	Outer_Full	27.938	29.160	PASS
N41	30	30	DFT-QPSK	M	Outer_Full	26.882	28.140	PASS
N41	30	30	DFT-PI2BPSK	M	Outer_Full	26.839	28.440	PASS
N41	30	30	CP-QPSK	M	Outer_Full	27.912	29.580	PASS
N41	30	30	CP-16QAM	M	Outer_Full	27.858	29.100	PASS
N41	30	30	CP-64QAM	M	Outer_Full	27.826	28.920	PASS
N41	30	30	CP-256QAM	M	Outer_Full	27.871	29.040	PASS
N41	30	30	DFT-QPSK	H	Outer_Full	26.826	28.380	PASS
N41	30	30	DFT-PI2BPSK	H	Outer_Full	26.804	28.320	PASS
N41	30	30	CP-QPSK	H	Outer_Full	28.115	28.740	PASS



N41	30	30	CP-16QAM	H	Outer_Full	27.889	29.160	PASS
N41	30	30	CP-64QAM	H	Outer_Full	27.91	29.280	PASS
N41	30	30	CP-256QAM	H	Outer_Full	27.894	29.280	PASS
N41	30	40	DFT-QPSK	L	Outer_Full	35.929	37.440	PASS
N41	30	40	DFT-PI2BPSK	L	Outer_Full	35.892	37.280	PASS
N41	30	40	CP-QPSK	L	Outer_Full	37.972	39.440	PASS
N41	30	40	CP-16QAM	L	Outer_Full	37.944	39.520	PASS
N41	30	40	CP-64QAM	L	Outer_Full	37.891	39.440	PASS
N41	30	40	CP-256QAM	L	Outer_Full	37.938	39.520	PASS
N41	30	40	DFT-QPSK	M	Outer_Full	35.97	37.680	PASS
N41	30	40	DFT-PI2BPSK	M	Outer_Full	35.926	38.000	PASS
N41	30	40	CP-QPSK	M	Outer_Full	37.959	39.520	PASS
N41	30	40	CP-16QAM	M	Outer_Full	38.033	39.760	PASS
N41	30	40	CP-64QAM	M	Outer_Full	37.992	39.680	PASS
N41	30	40	CP-256QAM	M	Outer_Full	38.018	39.520	PASS
N41	30	40	DFT-QPSK	H	Outer_Full	35.971	37.600	PASS
N41	30	40	DFT-PI2BPSK	H	Outer_Full	35.957	37.600	PASS
N41	30	40	CP-QPSK	H	Outer_Full	37.972	39.520	PASS
N41	30	40	CP-16QAM	H	Outer_Full	37.822	39.680	PASS
N41	30	40	CP-64QAM	H	Outer_Full	38.017	39.680	PASS
N41	30	40	CP-256QAM	H	Outer_Full	37.941	39.440	PASS
N41	30	50	DFT-QPSK	L	Outer_Full	45.913	48.000	PASS
N41	30	50	DFT-PI2BPSK	L	Outer_Full	45.685	47.500	PASS
N41	30	50	CP-QPSK	L	Outer_Full	47.457	49.500	PASS
N41	30	50	CP-16QAM	L	Outer_Full	47.58	49.100	PASS
N41	30	50	CP-64QAM	L	Outer_Full	47.451	49.300	PASS
N41	30	50	CP-256QAM	L	Outer_Full	47.485	49.300	PASS
N41	30	50	DFT-QPSK	M	Outer_Full	45.849	47.600	PASS
N41	30	50	DFT-PI2BPSK	M	Outer_Full	45.801	47.400	PASS
N41	30	50	CP-QPSK	M	Outer_Full	47.418	49.200	PASS
N41	30	50	CP-16QAM	M	Outer_Full	47.269	49.200	PASS
N41	30	50	CP-64QAM	M	Outer_Full	47.368	49.400	PASS
N41	30	50	CP-256QAM	M	Outer_Full	47.374	49.500	PASS
N41	30	50	DFT-QPSK	H	Outer_Full	45.621	47.800	PASS
N41	30	50	DFT-PI2BPSK	H	Outer_Full	45.851	47.400	PASS
N41	30	50	CP-QPSK	H	Outer_Full	47.545	49.000	PASS
N41	30	50	CP-16QAM	H	Outer_Full	47.544	49.400	PASS
N41	30	50	CP-64QAM	H	Outer_Full	47.57	49.600	PASS
N41	30	50	CP-256QAM	H	Outer_Full	47.457	49.400	PASS
N41	30	60	DFT-QPSK	L	Outer_Full	58.023	60.120	PASS



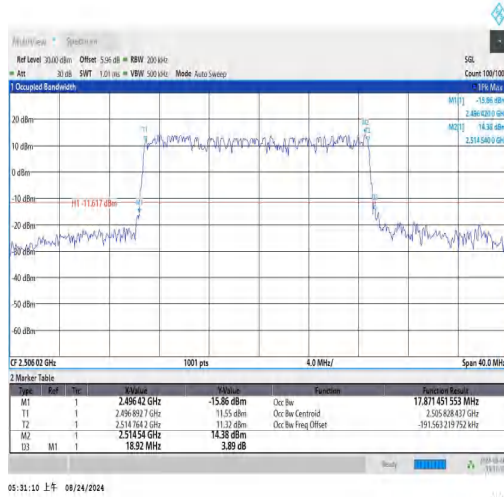
N41	30	60	DFT-PI2BPSK	L	Outer_Full	57.9	60.000	PASS
N41	30	60	CP-QPSK	L	Outer_Full	57.826	60.000	PASS
N41	30	60	CP-16QAM	L	Outer_Full	57.8	59.520	PASS
N41	30	60	CP-64QAM	L	Outer_Full	57.82	59.760	PASS
N41	30	60	CP-256QAM	L	Outer_Full	57.662	59.760	PASS
N41	30	60	DFT-QPSK	M	Outer_Full	57.941	59.880	PASS
N41	30	60	DFT-PI2BPSK	M	Outer_Full	57.905	59.880	PASS
N41	30	60	CP-QPSK	M	Outer_Full	57.815	60.000	PASS
N41	30	60	CP-16QAM	M	Outer_Full	57.804	59.640	PASS
N41	30	60	CP-64QAM	M	Outer_Full	57.772	59.760	PASS
N41	30	60	CP-256QAM	M	Outer_Full	57.81	59.880	PASS
N41	30	60	DFT-QPSK	H	Outer_Full	57.891	59.880	PASS
N41	30	60	DFT-PI2BPSK	H	Outer_Full	57.977	59.880	PASS
N41	30	60	CP-QPSK	H	Outer_Full	57.919	59.400	PASS
N41	30	60	CP-16QAM	H	Outer_Full	57.711	59.880	PASS
N41	30	60	CP-64QAM	H	Outer_Full	57.93	59.640	PASS
N41	30	60	CP-256QAM	H	Outer_Full	57.884	59.640	PASS
N41	30	80	DFT-QPSK	L	Outer_Full	77.224	80.480	PASS
N41	30	80	DFT-PI2BPSK	L	Outer_Full	77.098	80.320	PASS
N41	30	80	CP-QPSK	L	Outer_Full	77.346	80.480	PASS
N41	30	80	CP-16QAM	L	Outer_Full	77.482	80.640	PASS
N41	30	80	CP-64QAM	L	Outer_Full	77.609	80.800	PASS
N41	30	80	CP-256QAM	L	Outer_Full	77.456	80.640	PASS
N41	30	80	DFT-QPSK	M	Outer_Full	77.078	80.320	PASS
N41	30	80	DFT-PI2BPSK	M	Outer_Full	77.31	80.320	PASS
N41	30	80	CP-QPSK	M	Outer_Full	77.568	80.640	PASS
N41	30	80	CP-16QAM	M	Outer_Full	77.367	80.640	PASS
N41	30	80	CP-64QAM	M	Outer_Full	77.645	80.640	PASS
N41	30	80	CP-256QAM	M	Outer_Full	77.57	80.640	PASS
N41	30	80	DFT-QPSK	H	Outer_Full	77.18	80.320	PASS
N41	30	80	DFT-PI2BPSK	H	Outer_Full	77.137	80.320	PASS
N41	30	80	CP-QPSK	H	Outer_Full	77.658	80.640	PASS
N41	30	80	CP-16QAM	H	Outer_Full	77.397	80.640	PASS
N41	30	80	CP-64QAM	H	Outer_Full	77.532	80.640	PASS
N41	30	80	CP-256QAM	H	Outer_Full	77.489	80.640	PASS
N41	30	90	DFT-QPSK	L	Outer_Full	86.848	90.000	PASS
N41	30	90	DFT-PI2BPSK	L	Outer_Full	86.818	90.000	PASS
N41	30	90	CP-QPSK	L	Outer_Full	88.444	90.720	PASS
N41	30	90	CP-16QAM	L	Outer_Full	87.109	90.900	PASS
N41	30	90	CP-64QAM	L	Outer_Full	87.153	90.720	PASS



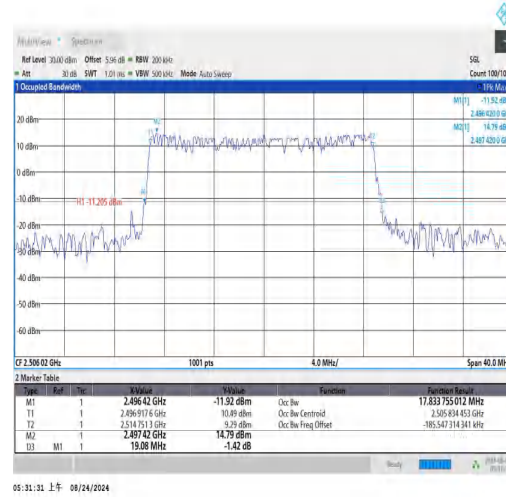
N41	30	90	CP-256QAM	L	Outer_Full	87.304	90.720	PASS
N41	30	90	DFT-QPSK	M	Outer_Full	86.909	90.360	PASS
N41	30	90	DFT-PI2BPSK	M	Outer_Full	86.754	90.180	PASS
N41	30	90	CP-QPSK	M	Outer_Full	87.324	90.540	PASS
N41	30	90	CP-16QAM	M	Outer_Full	87.352	90.720	PASS
N41	30	90	CP-64QAM	M	Outer_Full	87.297	90.720	PASS
N41	30	90	CP-256QAM	M	Outer_Full	87.362	90.720	PASS
N41	30	90	DFT-QPSK	H	Outer_Full	86.54	90.180	PASS
N41	30	90	DFT-PI2BPSK	H	Outer_Full	86.757	90.000	PASS
N41	30	90	CP-QPSK	H	Outer_Full	87.323	90.720	PASS
N41	30	90	CP-16QAM	H	Outer_Full	87.465	90.720	PASS
N41	30	90	CP-64QAM	H	Outer_Full	87.51	90.720	PASS
N41	30	90	CP-256QAM	H	Outer_Full	87.531	90.540	PASS
N41	30	100	DFT-QPSK	L	Outer_Full	96.244	99.800	PASS
N41	30	100	DFT-PI2BPSK	L	Outer_Full	96.243	99.800	PASS
N41	30	100	CP-QPSK	L	Outer_Full	97.131	100.800	PASS
N41	30	100	CP-16QAM	L	Outer_Full	97.056	100.600	PASS
N41	30	100	CP-64QAM	L	Outer_Full	97.402	100.800	PASS
N41	30	100	CP-256QAM	L	Outer_Full	97.345	100.800	PASS
N41	30	100	DFT-QPSK	M	Outer_Full	96.135	99.800	PASS
N41	30	100	DFT-PI2BPSK	M	Outer_Full	96.152	99.800	PASS
N41	30	100	CP-QPSK	M	Outer_Full	97.144	100.800	PASS
N41	30	100	CP-16QAM	M	Outer_Full	96.903	100.600	PASS
N41	30	100	CP-64QAM	M	Outer_Full	97.389	100.800	PASS
N41	30	100	CP-256QAM	M	Outer_Full	97.349	100.600	PASS
N41	30	100	DFT-QPSK	H	Outer_Full	96.143	99.800	PASS
N41	30	100	DFT-PI2BPSK	H	Outer_Full	96.309	100.000	PASS
N41	30	100	CP-QPSK	H	Outer_Full	97.348	101.000	PASS
N41	30	100	CP-16QAM	H	Outer_Full	97.033	101.000	PASS
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N41	30	100	CP-256QAM	H	Outer_Full	97.002	101.000	PASS



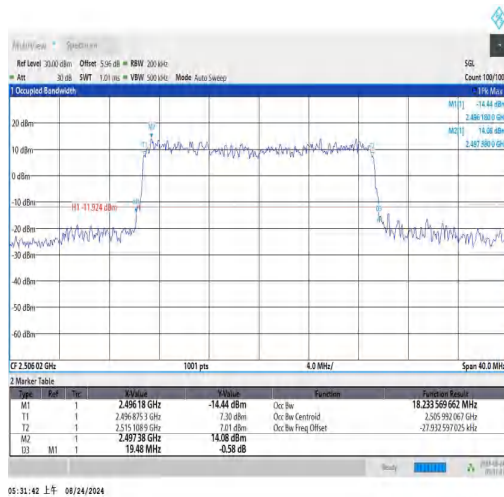
Test Graphs



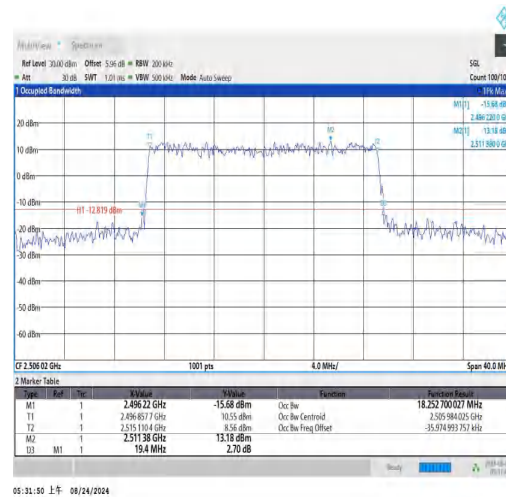
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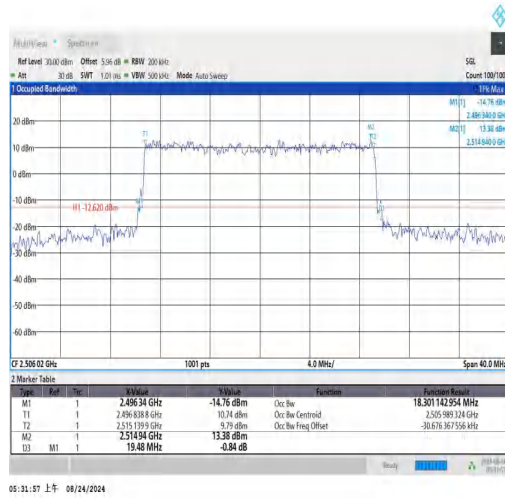
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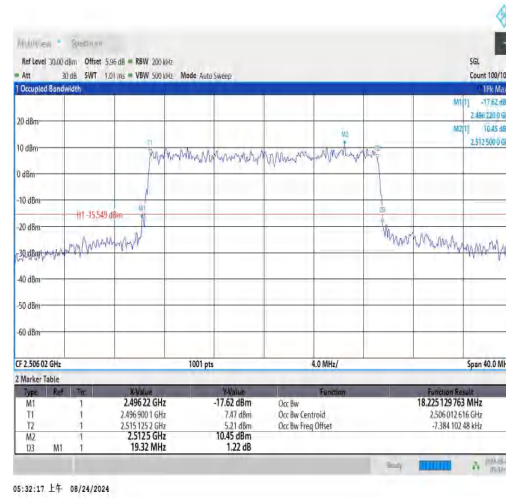
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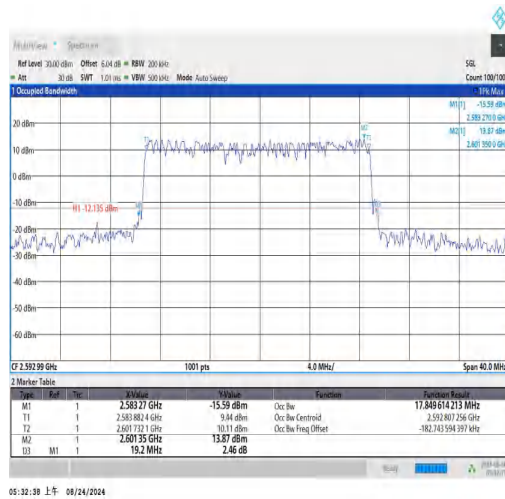
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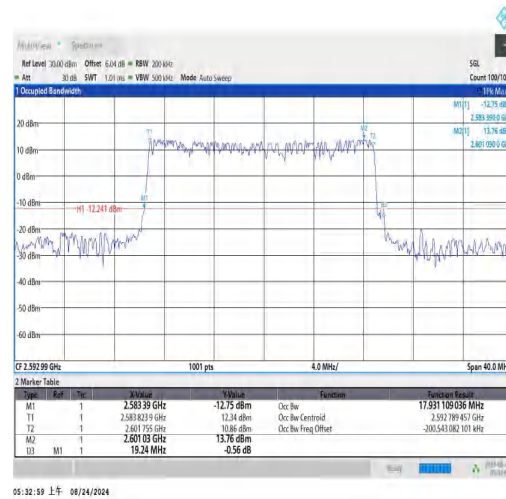
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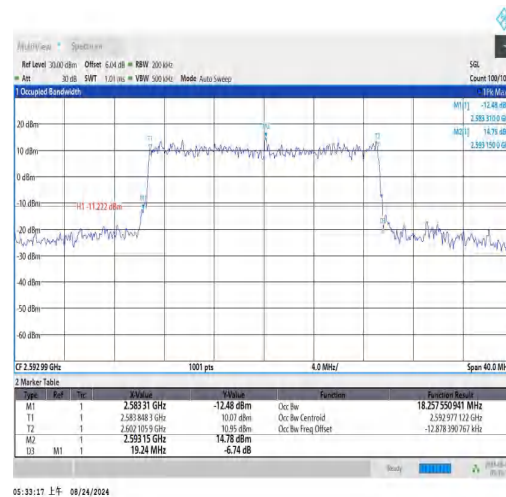
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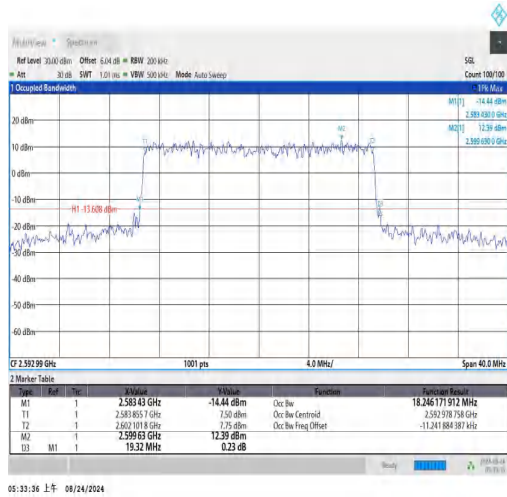


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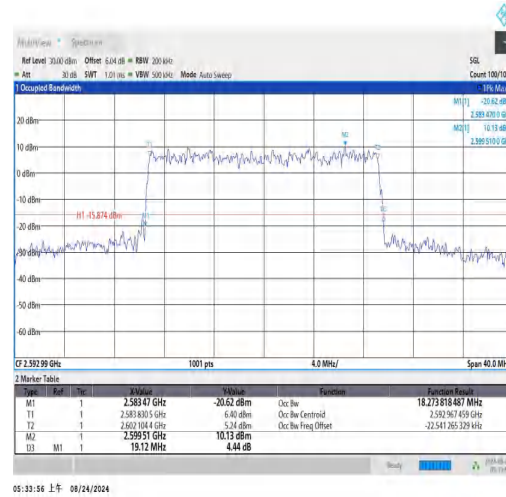




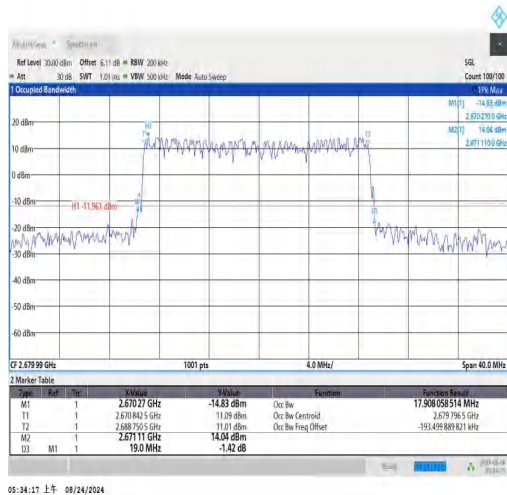
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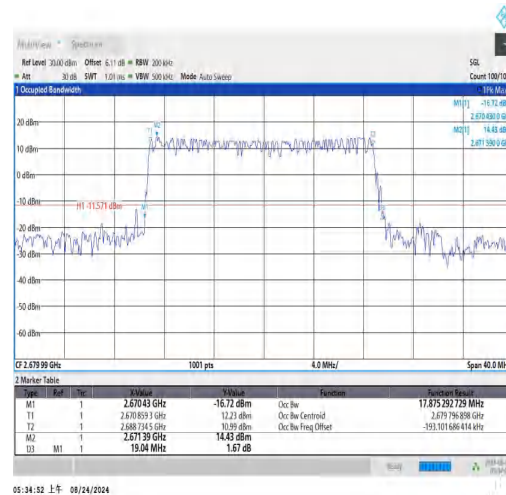
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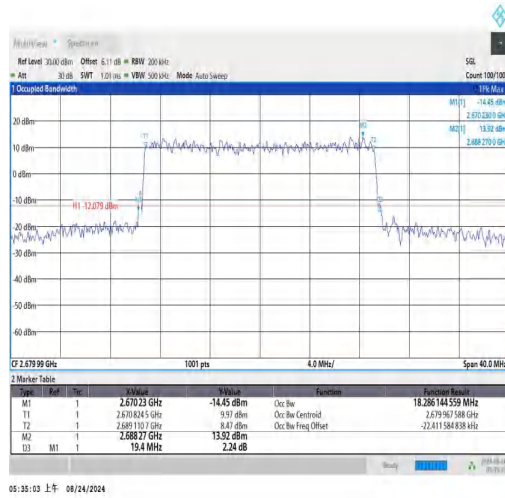


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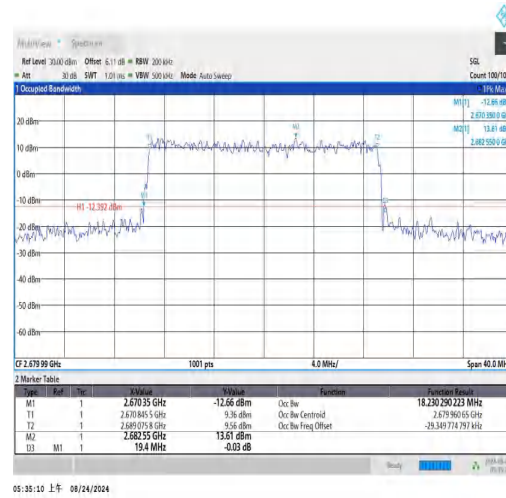


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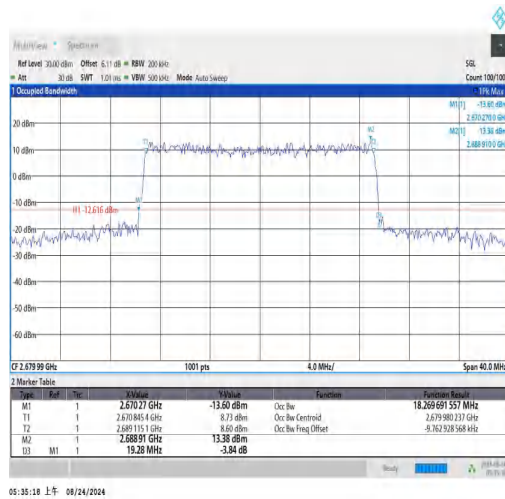
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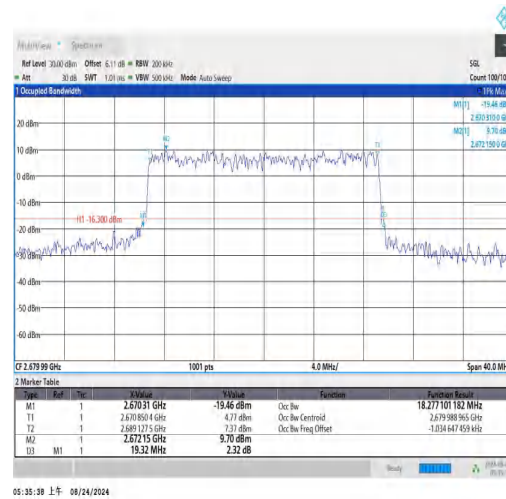
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NTVN_N41_PC3_30_20_H_TID6_Ant1#1



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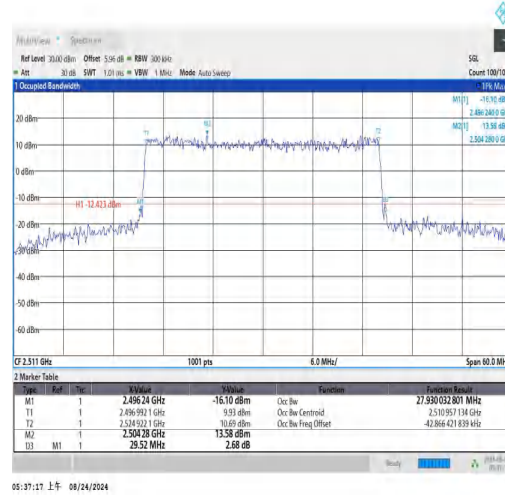


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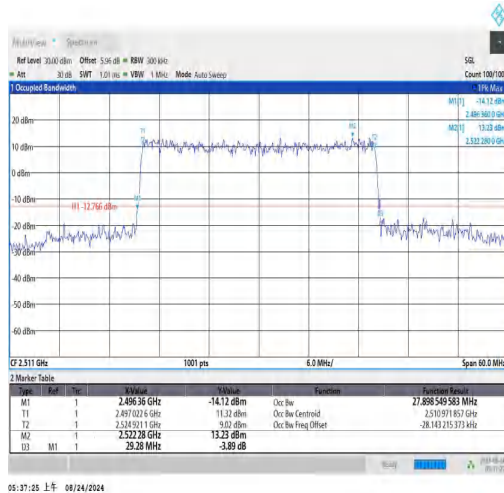
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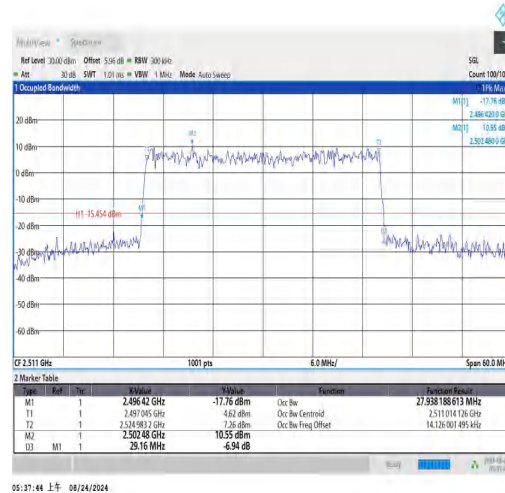
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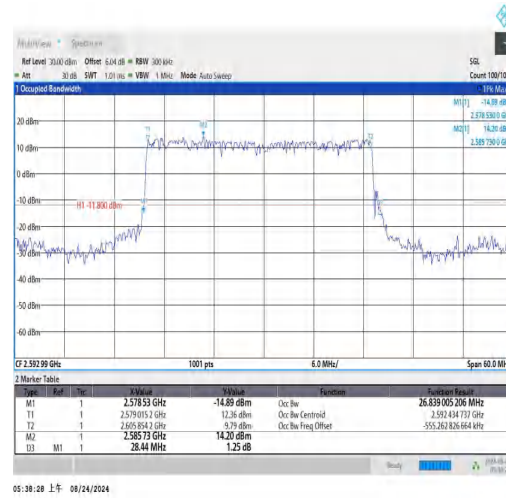
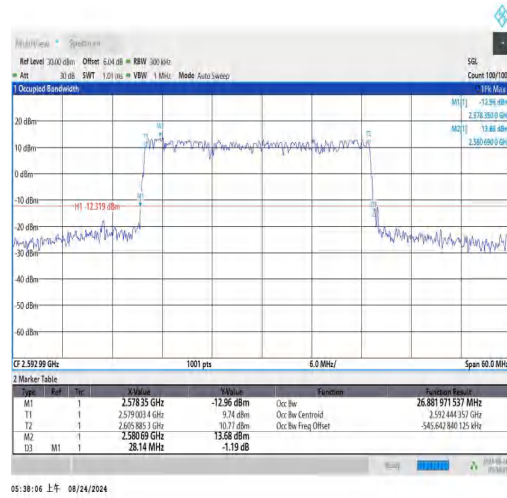
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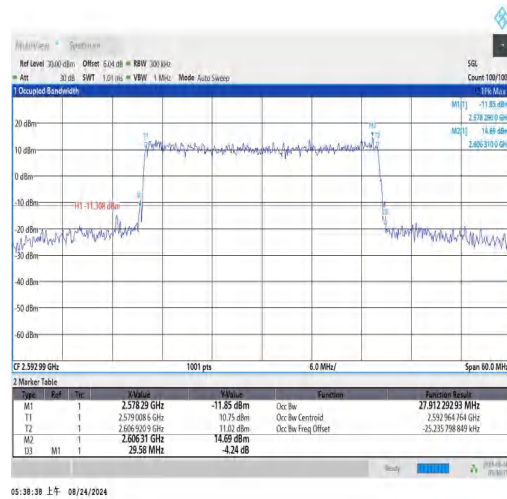
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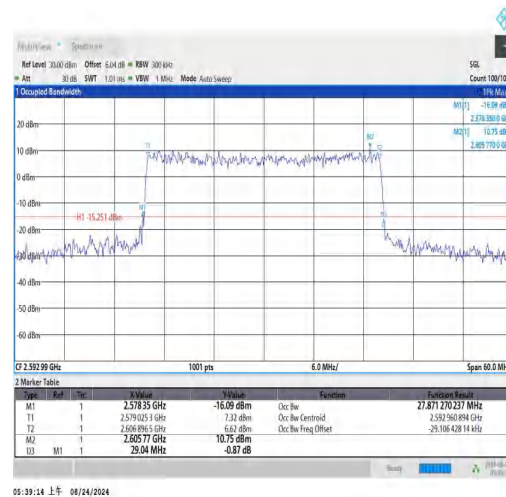
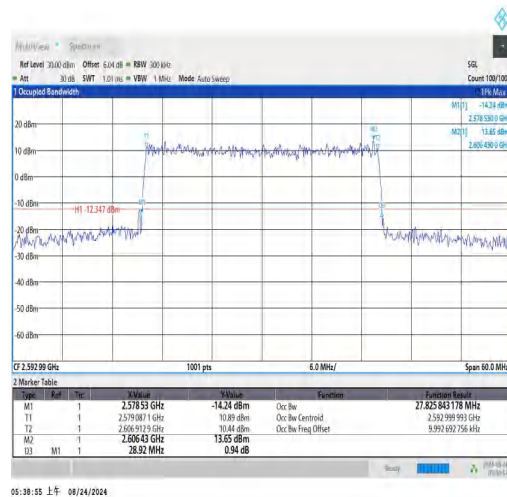
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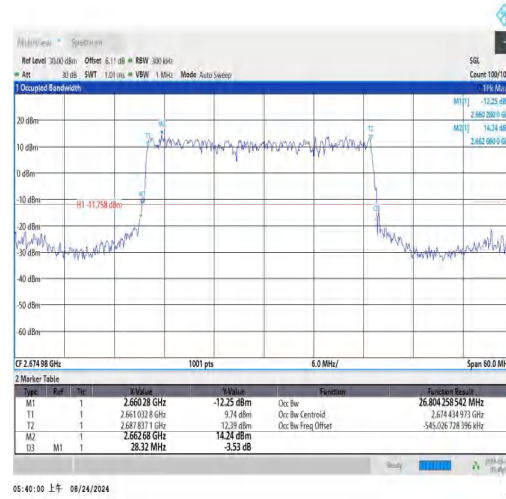




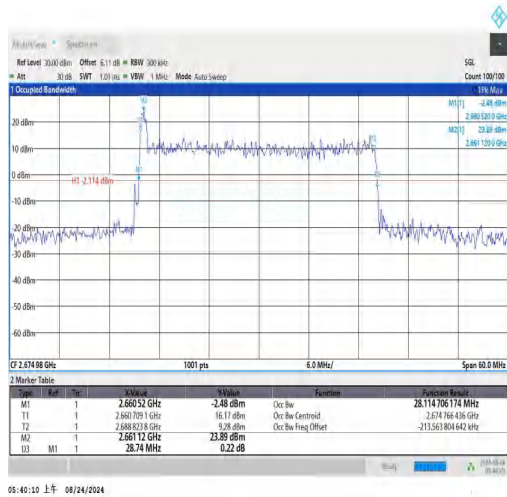
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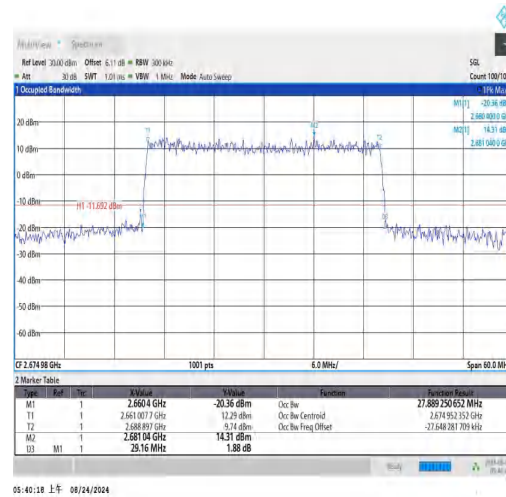
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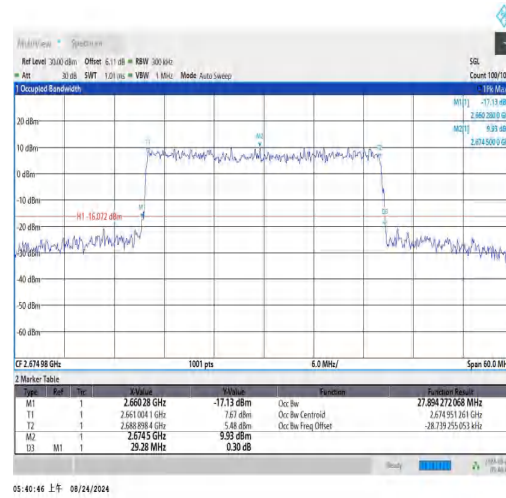
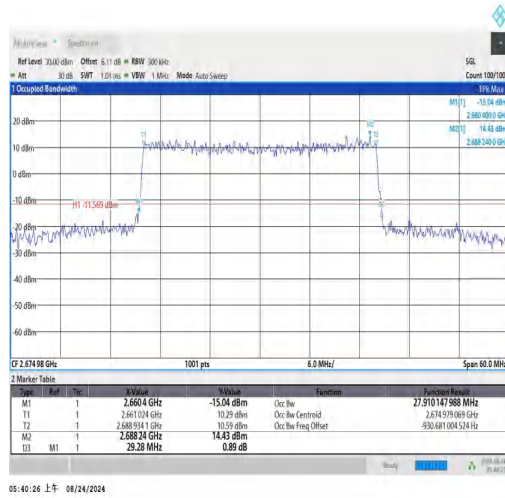


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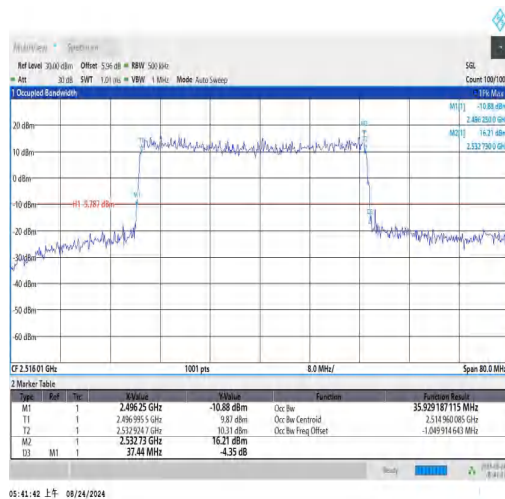
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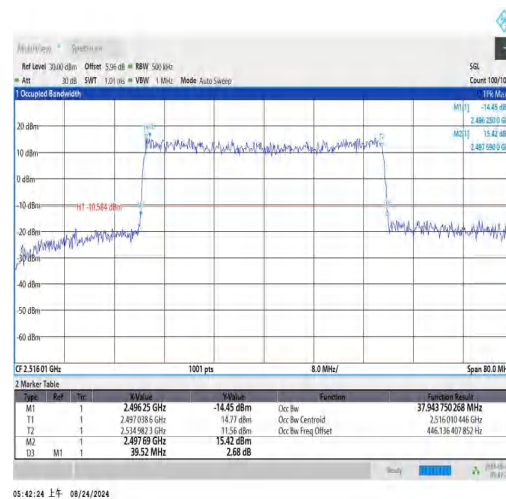
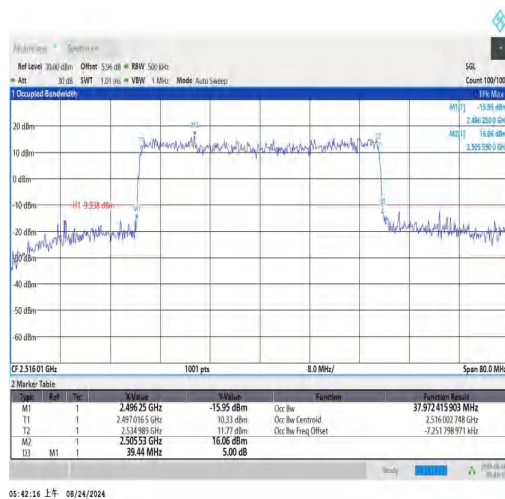
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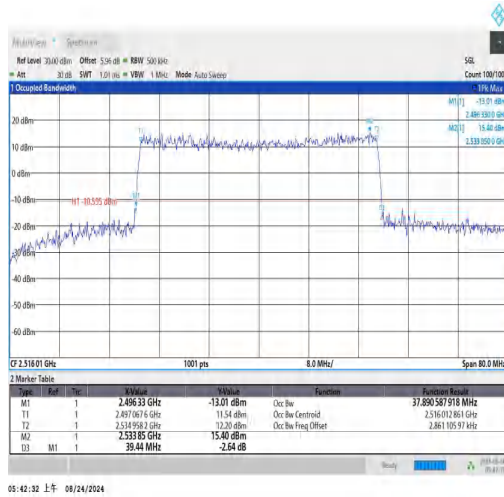
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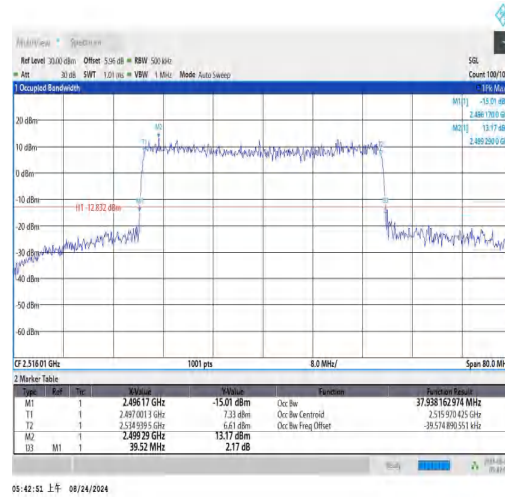




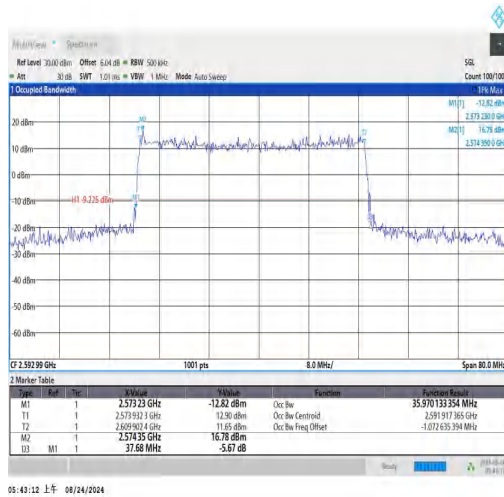
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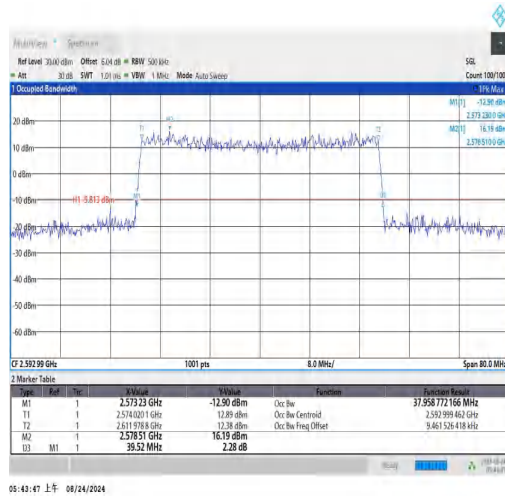


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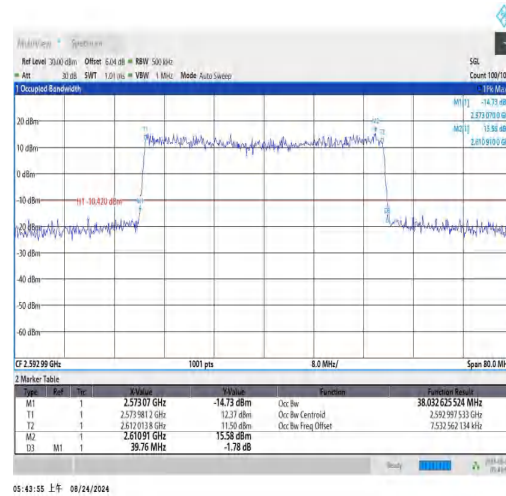


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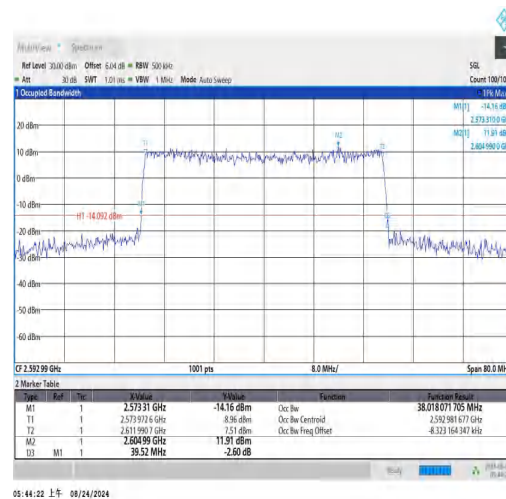
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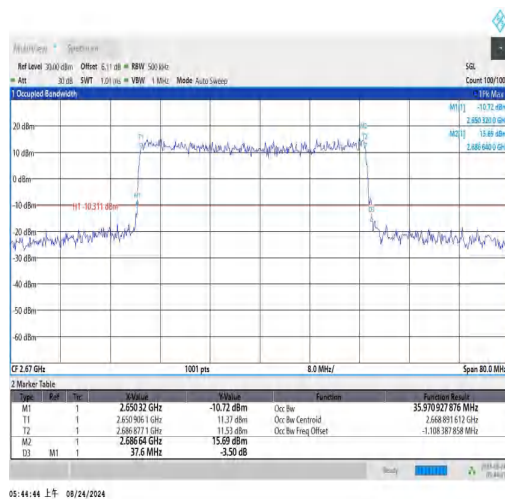
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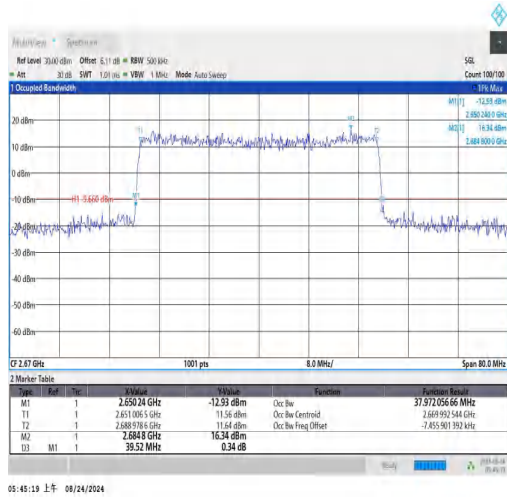


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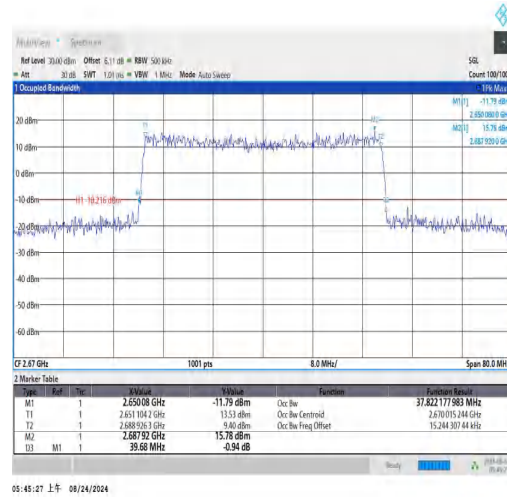




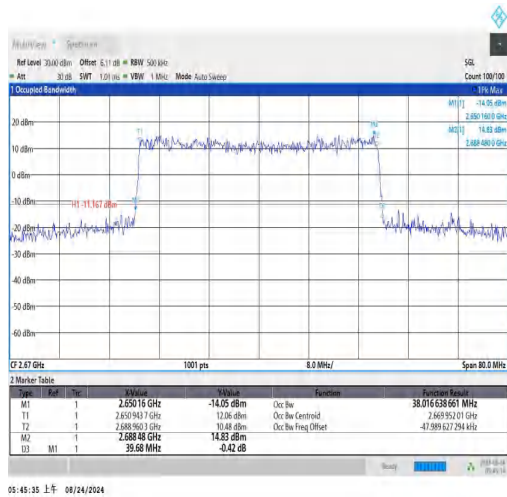
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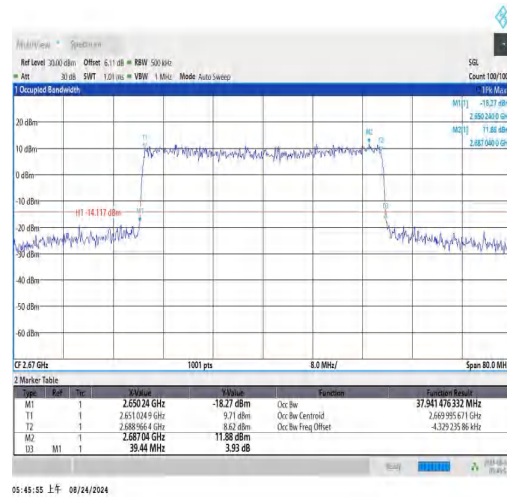
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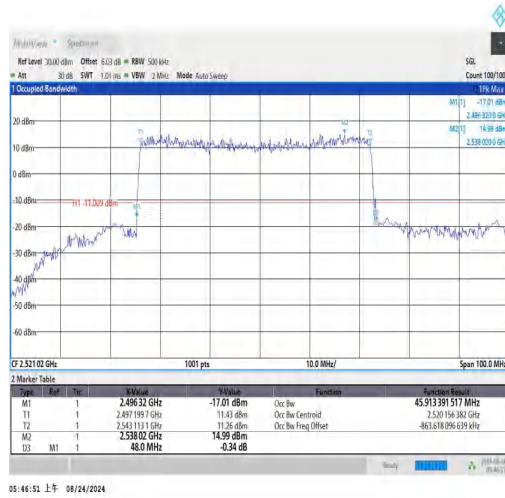


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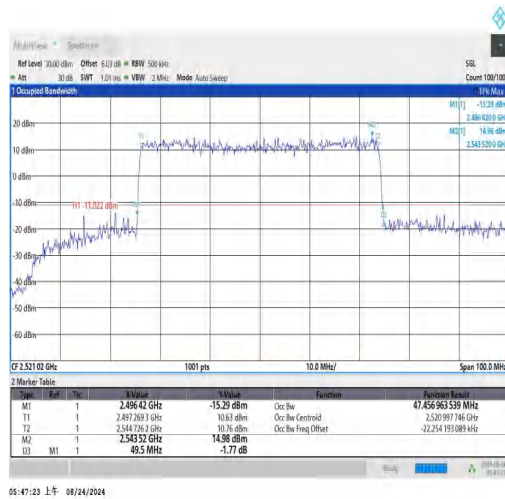
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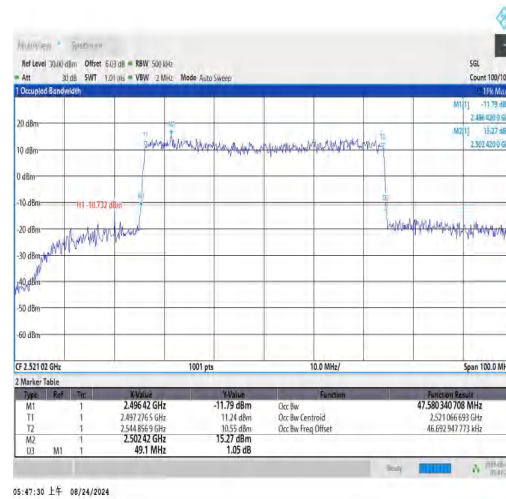
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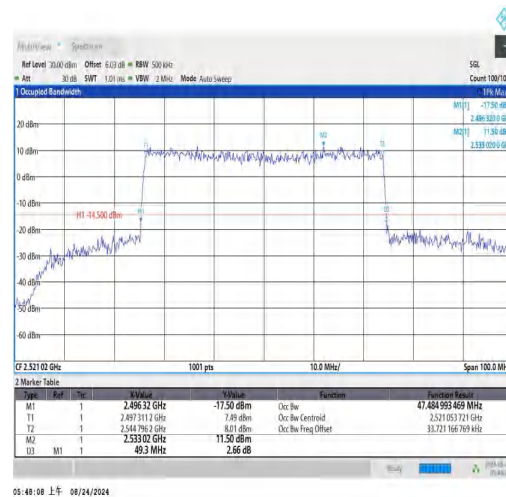
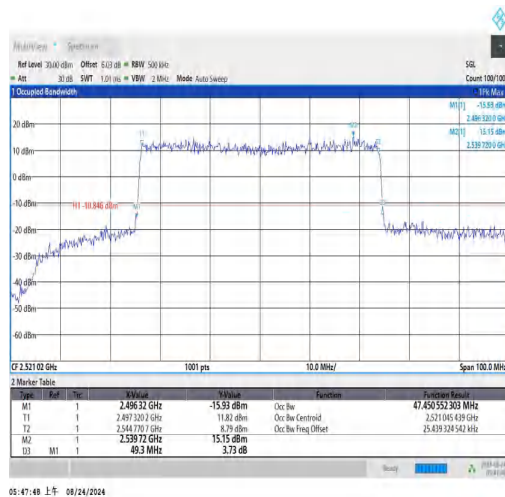
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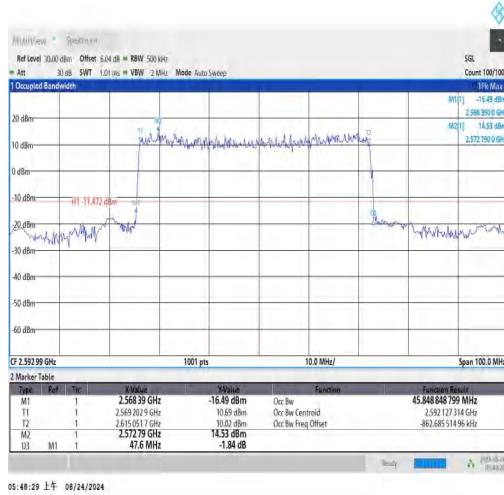


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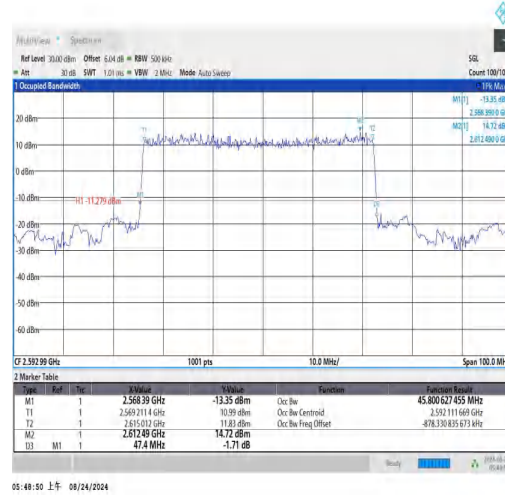




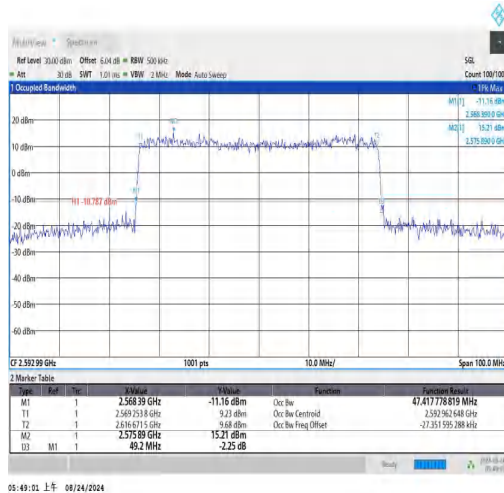
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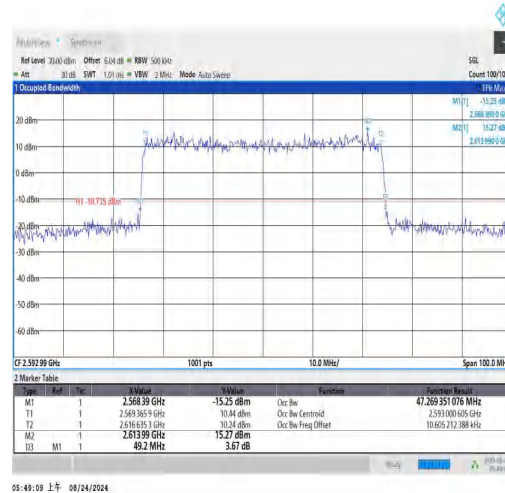
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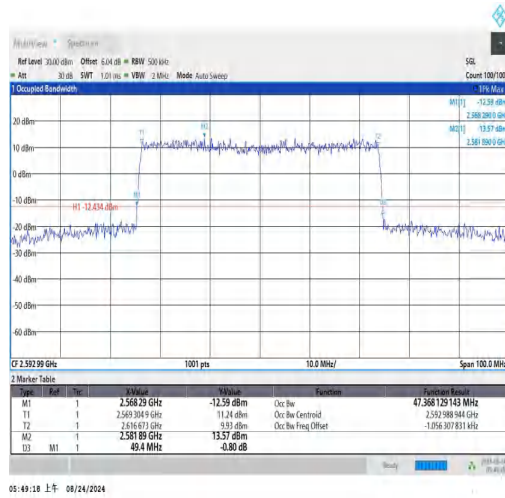


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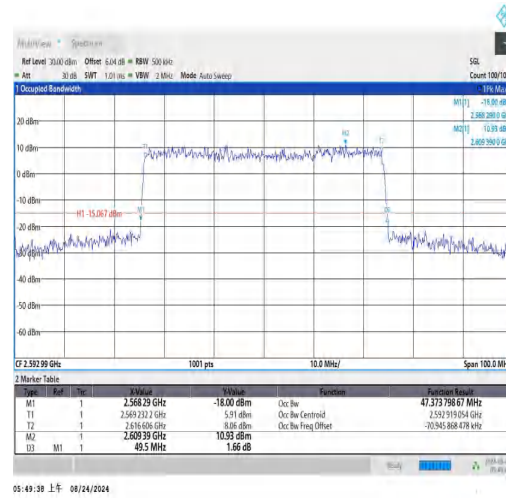


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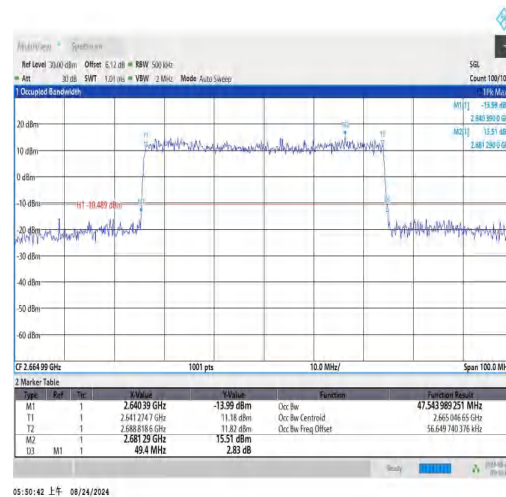
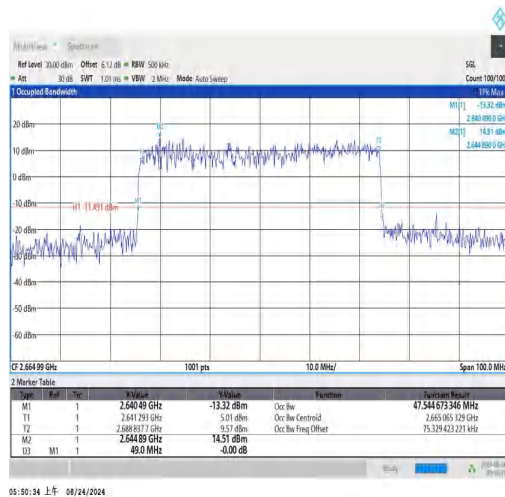
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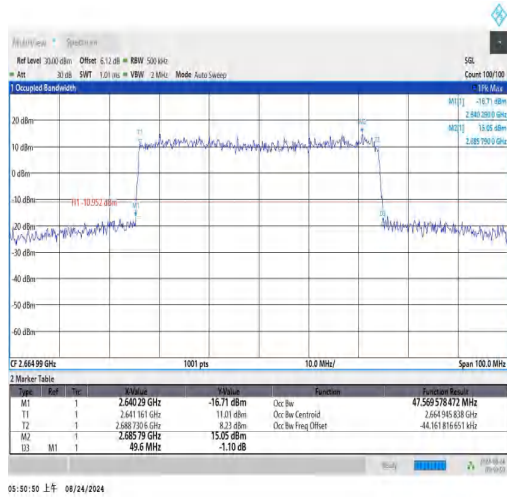


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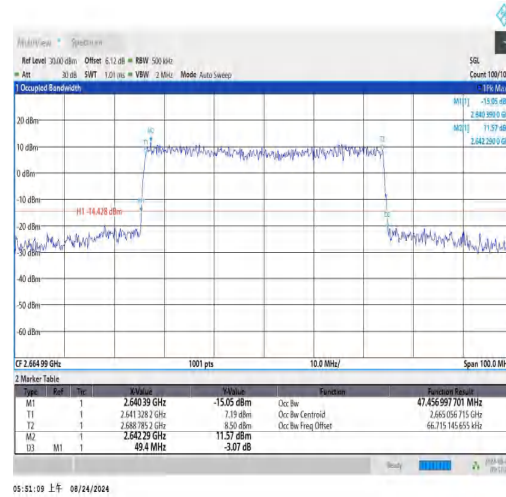




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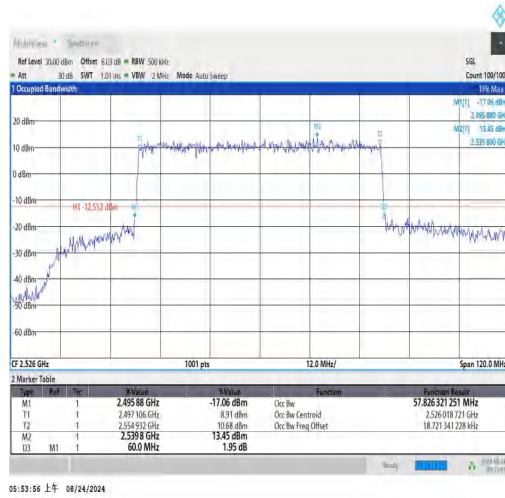


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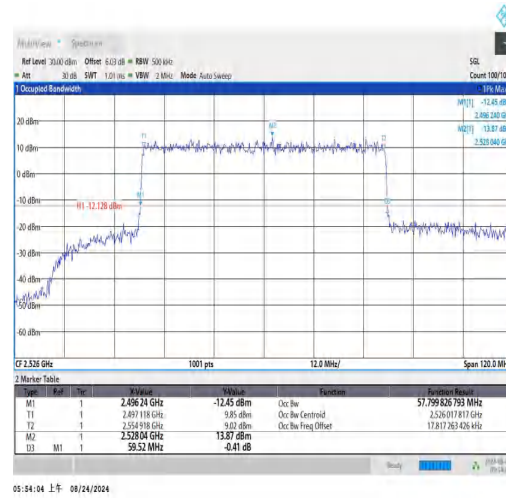


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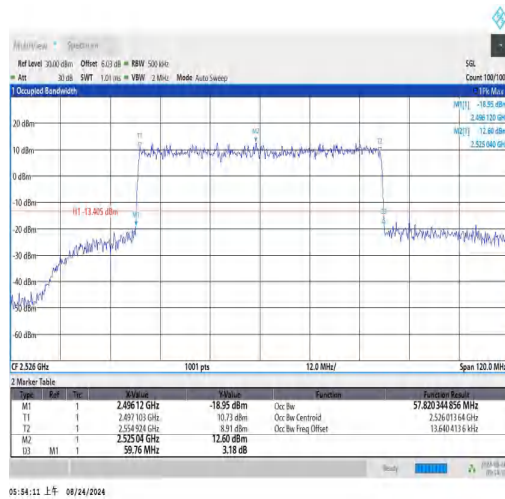
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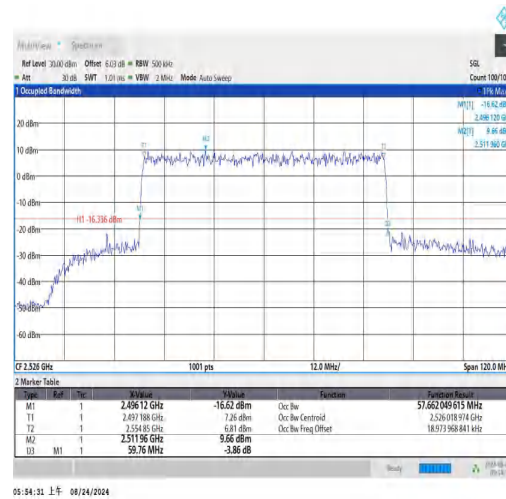
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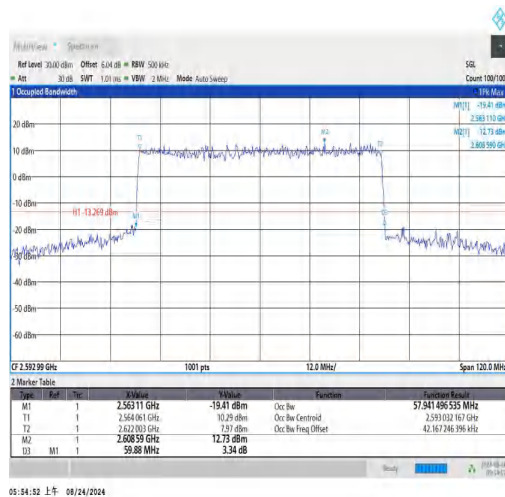
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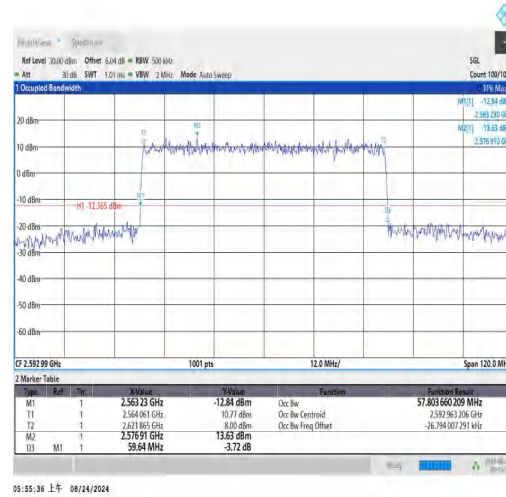




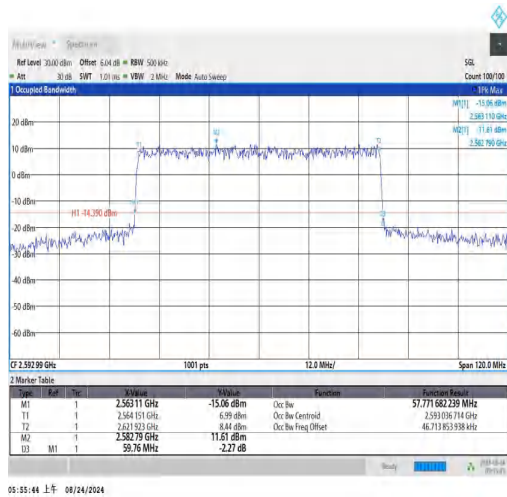
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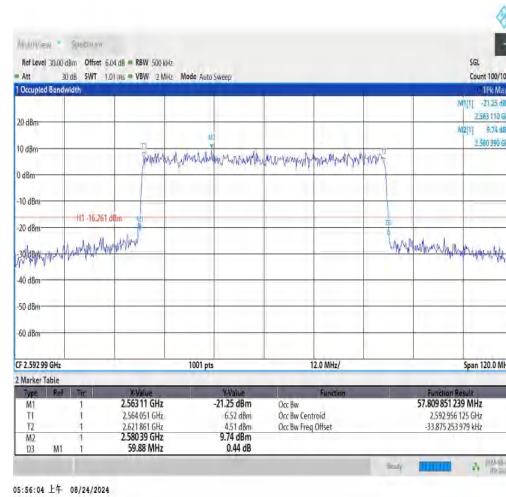
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NTNV_N41_PC3_30_60_M_TID4_Ant1#1



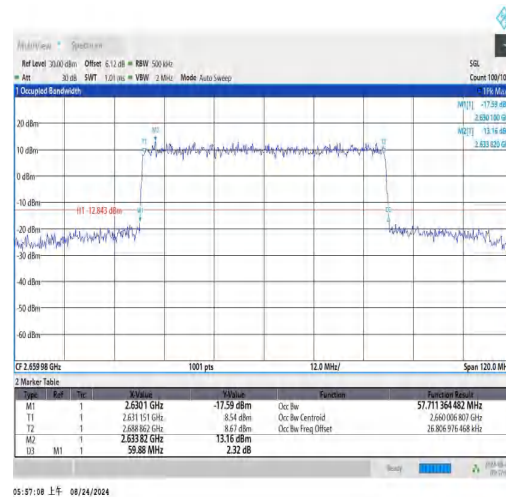
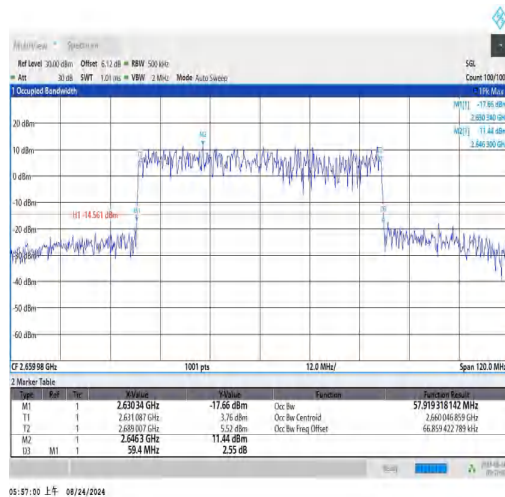
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NTNV_N41_PC3_30_60_M_TID6_Ant1#1



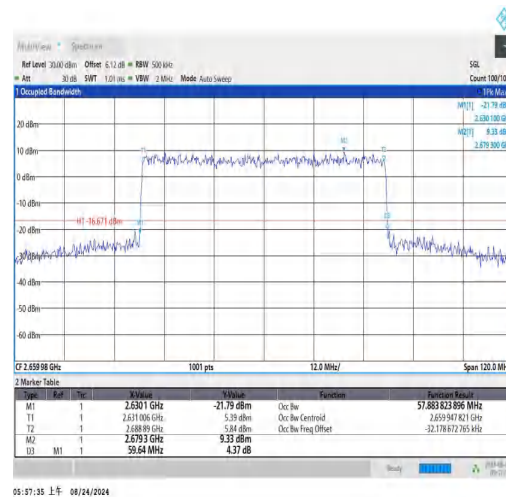
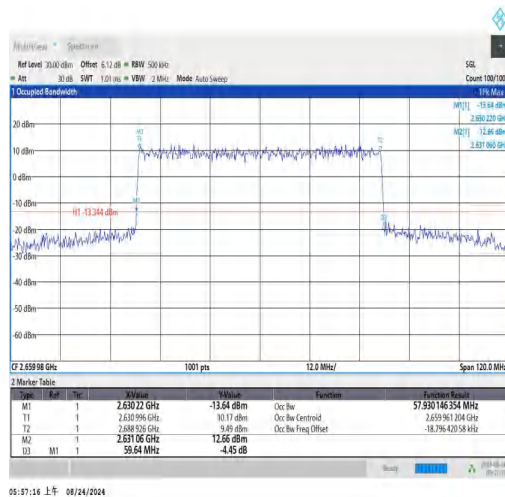
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NTNV_N41_PC3_30_60_H_TID2_Ant1#1



NTNV_N41_PC3_30_60_H_TID3_Ant1#1

NTNV_N41_PC3_30_60_H_TID4_Ant1#1

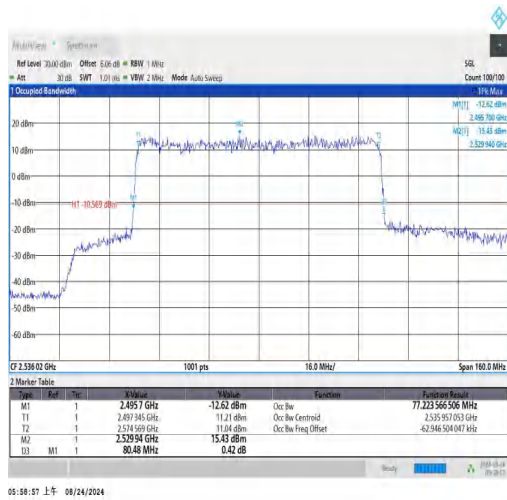


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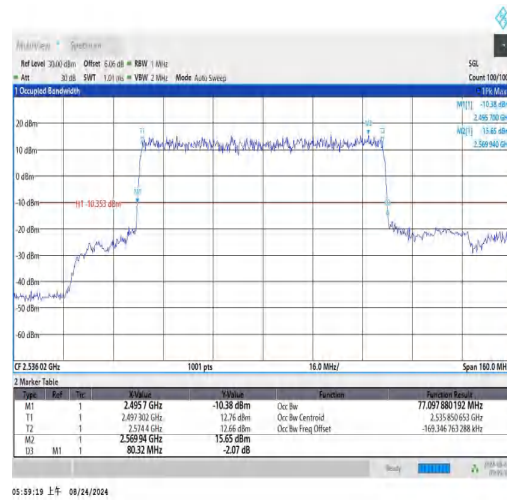
05:57:35 上午 08/24/2024



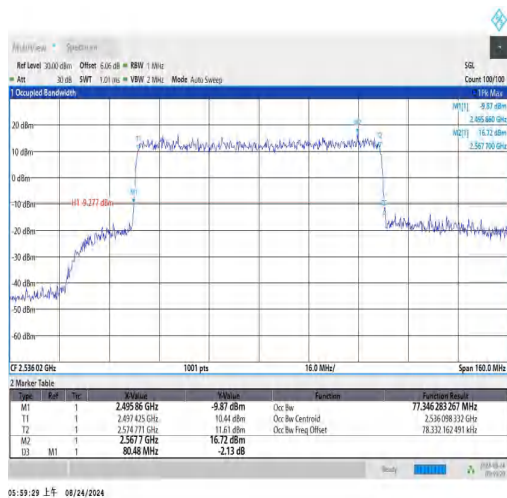
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NTNV_N41_PC3_30_60_H_TID6_Ant1#1



NTNV_N41_PC3_30_80_L_TID1_Ant1#1

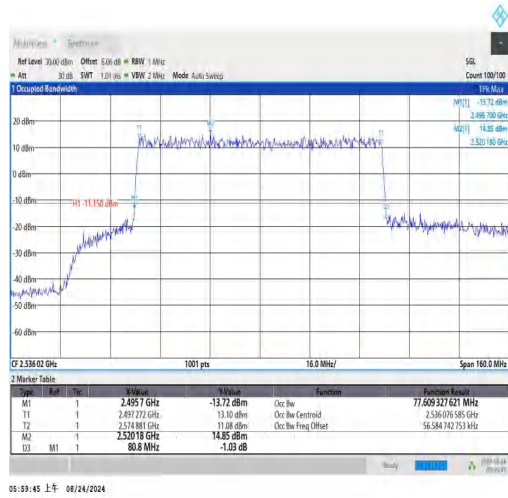


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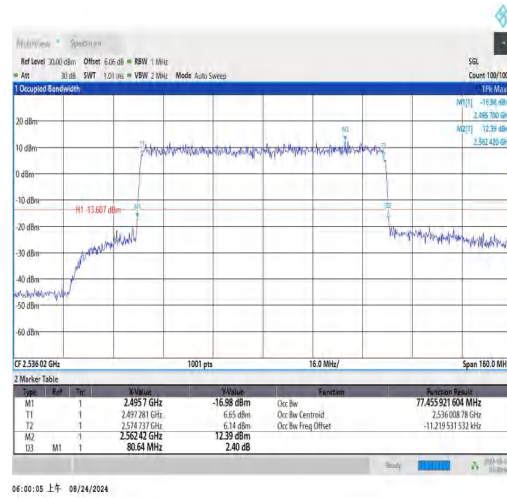


NTNV_N41_PC3_30_80_L_TID3_Ant1#1

NTNV_N41_PC3_30_80_L_TID4_Ant1#1



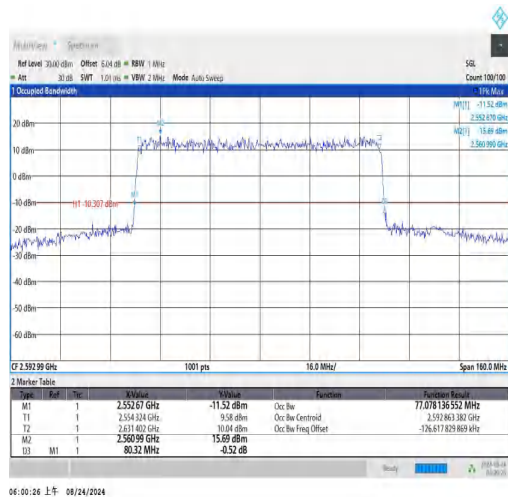
05:39:45 上午 08/24/2024



06:00:05 上午 08/24/2024

NTNV_N41_PC3_30_80_L_TID5_Ant1#1

NTNV_N41_PC3_30_80_L_TID6_Ant1#1



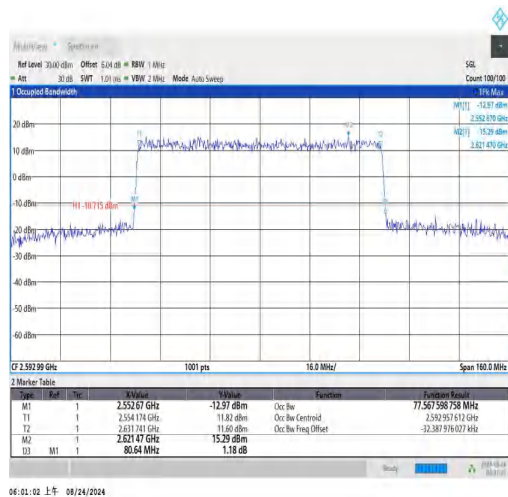
06:00:26 上午 08/24/2024



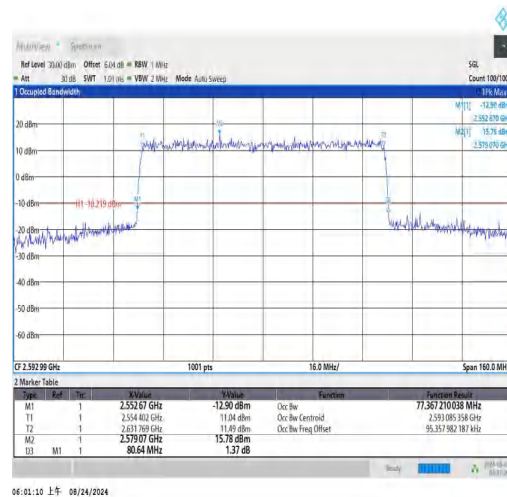
06:00:52 上午 08/24/2024

NTNV_N41_PC3_30_80_M_TID1_Ant1#1

NTNV_N41_PC3_30_80_M_TID2_Ant1#1



06:01:02 上午 08/24/2024



06:01:10 上午 08/24/2024

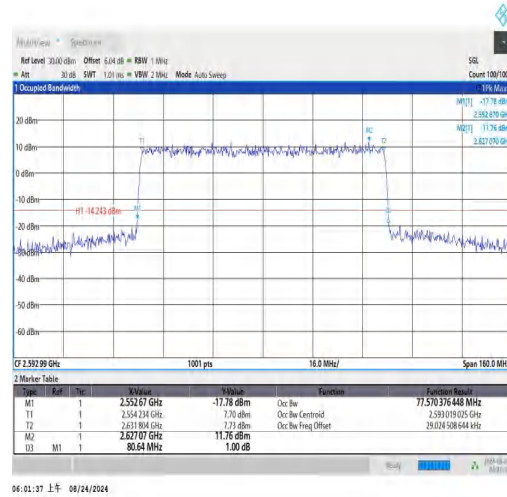


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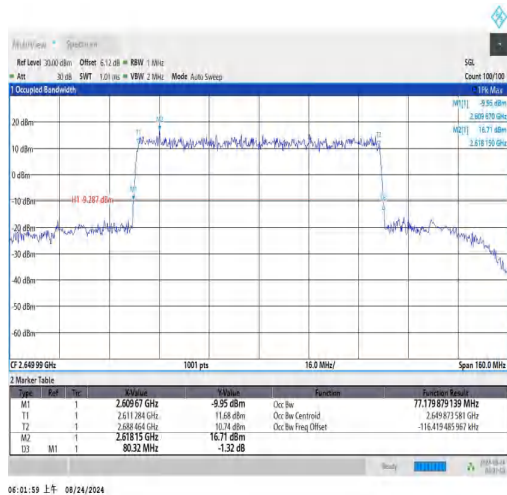
06:01:17 上午 08/24/2024

NTNV_N41_PC3_30_80_M_TID4_Ant1#1



06:01:17 上午 08/24/2024

NTNV_N41_PC3_30_80_M_TID5_Ant1#1



06:01:59 上午 08/24/2024

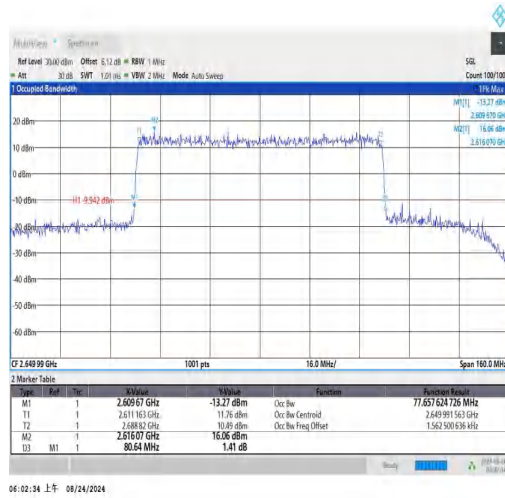
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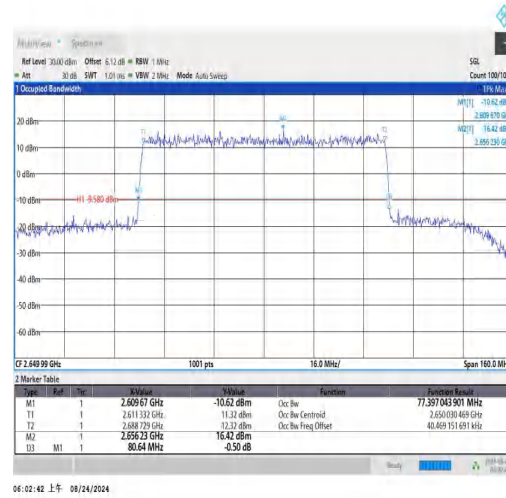
06:02:23 上午 08/24/2024

NTNV_N41_PC3_30_80_H_TID1_Ant1#1

NTNV_N41_PC3_30_80_H_TID2_Ant1#1



06:02:34 上午 08/24/2024



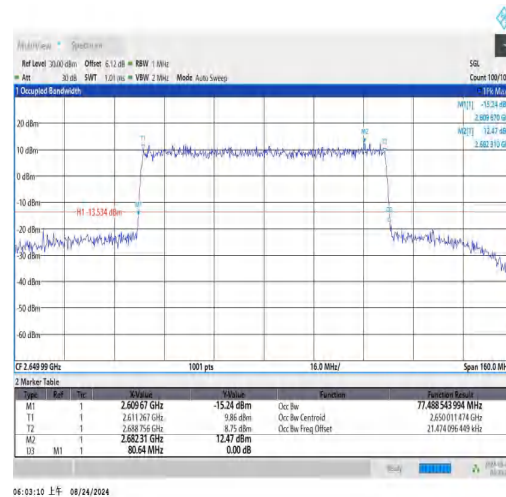
06:02:42 上午 08/24/2024

NTNV_N41_PC3_30_80_H_TID3_Ant1#1

NTNV_N41_PC3_30_80_H_TID4_Ant1#1



06:02:50 上午 08/24/2024



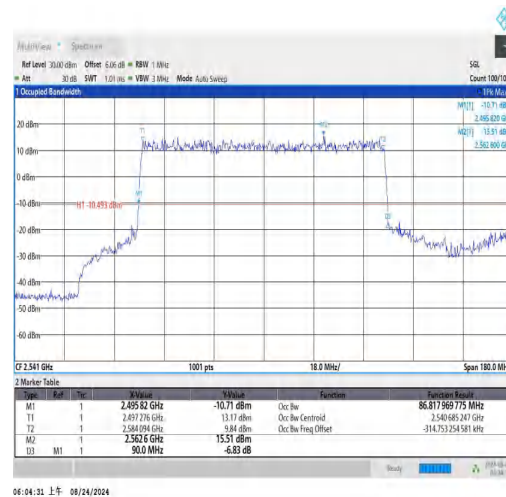
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NTNV_N41_PC3_30_80_H_TID5_Ant1#1

NTNV_N41_PC3_30_80_H_TID6_Ant1#1



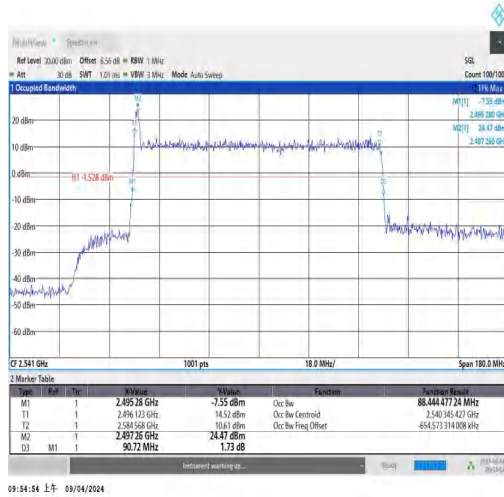
06:04:06 上午 08/24/2024



06:04:31 上午 08/24/2024



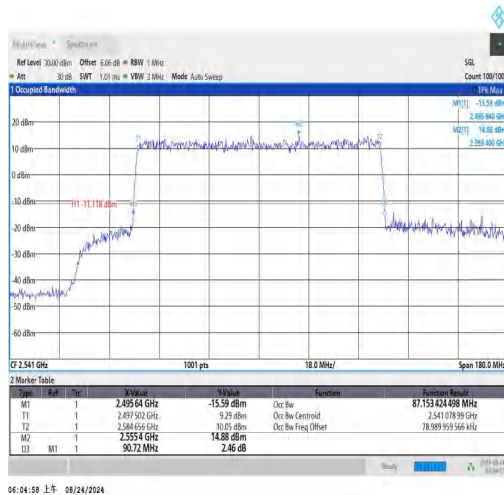
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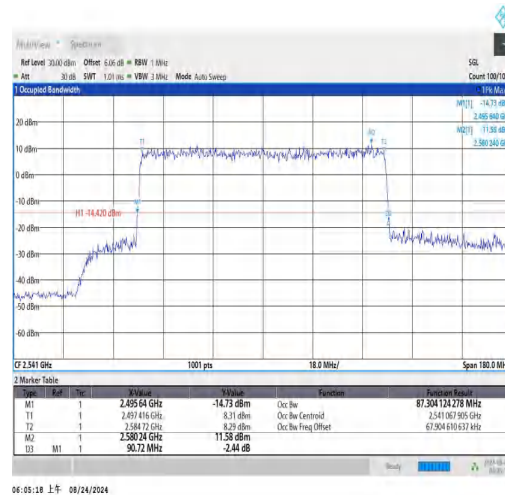
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NTNV_N41_PC3_30_90_L_TID3_Ant1#1

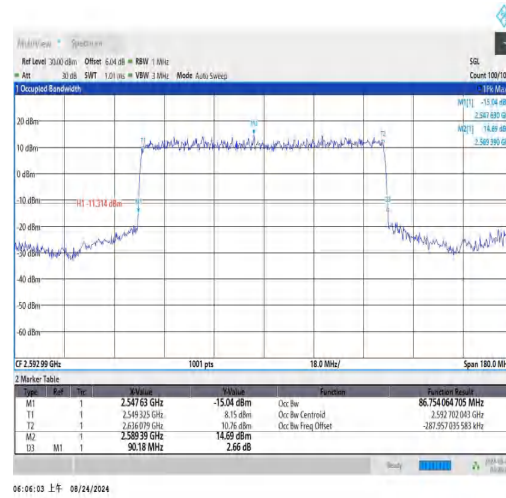
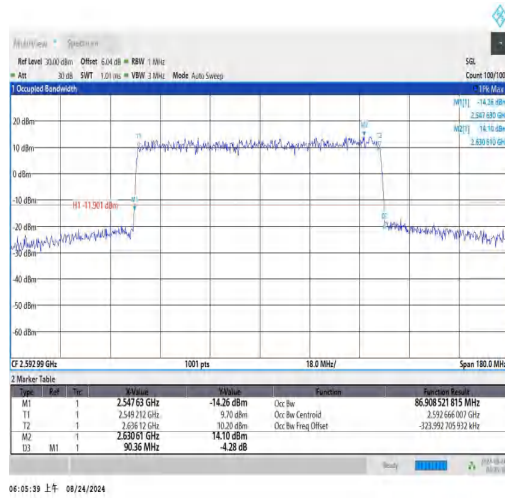


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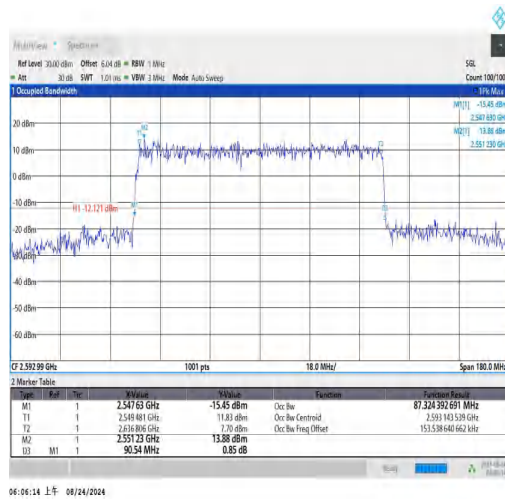
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NTNV_N41_PC3_30_90_L_TID6_Ant1#1



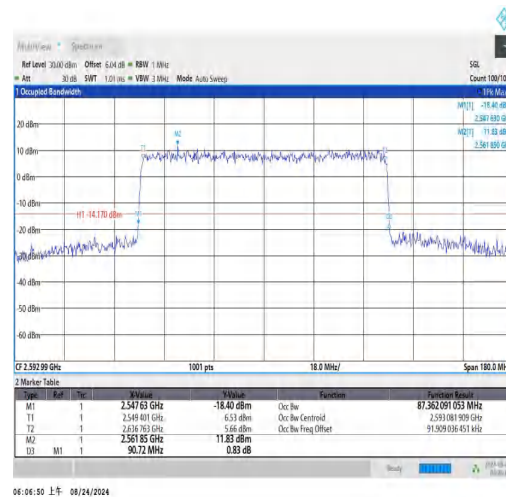
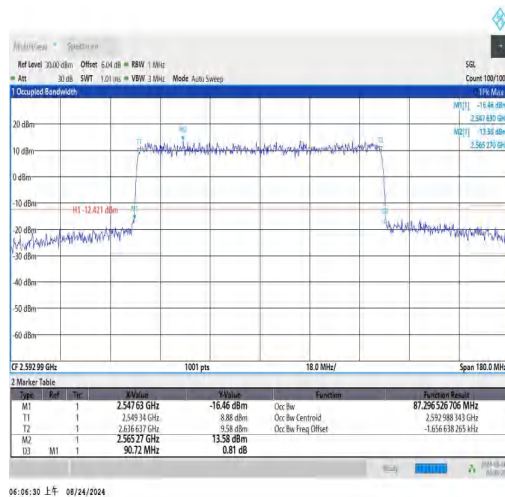
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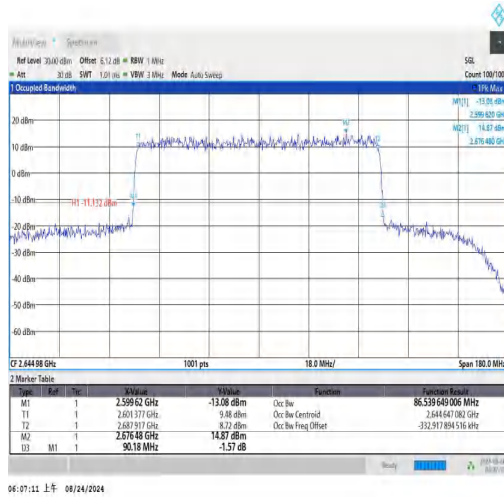
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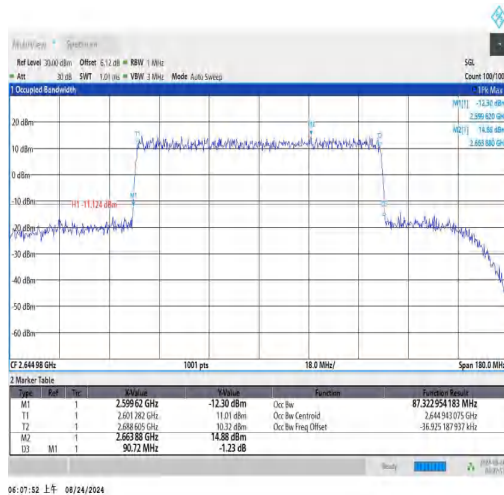
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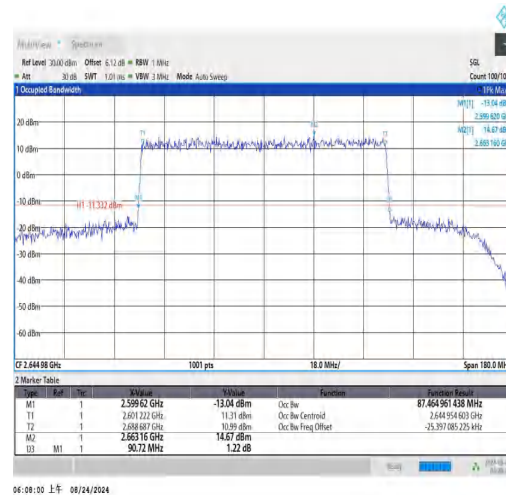
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NTNV_N41_PC3_30_90_H_TID1_Ant1#1



NTNV_N41_PC3_30_90_H_TID2_Ant1#1

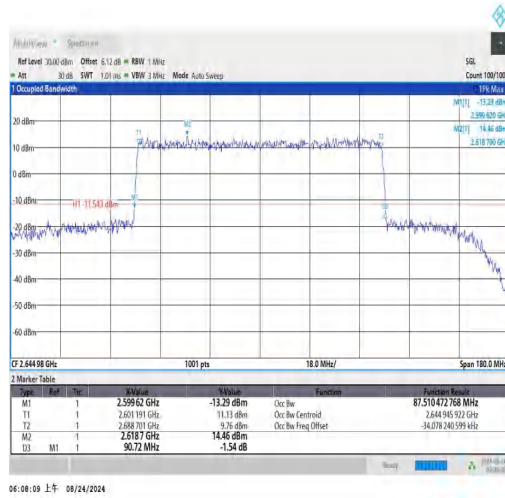


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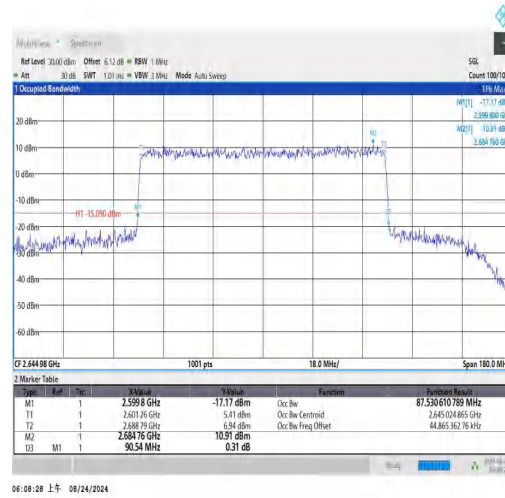


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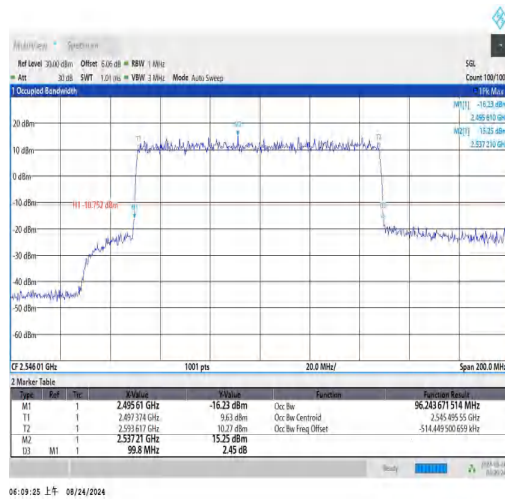




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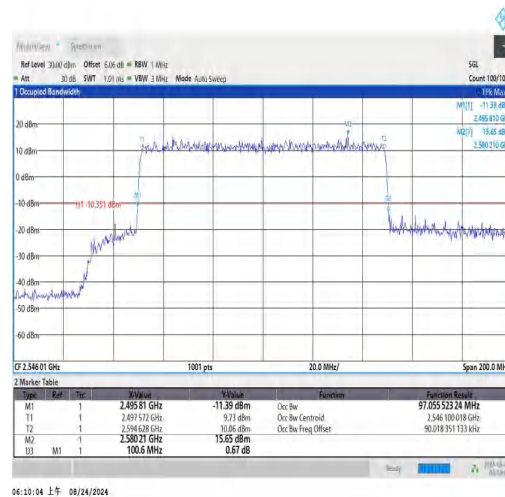
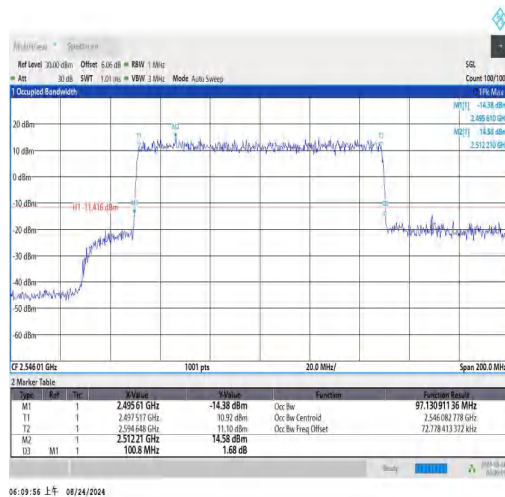
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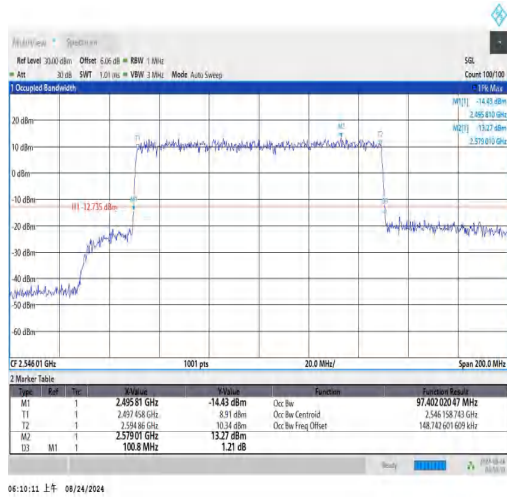


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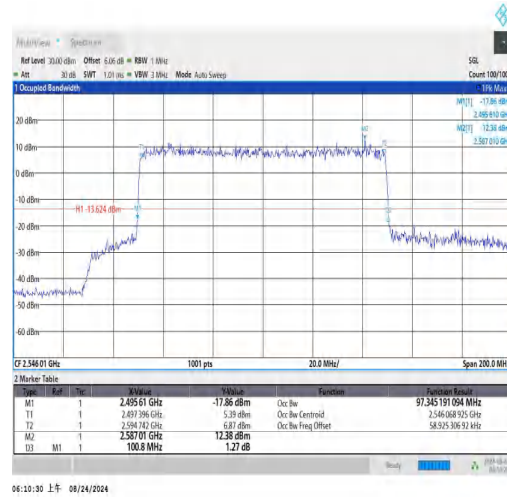


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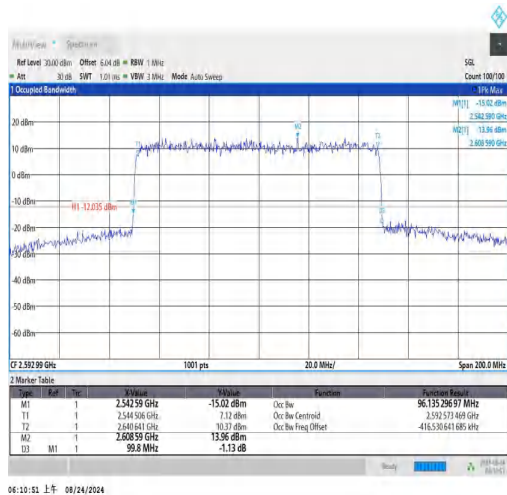
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NTNV_N41_PC3_30_100_L_TID4_Ant1#1



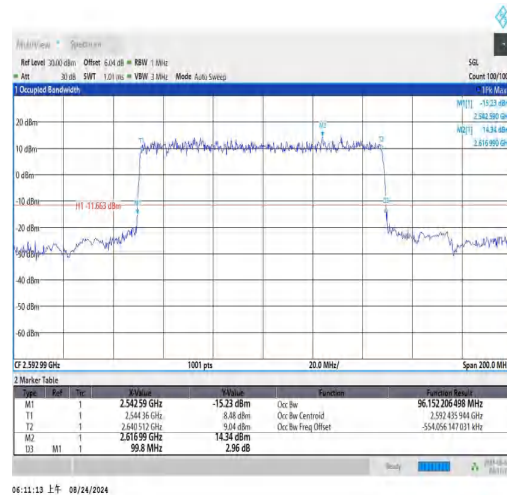
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NTNV_N41_PC3_30_100_L_TID5_Ant1#1



06:10:51 上午 08/24/2024

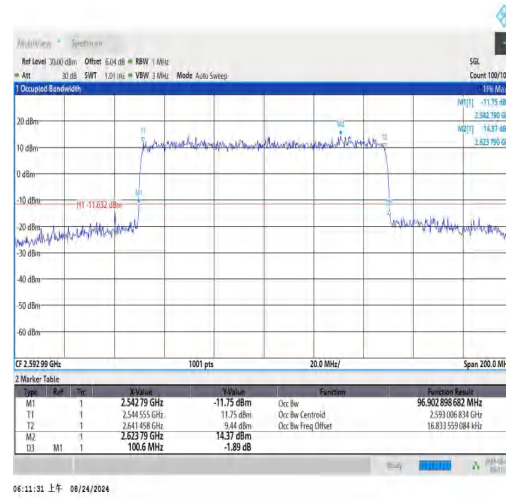
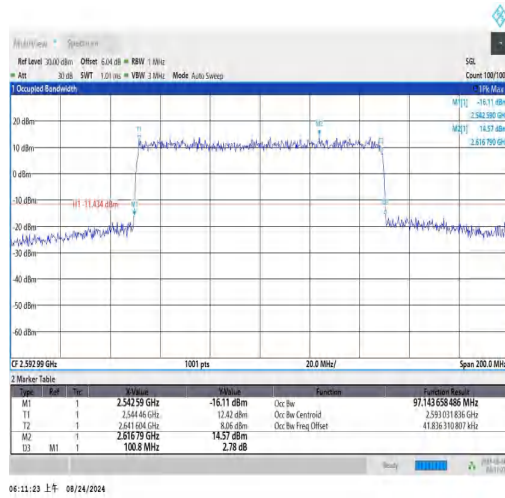
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06:11:13 上午 08/24/2024

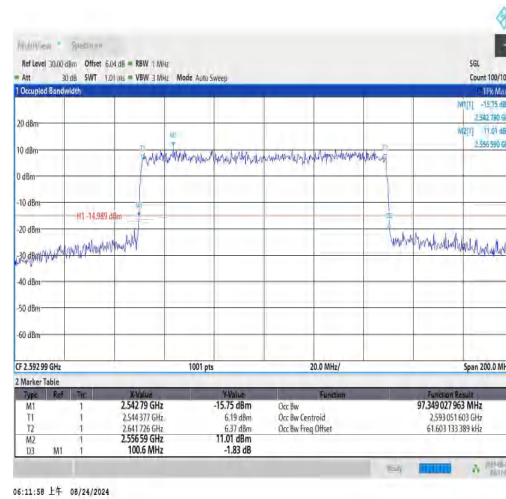
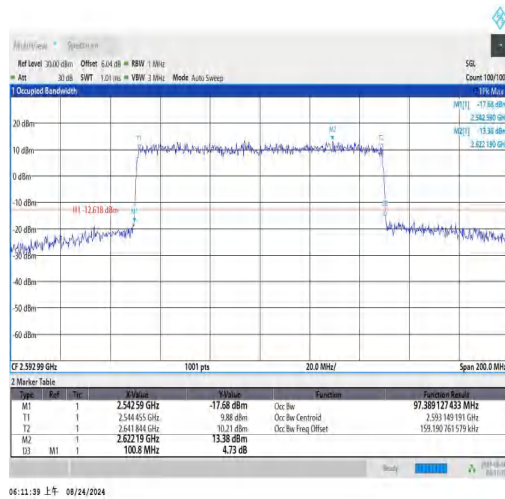
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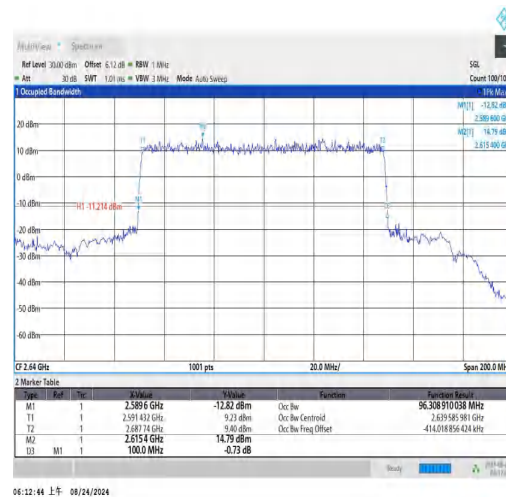
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NTNV_N41_PC3_30_100_M_TID4_Ant1#1



NTNV_N41_PC3_30_100_M_TID5_Ant1#1

NTNV_N41_PC3_30_100_M_TID6_Ant1#1

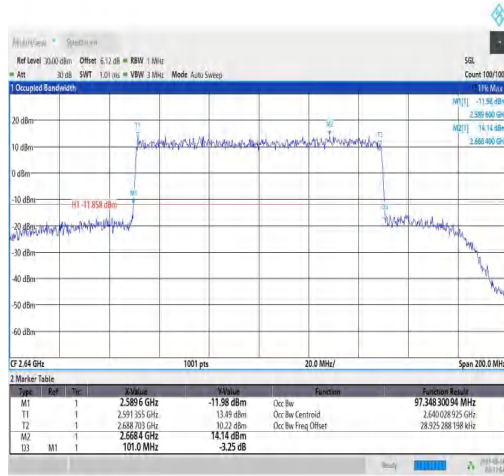


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06:12:44 上午 08/24/2024

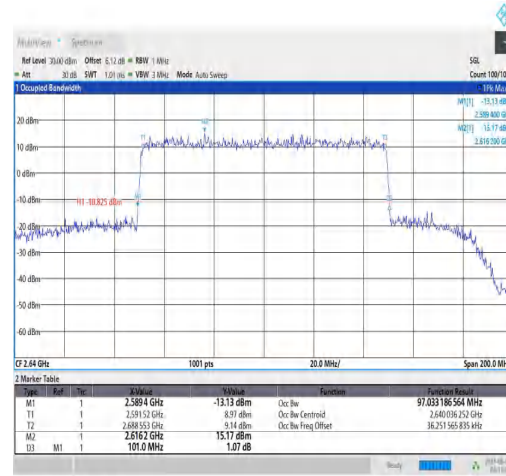


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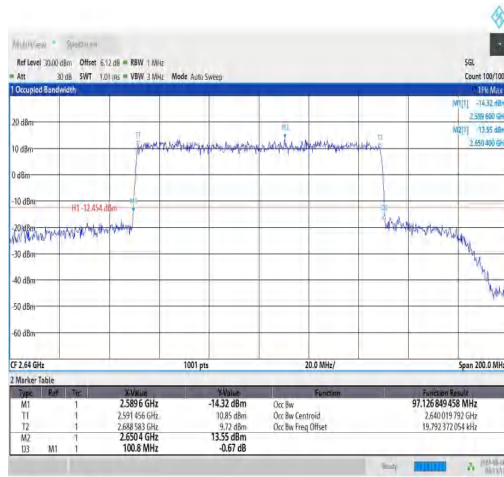
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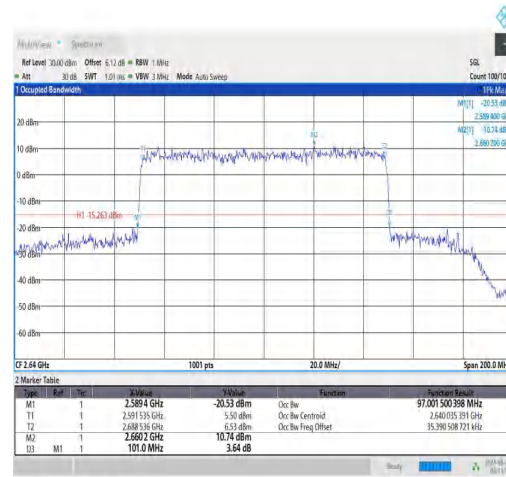
06:13:02 上午 08/24/2024

NTNV_N41_PC3_30_100_H_TID3_Ant1#1



06:13:11 上午 08/24/2024

NTNV_N41_PC3_30_100_H_TID4_Ant1#1



06:13:30 上午 08/24/2024

NTNV_N41_PC3_30_100_H_TID5_Ant1#1

NTNV_N41_PC3_30_100_H_TID6_Ant1#1



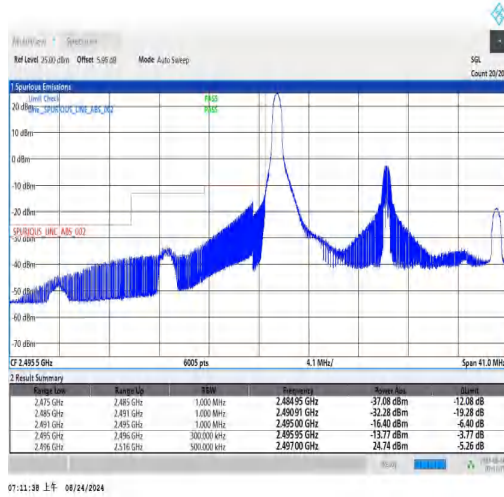
Appendix D: Band Edge

Test Result

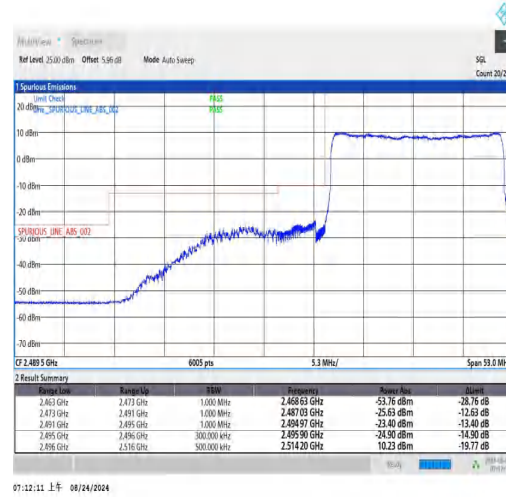
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Verdict
N41	30	20	DFT-PI2BPSK	L	Edge_1RB_Left	see graph	PASS
N41	30	20	DFT-PI2BPSK	L	Outer_Full	see graph	PASS
N41	30	20	DFT-QPSK	L	Edge_1RB_Left	see graph	PASS
N41	30	20	DFT-QPSK	L	Outer_Full	see graph	PASS
N41	30	20	DFT-PI2BPSK	H	Edge_1RB_Right	see graph	PASS
N41	30	20	DFT-PI2BPSK	H	Outer_Full	see graph	PASS
N41	30	20	DFT-QPSK	H	Edge_1RB_Right	see graph	PASS
N41	30	20	DFT-QPSK	H	Outer_Full	see graph	PASS
N41	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	see graph	PASS
N41	30	50	DFT-PI2BPSK	L	Outer_Full	see graph	PASS
N41	30	50	DFT-QPSK	L	Edge_1RB_Left	see graph	PASS
N41	30	50	DFT-QPSK	L	Outer_Full	see graph	PASS
N41	30	50	DFT-PI2BPSK	H	Edge_1RB_Right	see graph	PASS
N41	30	50	DFT-PI2BPSK	H	Outer_Full	see graph	PASS
N41	30	50	DFT-QPSK	H	Edge_1RB_Right	see graph	PASS
N41	30	50	DFT-QPSK	H	Outer_Full	see graph	PASS
N41	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	see graph	PASS
N41	30	100	DFT-PI2BPSK	L	Outer_Full	see graph	PASS
N41	30	100	DFT-QPSK	L	Edge_1RB_Left	see graph	PASS
N41	30	100	DFT-QPSK	L	Outer_Full	see graph	PASS
N41	30	100	DFT-PI2BPSK	H	Edge_1RB_Right	see graph	PASS
N41	30	100	DFT-PI2BPSK	H	Outer_Full	see graph	PASS
N41	30	100	DFT-QPSK	H	Edge_1RB_Right	see graph	PASS
N41	30	100	DFT-QPSK	H	Outer_Full	see graph	PASS



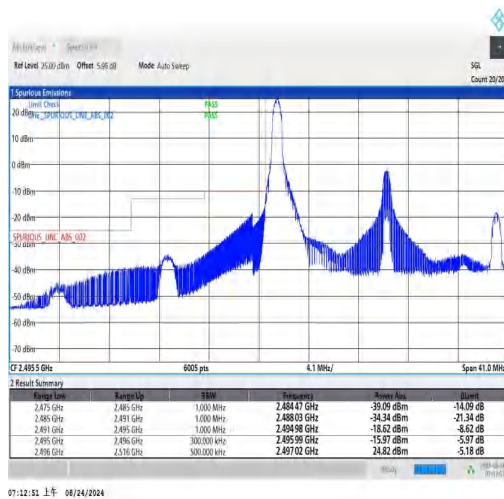
Test Graphs



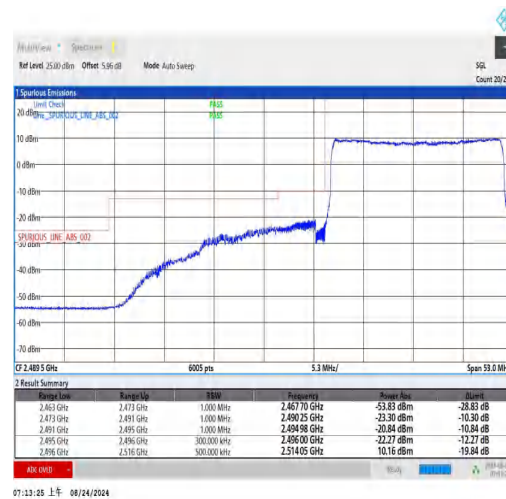
NTNV_N41_PC3_30_20_L_TID1_RID1#1



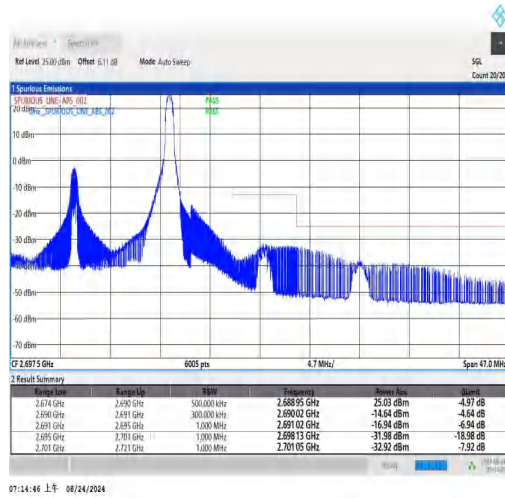
NTNV_N41_PC3_30_20_L_TID3_RID1#1



NTNV_N41_PC3_30_20_L_TID4_RID1#1



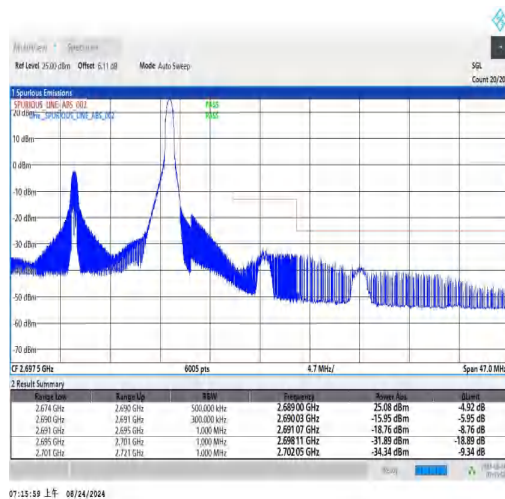
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NTNV_N41_PC3_30_20_H_TID2_RID1#1



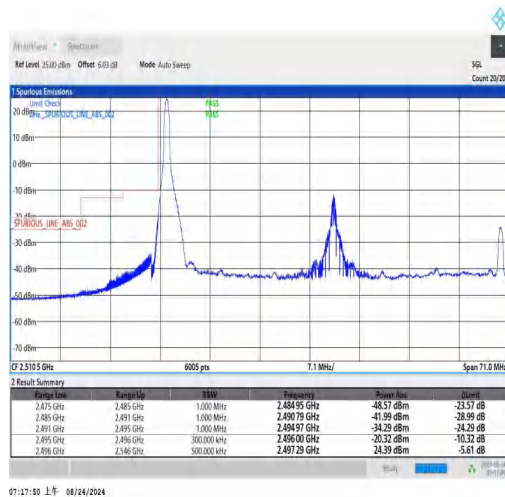
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NTNV_N41_PC3_30_20_H_TID5_RID1#1

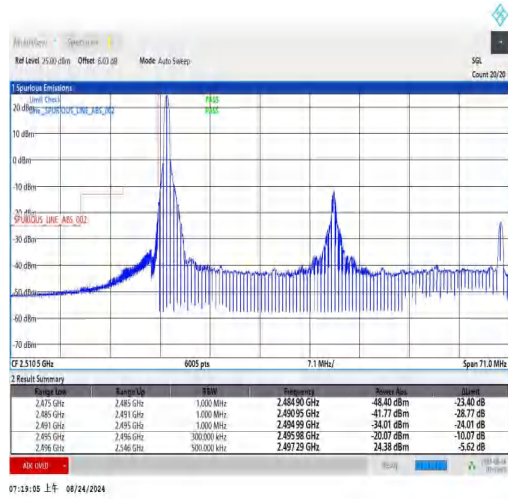


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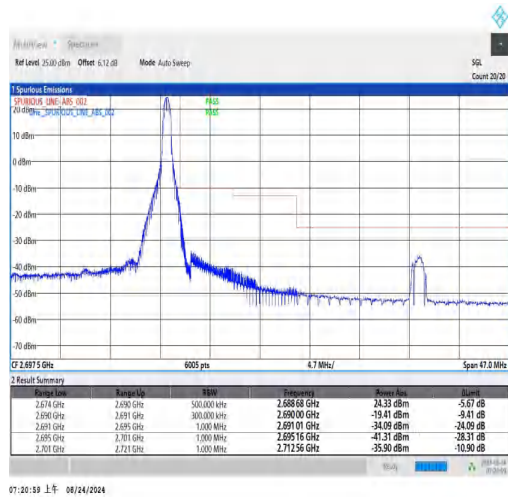
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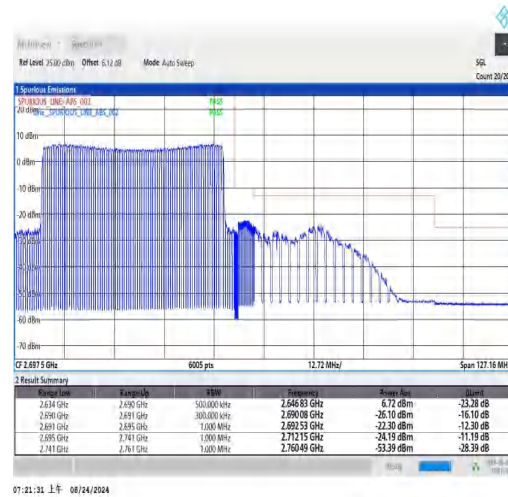
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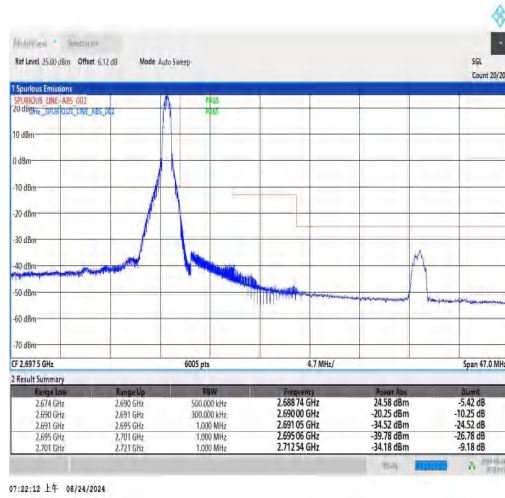


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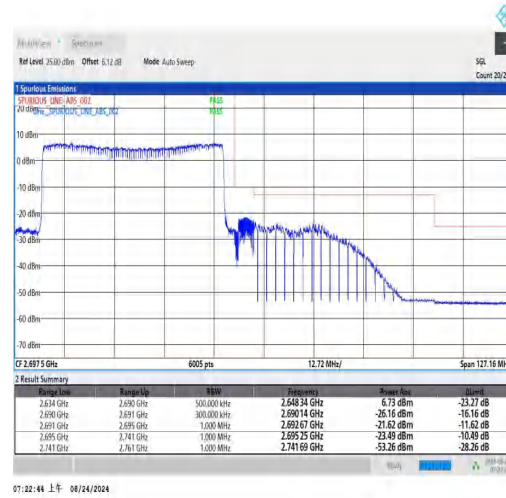


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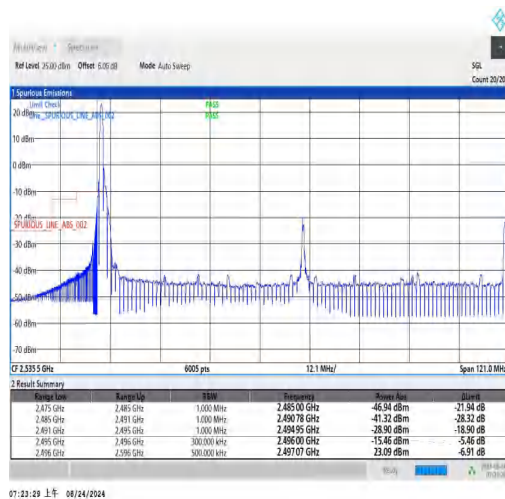
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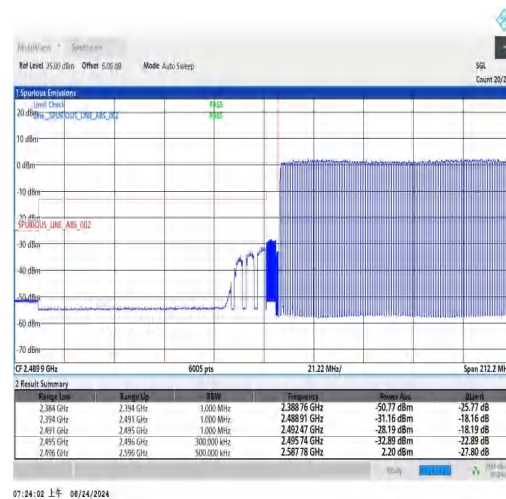
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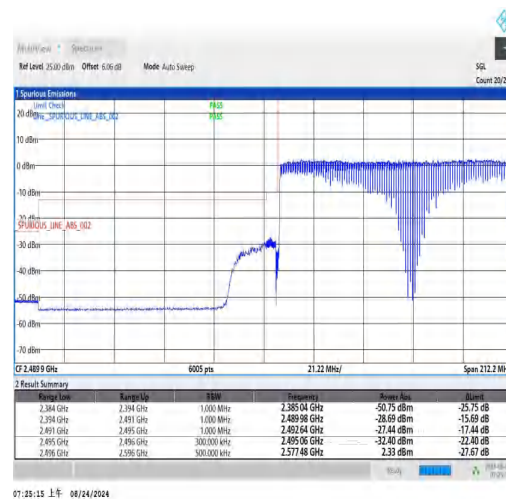
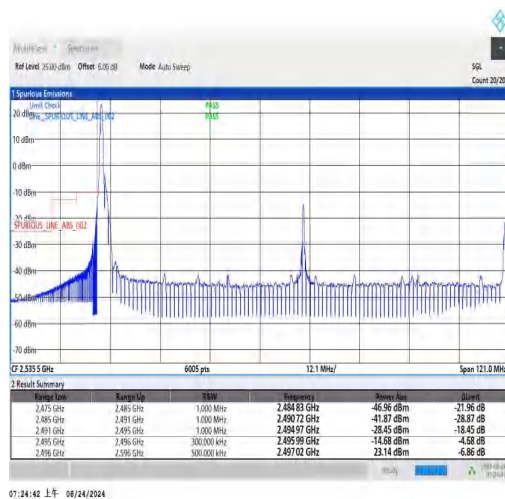
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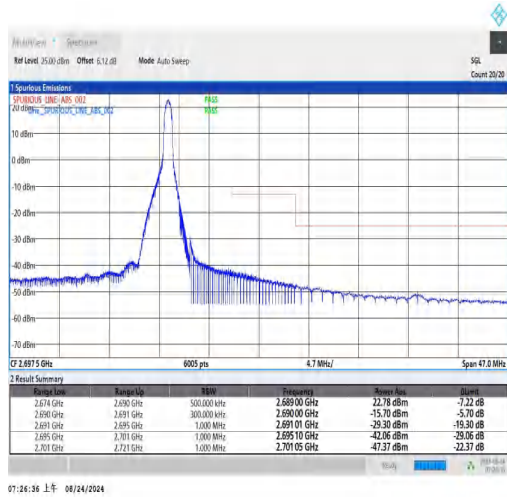


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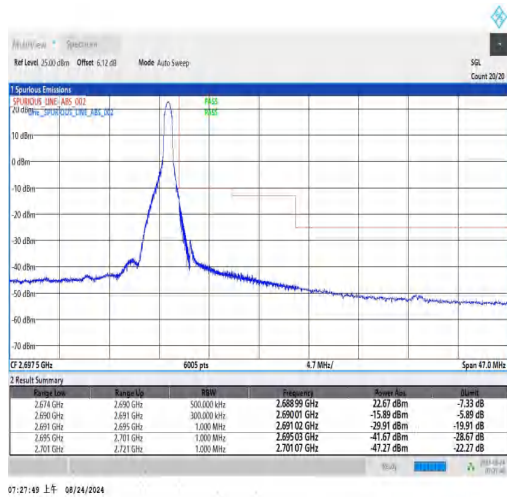
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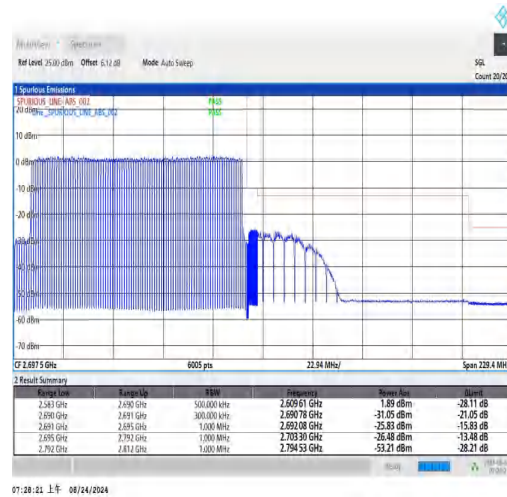
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NTNV_N41_PC3_30_100_H_TID5_RID1#1

NTNV_N41_PC3_30_100_H_TID6_RID1#1



Appendix E: Conducted Spurious Emission

Test Result

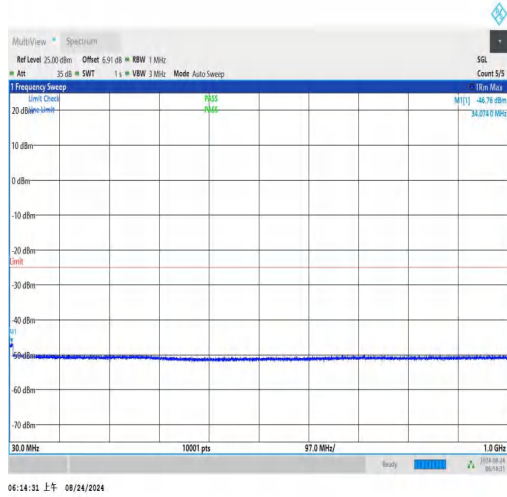
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Start Freq (MHz)	Stop Freq (MHz)	Result (dBm)	Limit (dBm)	Verdict
N41	30	20	DFT-PI2BPSK	L	Edge_1RB_Left	30	1000	-46.76	-25	PASS
N41	30	20	DFT-PI2BPSK	L	Edge_1RB_Left	1000	3000	-35.04	-25	PASS
N41	30	20	DFT-PI2BPSK	L	Edge_1RB_Left	3000	18000	-41.32	-25	PASS
N41	30	20	DFT-PI2BPSK	L	Edge_1RB_Left	18000	26500	-50.41	-25	PASS
N41	30	20	DFT-QPSK	L	Edge_1RB_Left	30	1000	-46.72	-25	PASS
N41	30	20	DFT-QPSK	L	Edge_1RB_Left	1000	3000	-35.79	-25	PASS
N41	30	20	DFT-QPSK	L	Edge_1RB_Left	3000	18000	-41.88	-25	PASS
N41	30	20	DFT-QPSK	L	Edge_1RB_Left	18000	26500	-49.88	-25	PASS
N41	30	20	DFT-PI2BPSK	M	Edge_1RB_Left	30	1000	-46.29	-25	PASS
N41	30	20	DFT-PI2BPSK	M	Edge_1RB_Left	1000	3000	-40.85	-25	PASS
N41	30	20	DFT-PI2BPSK	M	Edge_1RB_Left	3000	18000	-42.09	-25	PASS
N41	30	20	DFT-PI2BPSK	M	Edge_1RB_Left	18000	26500	-50.07	-25	PASS
N41	30	20	DFT-QPSK	M	Edge_1RB_Left	30	1000	-46.68	-25	PASS
N41	30	20	DFT-QPSK	M	Edge_1RB_Left	1000	3000	-37.81	-25	PASS
N41	30	20	DFT-QPSK	M	Edge_1RB_Left	3000	18000	-41.80	-25	PASS
N41	30	20	DFT-QPSK	M	Edge_1RB_Left	18000	26500	-50.40	-25	PASS
N41	30	20	DFT-PI2BPSK	H	Edge_1RB_Left	30	1000	-46.88	-25	PASS
N41	30	20	DFT-PI2BPSK	H	Edge_1RB_Left	1000	3000	-43.09	-25	PASS
N41	30	20	DFT-PI2BPSK	H	Edge_1RB_Left	3000	18000	-39.17	-25	PASS
N41	30	20	DFT-PI2BPSK	H	Edge_1RB_Left	18000	26500	-49.84	-25	PASS
N41	30	20	DFT-QPSK	H	Edge_1RB_Left	30	1000	-46.87	-25	PASS
N41	30	20	DFT-QPSK	H	Edge_1RB_Left	1000	3000	-42.93	-25	PASS
N41	30	20	DFT-QPSK	H	Edge_1RB_Left	3000	18000	-38.15	-25	PASS
N41	30	20	DFT-QPSK	H	Edge_1RB_Left	18000	26500	-50.20	-25	PASS
N41	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	30	1000	-46.67	-25	PASS
N41	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	1000	3000	-32.62	-25	PASS
N41	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	3000	18000	-41.79	-25	PASS
N41	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	18000	26500	-50.35	-25	PASS
N41	30	50	DFT-QPSK	L	Edge_1RB_Left	30	1000	-46.66	-25	PASS
N41	30	50	DFT-QPSK	L	Edge_1RB_Left	1000	3000	-32.73	-25	PASS
N41	30	50	DFT-QPSK	L	Edge_1RB_Left	3000	18000	-41.02	-25	PASS
N41	30	50	DFT-QPSK	L	Edge_1RB_Left	18000	26500	-50.17	-25	PASS



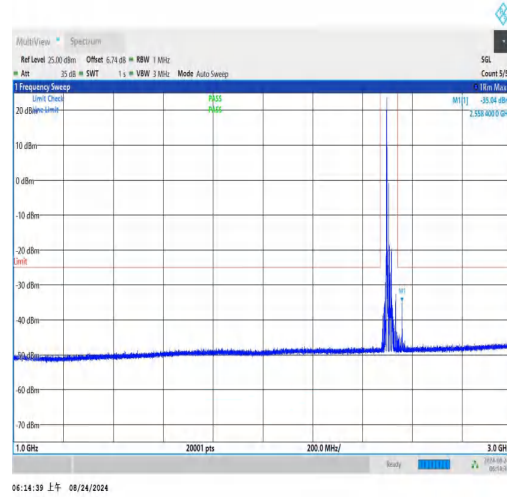
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N41	30	50	DFT-PI2BPSK	M	Edge_1RB_Left	3000	18000	-41.89	-25	PASS
N41	30	50	DFT-PI2BPSK	M	Edge_1RB_Left	18000	26500	-49.81	-25	PASS
N41	30	50	DFT-QPSK	M	Edge_1RB_Left	30	1000	-46.75	-25	PASS
N41	30	50	DFT-QPSK	M	Edge_1RB_Left	1000	3000	-35.68	-25	PASS
N41	30	50	DFT-QPSK	M	Edge_1RB_Left	3000	18000	-41.80	-25	PASS
N41	30	50	DFT-QPSK	M	Edge_1RB_Left	18000	26500	-50.22	-25	PASS
N41	30	50	DFT-PI2BPSK	H	Edge_1RB_Left	30	1000	-46.72	-25	PASS
N41	30	50	DFT-PI2BPSK	H	Edge_1RB_Left	1000	3000	-42.54	-25	PASS
N41	30	50	DFT-PI2BPSK	H	Edge_1RB_Left	3000	18000	-41.90	-25	PASS
N41	30	50	DFT-PI2BPSK	H	Edge_1RB_Left	18000	26500	-49.95	-25	PASS
N41	30	50	DFT-QPSK	H	Edge_1RB_Left	30	1000	-46.54	-25	PASS
N41	30	50	DFT-QPSK	H	Edge_1RB_Left	1000	3000	-42.48	-25	PASS
N41	30	50	DFT-QPSK	H	Edge_1RB_Left	3000	18000	-41.84	-25	PASS
N41	30	50	DFT-QPSK	H	Edge_1RB_Left	18000	26500	-50.13	-25	PASS
N41	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	30	1000	-46.65	-25	PASS
N41	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	1000	3000	-46.63	-25	PASS
N41	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	3000	18000	-42.04	-25	PASS
N41	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	18000	26500	-50.00	-25	PASS
N41	30	100	DFT-QPSK	L	Edge_1RB_Left	30	1000	-46.61	-25	PASS
N41	30	100	DFT-QPSK	L	Edge_1RB_Left	1000	3000	-46.28	-25	PASS
N41	30	100	DFT-QPSK	L	Edge_1RB_Left	3000	18000	-41.97	-25	PASS
N41	30	100	DFT-QPSK	L	Edge_1RB_Left	18000	26500	-49.75	-25	PASS
N41	30	100	DFT-PI2BPSK	M	Edge_1RB_Left	30	1000	-46.60	-25	PASS
N41	30	100	DFT-PI2BPSK	M	Edge_1RB_Left	1000	3000	-46.60	-25	PASS
N41	30	100	DFT-PI2BPSK	M	Edge_1RB_Left	3000	18000	-41.67	-25	PASS
N41	30	100	DFT-PI2BPSK	M	Edge_1RB_Left	18000	26500	-50.45	-25	PASS
N41	30	100	DFT-QPSK	M	Edge_1RB_Left	30	1000	-46.50	-25	PASS
N41	30	100	DFT-QPSK	M	Edge_1RB_Left	1000	3000	-46.35	-25	PASS
N41	30	100	DFT-QPSK	M	Edge_1RB_Left	3000	18000	-41.65	-25	PASS
N41	30	100	DFT-QPSK	M	Edge_1RB_Left	18000	26500	-50.04	-25	PASS
N41	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	30	1000	-46.83	-25	PASS
N41	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	1000	3000	-46.02	-25	PASS
N41	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	3000	18000	-41.90	-25	PASS
N41	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	18000	26500	-50.32	-25	PASS
N41	30	100	DFT-QPSK	H	Edge_1RB_Left	30	1000	-46.82	-25	PASS
N41	30	100	DFT-QPSK	H	Edge_1RB_Left	1000	3000	-46.41	-25	PASS
N41	30	100	DFT-QPSK	H	Edge_1RB_Left	3000	18000	-41.12	-25	PASS
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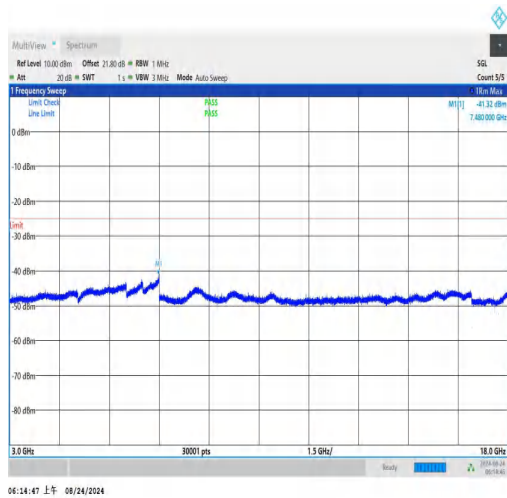
Test Graphs



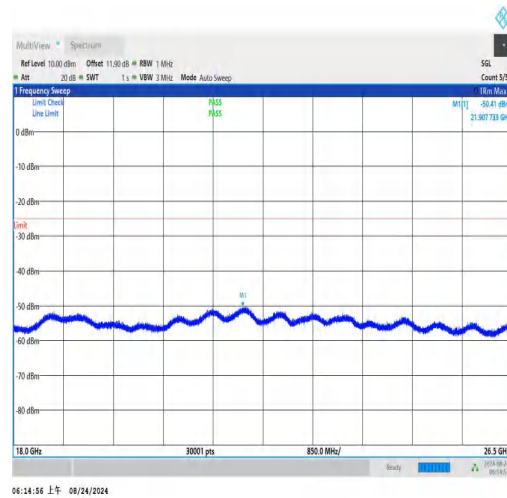
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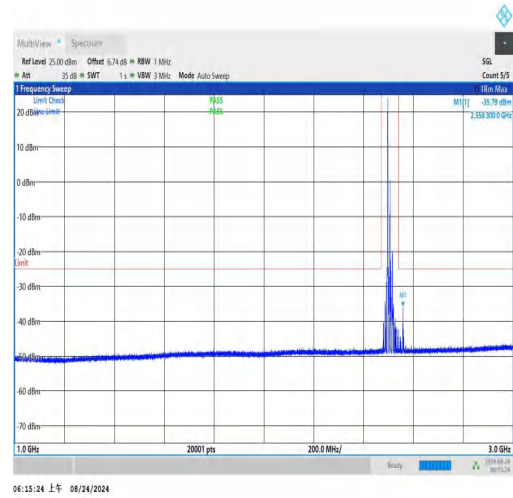
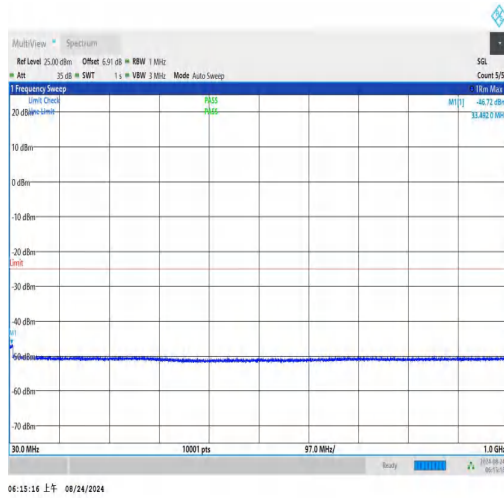
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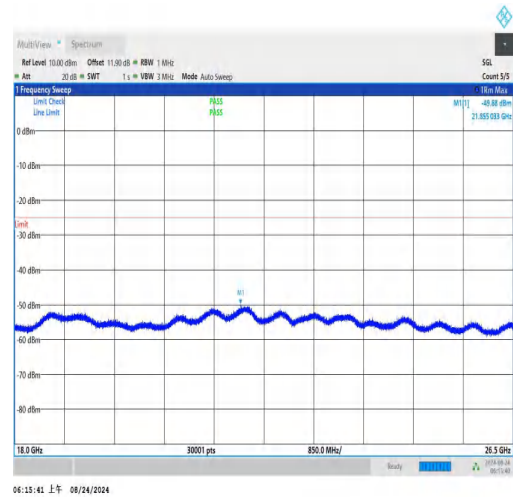
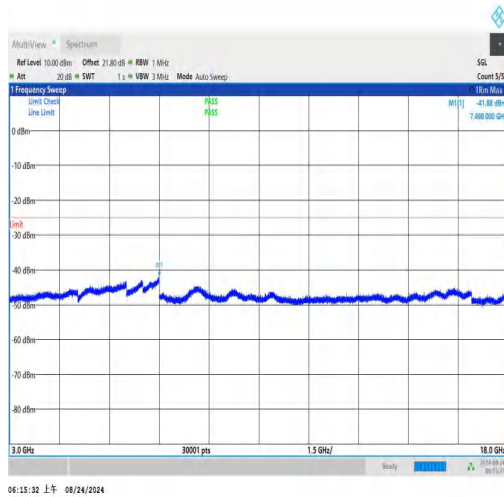


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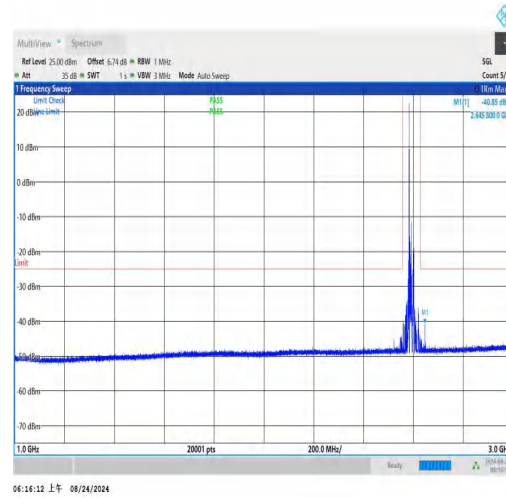
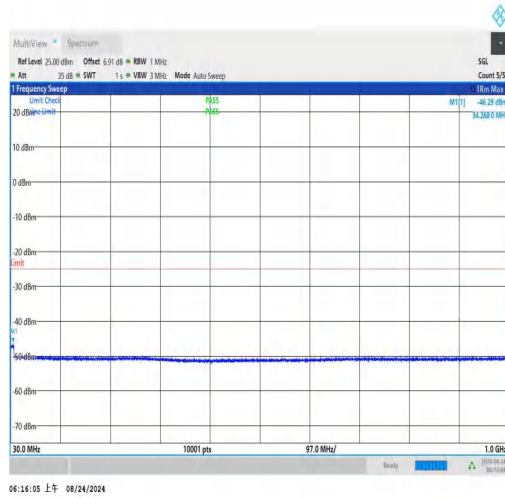
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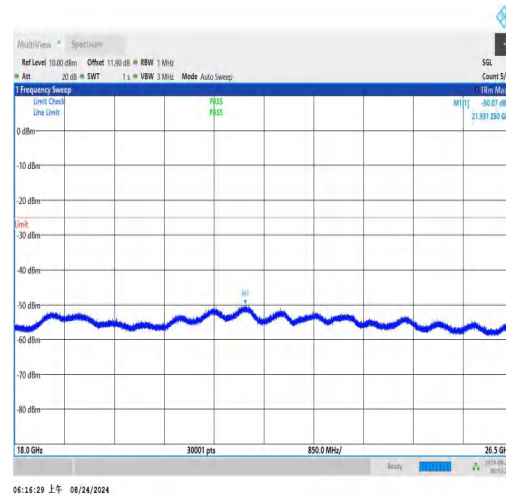
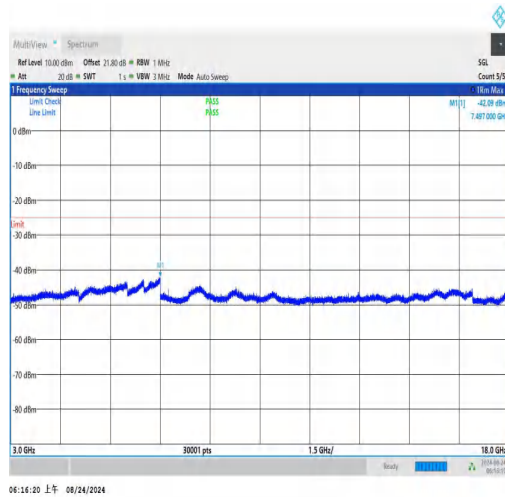


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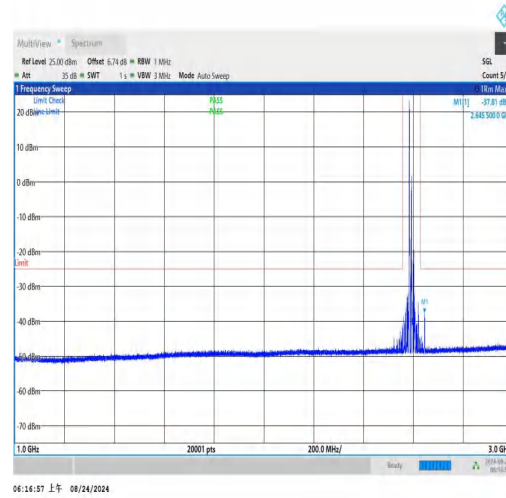
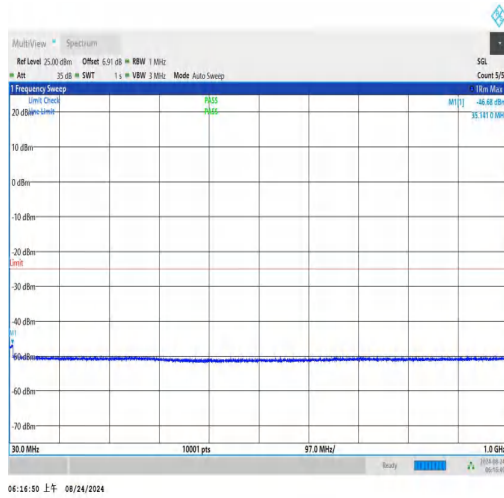


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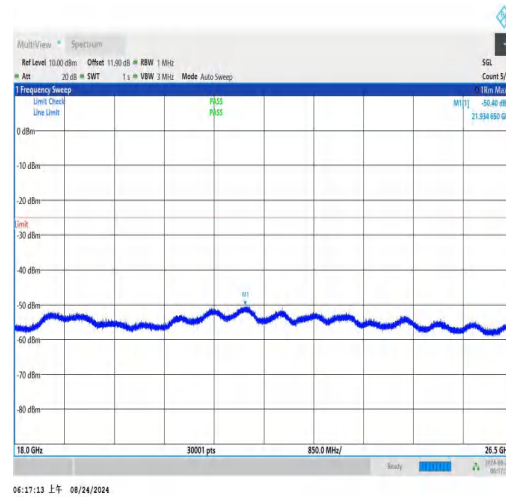
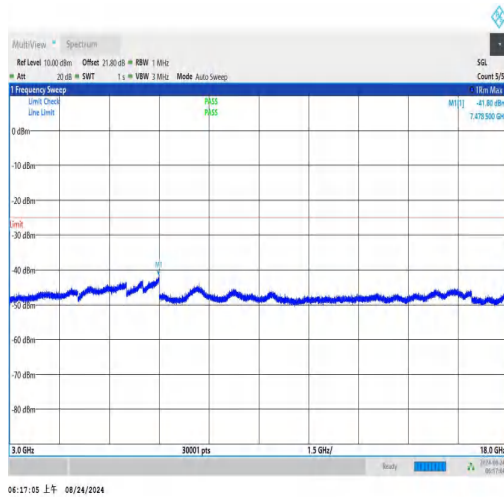


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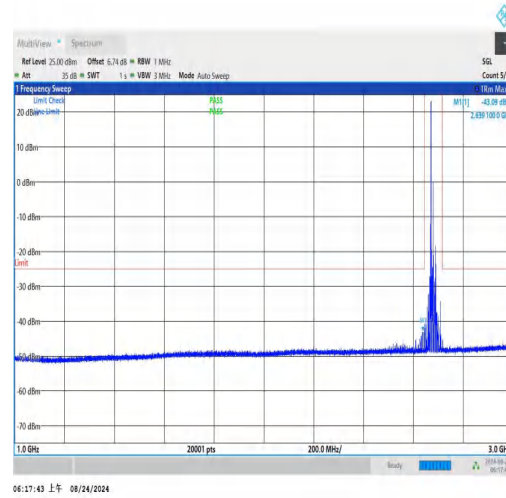
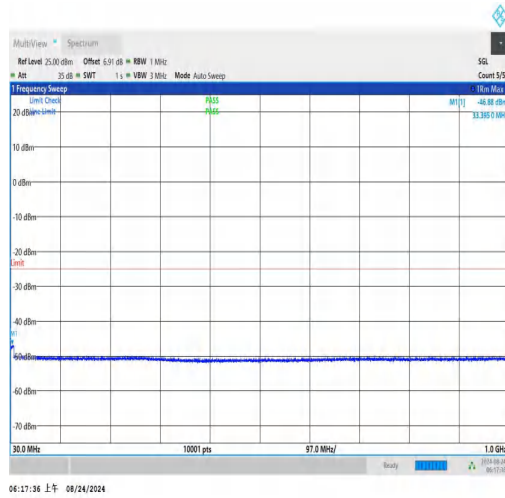


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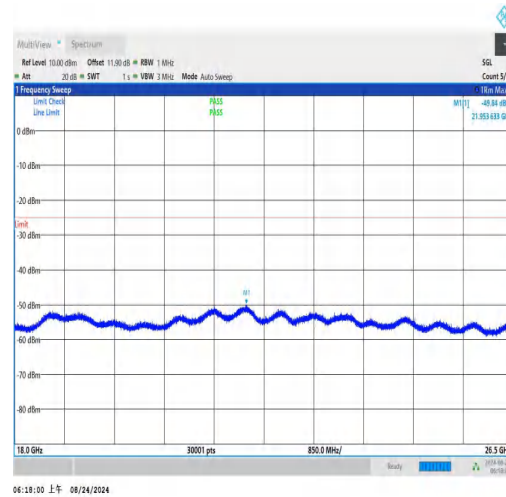
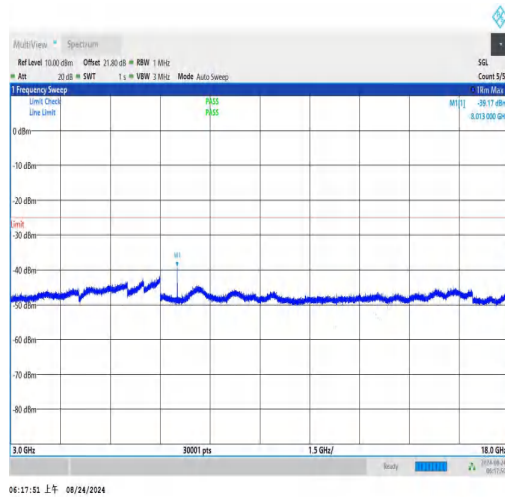


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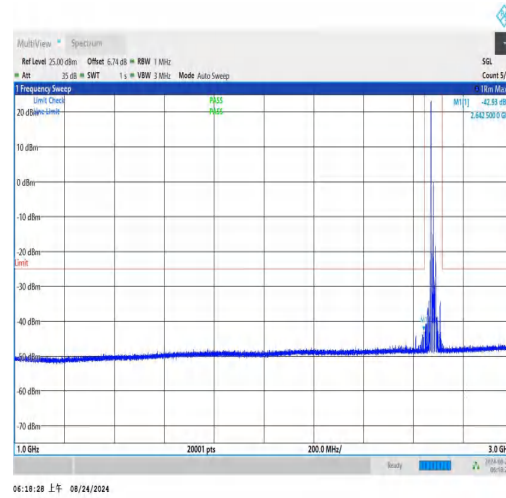
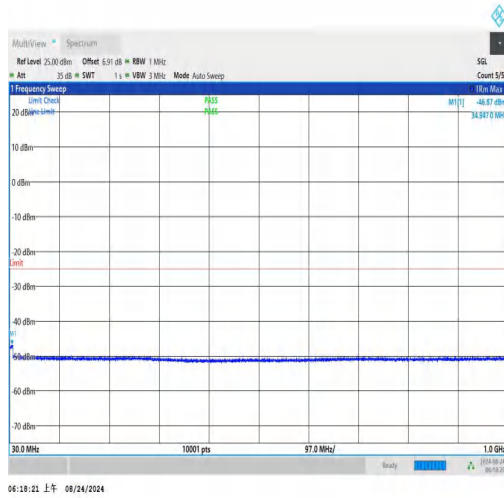


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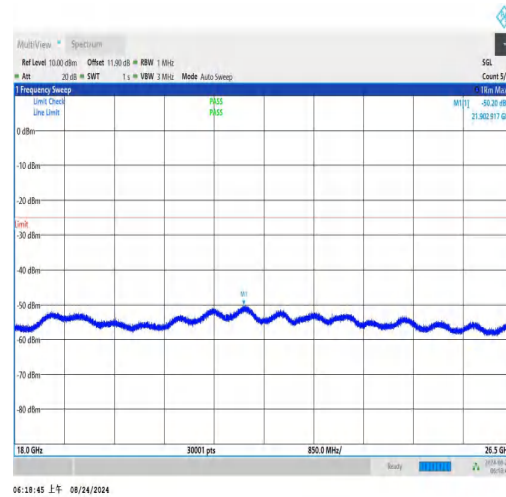
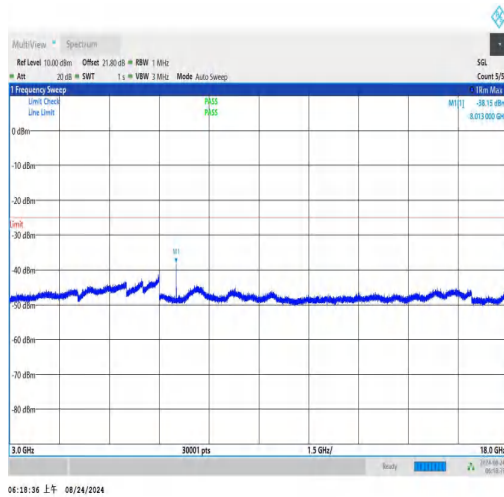


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NTNV_N41_PC3_30_20_H_TID2_N/A_1000_300

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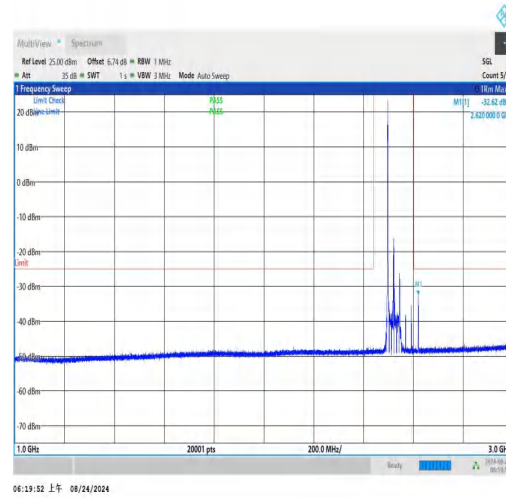
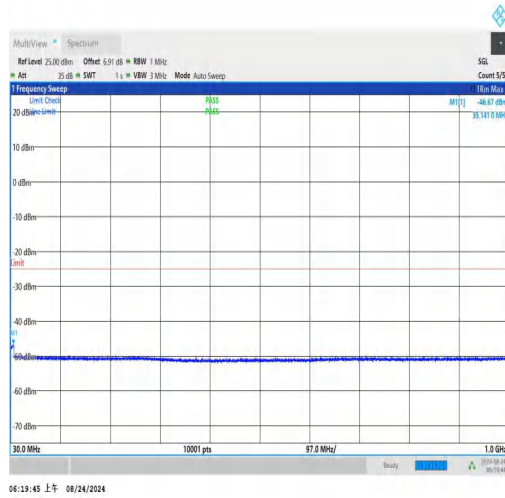


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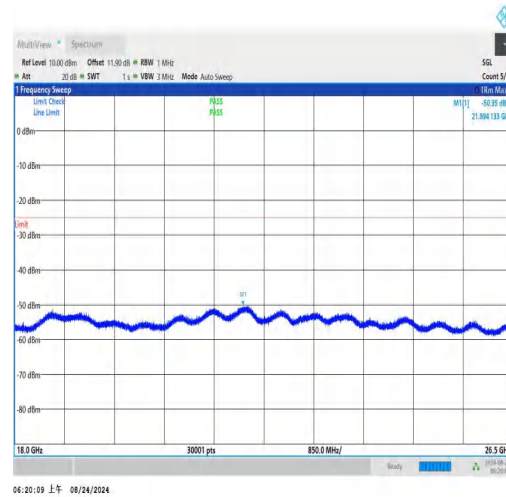
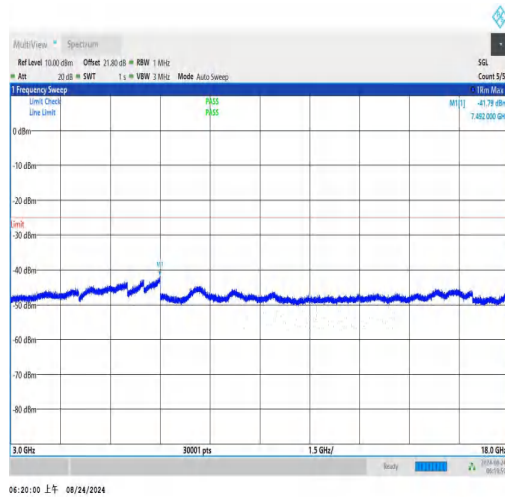
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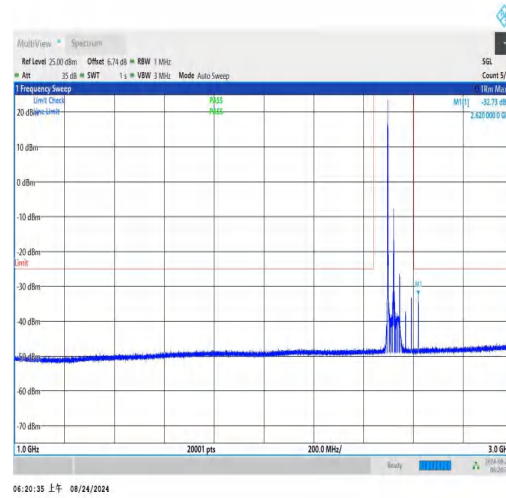
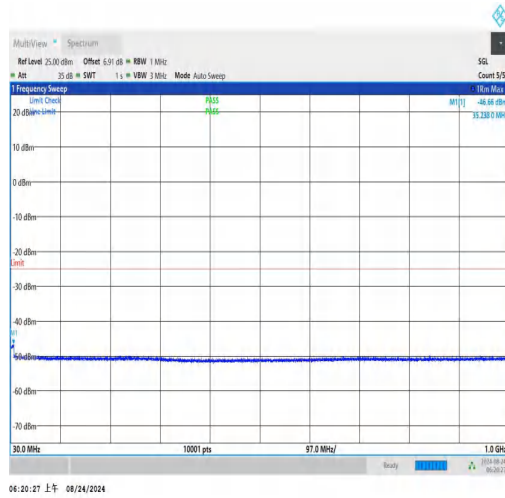
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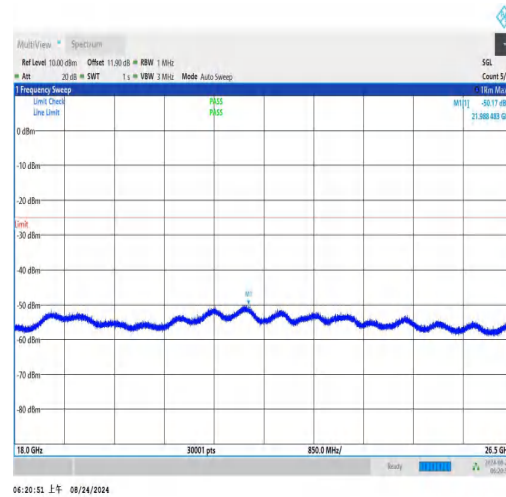
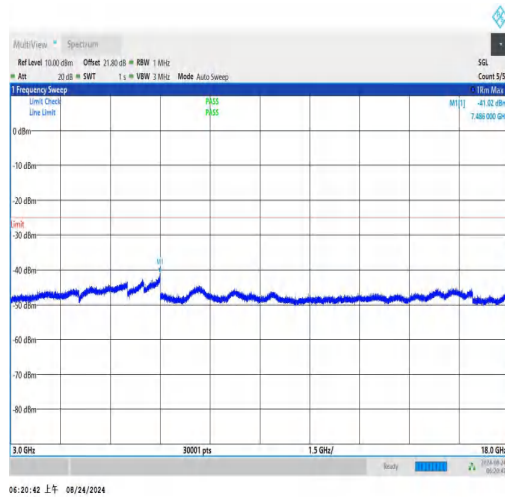
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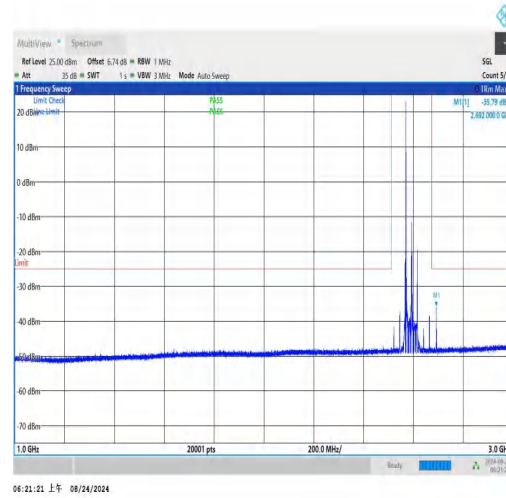
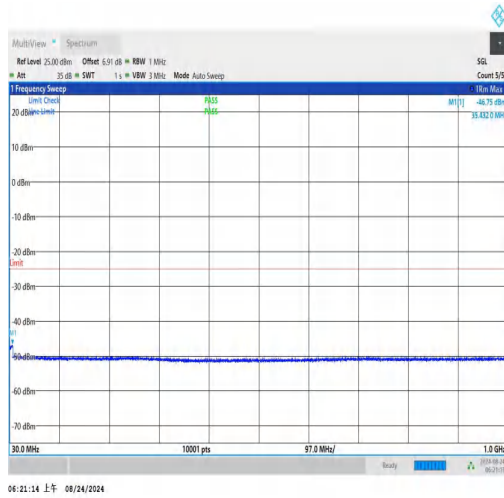
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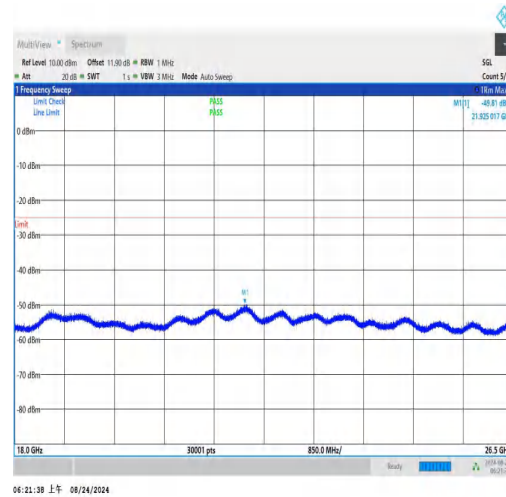
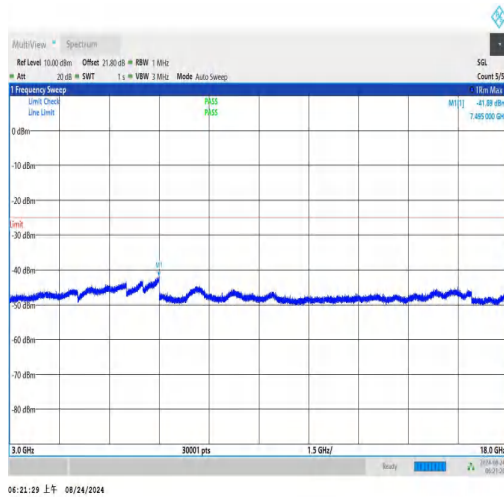


NTNV_N41_PC3_30_50_M_TID1_N/A_30_1000_

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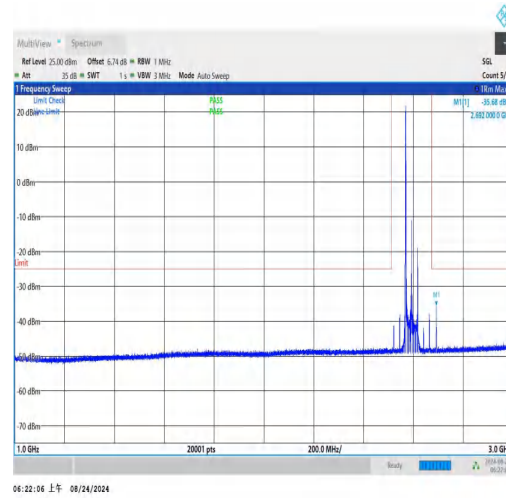
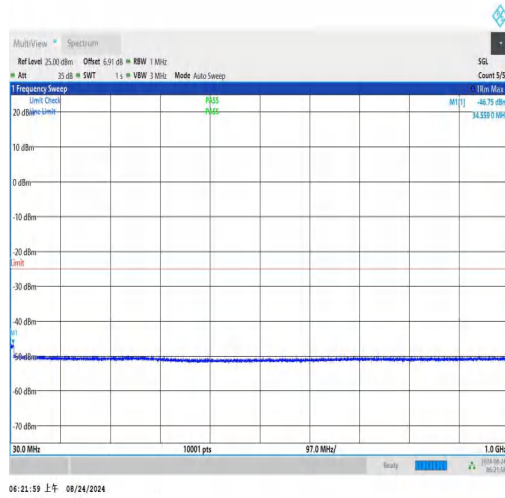


NTNV_N41_PC3_30_50_M_TID1_N/A_3000_180

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NTNV_N41_PC3_30_50_M_TID1_N/A_18000_26

500_#1

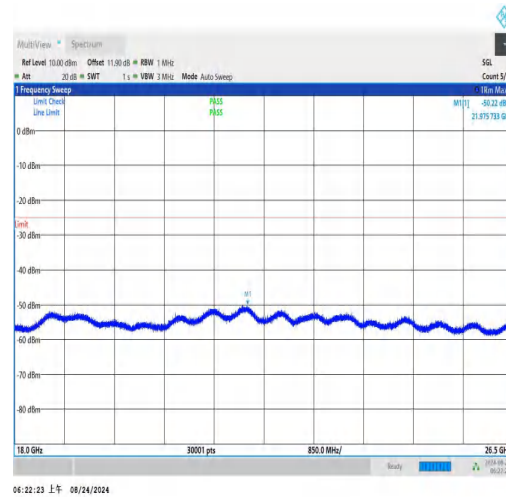
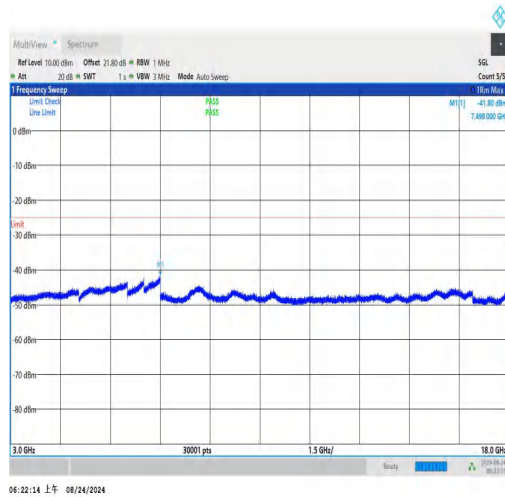


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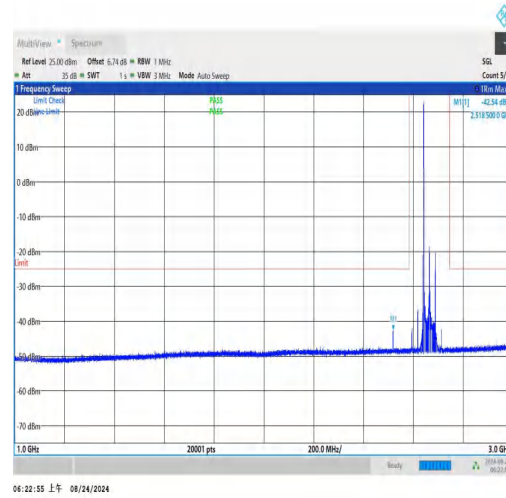
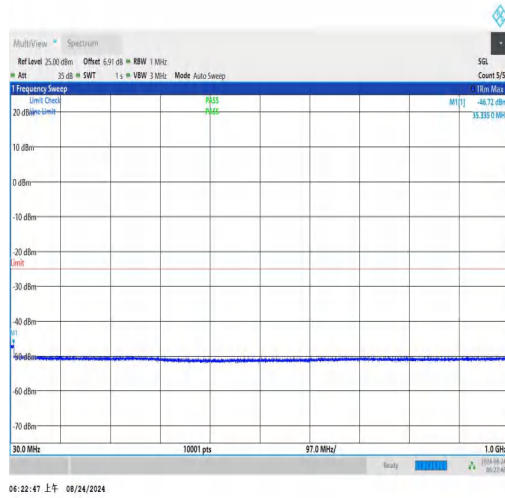


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NTNV_N41_PC3_30_50_M_TID2_N/A_18000_26

500_#1

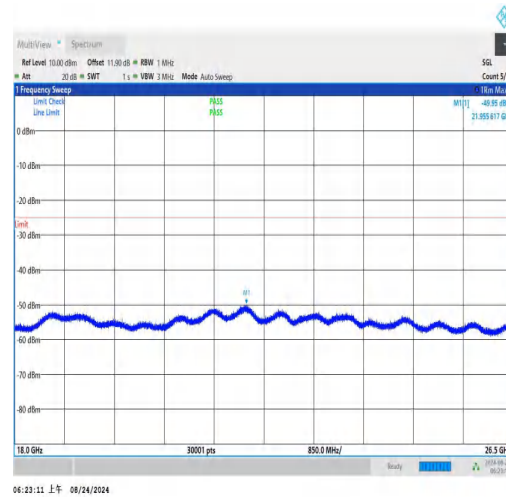
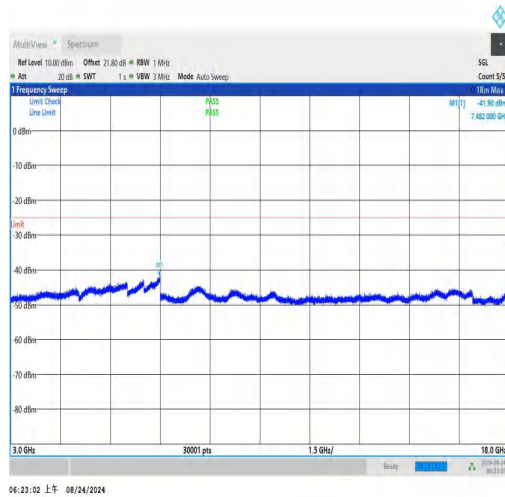


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0_#1

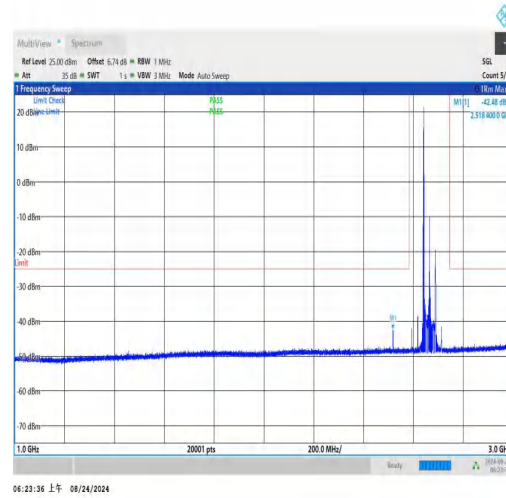
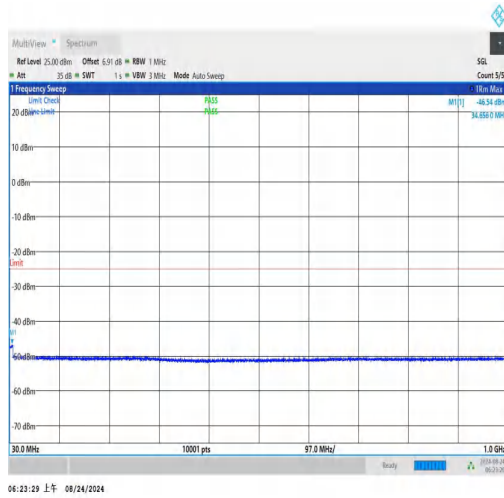


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NTNV_N41_PC3_30_50_H_TID1_N/A_18000_26

500_#1

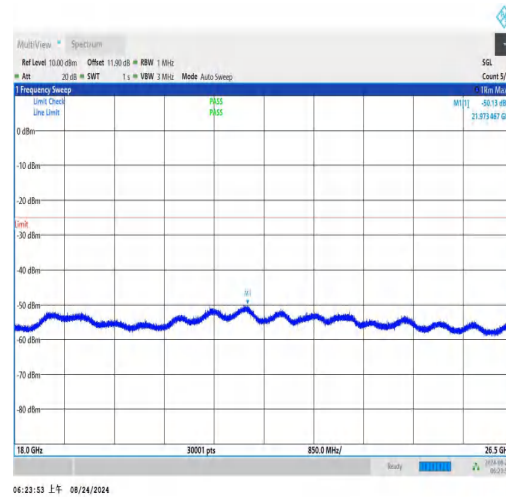
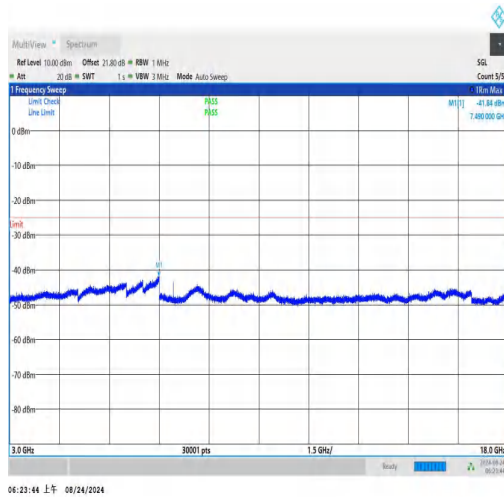


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NTNV_N41_PC3_30_50_H_TID2_N/A_1000_300

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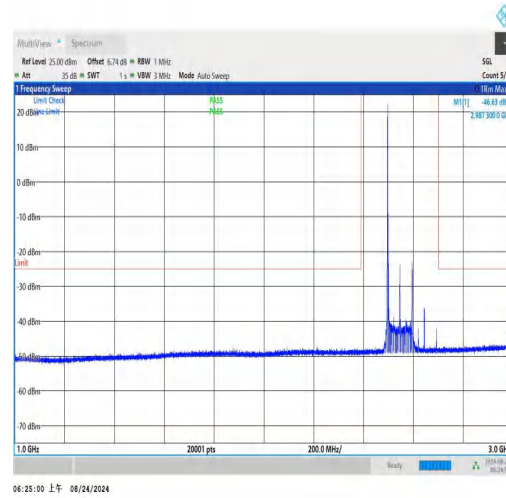
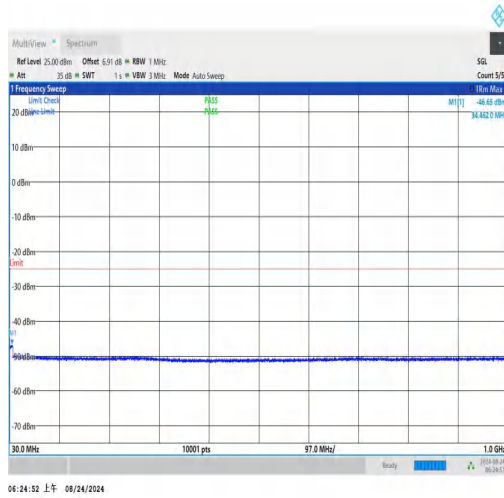


NTNV_N41_PC3_30_50_H_TID2_N/A_3000_180

00_#1

NTNV_N41_PC3_30_50_H_TID2_N/A_18000_26

500_#1

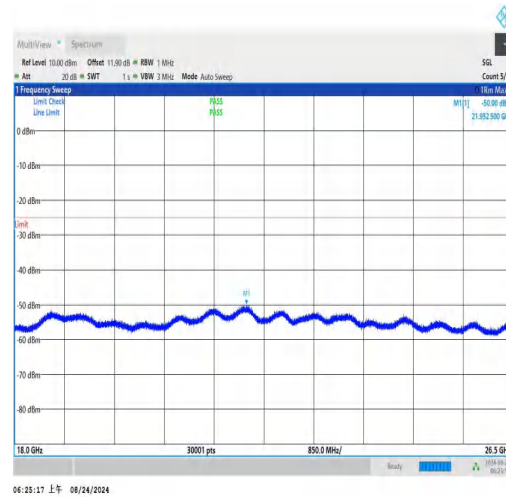
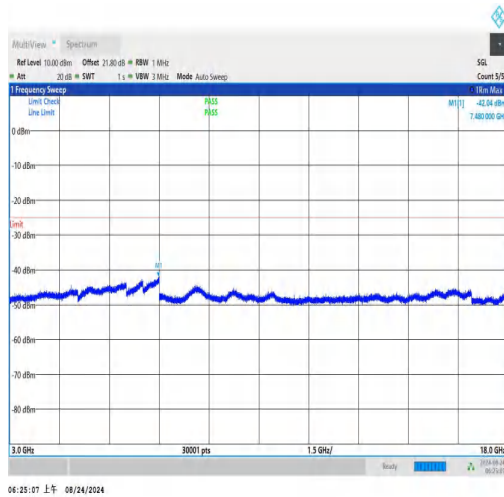


NTNV_N41_PC3_30_100_L_TID1_N/A_30_1000

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00_#1

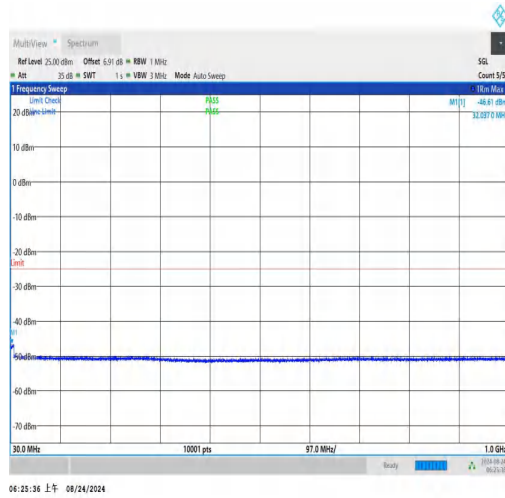


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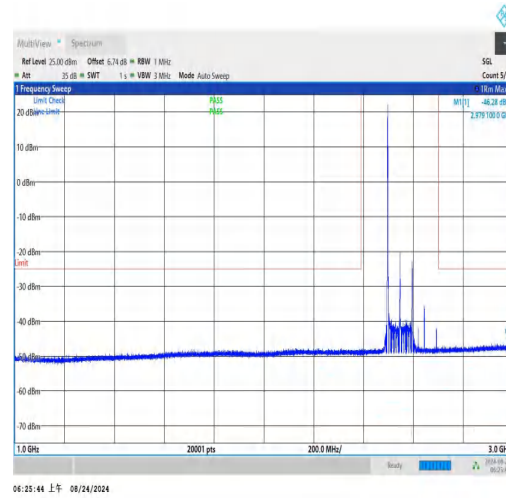
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6500_#1



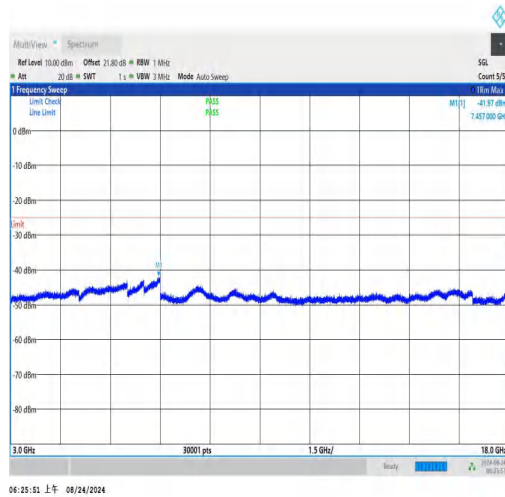
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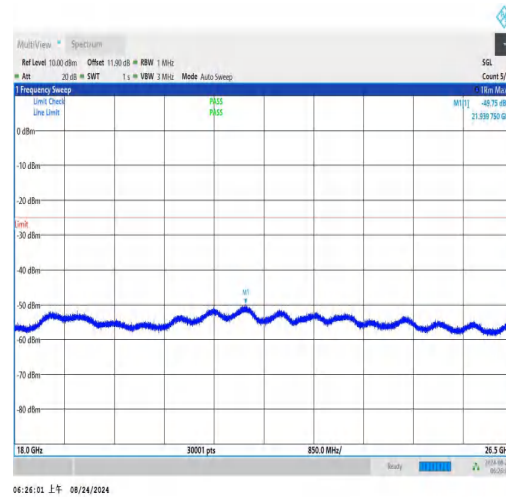
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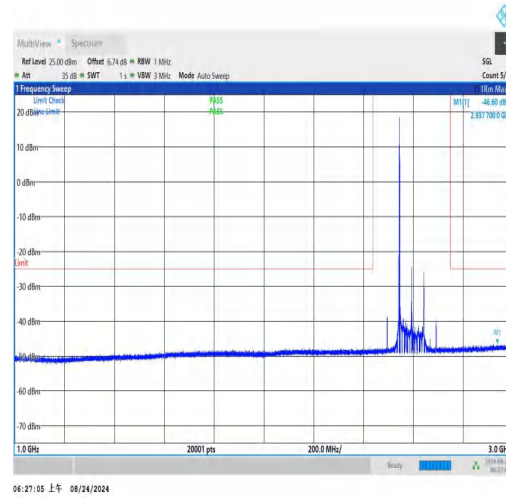
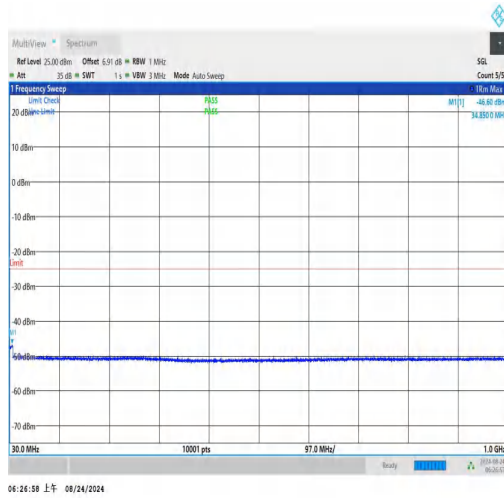
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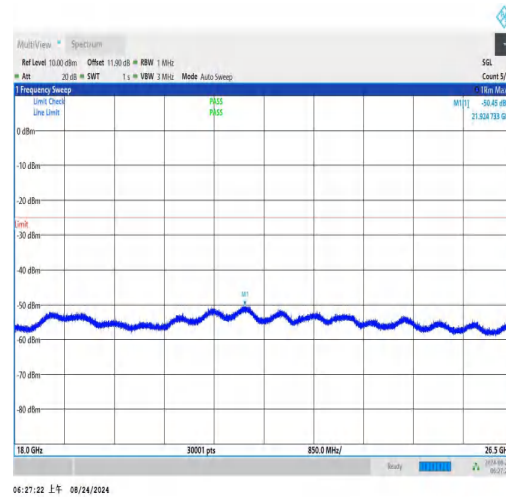
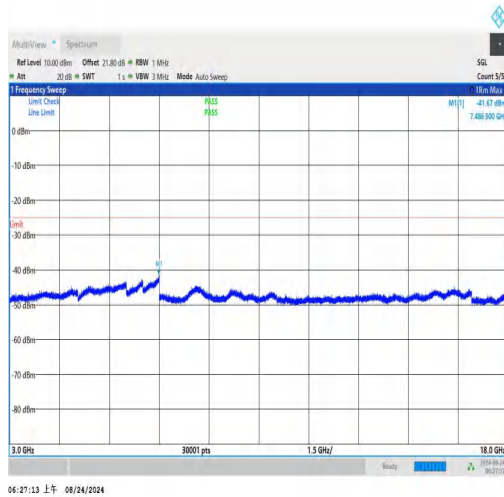


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NTNV_N41_PC3_30_100_M_TID1_N/A_1000_30

_#1

00_#1

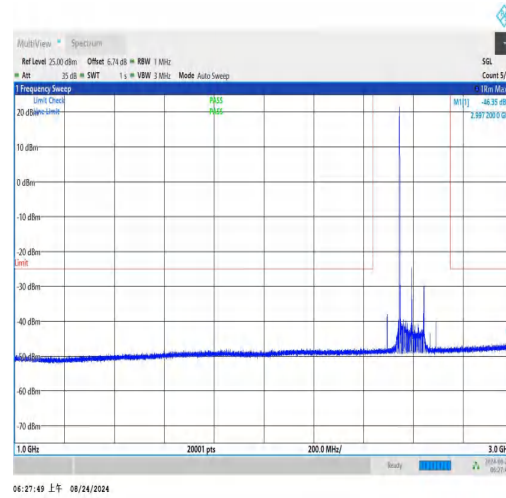
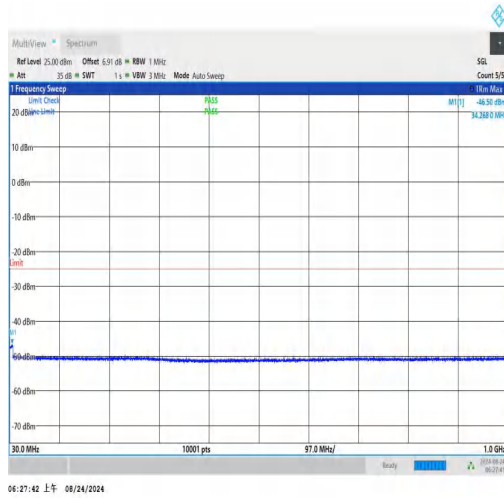


NTNV_N41_PC3_30_100_M_TID1_N/A_3000_18

NTNV_N41_PC3_30_100_M_TID1_N/A_18000_26500_#1

000_#1

6500_#1

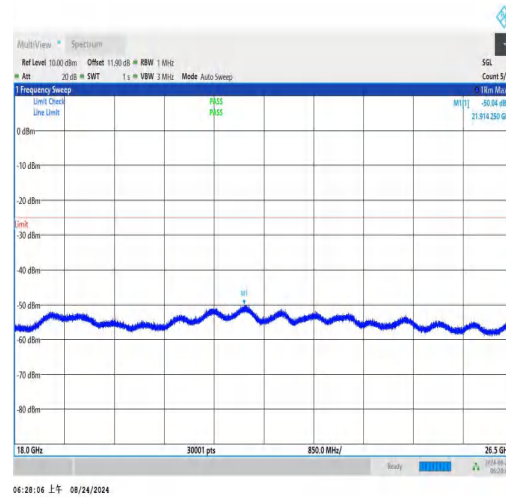
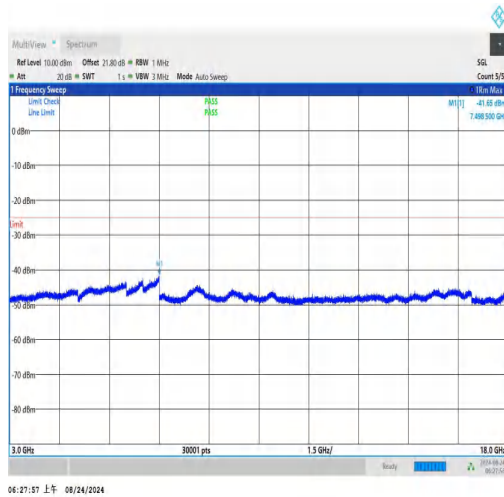


NTNV_N41_PC3_30_100_M_TID2_N/A_30_1000

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00_#1

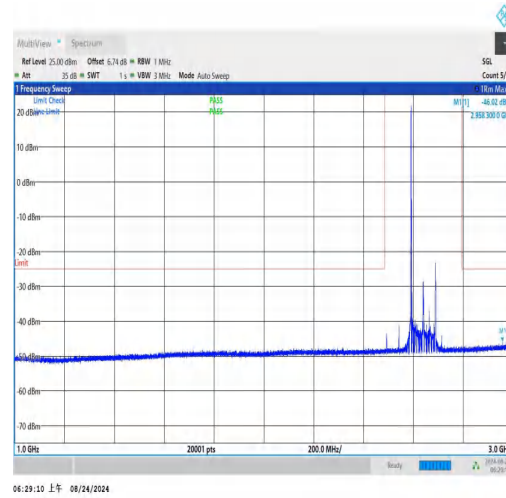
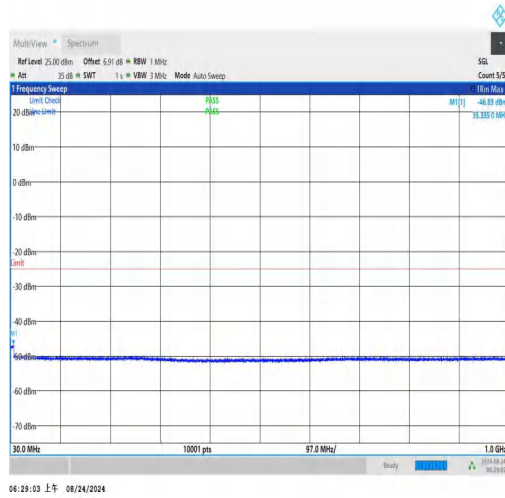


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000_#1

6500_#1

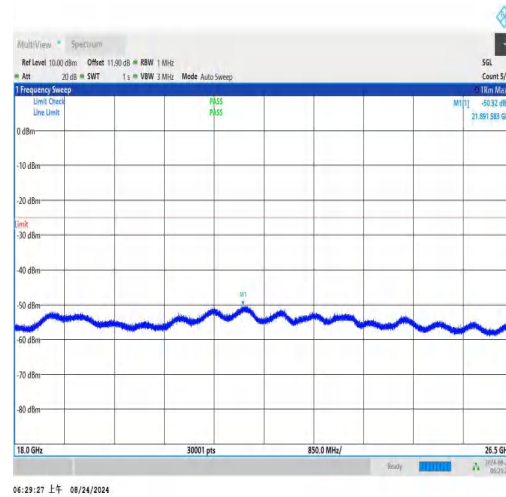
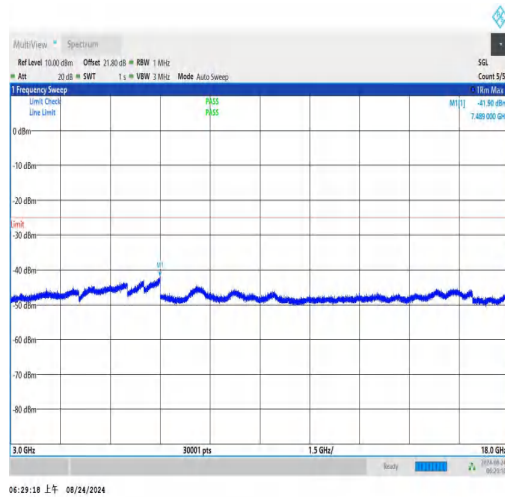


NTNV_N41_PC3_30_100_H_TID1_N/A_30_1000

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00_#1

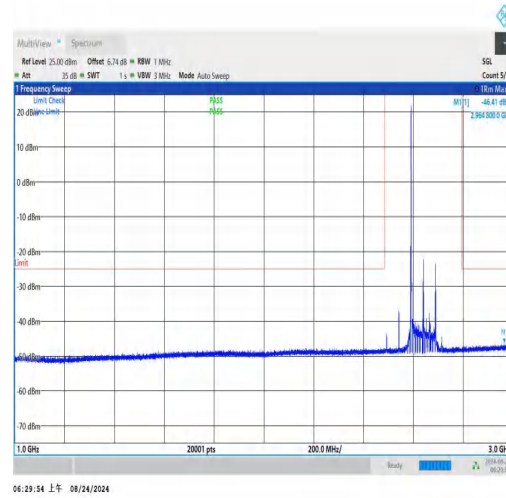
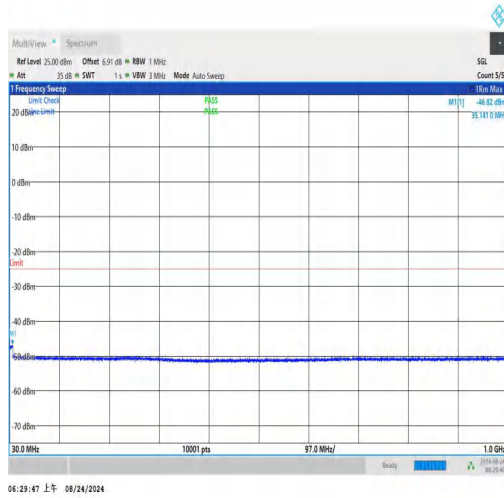


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NTNV_N41_PC3_30_100_H_TID1_N/A_18000_2

6500_#1

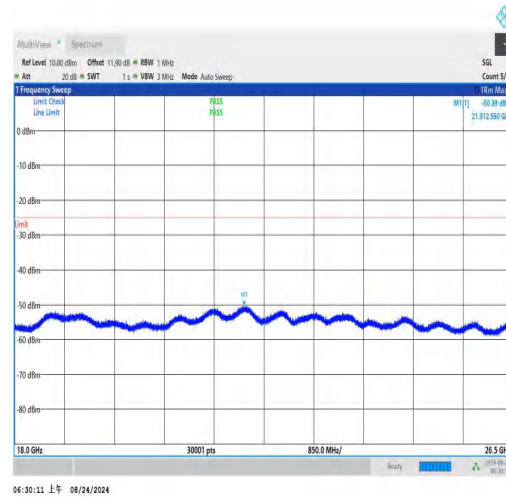
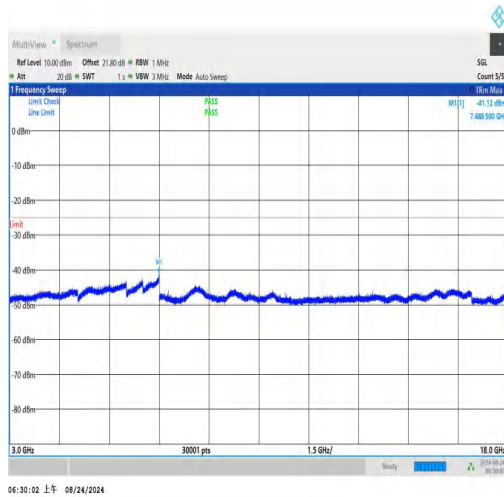


NTNV_N41_PC3_30_100_H_TID2_N/A_30_1000

NTNV_N41_PC3_30_100_H_TID2_N/A_1000_30

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00_#1



NTNV_N41_PC3_30_100_H_TID2_N/A_3000_18

NTNV_N41_PC3_30_100_H_TID2_N/A_18000_2

000_#1

6500_#1



Appendix F: Frequency Stability

Test Result

Frequency Error VS. Voltage

Voltage										
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Voltage	Temperature	Deviation (Hz)	Deviation (ppm)	Verdict
N41	30	100	DFT-PI2BPSK	L	Outer_Full	VN	NT	17.00	0.01	PASS
N41	30	100	DFT-PI2BPSK	L	Outer_Full	VL	NT	16.00	0.01	PASS
N41	30	100	DFT-PI2BPSK	L	Outer_Full	VH	NT	2.00	0.00	PASS
N41	30	100	DFT-QPSK	L	Outer_Full	VN	NT	16.00	0.01	PASS
N41	30	100	DFT-QPSK	L	Outer_Full	VL	NT	3.00	0.00	PASS
N41	30	100	DFT-QPSK	L	Outer_Full	VH	NT	9.00	0.00	PASS
N41	30	100	DFT-PI2BPSK	M	Outer_Full	VN	NT	18.00	0.01	PASS
N41	30	100	DFT-PI2BPSK	M	Outer_Full	VL	NT	11.00	0.00	PASS
N41	30	100	DFT-PI2BPSK	M	Outer_Full	VH	NT	17.00	0.01	PASS
N41	30	100	DFT-QPSK	M	Outer_Full	VN	NT	2.00	0.00	PASS
N41	30	100	DFT-QPSK	M	Outer_Full	VL	NT	1.00	0.00	PASS
N41	30	100	DFT-QPSK	M	Outer_Full	VH	NT	14.00	0.01	PASS
N41	30	100	DFT-PI2BPSK	H	Outer_Full	VN	NT	1.00	0.00	PASS
N41	30	100	DFT-PI2BPSK	H	Outer_Full	VL	NT	1.00	0.00	PASS
N41	30	100	DFT-PI2BPSK	H	Outer_Full	VH	NT	7.00	0.00	PASS
N41	30	100	DFT-QPSK	H	Outer_Full	VN	NT	18.00	0.01	PASS
N41	30	100	DFT-QPSK	H	Outer_Full	VL	NT	4.00	0.00	PASS
N41	30	100	DFT-QPSK	H	Outer_Full	VH	NT	10.00	0.00	PASS

Frequency Error VS. Temperature

Temperature										
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Voltage	Temperature	Deviation (Hz)	Deviation (ppm)	Verdict
N41	30	100	DFT-PI2BPSK	L	Outer_Full	NV	-30	15.000000	0.005892	PASS
N41	30	100	DFT-PI2BPSK	L	Outer_Full	NV	-20	2.000000	0.000786	PASS
N41	30	100	DFT-PI2BPSK	L	Outer_Full	NV	-10	4.000000	0.001571	PASS
N41	30	100	DFT-PI2BPSK	L	Outer_Full	NV	0	10.000000	0.003928	PASS
N41	30	100	DFT-PI2BPSK	L	Outer_Full	NV	10	16.000000	0.006284	PASS



N41	30	100	DFT-PI2BPSK	L	Outer_Full	NV	20	3.000000	0.001178	PASS
N41	30	100	DFT-PI2BPSK	L	Outer_Full	NV	30	8.000000	0.003142	PASS
N41	30	100	DFT-PI2BPSK	L	Outer_Full	NV	40	14.000000	0.005499	PASS
N41	30	100	DFT-PI2BPSK	L	Outer_Full	NV	50	1.000000	0.000393	PASS
N41	30	100	DFT-QPSK	L	Outer_Full	NV	-30	14.000000	0.005499	PASS
N41	30	100	DFT-QPSK	L	Outer_Full	NV	-20	1.000000	0.000393	PASS
N41	30	100	DFT-QPSK	L	Outer_Full	NV	-10	7.000000	0.002749	PASS
N41	30	100	DFT-QPSK	L	Outer_Full	NV	0	16.000000	0.006284	PASS
N41	30	100	DFT-QPSK	L	Outer_Full	NV	10	3.000000	0.001178	PASS
N41	30	100	DFT-QPSK	L	Outer_Full	NV	20	9.000000	0.003535	PASS
N41	30	100	DFT-QPSK	L	Outer_Full	NV	30	15.000000	0.005892	PASS
N41	30	100	DFT-QPSK	L	Outer_Full	NV	40	1.000000	0.000393	PASS
N41	30	100	DFT-QPSK	L	Outer_Full	NV	50	7.000000	0.002749	PASS
N41	30	100	DFT-PI2BPSK	M	Outer_Full	NV	-30	4.000000	0.001543	PASS
N41	30	100	DFT-PI2BPSK	M	Outer_Full	NV	-20	9.000000	0.003471	PASS
N41	30	100	DFT-PI2BPSK	M	Outer_Full	NV	-10	15.000000	0.005785	PASS
N41	30	100	DFT-PI2BPSK	M	Outer_Full	NV	0	2.000000	0.000771	PASS
N41	30	100	DFT-PI2BPSK	M	Outer_Full	NV	10	8.000000	0.003085	PASS
N41	30	100	DFT-PI2BPSK	M	Outer_Full	NV	20	17.000000	0.006556	PASS
N41	30	100	DFT-PI2BPSK	M	Outer_Full	NV	30	4.000000	0.001543	PASS
N41	30	100	DFT-PI2BPSK	M	Outer_Full	NV	40	10.000000	0.003857	PASS
N41	30	100	DFT-PI2BPSK	M	Outer_Full	NV	50	15.000000	0.005785	PASS
N41	30	100	DFT-QPSK	M	Outer_Full	NV	-30	1.000000	0.000386	PASS
N41	30	100	DFT-QPSK	M	Outer_Full	NV	-20	7.000000	0.002700	PASS
N41	30	100	DFT-QPSK	M	Outer_Full	NV	-10	12.000000	0.004628	PASS
N41	30	100	DFT-QPSK	M	Outer_Full	NV	0	18.000000	0.006942	PASS
N41	30	100	DFT-QPSK	M	Outer_Full	NV	10	5.000000	0.001928	PASS
N41	30	100	DFT-QPSK	M	Outer_Full	NV	20	7.000000	0.002700	PASS
N41	30	100	DFT-QPSK	M	Outer_Full	NV	30	13.000000	0.005014	PASS
N41	30	100	DFT-QPSK	M	Outer_Full	NV	40	19.000000	0.007327	PASS
N41	30	100	DFT-QPSK	M	Outer_Full	NV	50	6.000000	0.002314	PASS
N41	30	100	DFT-PI2BPSK	H	Outer_Full	NV	-30	13.000000	0.004924	PASS
N41	30	100	DFT-PI2BPSK	H	Outer_Full	NV	-20	19.000000	0.007197	PASS
N41	30	100	DFT-PI2BPSK	H	Outer_Full	NV	-10	5.000000	0.001894	PASS
N41	30	100	DFT-PI2BPSK	H	Outer_Full	NV	0	8.000000	0.003030	PASS
N41	30	100	DFT-PI2BPSK	H	Outer_Full	NV	10	14.000000	0.005303	PASS
N41	30	100	DFT-PI2BPSK	H	Outer_Full	NV	20	19.000000	0.007197	PASS
N41	30	100	DFT-PI2BPSK	H	Outer_Full	NV	30	6.000000	0.002273	PASS
N41	30	100	DFT-PI2BPSK	H	Outer_Full	NV	40	12.000000	0.004545	PASS



N41	30	100	DFT-PI2BPSK	H	Outer_Full	NV	50	18.000000	0.006818	PASS
N41	30	100	DFT-QPSK	H	Outer_Full	NV	-30	16.000000	0.006061	PASS
N41	30	100	DFT-QPSK	H	Outer_Full	NV	-20	3.000000	0.001136	PASS
N41	30	100	DFT-QPSK	H	Outer_Full	NV	-10	8.000000	0.003030	PASS
N41	30	100	DFT-QPSK	H	Outer_Full	NV	0	2.000000	0.000758	PASS
N41	30	100	DFT-QPSK	H	Outer_Full	NV	10	8.000000	0.003030	PASS
N41	30	100	DFT-QPSK	H	Outer_Full	NV	20	14.000000	0.005303	PASS
N41	30	100	DFT-QPSK	H	Outer_Full	NV	30	19.000000	0.007197	PASS
N41	30	100	DFT-QPSK	H	Outer_Full	NV	40	6.000000	0.002273	PASS
N41	30	100	DFT-QPSK	H	Outer_Full	NV	50	12.000000	0.004545	PASS

-----END-----