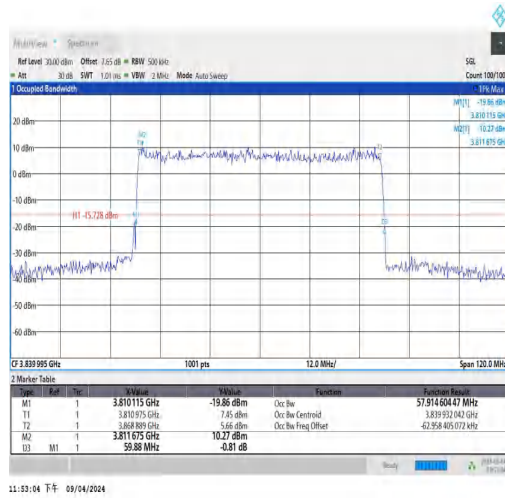
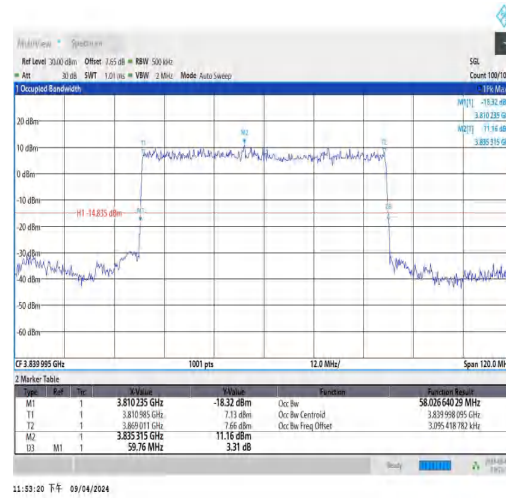




11:53:04 下午 09/04/2024



11:53:20 下午 09/04/2024

NTNV_N77-3700-3980_PC3_30_60_M_TID1_Ant

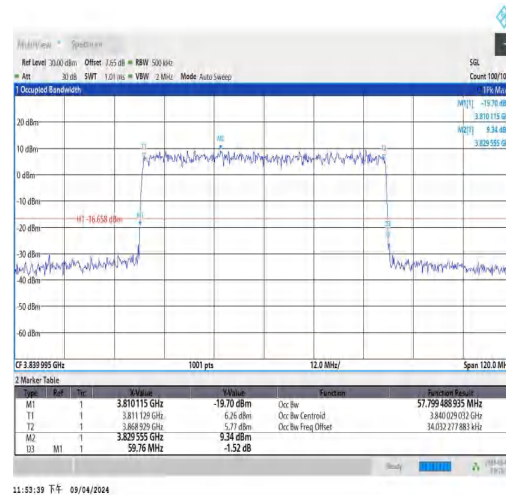
1#1

NTNV_N77-3700-3980_PC3_30_60_M_TID2_Ant

1#1



11:53:30 下午 09/04/2024



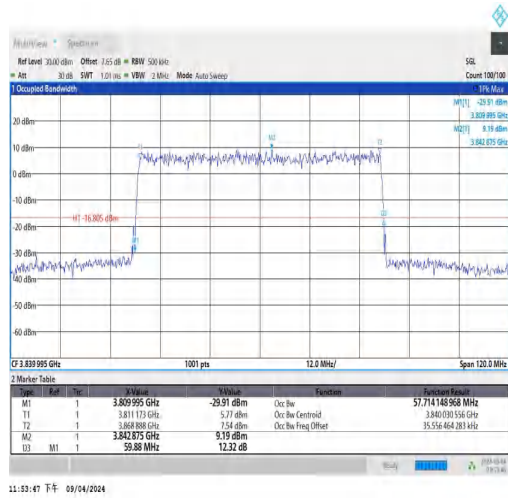
11:53:39 下午 09/04/2024

NTNV_N77-3700-3980_PC3_30_60_M_TID3_Ant

1#1

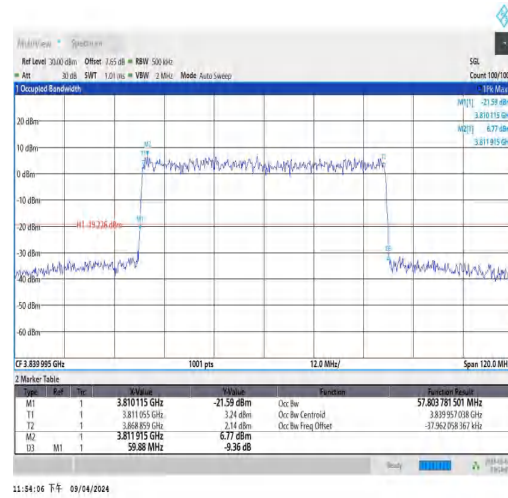
NTNV_N77-3700-3980_PC3_30_60_M_TID4_Ant

1#1



NTNV_N77-3700-3980_PC3_30_60_M_TID5_Ant

1#1



NTNV_N77-3700-3980_PC3_30_60_M_TID6_Ant

1#1



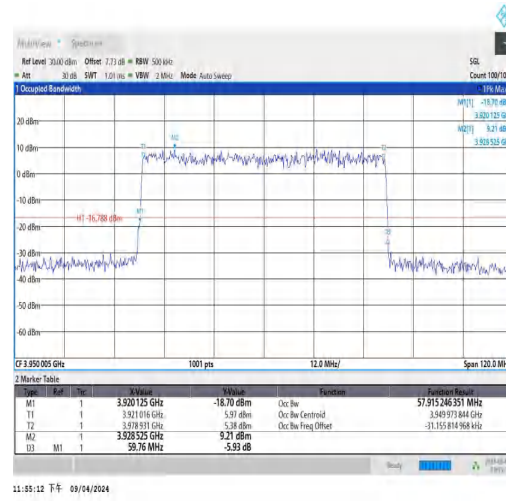
NTNV_N77-3700-3980_PC3_30_60_H_TID1_Ant

1#1



NTNV_N77-3700-3980_PC3_30_60_H_TID2_Ant

1#1

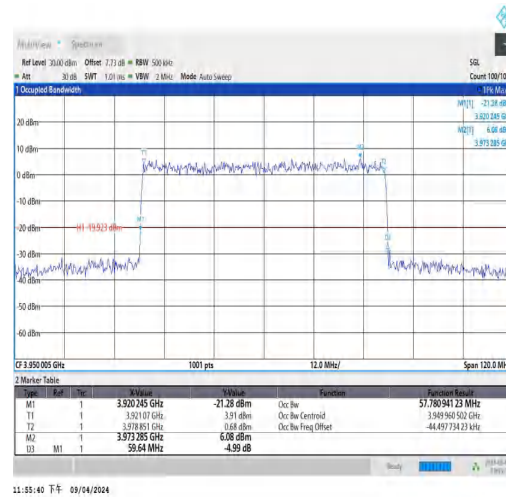
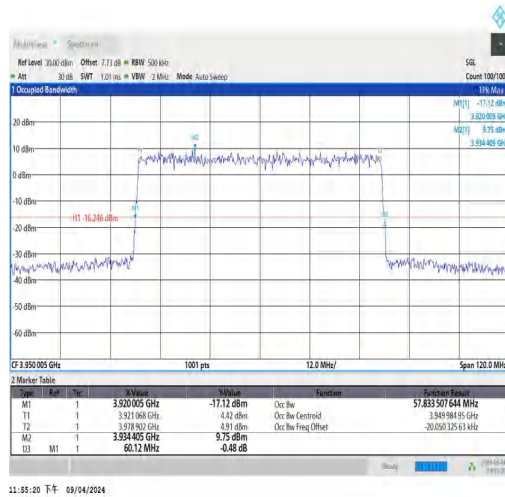


NTNV_N77-3700-3980_PC3_30_60_H_TID3_Ant

1#1

NTNV_N77-3700-3980_PC3_30_60_H_TID4_Ant

1#1

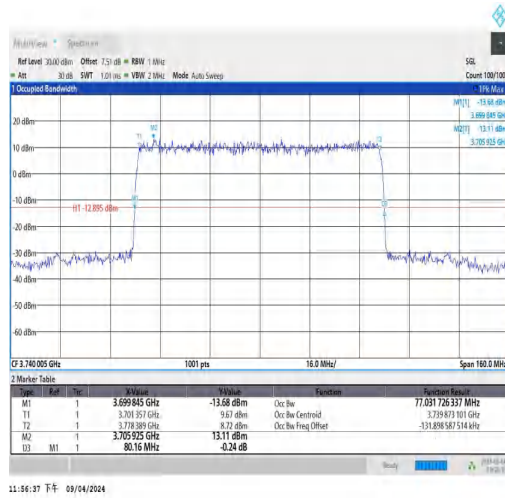


NTNV_N77-3700-3980_PC3_30_60_H_TID5_Ant

1#1

NTNV_N77-3700-3980_PC3_30_60_H_TID6_Ant

1#1



11:56:37 下午 09/04/2024



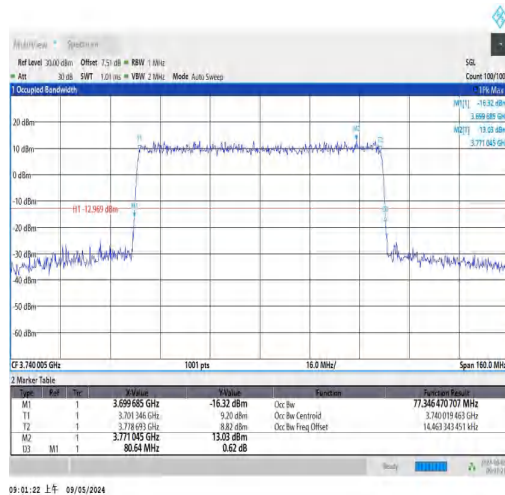
11:56:53 下午 09/04/2024

NTNV_N77-3700-3980_PC3_30_80_L_TID1_Ant

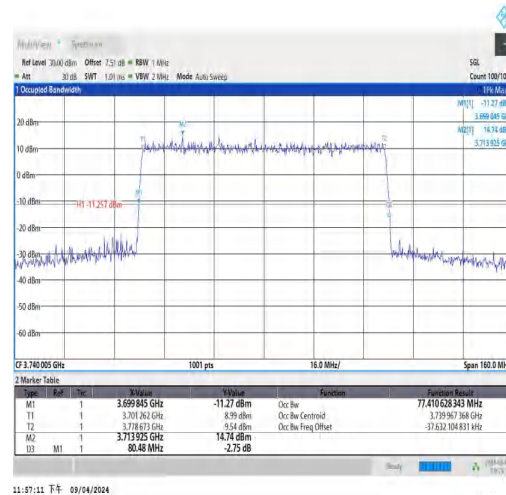
1#1

NTNV_N77-3700-3980_PC3_30_80_L_TID2_Ant

1#1



09:01:22 上午 09/05/2024



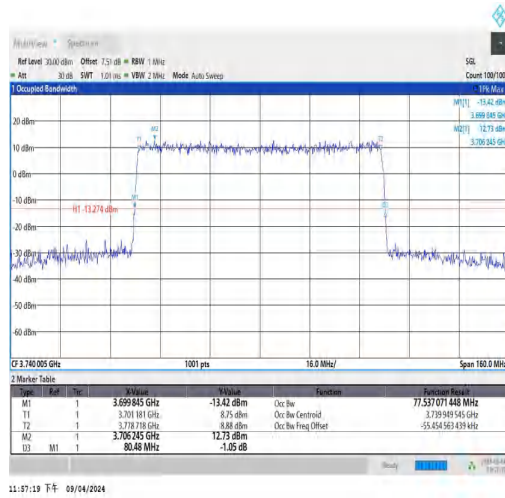
11:57:12 下午 09/04/2024

NTNV_N77-3700-3980_PC3_30_80_L_TID3_Ant

1#1

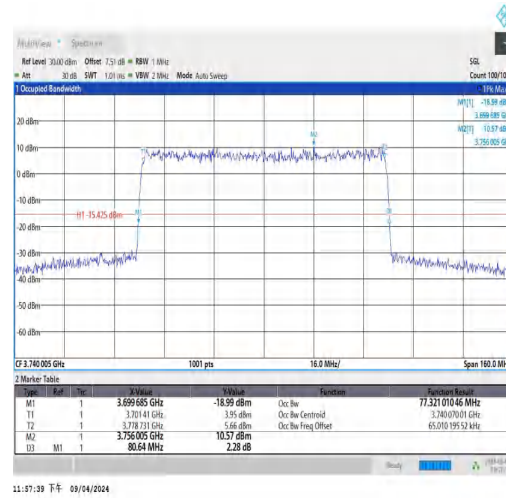
NTNV_N77-3700-3980_PC3_30_80_L_TID4_Ant

1#1



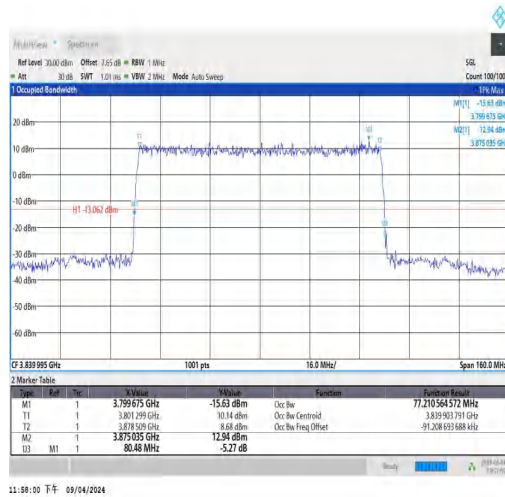
NTNV_N77-3700-3980_PC3_30_80_L_TID5_Ant

1#1



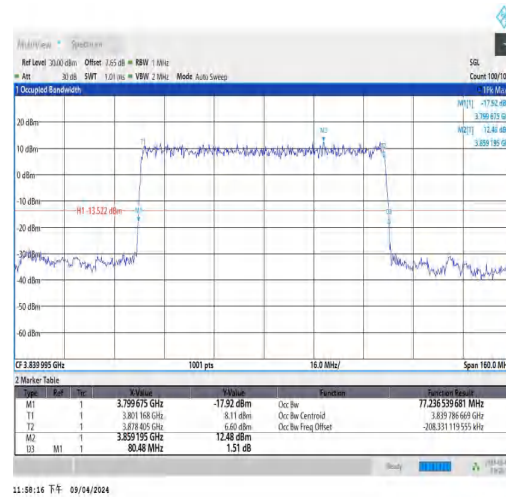
NTNV_N77-3700-3980_PC3_30_80_L_TID6_Ant

1#1



NTNV_N77-3700-3980_PC3_30_80_M_TID1_Ant

1#1

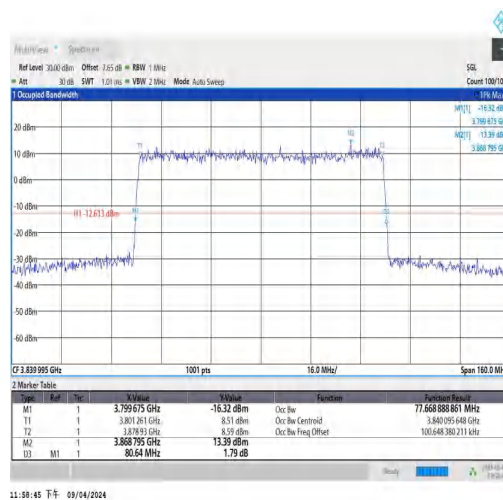


NTNV_N77-3700-3980_PC3_30_80_M_TID2_Ant

1#1

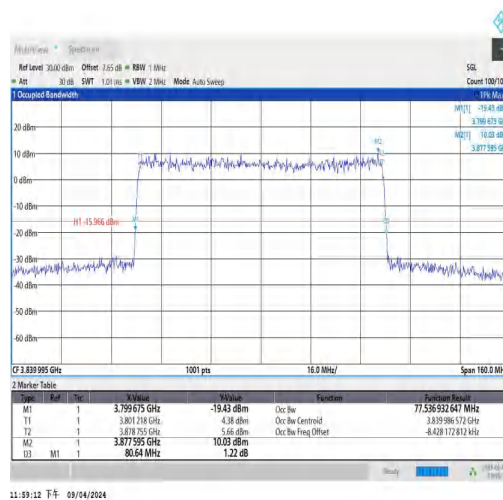


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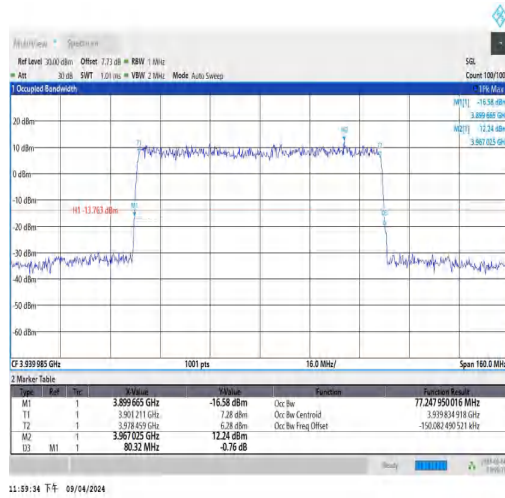
NTNV_N77-3700-3980_PC3_30_80_M_TID4_Ant

1#1

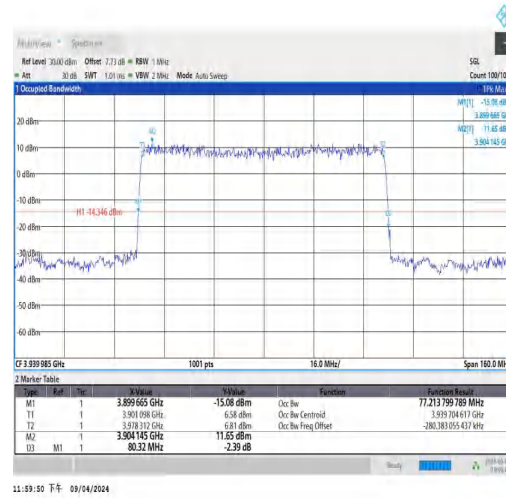


NTNV N77-3700-3980 PC3 30 80 M TID6 Ant

1#1



11:59:34 下午 09/04/2024



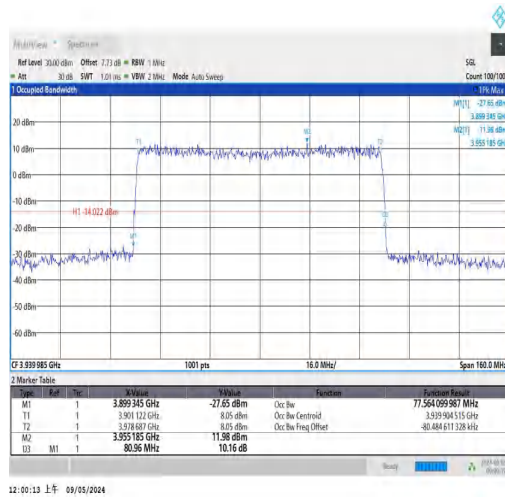
11:59:50 下午 09/04/2024

NTNV_N77-3700-3980_PC3_30_80_H_TID1_Ant

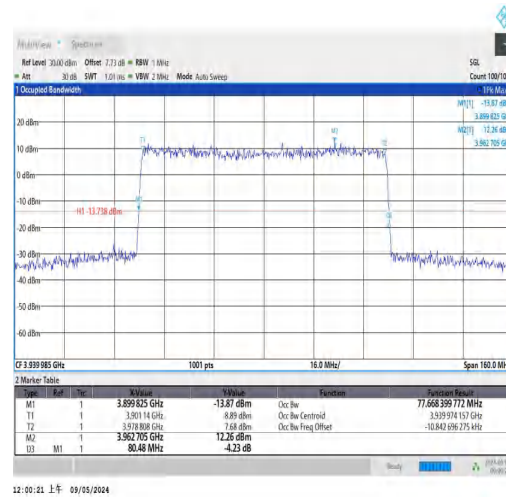
1#1

NTNV_N77-3700-3980_PC3_30_80_H_TID2_Ant

1#1



12:00:13 上午 09/05/2024



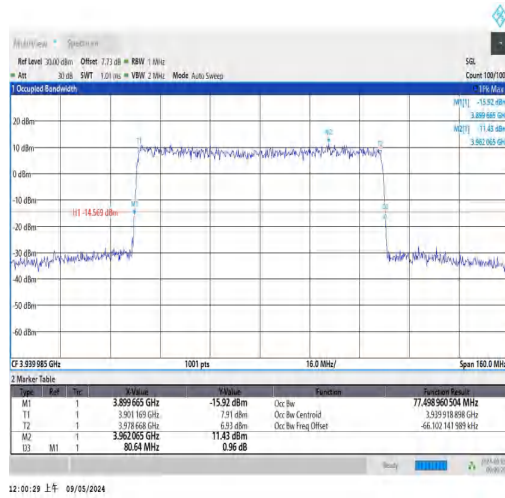
12:00:21 上午 09/05/2024

NTNV_N77-3700-3980_PC3_30_80_H_TID3_Ant

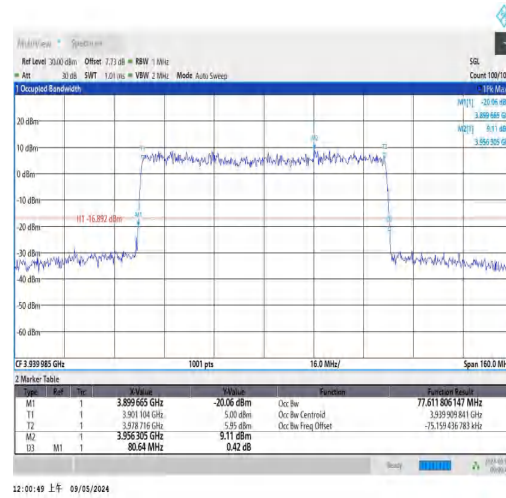
1#1

NTNV_N77-3700-3980_PC3_30_80_H_TID4_Ant

1#1



12:00:29 上午 09/05/2024



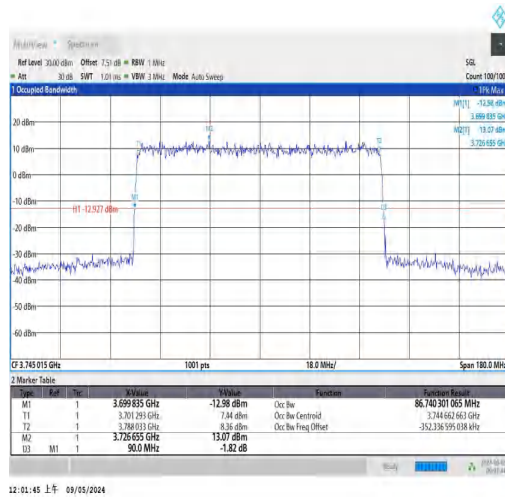
12:00:49 上午 09/05/2024

NTNV_N77-3700-3980_PC3_30_80_H_TID5_Ant

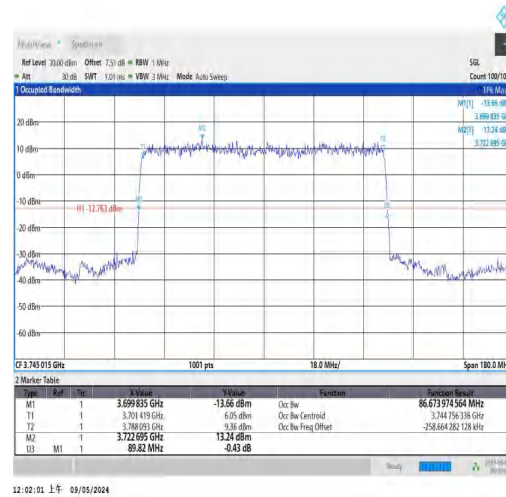
NTNV_N77-3700-3980_PC3_30_80_H_TID6_Ant

1#1

1#1



12:01:45 上午 09/05/2024



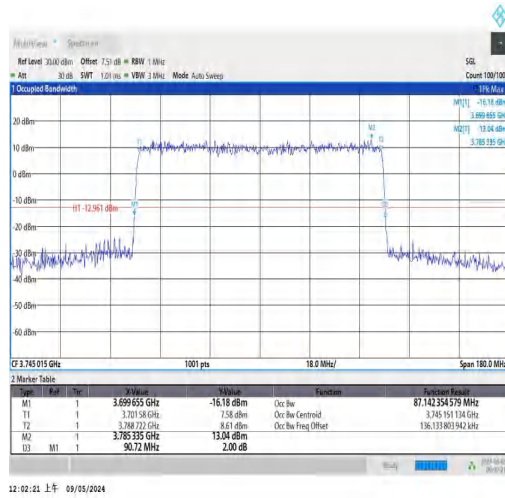
12:02:02 上午 09/05/2024

NTNV_N77-3700-3980_PC3_30_90_L_TID1_Ant

NTNV_N77-3700-3980_PC3_30_90_L_TID2_Ant

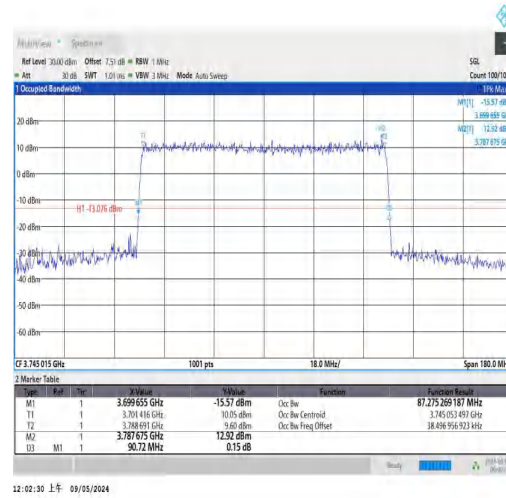
1#1

1#1



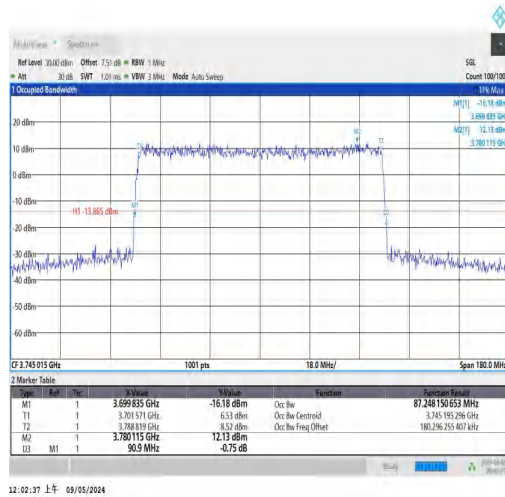
NTNV_N77-3700-3980_PC3_30_90_L_TID3_Ant

1#1



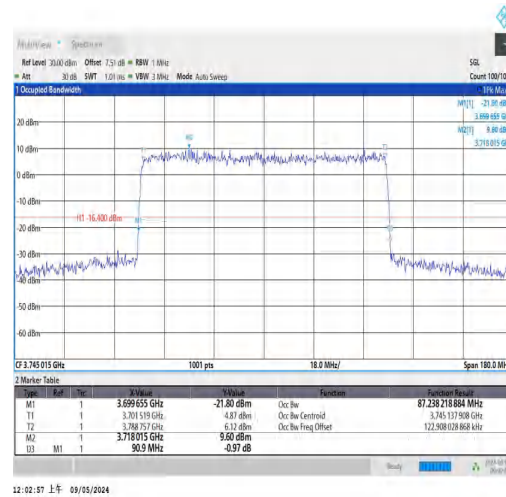
NTNV_N77-3700-3980_PC3_30_90_L_TID4_Ant

1#1



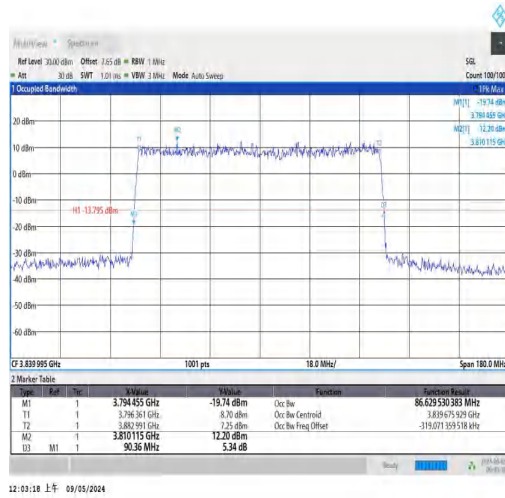
NTNV_N77-3700-3980_PC3_30_90_L_TID5_Ant

1#1

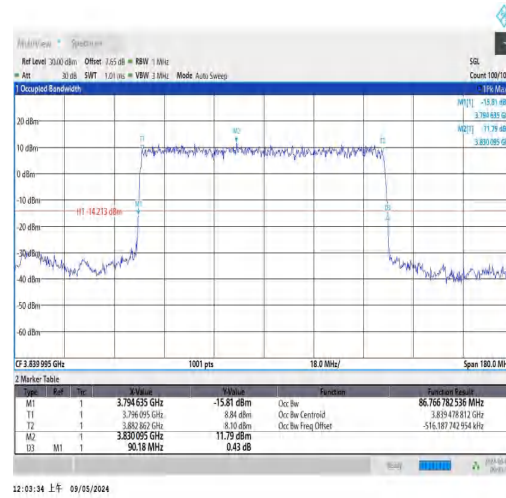


NTNV_N77-3700-3980_PC3_30_90_L_TID6_Ant

1#1



12:03:18 上午 09/05/2024



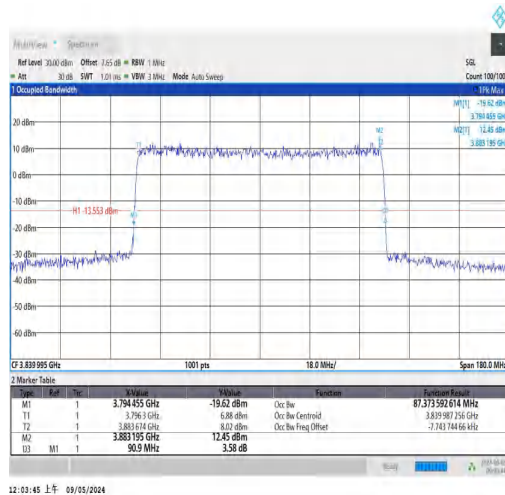
12:03:34 上午 09/05/2024

NTNV_N77-3700-3980_PC3_30_90_M_TID1_Ant

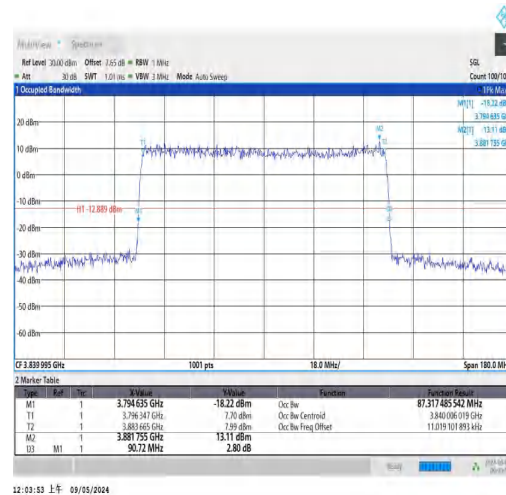
1#1

NTNV_N77-3700-3980_PC3_30_90_M_TID2_Ant

1#1



12:03:45 上午 09/05/2024



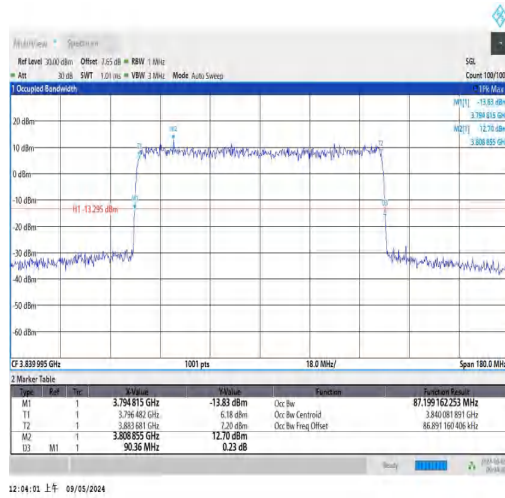
12:03:53 上午 09/05/2024

NTNV_N77-3700-3980_PC3_30_90_M_TID3_Ant

1#1

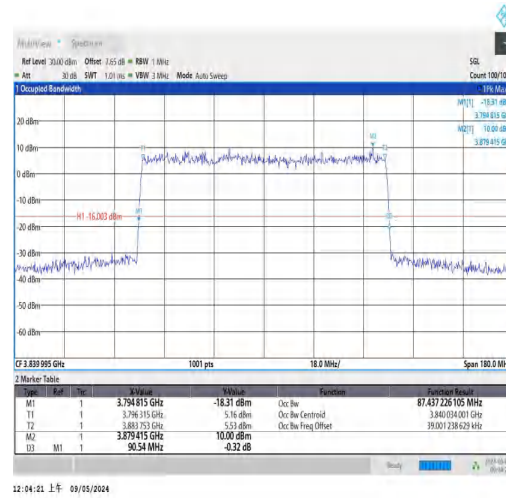
NTNV_N77-3700-3980_PC3_30_90_M_TID4_Ant

1#1



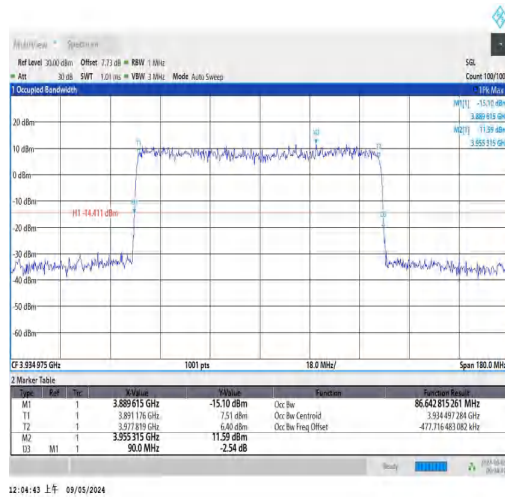
NTNV_N77-3700-3980_PC3_30_90_M_TID5_Ant

1#1



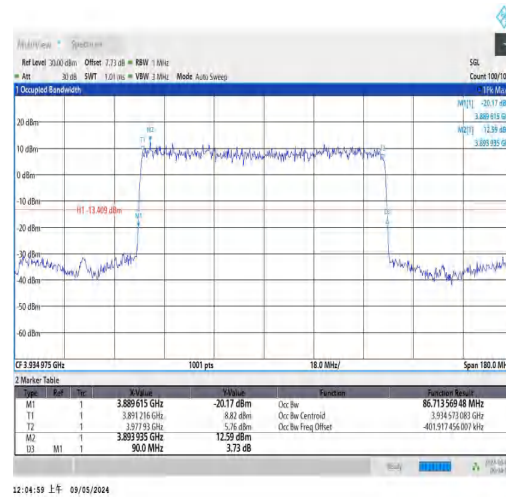
NTNV_N77-3700-3980_PC3_30_90_M_TID6_Ant

1#1



NTNV_N77-3700-3980_PC3_30_90_H_TID1_Ant

1#1



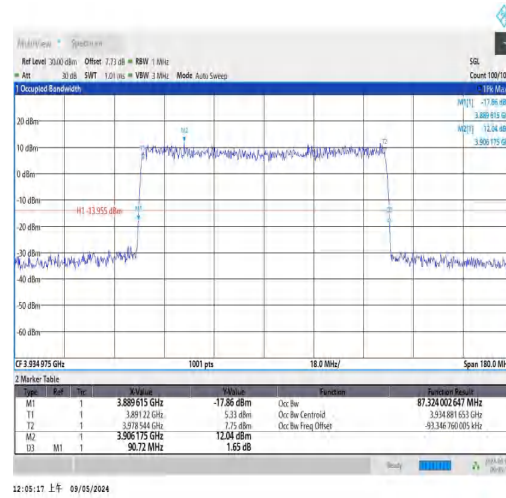
NTNV_N77-3700-3980_PC3_30_90_H_TID2_Ant

1#1



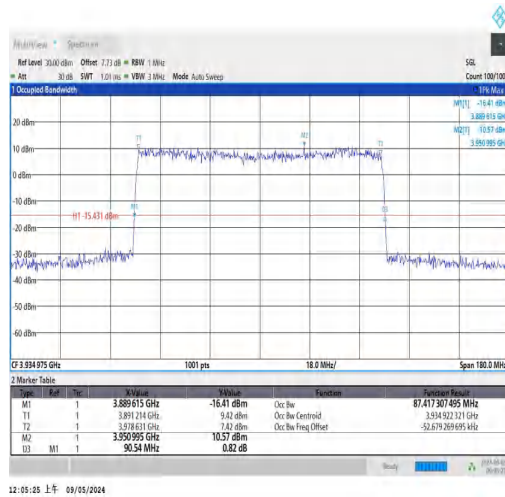
NTNV_N77-3700-3980_PC3_30_90_H_TID3_Ant

1#1



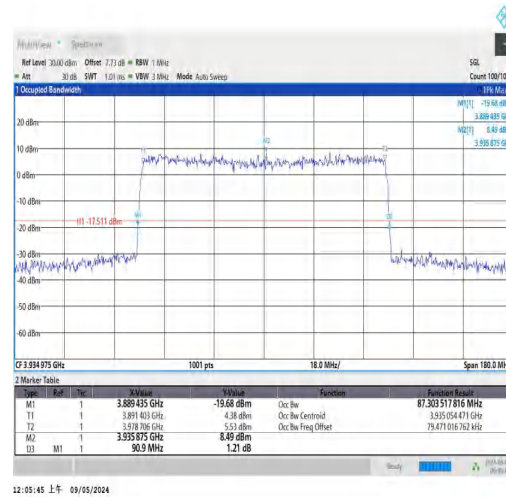
NTNV_N77-3700-3980_PC3_30_90_H_TID4_Ant

1#1



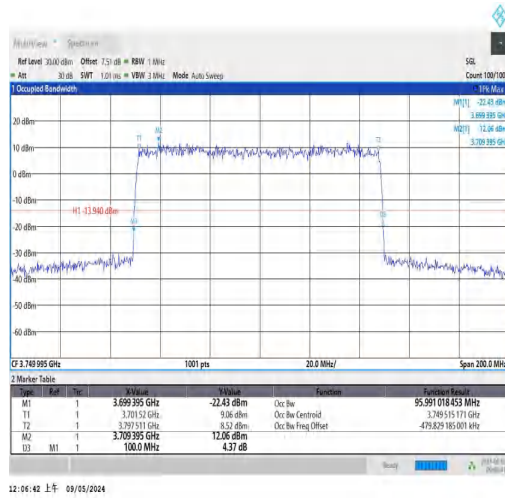
NTNV_N77-3700-3980_PC3_30_90_H_TID5_Ant

1#1

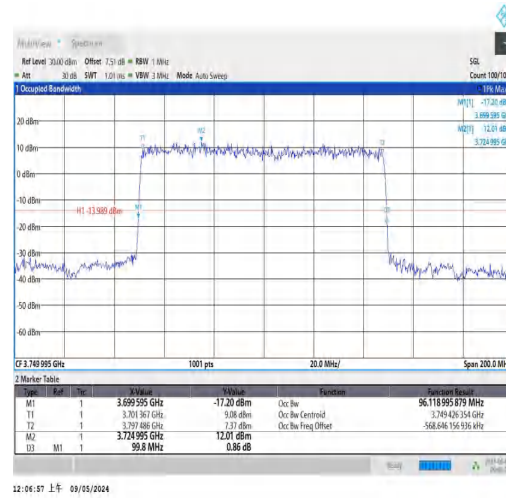


NTNV_N77-3700-3980_PC3_30_90_H_TID6_Ant

1#1



12:06:42 上午 09/05/2024



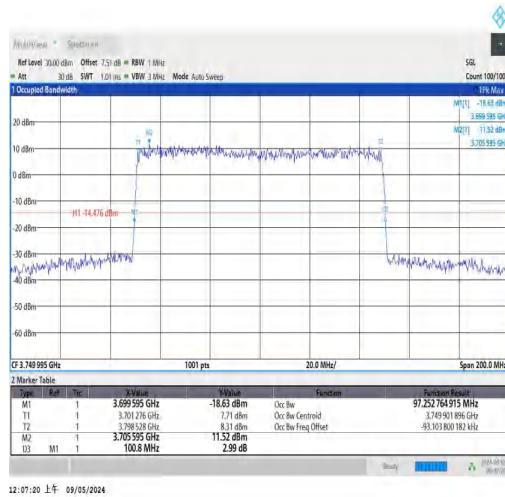
12:06:57 上午 09/05/2024

NTNV_N77-3700-3980_PC3_30_100_L_TID1_An

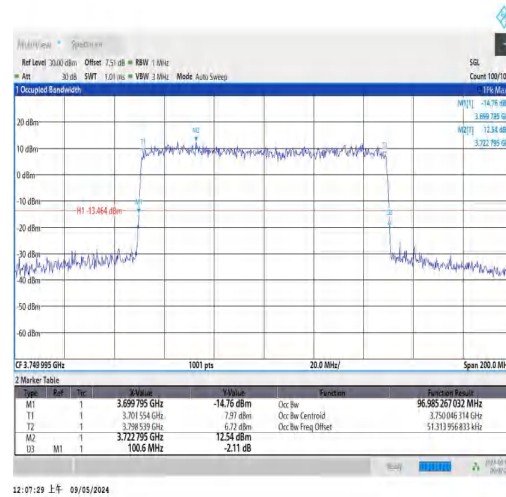
NTNV_N77-3700-3980_PC3_30_100_L_TID2_An

t1#1

t1#1



12:07:20 上午 09/05/2024



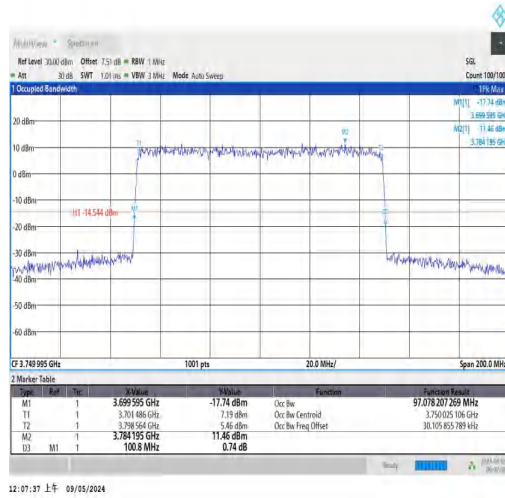
12:07:29 上午 09/05/2024

NTNV_N77-3700-3980_PC3_30_100_L_TID3_An

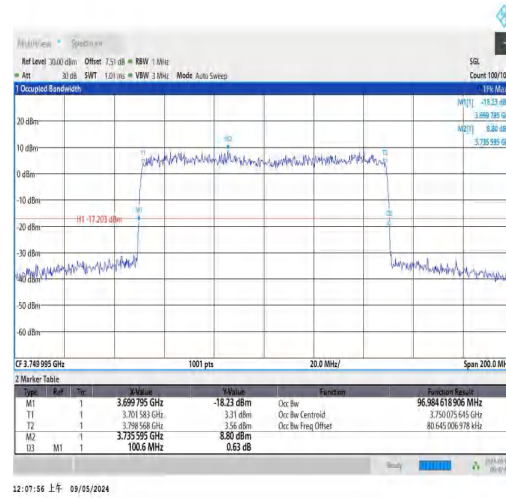
NTNV_N77-3700-3980_PC3_30_100_L_TID4_An

t1#1

t1#1



12:07:37 上午 09/05/2024



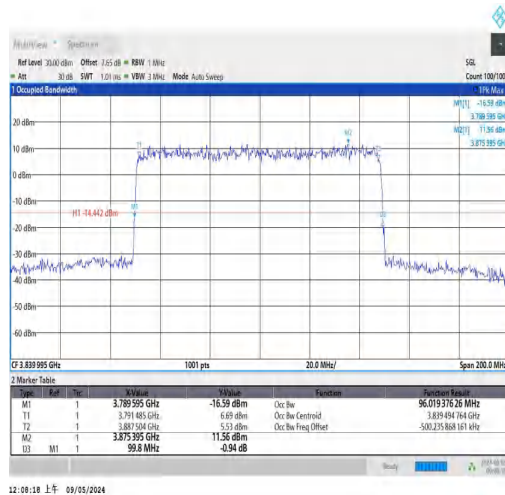
12:07:56 上午 09/05/2024

NTNV_N77-3700-3980_PC3_30_100_L_TID5_An

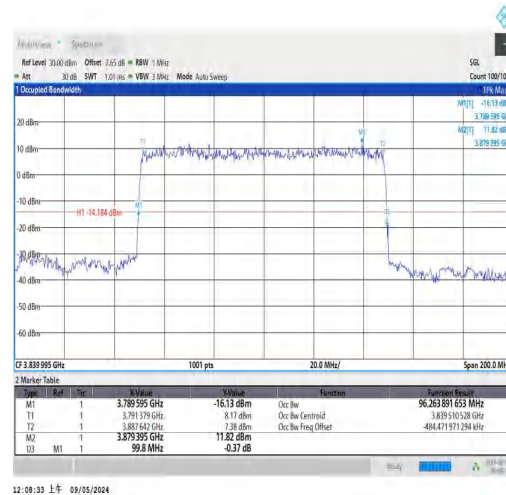
NTNV_N77-3700-3980_PC3_30_100_L_TID6_An

t1#1

t1#1



12:08:18 上午 09/05/2024



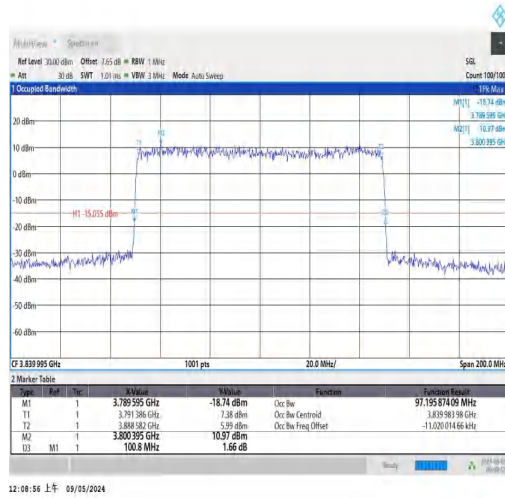
12:08:33 上午 09/05/2024

NTNV_N77-3700-3980_PC3_30_100_M_TID1_A

NTNV_N77-3700-3980_PC3_30_100_M_TID2_A

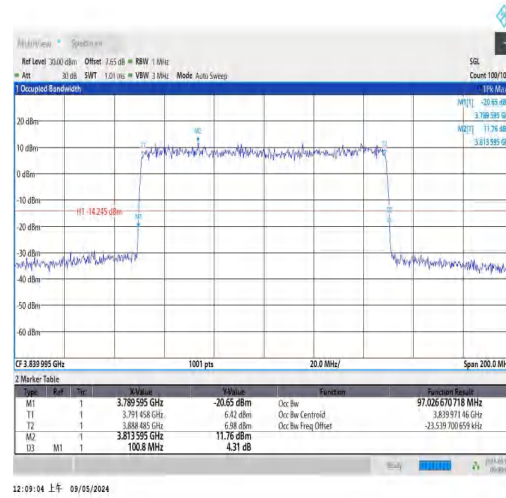
nt1#1

nt1#1



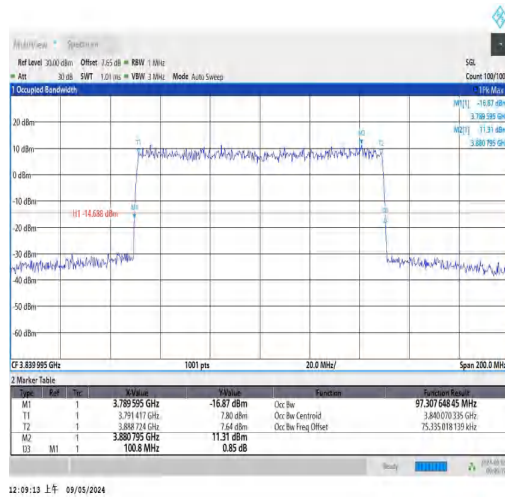
NTNV_N77-3700-3980_PC3_30_100_M_TID3_A

nt1#1



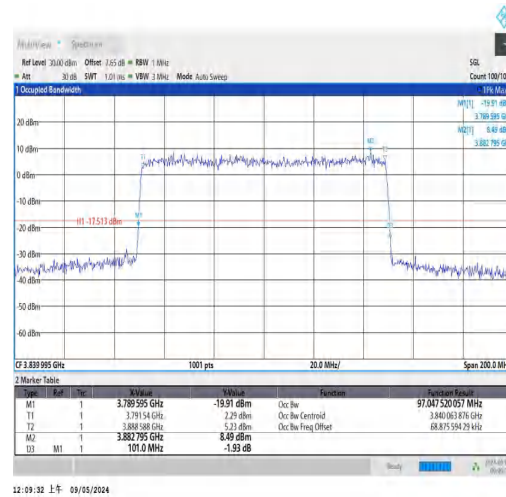
NTNV_N77-3700-3980_PC3_30_100_M_TID4_A

nt1#1



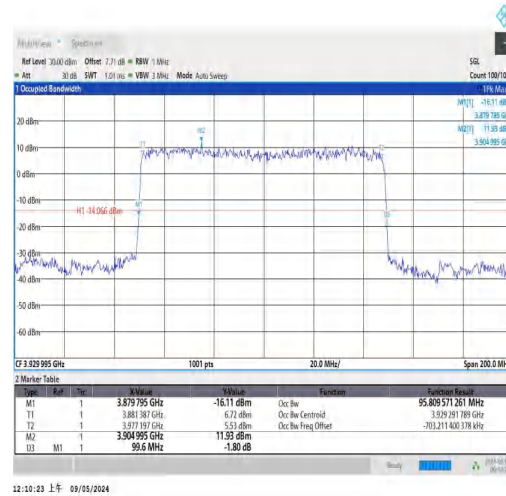
NTNV_N77-3700-3980_PC3_30_100_M_TID5_A

nt1#1



NTNV_N77-3700-3980_PC3_30_100_M_TID6_A

nt1#1

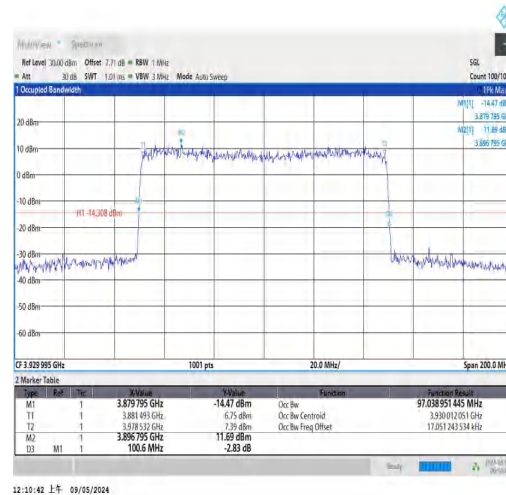
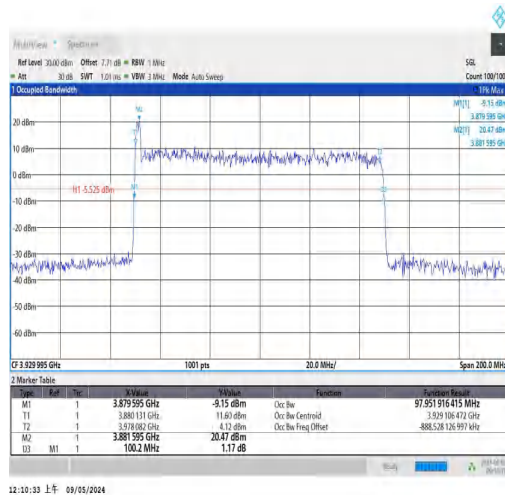


NTNV_N77-3700-3980_PC3_30_100_H_TID1_An

NTNV_N77-3700-3980_PC3_30_100_H_TID2_An

t1#1

t1#1

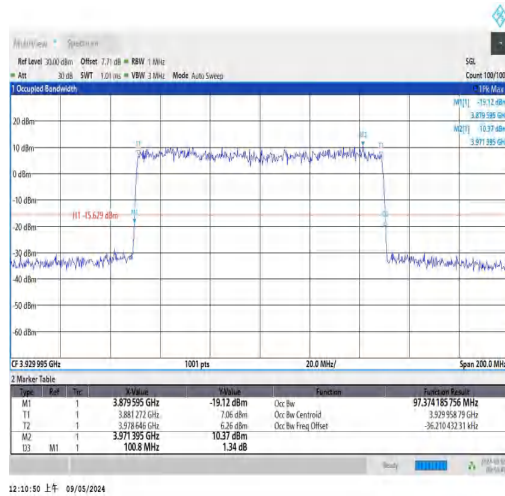


NTNV_N77-3700-3980_PC3_30_100_H_TID3_An

NTNV_N77-3700-3980_PC3_30_100_H_TID4_An

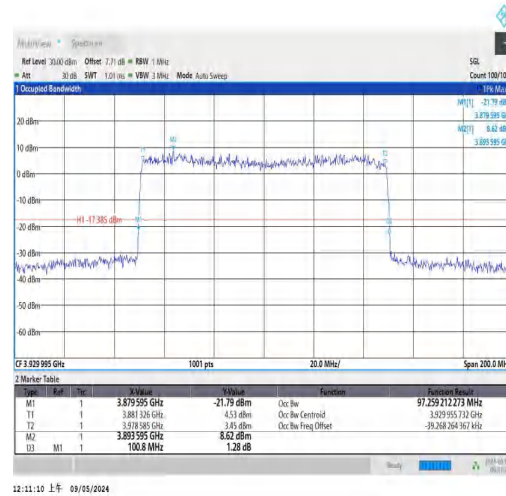
t1#1

t1#1



NTNV_N77-3700-3980_PC3_30_100_H_TID5_An

t1#1



NTNV_N77-3700-3980_PC3_30_100_H_TID6_An

t1#1



Appendix D: Band Edge

Test Result

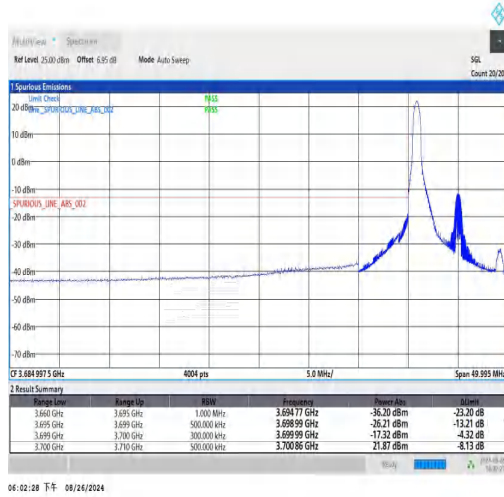
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Verdict
N77-3700-3980	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	see graph	PASS
N77-3700-3980	30	10	DFT-PI2BPSK	L	Outer_Full	see graph	PASS
N77-3700-3980	30	10	DFT-QPSK	L	Edge_1RB_Left	see graph	PASS
N77-3700-3980	30	10	DFT-QPSK	L	Outer_Full	see graph	PASS
N77-3700-3980	30	10	DFT-PI2BPSK	H	Outer_Full	see graph	PASS
N77-3700-3980	30	10	DFT-QPSK	H	Edge_1RB_Right	see graph	PASS
N77-3700-3980	30	10	DFT-QPSK	H	Outer_Full	see graph	PASS
N77-3700-3980	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	see graph	PASS
N77-3700-3980	30	50	DFT-PI2BPSK	L	Outer_Full	see graph	PASS
N77-3700-3980	30	50	DFT-QPSK	L	Edge_1RB_Left	see graph	PASS
N77-3700-3980	30	50	DFT-QPSK	L	Outer_Full	see graph	PASS
N77-3700-3980	30	50	DFT-PI2BPSK	H	Edge_1RB_Right	see graph	PASS
N77-3700-3980	30	50	DFT-PI2BPSK	H	Outer_Full	see graph	PASS
N77-3700-3980	30	50	DFT-QPSK	H	Edge_1RB_Right	see graph	PASS
N77-3700-3980	30	50	DFT-QPSK	H	Outer_Full	see graph	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	see graph	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	L	Outer_Full	see graph	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Edge_1RB_Left	see graph	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Outer_Full	see graph	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	H	Edge_1RB_Right	see graph	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	H	Outer_Full	see graph	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Edge_1RB_Right	see graph	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Outer_Full	see graph	PASS
N77-3700-3980	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	see graph	PASS
N77-3700-3980	30	10	DFT-PI2BPSK	L	Outer_Full	see graph	PASS
N77-3700-3980	30	10	DFT-QPSK	L	Edge_1RB_Left	see graph	PASS
N77-3700-3980	30	10	DFT-QPSK	L	Outer_Full	see graph	PASS
N77-3700-3980	30	10	DFT-PI2BPSK	H	Edge_1RB_Right	see graph	PASS
N77-3700-3980	30	10	DFT-PI2BPSK	H	Outer_Full	see graph	PASS
N77-3700-3980	30	10	DFT-QPSK	H	Edge_1RB_Right	see graph	PASS
N77-3700-3980	30	10	DFT-QPSK	H	Outer_Full	see graph	PASS
N77-3700-3980	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	see graph	PASS
N77-3700-3980	30	50	DFT-PI2BPSK	L	Outer_Full	see graph	PASS
N77-3700-3980	30	50	DFT-QPSK	L	Edge_1RB_Left	see graph	PASS



N77-3700-3980	30	50	DFT-QPSK	L	Outer_Full	see graph	PASS
N77-3700-3980	30	50	DFT-PI2BPSK	H	Edge_1RB_Right	see graph	PASS
N77-3700-3980	30	50	DFT-PI2BPSK	H	Outer_Full	see graph	PASS
N77-3700-3980	30	50	DFT-QPSK	H	Edge_1RB_Right	see graph	PASS
N77-3700-3980	30	50	DFT-QPSK	H	Outer_Full	see graph	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	see graph	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	L	Outer_Full	see graph	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Edge_1RB_Left	see graph	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Outer_Full	see graph	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	H	Edge_1RB_Right	see graph	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	H	Outer_Full	see graph	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Edge_1RB_Right	see graph	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Outer_Full	see graph	PASS



Test Graphs



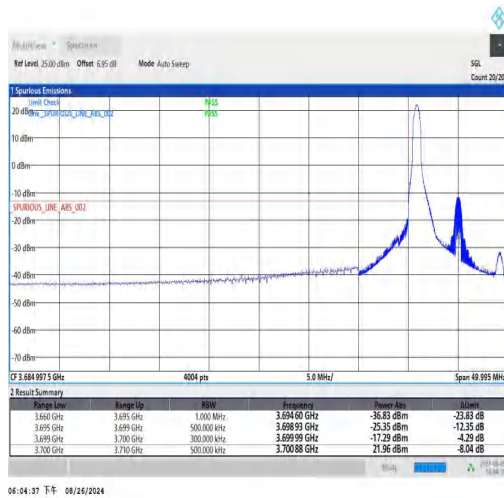
NTNV_N77-3700-3980_PC2_30_10_L_TID1_RID

1#1



NTNV_N77-3700-3980_PC2_30_10_L_TID3_RID

1#1



NTNV_N77-3700-3980_PC2_30_10_L_TID4_RID

1#1



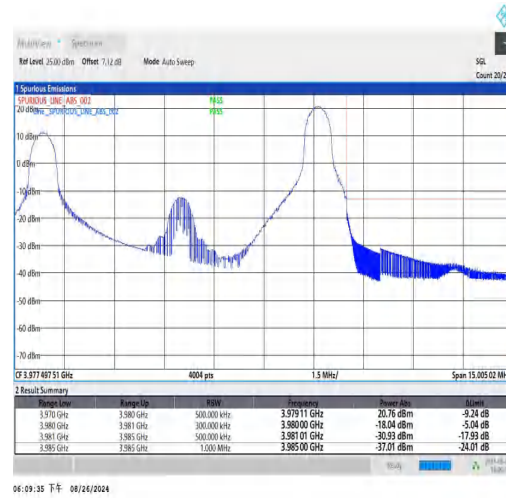
NTNV_N77-3700-3980_PC2_30_10_L_TID6_RID

1#1



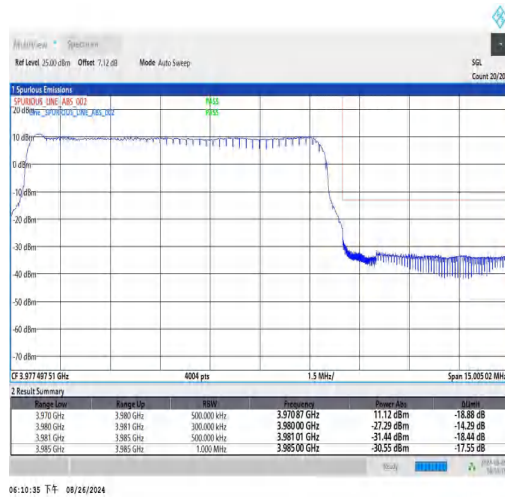
NTNV_N77-3700-3980_PC2_30_10_H_TID3_RID

1#1



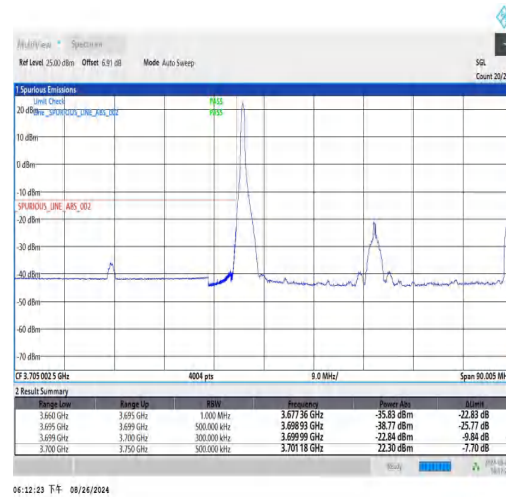
NTNV_N77-3700-3980_PC2_30_10_H_TID5_RID

1#1



NTNV_N77-3700-3980_PC2_30_10_H_TID6_RID

1#1



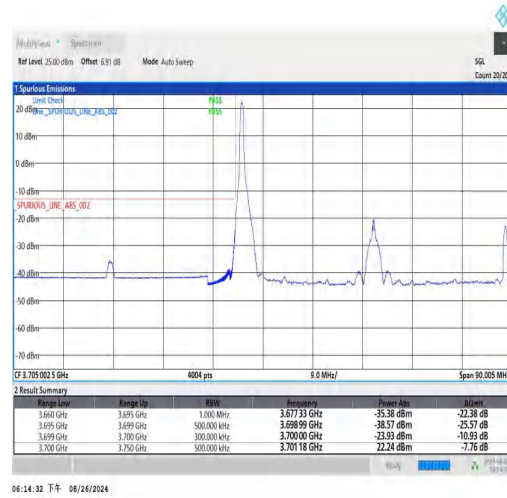
NTNV_N77-3700-3980_PC2_30_50_L_TID1_RID

1#1



NTNV_N77-3700-3980_PC2_30_50_L_TID3_RID

1#1



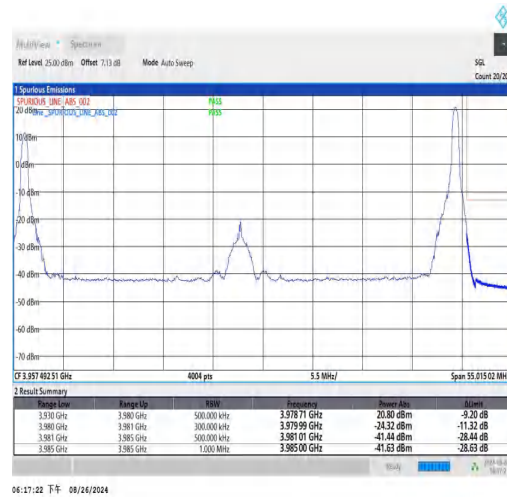
NTNV_N77-3700-3980_PC2_30_50_L_TID4_RID

1#1



NTNV_N77-3700-3980_PC2_30_50_L_TID6_RID

1#1



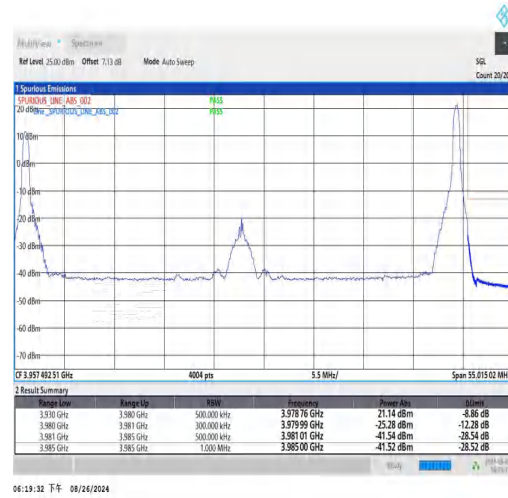
NTNV_N77-3700-3980_PC2_30_50_H_TID2_RID

1#1



NTNV_N77-3700-3980_PC2_30_50_H_TID3_RID

1#1



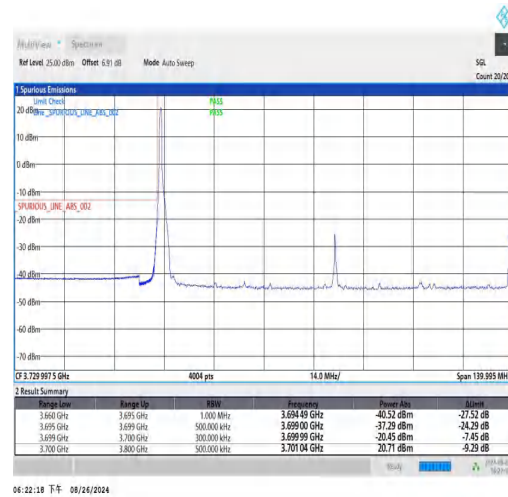
NTNV_N77-3700-3980_PC2_30_50_H_TID5_RID

1#1



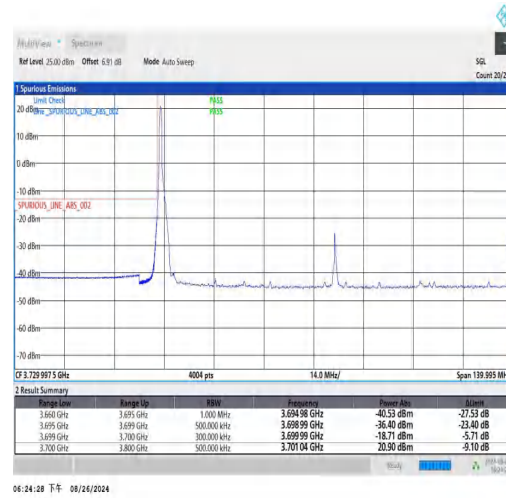
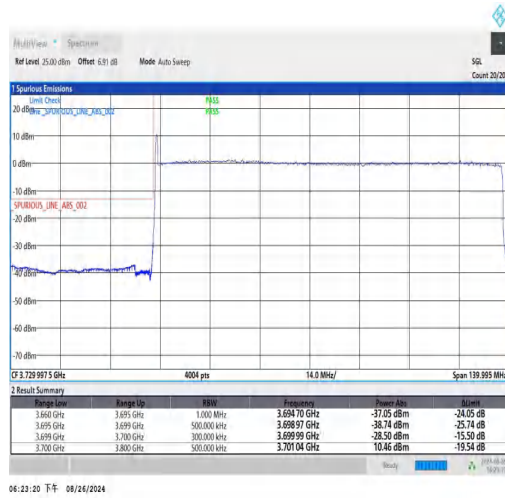
NTNV_N77-3700-3980_PC2_30_50_H_TID6_RID

1#1



NTNV_N77-3700-3980_PC2_30_100_L_TID1_RID

D1#1

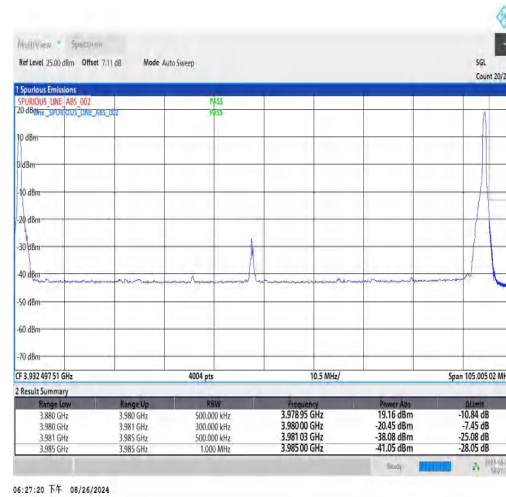
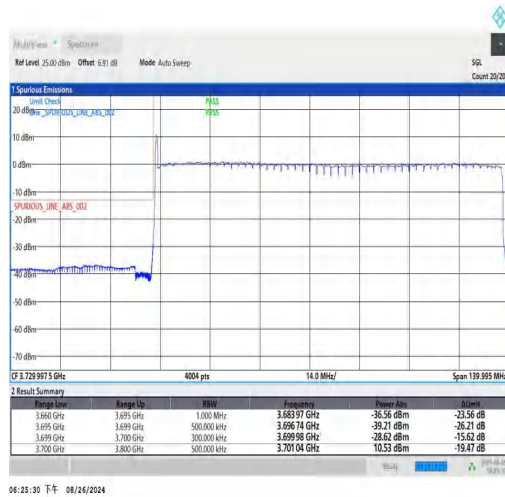


NTNV_N77-3700-3980_PC2_30_100_L_TID3_RI

D1#1

NTNV_N77-3700-3980_PC2_30_100_L_TID4_RI

D1#1

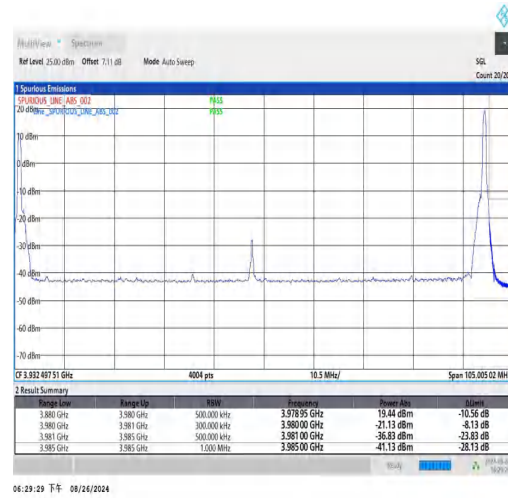


NTNV_N77-3700-3980_PC2_30_100_L_TID6_RI

D1#1

NTNV_N77-3700-3980_PC2_30_100_H_TID2_RI

D1#1

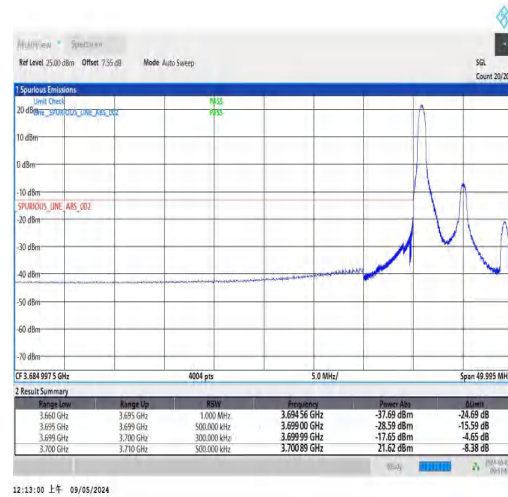
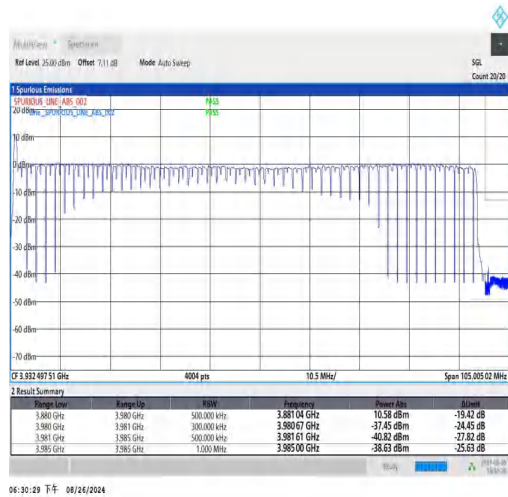


NTNV_N77-3700-3980_PC2_30_100_H_TID3_RI

D1#1

NTNV_N77-3700-3980_PC2_30_100_H_TID5_RI

D1#1



NTNV_N77-3700-3980_PC2_30_100_H_TID6_RI

D1#1

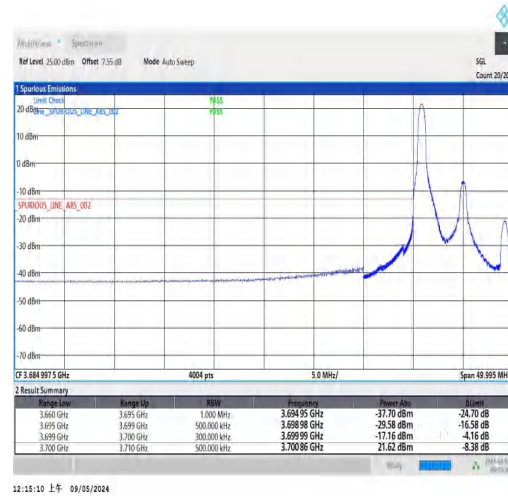
NTNV_N77-3700-3980_PC3_30_10_L_TID1_RID

1#1



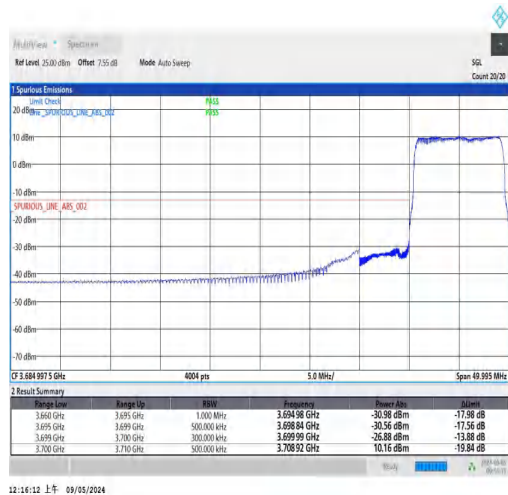
NTNV_N77-3700-3980_PC3_30_10_L_TID3_RID

1#1



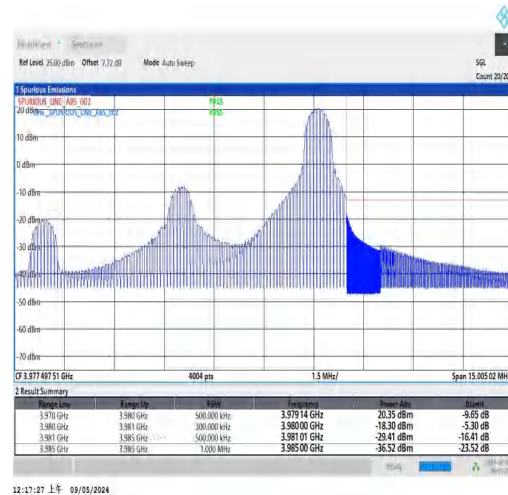
NTNV_N77-3700-3980_PC3_30_10_L_TID4_RID

1#1



NTNV_N77-3700-3980_PC3_30_10_L_TID6_RID

1#1



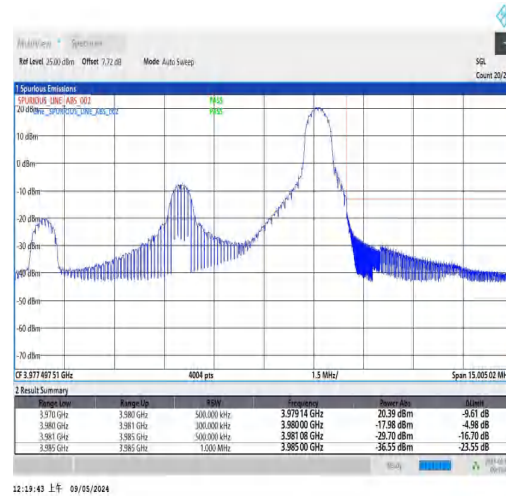
NTNV_N77-3700-3980_PC3_30_10_H_TID2_RID

1#1



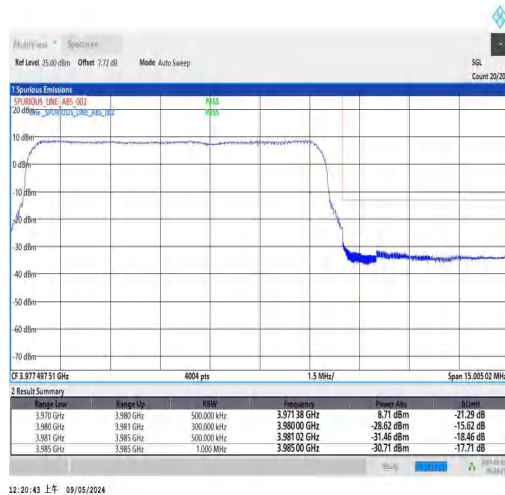
NTNV_N77-3700-3980_PC3_30_10_H_TID3_RID

1#1



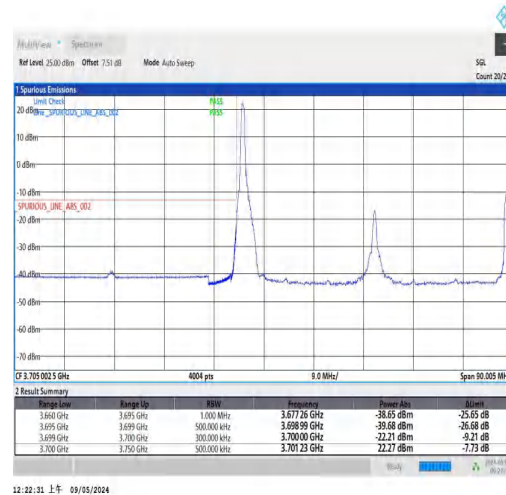
NTNV_N77-3700-3980_PC3_30_10_H_TID5_RID

1#1



NTNV_N77-3700-3980_PC3_30_10_H_TID6_RID

1#1



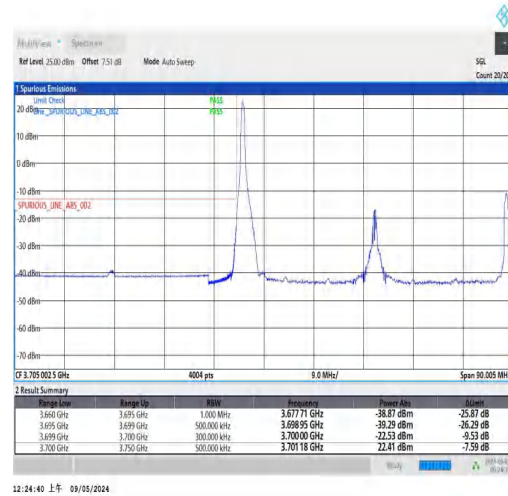
NTNV_N77-3700-3980_PC3_30_50_L_TID1_RID

1#1



NTNV_N77-3700-3980_PC3_30_50_L_TID3_RID

1#1



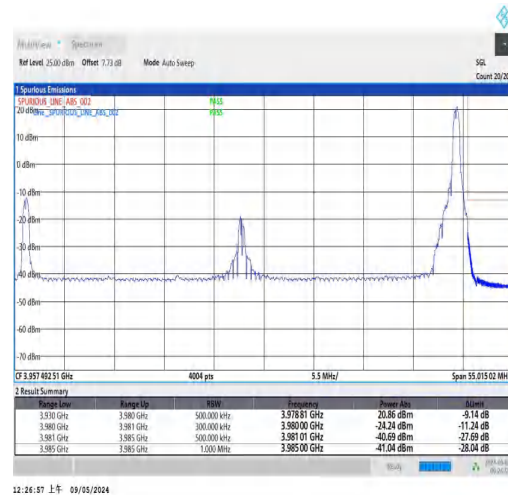
NTNV_N77-3700-3980_PC3_30_50_L_TID4_RID

1#1



NTNV_N77-3700-3980_PC3_30_50_L_TID6_RID

1#1



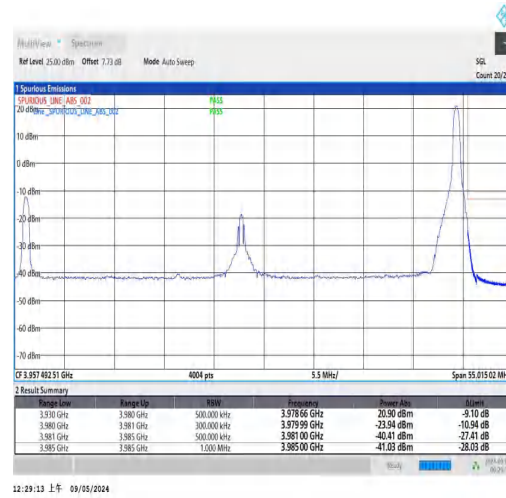
NTNV_N77-3700-3980_PC3_30_50_H_TID2_RID

1#1



NTNV_N77-3700-3980_PC3_30_50_H_TID3_RID

1#1



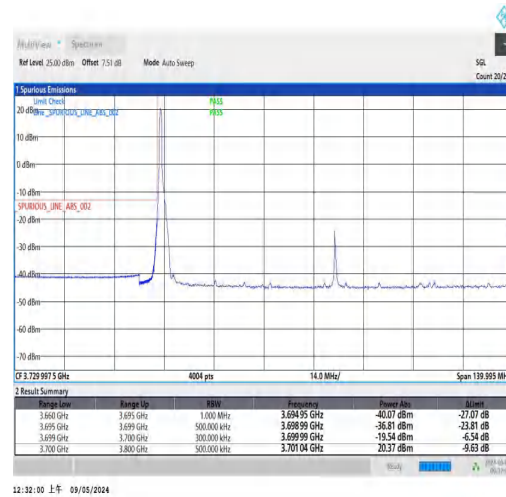
NTNV_N77-3700-3980_PC3_30_50_H_TID5_RID

1#1



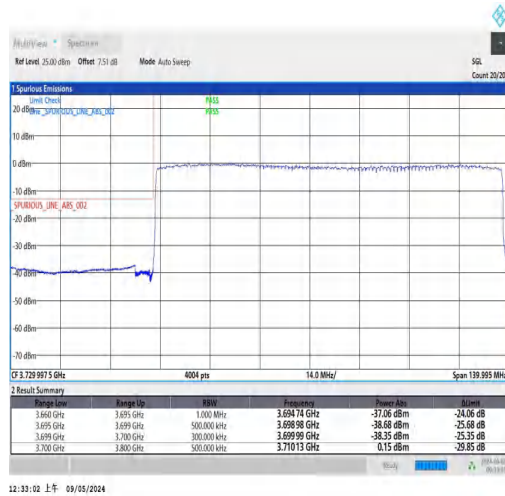
NTNV_N77-3700-3980_PC3_30_50_H_TID6_RID

1#1

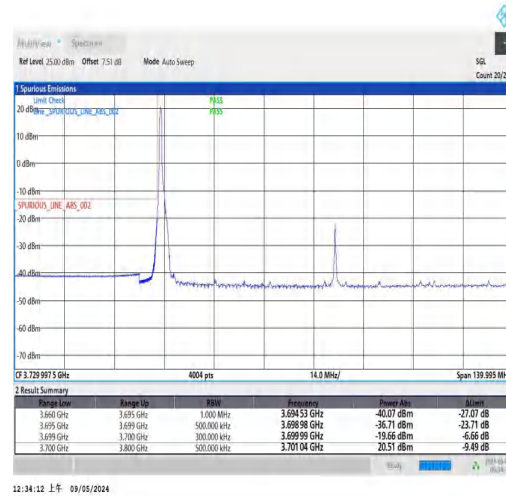


NTNV_N77-3700-3980_PC3_30_100_L_TID1_RI

D1#1



12:33:02 上午 09/05/2024



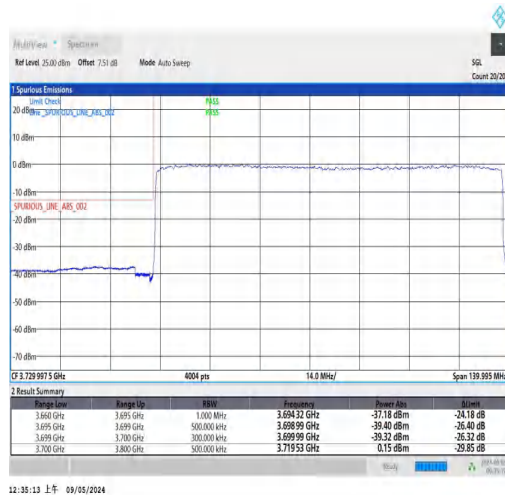
12:34:12 上午 09/05/2024

NTNV_N77-3700-3980_PC3_30_100_L_TID3_RI

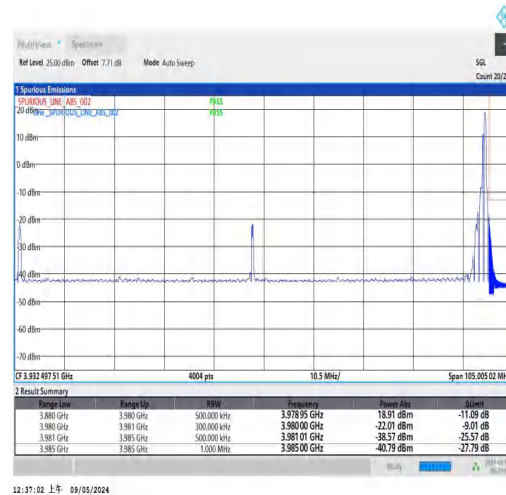
D1#1

NTNV_N77-3700-3980_PC3_30_100_L_TID4_RI

D1#1



12:35:13 上午 09/05/2024



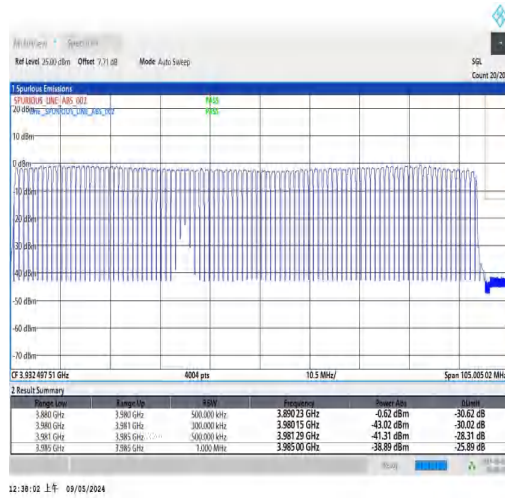
12:37:02 上午 09/05/2024

NTNV_N77-3700-3980_PC3_30_100_L_TID6_RI

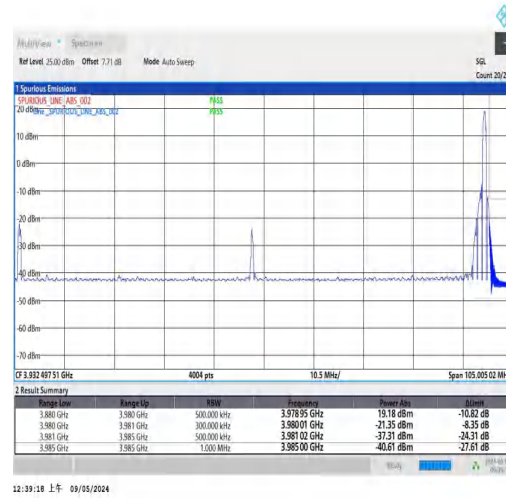
D1#1

NTNV_N77-3700-3980_PC3_30_100_H_TID2_RI

D1#1



12:39:02 上午 09/05/2024



12:39:18 上午 09/05/2024

NTNV_N77-3700-3980_PC3_30_100_H_TID3_RI

D1#1

NTNV_N77-3700-3980_PC3_30_100_H_TID5_RI

D1#1



12:40:24 上午 09/05/2024

NTNV_N77-3700-3980_PC3_30_100_H_TID6_RI

D1#1



Appendix E: Conducted Spurious Emission

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Start Freq (MHz)	Stop Freq (MHz)	Result (dBm)	Limit (dBm)	Verdict
N77-3700-3980	30	10	DFT-QPSK	L	Edge_1RB_Left	30	1000	-49.11	-13	PASS
N77-3700-3980	30	10	DFT-QPSK	L	Edge_1RB_Left	1000	3000	-46.28	-13	PASS
N77-3700-3980	30	10	DFT-QPSK	L	Edge_1RB_Left	3000	6000	-33.71	-13	PASS
N77-3700-3980	30	10	DFT-QPSK	L	Edge_1RB_Left	6000	26000	-42.34	-13	PASS
N77-3700-3980	30	10	DFT-QPSK	L	Edge_1RB_Left	26000	40000	-43.29	-13	PASS
N77-3700-3980	30	10	DFT-PI2BPSK	M	Edge_1RB_Left	30	1000	-49.20	-13	PASS
N77-3700-3980	30	10	DFT-PI2BPSK	M	Edge_1RB_Left	1000	3000	-46.40	-13	PASS
N77-3700-3980	30	10	DFT-PI2BPSK	M	Edge_1RB_Left	3000	6000	-36.75	-13	PASS
N77-3700-3980	30	10	DFT-PI2BPSK	M	Edge_1RB_Left	6000	26000	-34.18	-13	PASS
N77-3700-3980	30	10	DFT-PI2BPSK	M	Edge_1RB_Left	26000	40000	-43.38	-13	PASS
N77-3700-3980	30	10	DFT-QPSK	M	Edge_1RB_Left	30	1000	-49.15	-13	PASS
N77-3700-3980	30	10	DFT-QPSK	M	Edge_1RB_Left	1000	3000	-46.31	-13	PASS
N77-3700-3980	30	10	DFT-QPSK	M	Edge_1RB_Left	3000	6000	-34.46	-13	PASS
N77-3700-3980	30	10	DFT-QPSK	M	Edge_1RB_Left	6000	26000	-44.43	-13	PASS
N77-3700-3980	30	10	DFT-QPSK	M	Edge_1RB_Left	26000	40000	-43.25	-13	PASS
N77-3700-3980	30	10	DFT-PI2BPSK	H	Edge_1RB_Left	30	1000	-49.24	-13	PASS
N77-3700-3980	30	10	DFT-PI2BPSK	H	Edge_1RB_Left	1000	3000	-46.13	-13	PASS
N77-3700-3980	30	10	DFT-PI2BPSK	H	Edge_1RB_Left	3000	6000	-35.63	-13	PASS
N77-3700-3980	30	10	DFT-PI2BPSK	H	Edge_1RB_Left	6000	26000	-44.63	-13	PASS
N77-3700-3980	30	10	DFT-PI2BPSK	H	Edge_1RB_Left	26000	40000	-42.99	-13	PASS
N77-3700-3980	30	10	DFT-QPSK	H	Edge_1RB_Left	30	1000	-49.32	-13	PASS
N77-3700-3980	30	10	DFT-QPSK	H	Edge_1RB_Left	1000	3000	-46.43	-13	PASS
N77-3700-3980	30	10	DFT-QPSK	H	Edge_1RB_Left	3000	6000	-36.07	-13	PASS
N77-3700-3980	30	10	DFT-QPSK	H	Edge_1RB_Left	6000	26000	-44.41	-13	PASS
N77-3700-3980	30	10	DFT-QPSK	H	Edge_1RB_Left	26000	40000	-43.57	-13	PASS
N77-3700-3980	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	30	1000	-49.33	-13	PASS
N77-3700-3980	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	1000	3000	-46.46	-13	PASS
N77-3700-3980	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	3000	6000	-32.33	-13	PASS
N77-3700-3980	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	6000	26000	-35.99	-13	PASS
N77-3700-3980	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	26000	40000	-43.30	-13	PASS
N77-3700-3980	30	50	DFT-QPSK	L	Edge_1RB_Left	30	1000	-49.01	-13	PASS
N77-3700-3980	30	50	DFT-QPSK	L	Edge_1RB_Left	1000	3000	-46.51	-13	PASS



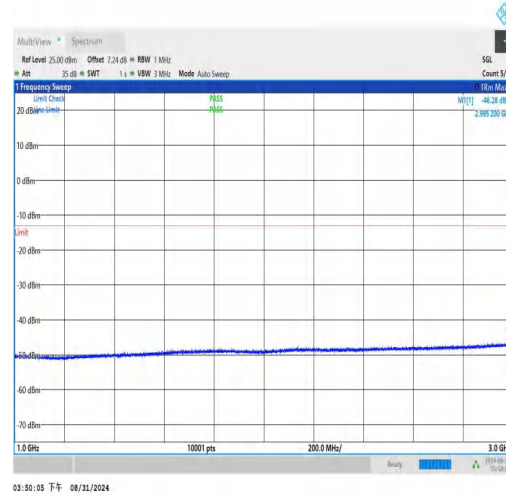
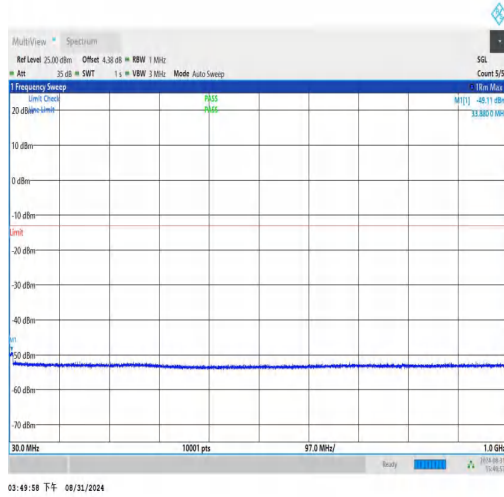
N77-3700-3980	30	50	DFT-QPSK	L	Edge_1RB_Left	3000	6000	-35.78	-13	PASS
N77-3700-3980	30	50	DFT-QPSK	L	Edge_1RB_Left	6000	26000	-36.68	-13	PASS
N77-3700-3980	30	50	DFT-QPSK	L	Edge_1RB_Left	26000	40000	-43.54	-13	PASS
N77-3700-3980	30	50	DFT-QPSK	M	Edge_1RB_Left	30	1000	-49.01	-13	PASS
N77-3700-3980	30	50	DFT-QPSK	M	Edge_1RB_Left	1000	3000	-46.63	-13	PASS
N77-3700-3980	30	50	DFT-QPSK	M	Edge_1RB_Left	3000	6000	-34.18	-13	PASS
N77-3700-3980	30	50	DFT-QPSK	M	Edge_1RB_Left	6000	26000	-42.58	-13	PASS
N77-3700-3980	30	50	DFT-QPSK	M	Edge_1RB_Left	26000	40000	-43.45	-13	PASS
N77-3700-3980	30	50	DFT-PI2BPSK	H	Edge_1RB_Left	30	1000	-49.27	-13	PASS
N77-3700-3980	30	50	DFT-PI2BPSK	H	Edge_1RB_Left	1000	3000	-46.07	-13	PASS
N77-3700-3980	30	50	DFT-PI2BPSK	H	Edge_1RB_Left	3000	6000	-31.37	-13	PASS
N77-3700-3980	30	50	DFT-PI2BPSK	H	Edge_1RB_Left	6000	26000	-44.42	-13	PASS
N77-3700-3980	30	50	DFT-PI2BPSK	H	Edge_1RB_Left	26000	40000	-43.68	-13	PASS
N77-3700-3980	30	50	DFT-QPSK	H	Edge_1RB_Left	30	1000	-48.95	-13	PASS
N77-3700-3980	30	50	DFT-QPSK	H	Edge_1RB_Left	1000	3000	-45.45	-13	PASS
N77-3700-3980	30	50	DFT-QPSK	H	Edge_1RB_Left	3000	6000	-31.30	-13	PASS
N77-3700-3980	30	50	DFT-QPSK	H	Edge_1RB_Left	6000	26000	-42.15	-13	PASS
N77-3700-3980	30	50	DFT-QPSK	H	Edge_1RB_Left	26000	40000	-43.52	-13	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	30	1000	-49.07	-13	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	1000	3000	-46.14	-13	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	3000	6000	-34.72	-13	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	6000	26000	-44.10	-13	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	26000	40000	-43.59	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Edge_1RB_Left	30	1000	-49.06	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Edge_1RB_Left	1000	3000	-46.33	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Edge_1RB_Left	3000	6000	-36.20	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Edge_1RB_Left	6000	26000	-41.99	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Edge_1RB_Left	26000	40000	-43.33	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Edge_1RB_Left	30	1000	-48.94	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Edge_1RB_Left	1000	3000	-46.45	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Edge_1RB_Left	3000	6000	-32.91	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Edge_1RB_Left	6000	26000	-34.53	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Edge_1RB_Left	26000	40000	-43.06	-13	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	30	1000	-49.02	-13	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	1000	3000	-46.56	-13	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	3000	6000	-31.73	-13	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	6000	26000	-41.79	-13	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	26000	40000	-42.98	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Edge_1RB_Left	30	1000	-49.40	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Edge_1RB_Left	1000	3000	-46.70	-13	PASS



N77-3700-3980	30	100	DFT-QPSK	H	Edge_1RB_Left	3000	6000	-35.15	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Edge_1RB_Left	6000	26000	-38.04	-13	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Edge_1RB_Left	26000	40000	-43.44	-13	PASS

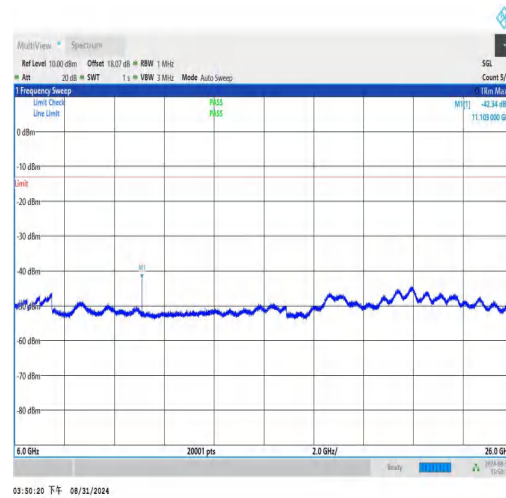
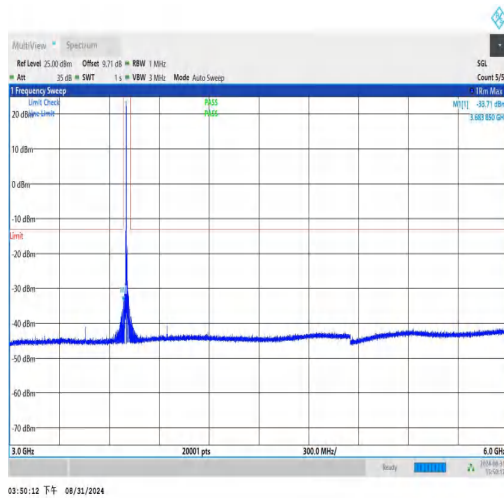


Test Graphs



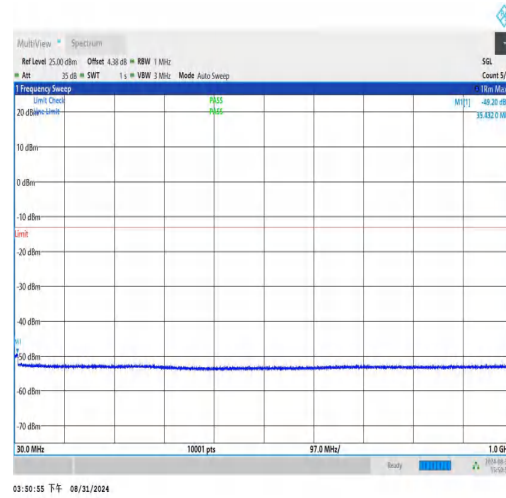
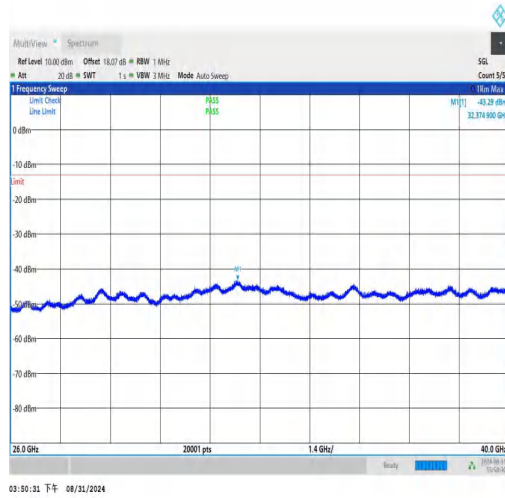
NTNV_N77-3700-3980_PC3_30_10_L_TID2_NS_01_30_1000_#1

NTNV_N77-3700-3980_PC3_30_10_L_TID2_NS_01_1000_3000_#1



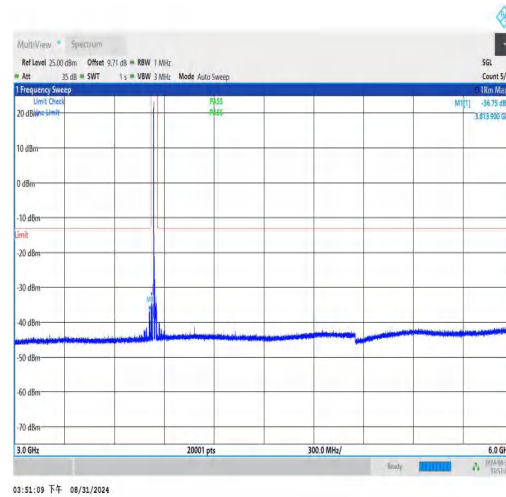
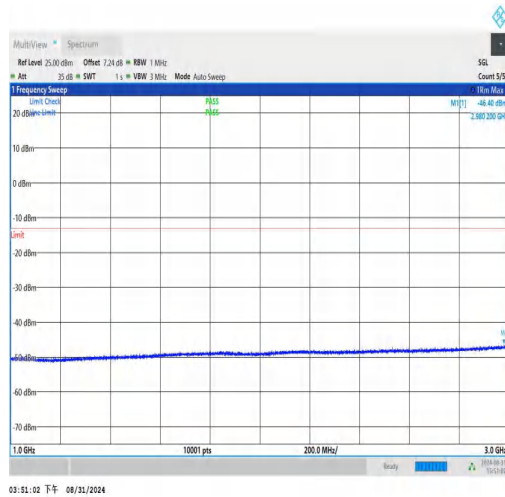
NTNV_N77-3700-3980_PC3_30_10_L_TID2_NS_01_3000_6000_#1

NTNV_N77-3700-3980_PC3_30_10_L_TID2_NS_01_6000_26000_#1



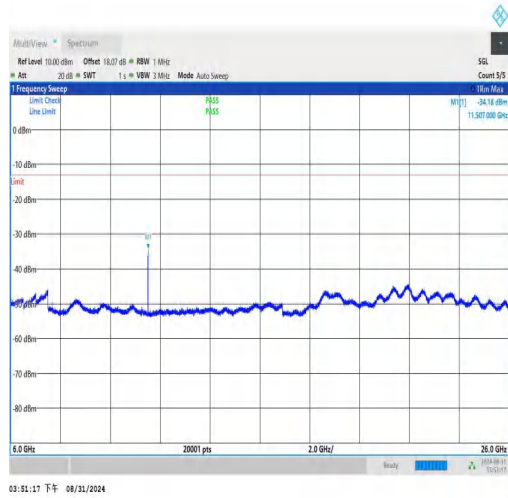
NTNV_N77-3700-3980_PC3_30_10_L_TID2_NS_01_26000_40000_#1

NTNV_N77-3700-3980_PC3_30_10_M_TID1_NS_01_30_1000_#1

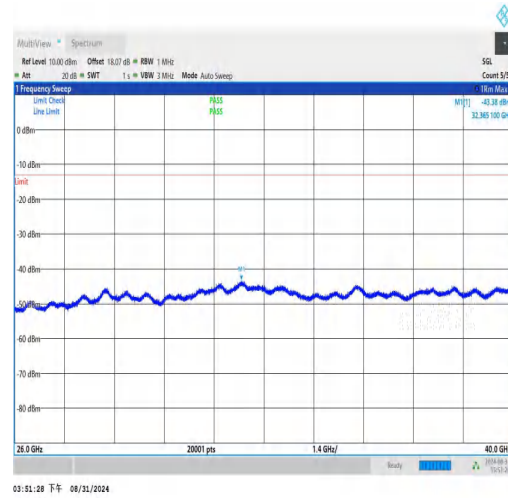


NTNV_N77-3700-3980_PC3_30_10_M_TID1_NS_01_1000_3000_#1

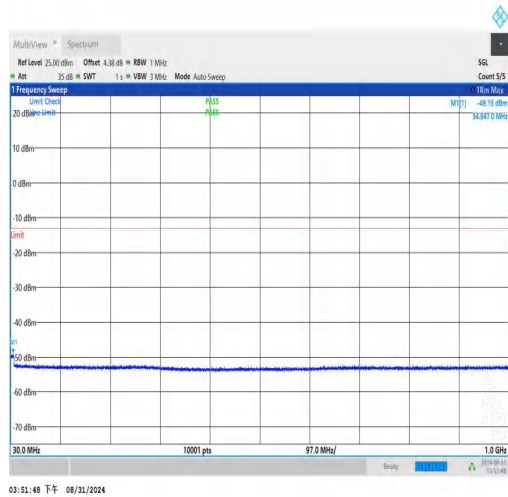
NTNV_N77-3700-3980_PC3_30_10_M_TID1_NS_01_3000_6000_#1



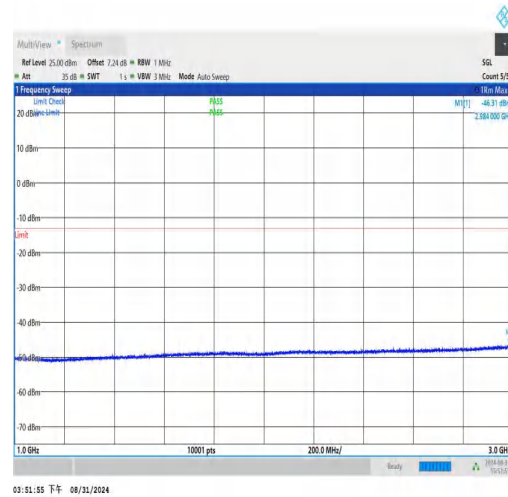
NTNV_N77-3700-3980_PC3_30_10_M_TID1_NS
_01_6000_26000_#1



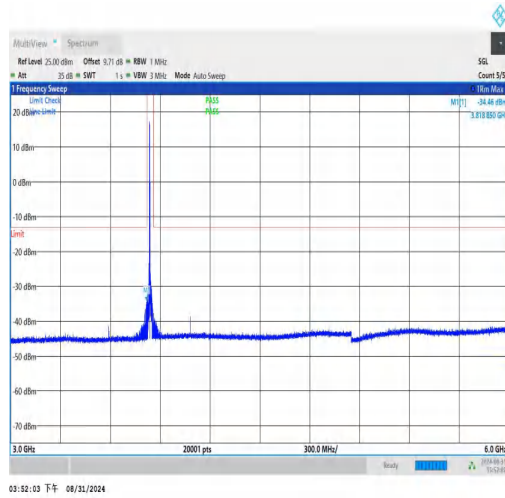
NTNV_N77-3700-3980_PC3_30_10_M_TID1_NS
_01_26000_40000_#1



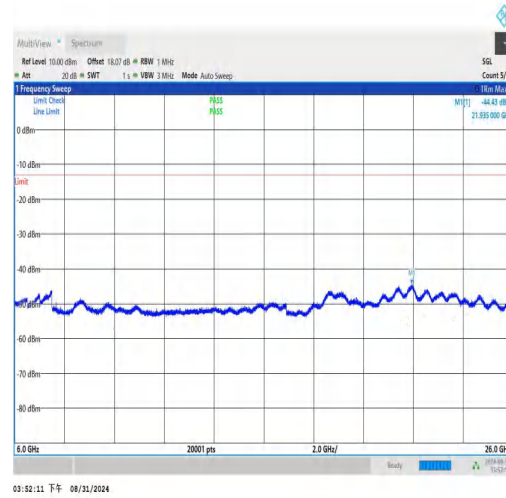
NTNV_N77-3700-3980_PC3_30_10_M_TID2_NS
_01_30_1000_#1



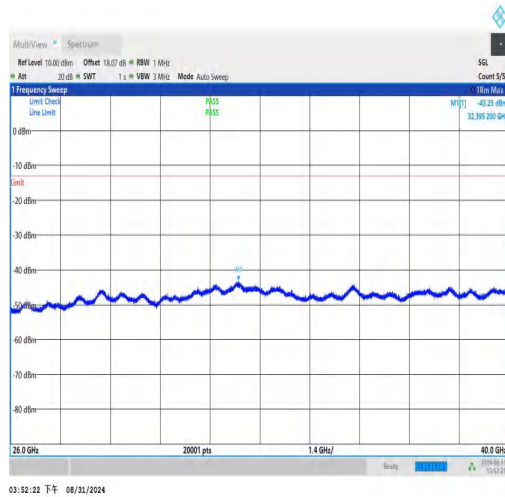
NTNV_N77-3700-3980_PC3_30_10_M_TID2_NS
_01_1000_3000_#1



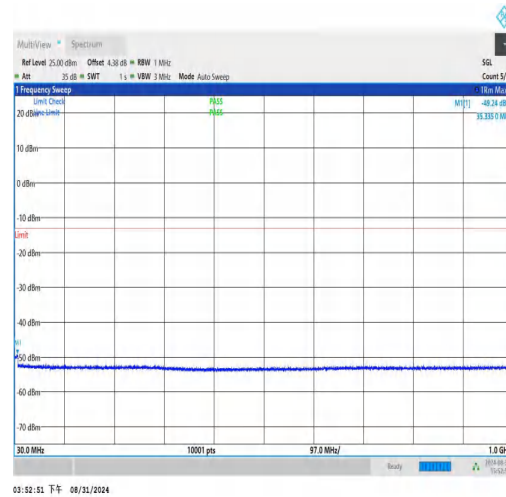
NTNV_N77-3700-3980_PC3_30_10_M_TID2_NS
_01_3000_6000_#1



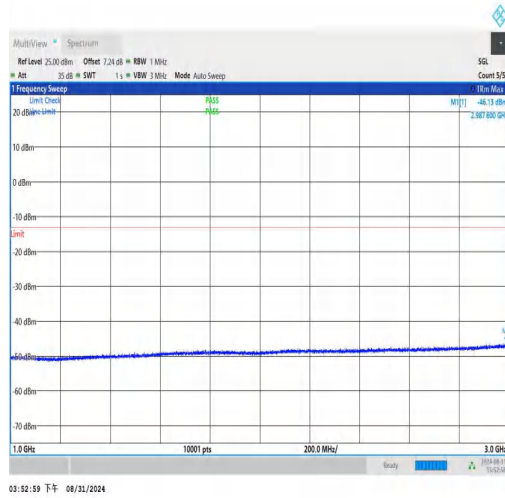
NTNV_N77-3700-3980_PC3_30_10_M_TID2_NS
_01_6000_26000_#1



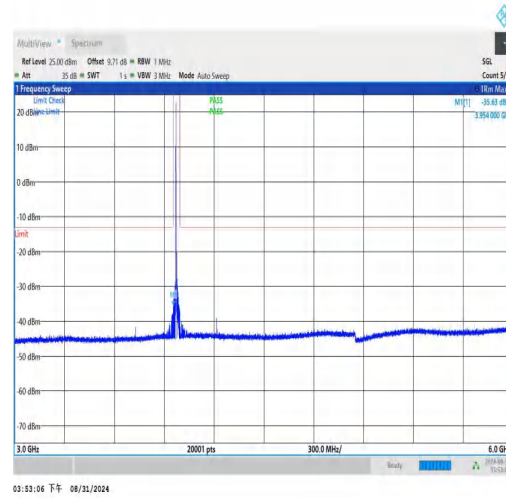
NTNV_N77-3700-3980_PC3_30_10_M_TID2_NS
_01_26000_40000_#1



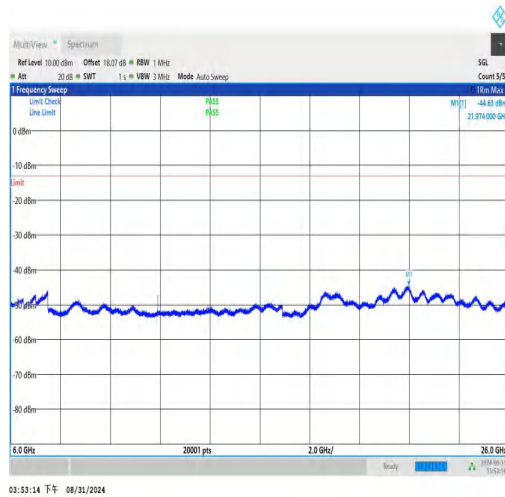
NTNV_N77-3700-3980_PC3_30_10_H_TID1_NS
_01_30_1000_#1



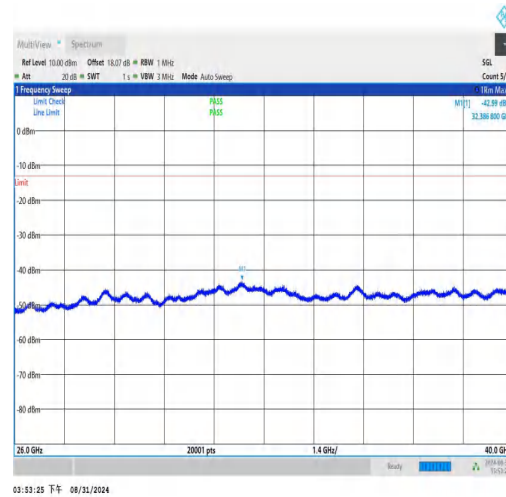
NTNV_N77-3700-3980_PC3_30_10_H_TID1_NS
_01_1000_3000_#1



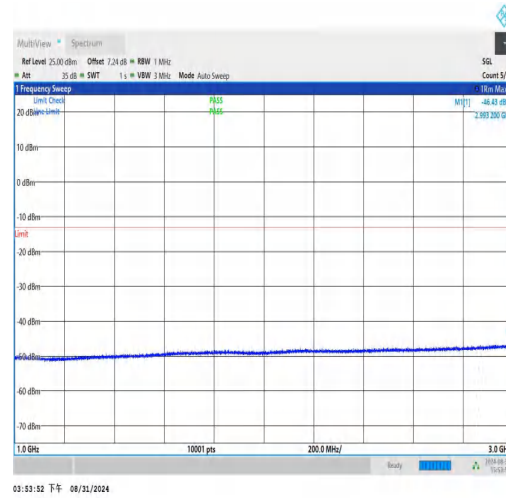
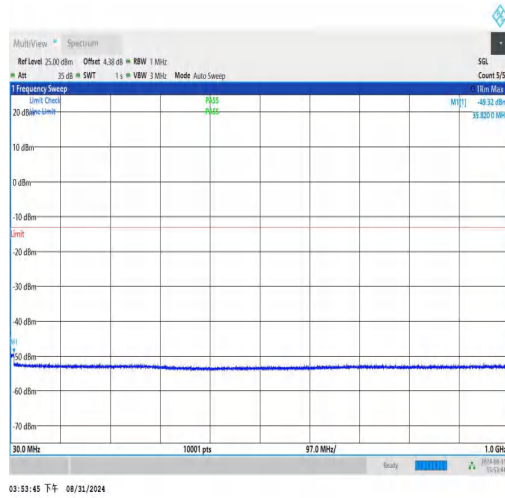
NTNV_N77-3700-3980_PC3_30_10_H_TID1_NS
_01_3000_6000_#1



NTNV_N77-3700-3980_PC3_30_10_H_TID1_NS
_01_6000_26000_#1

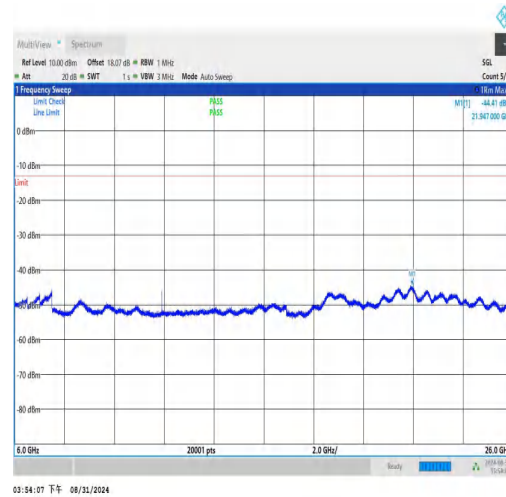
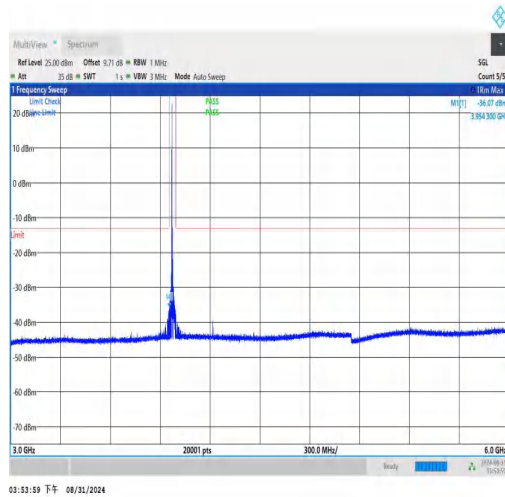


NTNV_N77-3700-3980_PC3_30_10_H_TID1_NS
_01_26000_40000_#1



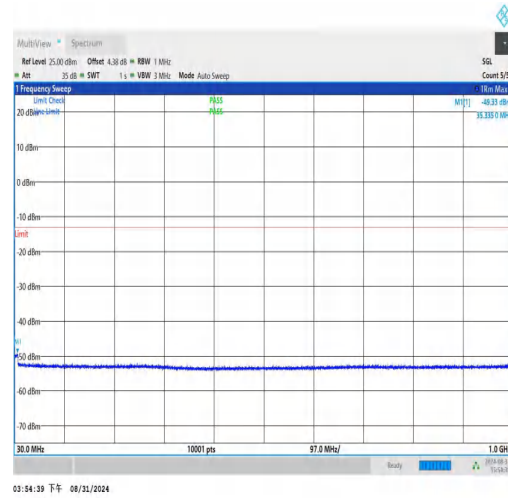
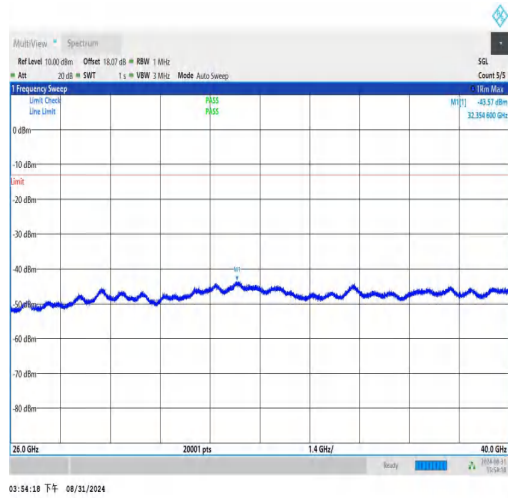
NTNV_N77-3700-3980_PC3_30_10_H_TID2_NS
_01_30_1000_#1

NTNV_N77-3700-3980_PC3_30_10_H_TID2_NS
_01_1000_3000_#1



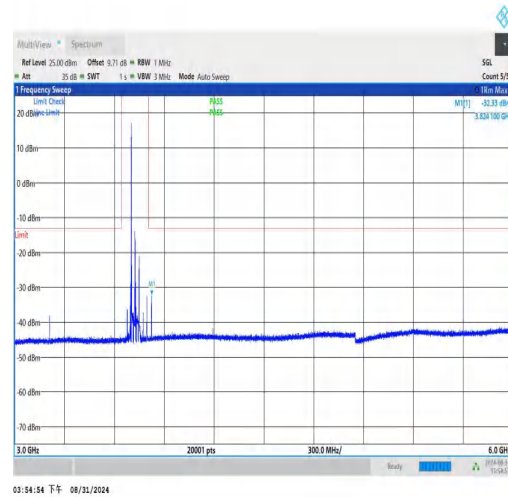
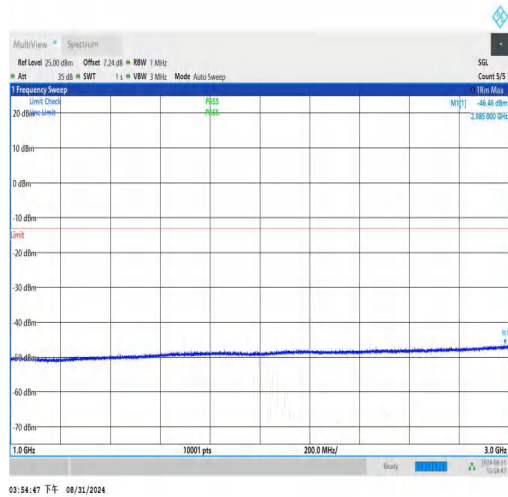
NTNV_N77-3700-3980_PC3_30_10_H_TID2_NS
_01_3000_6000_#1

NTNV_N77-3700-3980_PC3_30_10_H_TID2_NS
_01_6000_26000_#1



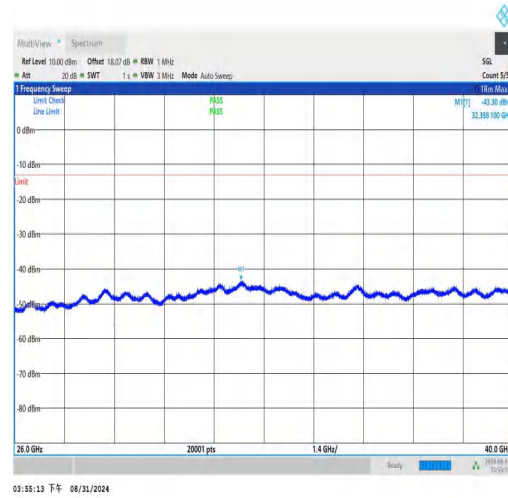
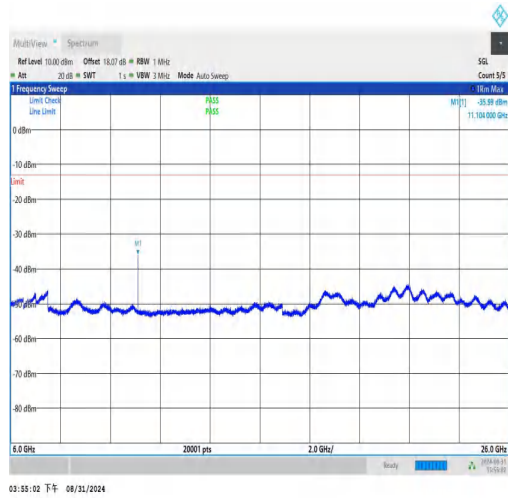
NTNV_N77-3700-3980_PC3_30_10_H_TID2_NS_01_26000_40000_#1

NTNV_N77-3700-3980_PC3_30_50_L_TID1_NS_01_30_1000_#1



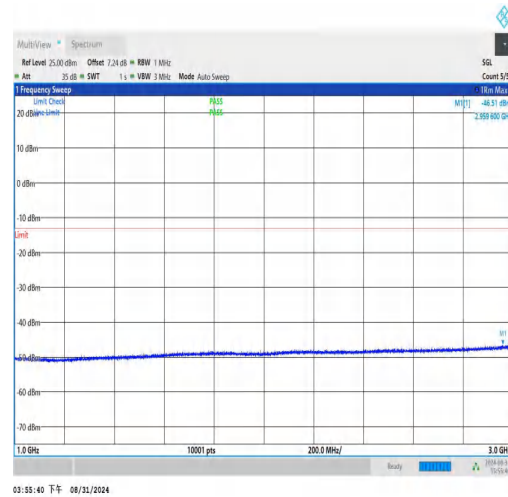
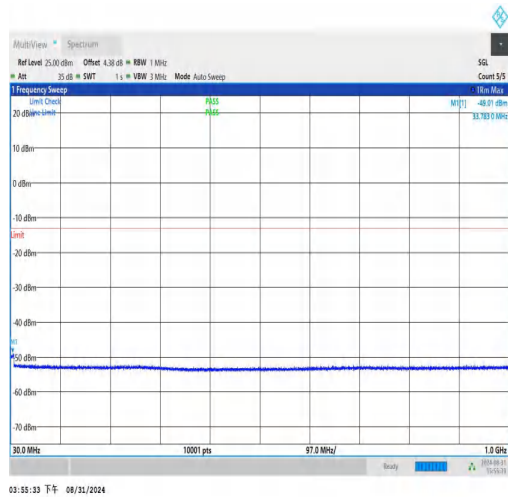
NTNV_N77-3700-3980_PC3_30_50_L_TID1_NS_01_1000_3000_#1

NTNV_N77-3700-3980_PC3_30_50_L_TID1_NS_01_3000_6000_#1



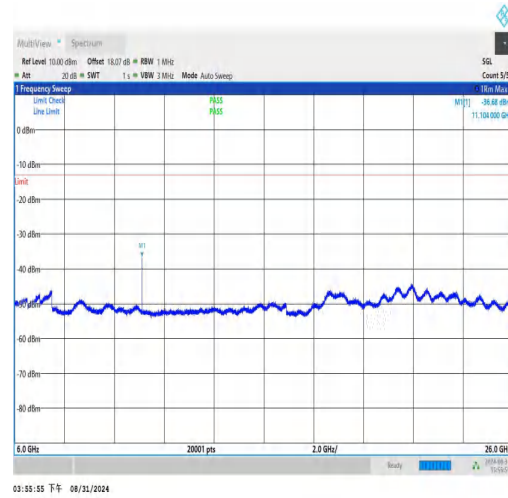
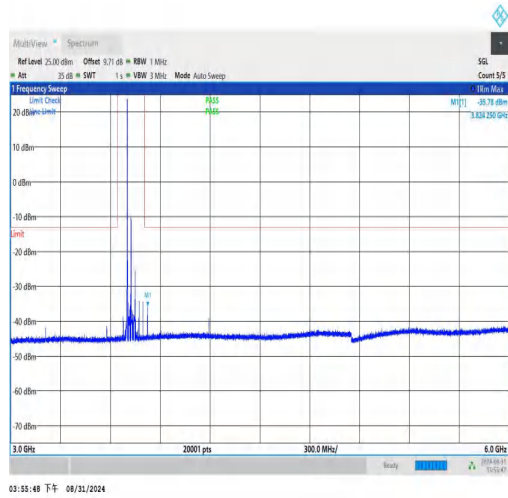
NTNV_N77-3700-3980_PC3_30_50_L_TID1_NS_01_6000_26000_#1

NTNV_N77-3700-3980_PC3_30_50_L_TID1_NS_01_26000_40000_#1



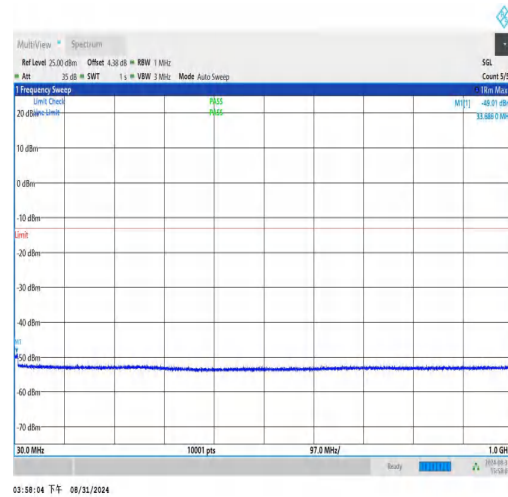
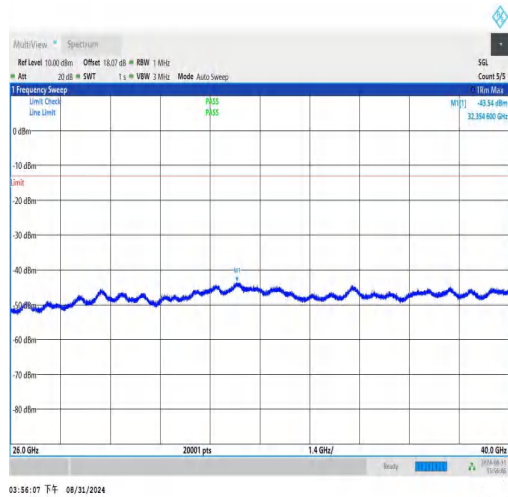
NTNV_N77-3700-3980_PC3_30_50_L_TID2_NS_01_30_1000_#1

NTNV_N77-3700-3980_PC3_30_50_L_TID2_NS_01_1000_3000_#1



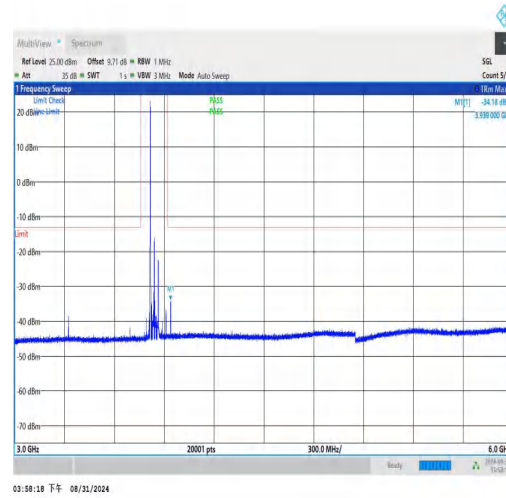
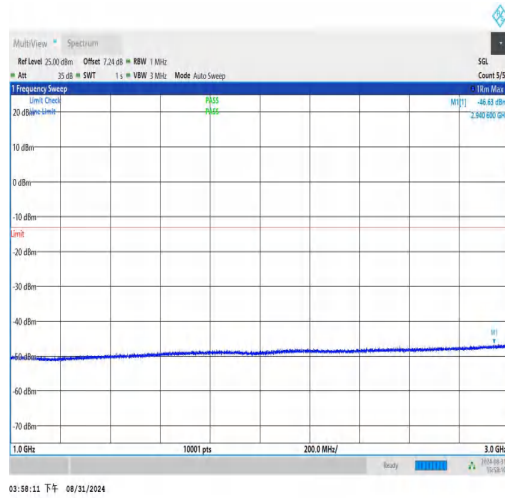
NTNV_N77-3700-3980_PC3_30_50_L_TID2_NS_01_3000_6000_#1

NTNV_N77-3700-3980_PC3_30_50_L_TID2_NS_01_6000_26000_#1



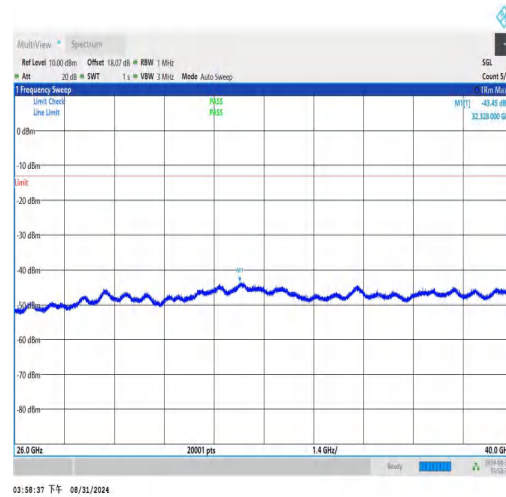
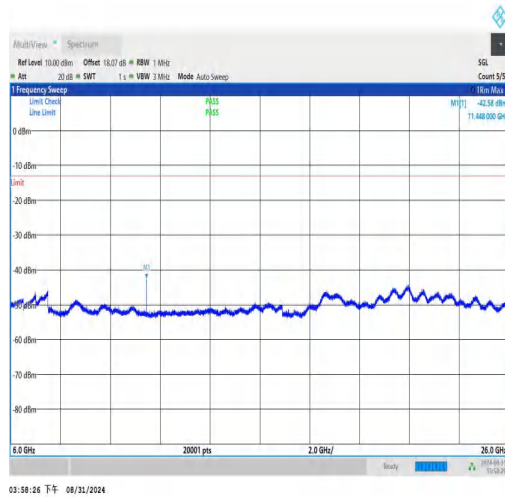
NTNV_N77-3700-3980_PC3_30_50_L_TID2_NS_01_26000_40000_#1

NTNV_N77-3700-3980_PC3_30_50_M_TID2_NS_01_30_1000_#1



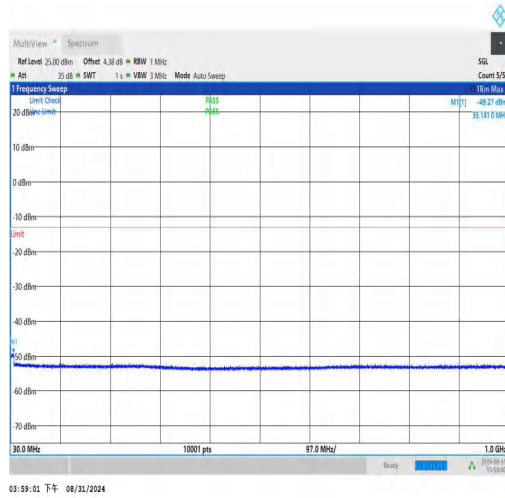
NTNV_N77-3700-3980_PC3_30_50_M_TID2_NS
_01_1000_3000_#1

NTNV_N77-3700-3980_PC3_30_50_M_TID2_NS
_01_3000_6000_#1

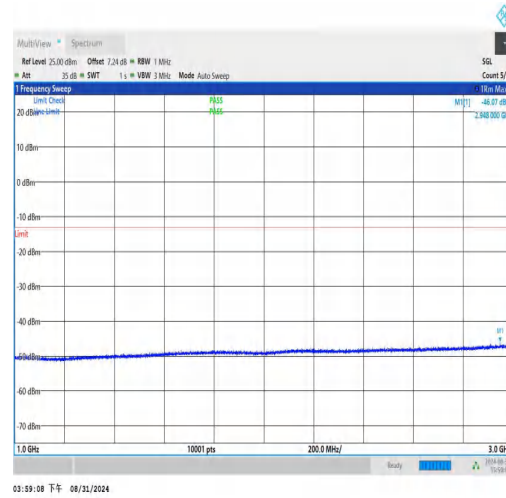


NTNV_N77-3700-3980_PC3_30_50_M_TID2_NS
_01_6000_26000_#1

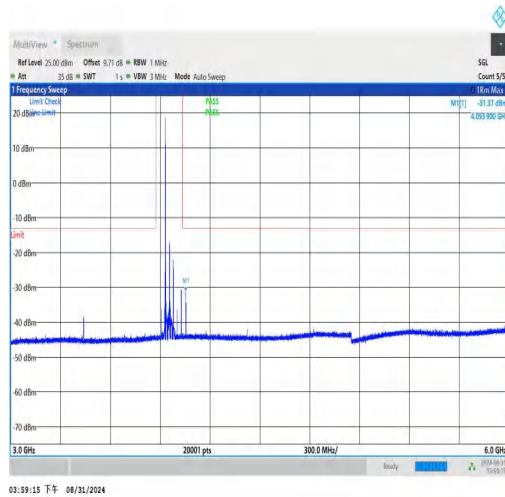
NTNV_N77-3700-3980_PC3_30_50_M_TID2_NS
_01_26000_40000_#1



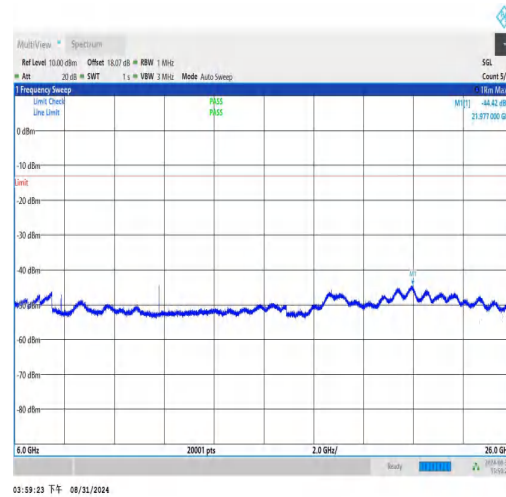
NTNV_N77-3700-3980_PC3_30_50_H_TID1_NS
_01_30_1000_#1



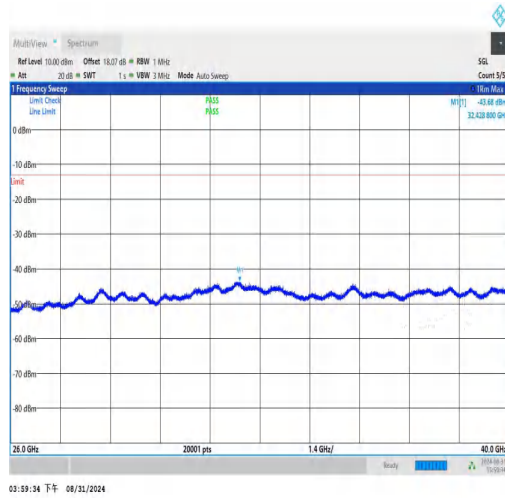
NTNV_N77-3700-3980_PC3_30_50_H_TID1_NS
_01_1000_3000_#1



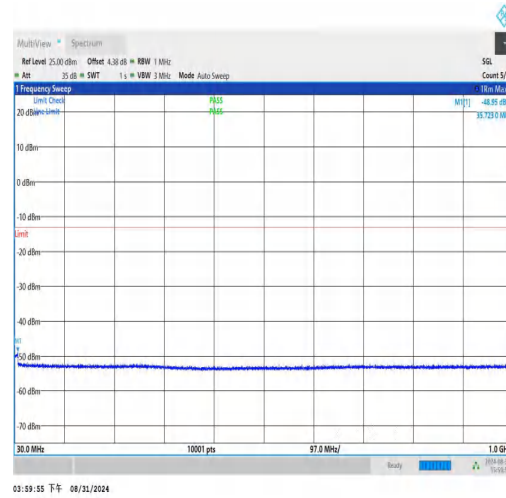
NTNV_N77-3700-3980_PC3_30_50_H_TID1_NS
_01_3000_6000_#1



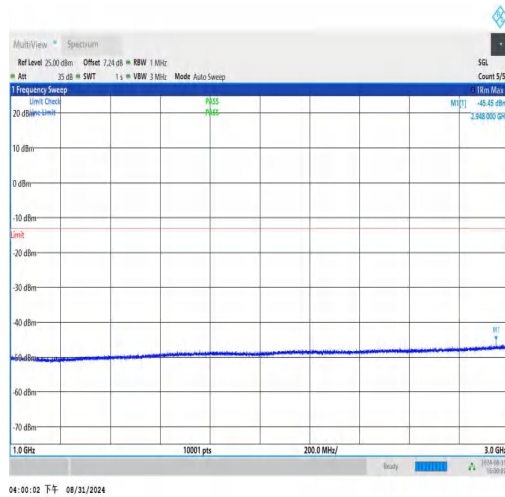
NTNV_N77-3700-3980_PC3_30_50_H_TID1_NS
_01_6000_26000_#1



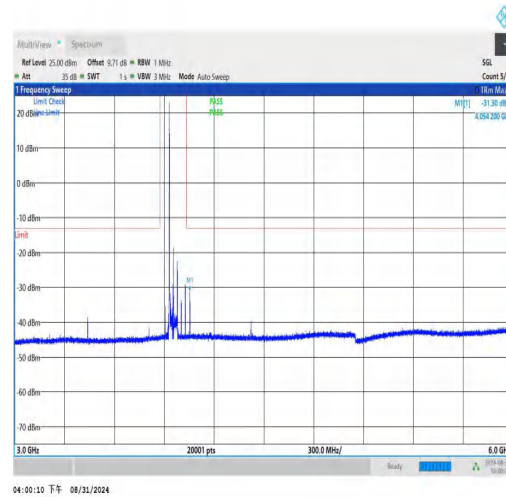
NTNV_N77-3700-3980_PC3_30_50_H_TID1_NS
_01_26000_40000_#1



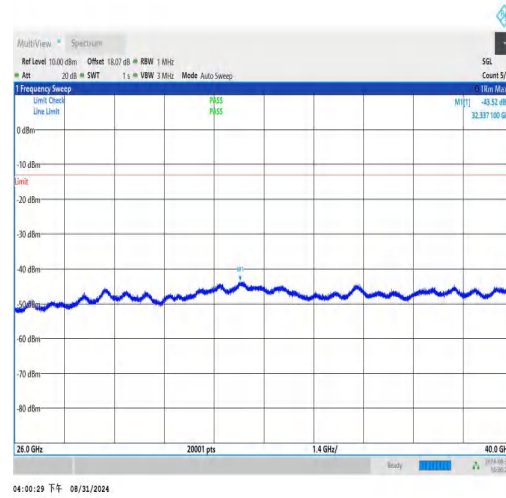
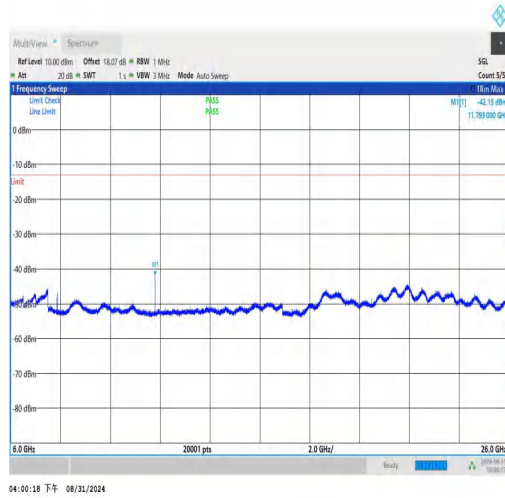
NTNV_N77-3700-3980_PC3_30_50_H_TID2_NS
_01_30_1000_#1



NTNV_N77-3700-3980_PC3_30_50_H_TID2_NS
_01_1000_3000_#1

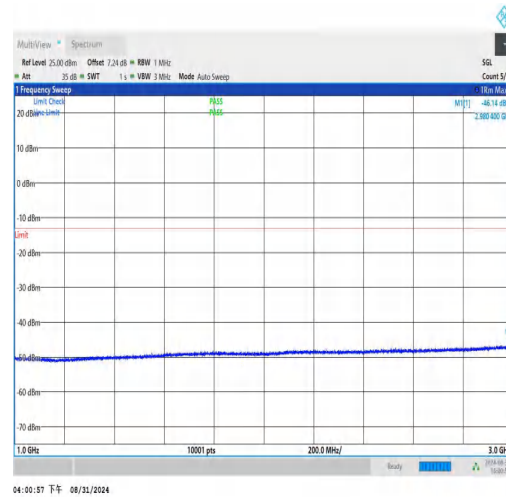
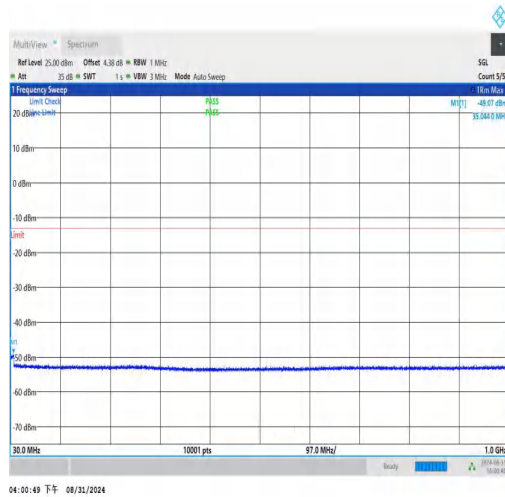


NTNV_N77-3700-3980_PC3_30_50_H_TID2_NS
_01_3000_6000_#1



NTNV_N77-3700-3980_PC3_30_50_H_TID2_NS
_01_6000_26000_#1

NTNV_N77-3700-3980_PC3_30_50_H_TID2_NS
_01_26000_40000_#1

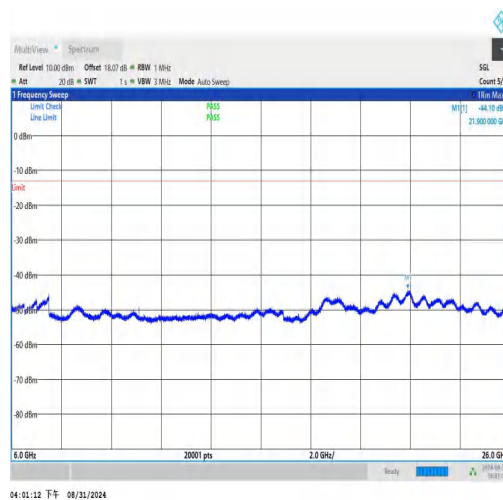


NTNV_N77-3700-3980_PC3_30_100_L_TID1_NS
_01_30_1000_#1

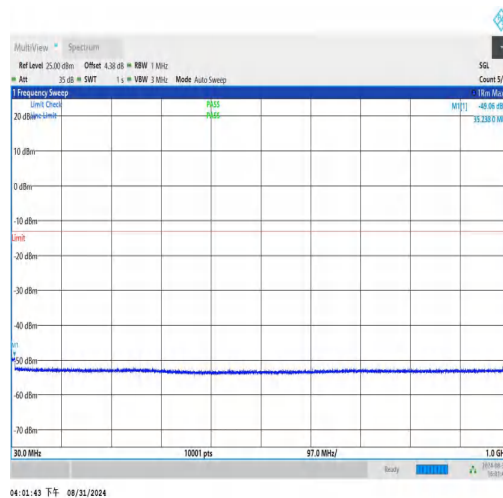
NTNV_N77-3700-3980_PC3_30_100_L_TID1_NS
_01_1000_3000_#1

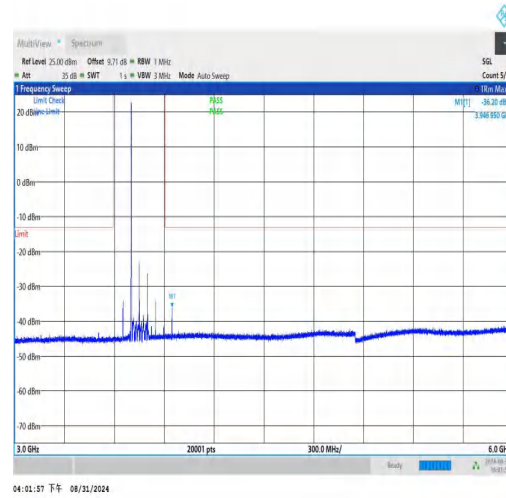
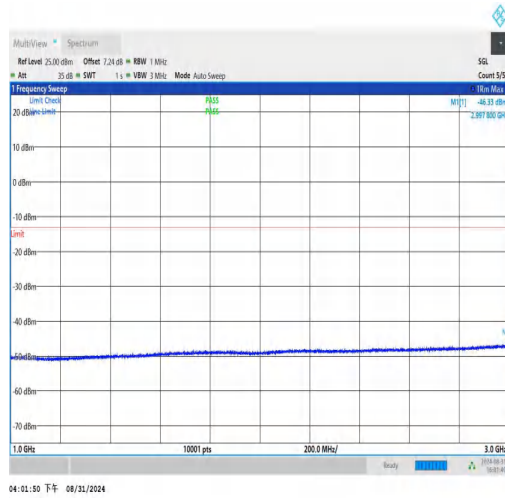


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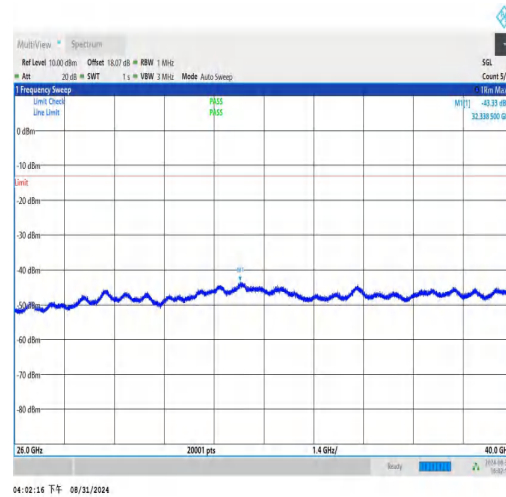
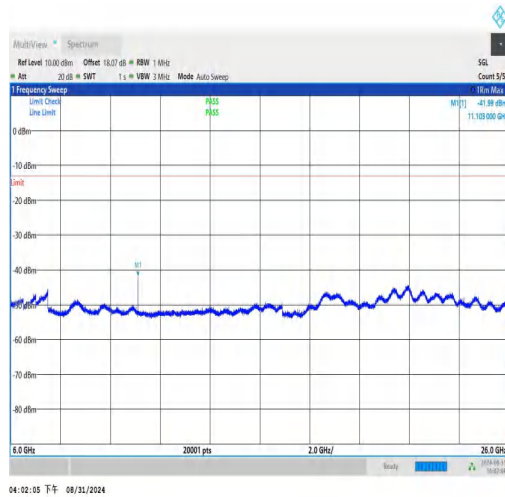
NTNV_N77-3700-3980_PC3_30_100_L_TID1_NS
01 6000 26000 #1

NTNV_N77-3700-3980_PC3_30_100_L_TID2_NS
01 30 1000 #1



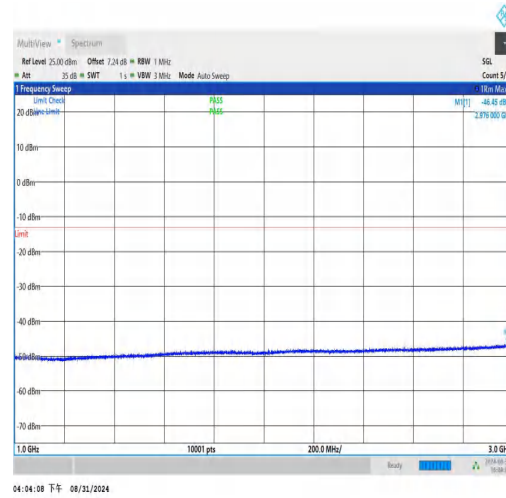
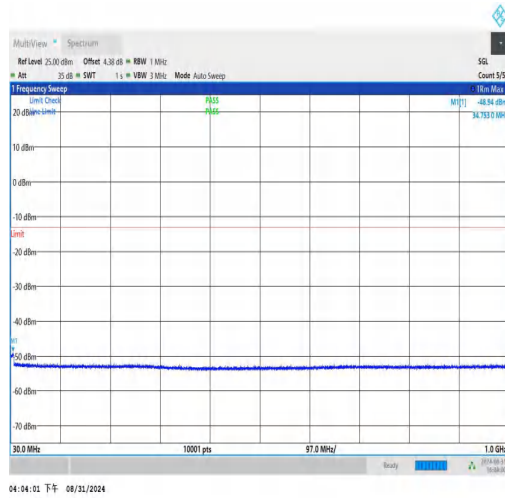
NTNV_N77-3700-3980_PC3_30_100_L_TID2_NS
_01_1000_3000_#1

NTNV_N77-3700-3980_PC3_30_100_L_TID2_NS
_01_3000_6000_#1



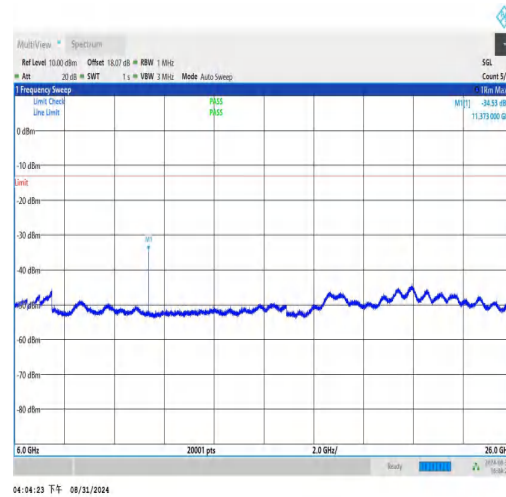
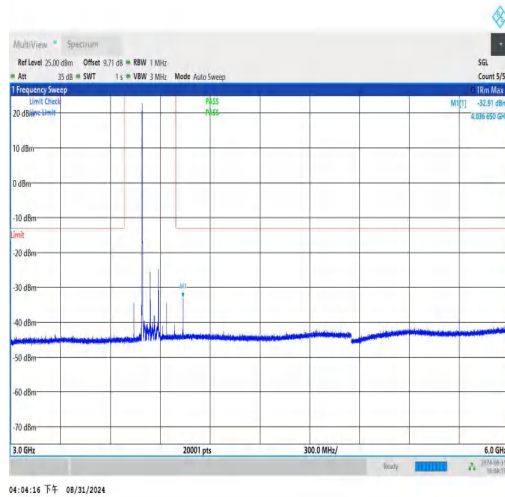
NTNV_N77-3700-3980_PC3_30_100_L_TID2_NS
_01_6000_26000_#1

NTNV_N77-3700-3980_PC3_30_100_L_TID2_NS
_01_26000_40000_#1



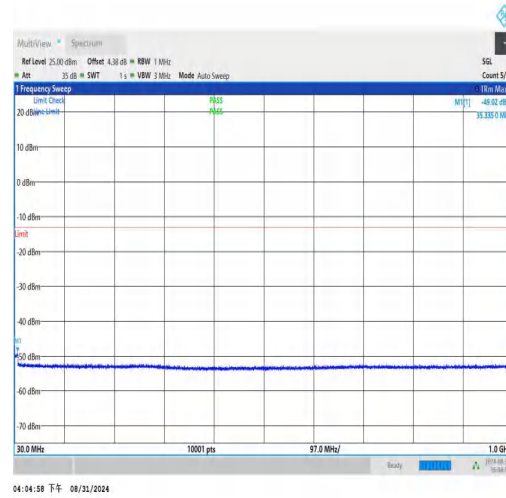
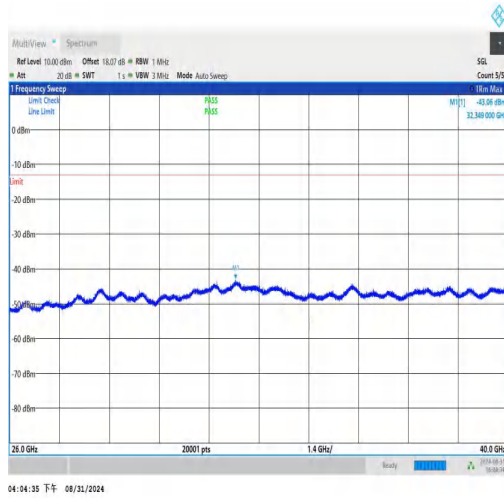
NTNV_N77-3700-3980_PC3_30_100_M_TID2_N
S_01_30_1000_#1

NTNV_N77-3700-3980_PC3_30_100_M_TID2_N
S_01_1000_3000_#1



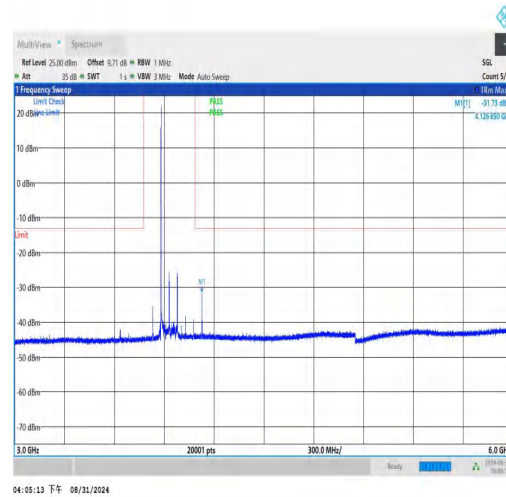
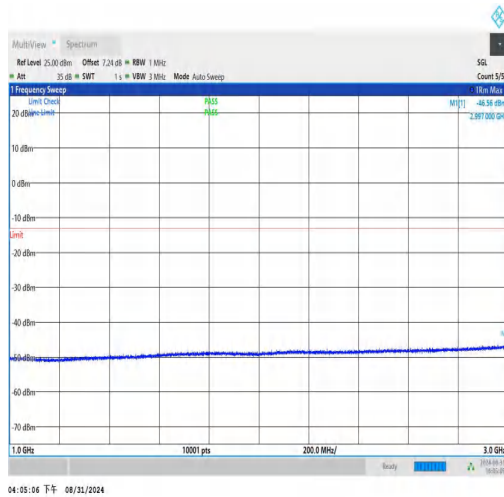
NTNV_N77-3700-3980_PC3_30_100_M_TID2_N
S_01_3000_6000_#1

NTNV_N77-3700-3980_PC3_30_100_M_TID2_N
S_01_6000_26000_#1



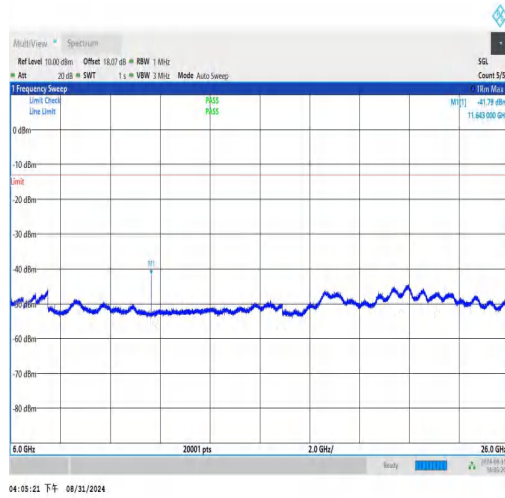
NTNV_N77-3700-3980_PC3_30_100_M_TID2_N
S_01_26000_40000_#1

NTNV_N77-3700-3980_PC3_30_100_H_TID1_N
S_01_30_1000_#1

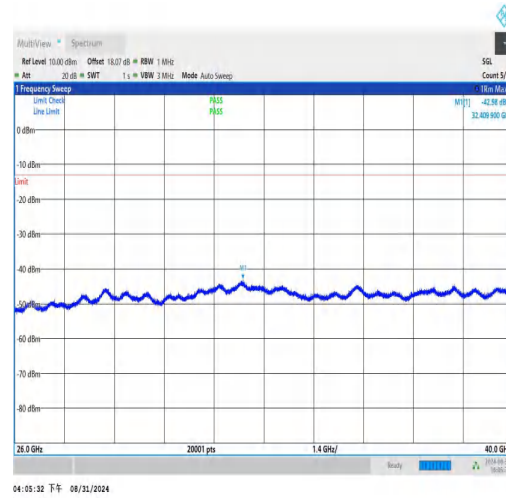


NTNV_N77-3700-3980_PC3_30_100_H_TID1_N
S_01_1000_3000_#1

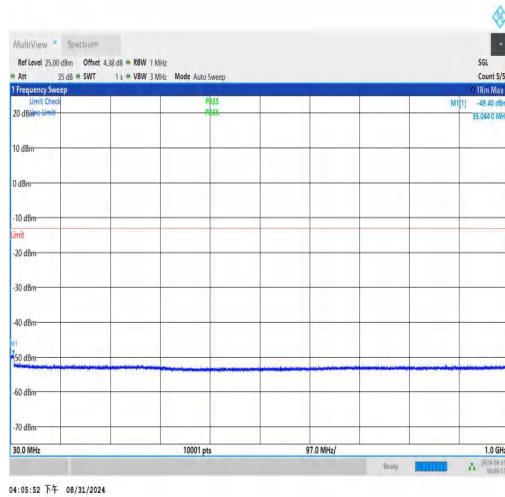
NTNV_N77-3700-3980_PC3_30_100_H_TID1_N
S_01_3000_6000_#1



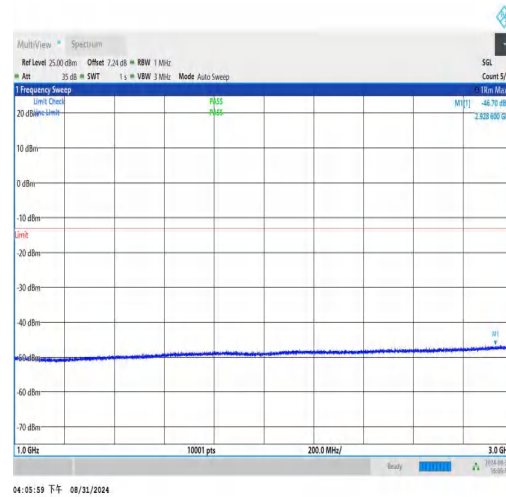
NTNV_N77-3700-3980_PC3_30_100_H_TID1_N
S_01_6000_26000_#1



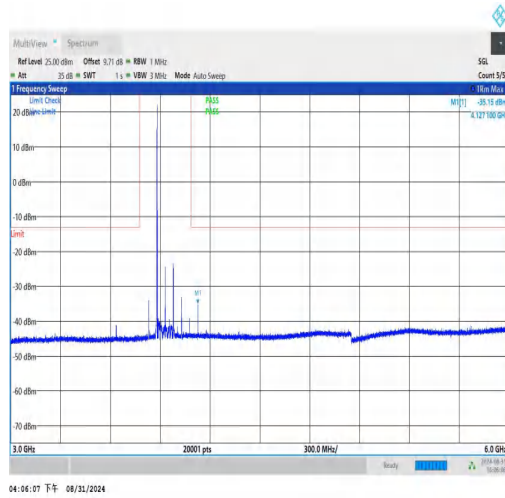
NTNV_N77-3700-3980_PC3_30_100_H_TID1_N
S_01_26000_40000_#1



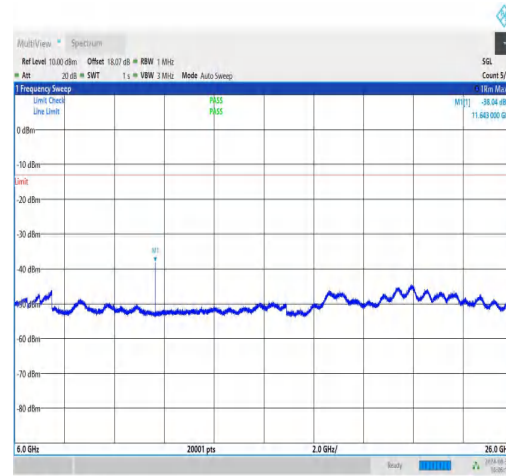
NTNV_N77-3700-3980_PC3_30_100_H_TID2_N
S_01_30_1000_#1



NTNV_N77-3700-3980_PC3_30_100_H_TID2_N
S_01_1000_3000_#1



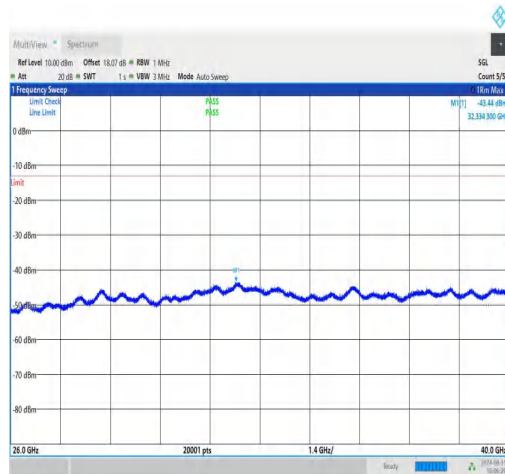
04:04:07 下午 08/31/2024



04:06:14 下午 08/31/2024

NTNV_N77-3700-3980_PC3_30_100_H_TID2_N
S_01_3000_6000_#1

NTNV_N77-3700-3980_PC3_30_100_H_TID2_N
S_01_6000_26000_#1



04:06:26 下午 08/31/2024

NTNV_N77-3700-3980_PC3_30_100_H_TID2_N
S_01_26000_40000_#1



Appendix F: Frequency Stability

Test Result

Frequency Error VS. Voltage

Voltage										
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Voltage	Temperature	Deviation (Hz)	Deviation (ppm)	Verdict
N77-3700-3980	30	100	DFT-PI2BPSK	L	Outer_Full	VN	NT	7.00	0.00	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	L	Outer_Full	VL	NT	17.00	0.00	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	L	Outer_Full	VH	NT	10.00	0.00	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Outer_Full	VN	NT	3.00	0.00	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Outer_Full	VL	NT	9.00	0.00	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Outer_Full	VH	NT	2.00	0.00	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	M	Outer_Full	VN	NT	17.00	0.00	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	M	Outer_Full	VL	NT	8.00	0.00	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	M	Outer_Full	VH	NT	1.00	0.00	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Outer_Full	VN	NT	9.00	0.00	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Outer_Full	VL	NT	11.00	0.00	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Outer_Full	VH	NT	12.00	0.00	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	H	Outer_Full	VN	NT	16.00	0.00	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	H	Outer_Full	VL	NT	7.00	0.00	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	H	Outer_Full	VH	NT	19.00	0.00	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Outer_Full	VN	NT	16.00	0.00	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Outer_Full	VL	NT	6.00	0.00	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Outer_Full	VH	NT	19.00	0.00	PASS

Frequency Error VS. Temperature

Temperature										
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Voltage	Temperature	Deviation (Hz)	Deviation (ppm)	Verdict
N77-3700-3980	30	100	DFT-PI2BPSK	L	Outer_Full	NV	-30	1.000000	0.000267	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	L	Outer_Full	NV	-20	10.000000	0.002667	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	L	Outer_Full	NV	-10	1.000000	0.000267	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	L	Outer_Full	NV	0	1.000000	0.000267	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	L	Outer_Full	NV	10	8.000000	0.002133	PASS



N77-3700-3980	30	100	DFT-PI2BPSK	L	Outer_Full	NV	20	8.000000	0.002133	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	L	Outer_Full	NV	30	11.000000	0.002933	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	L	Outer_Full	NV	40	4.000000	0.001067	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	L	Outer_Full	NV	50	2.000000	0.000533	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Outer_Full	NV	-30	12.000000	0.003200	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Outer_Full	NV	-20	9.000000	0.002400	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Outer_Full	NV	-10	19.000000	0.005067	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Outer_Full	NV	0	19.000000	0.005067	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Outer_Full	NV	10	9.000000	0.002400	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Outer_Full	NV	20	3.000000	0.000800	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Outer_Full	NV	30	6.000000	0.001600	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Outer_Full	NV	40	18.000000	0.004800	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Outer_Full	NV	50	16.000000	0.004267	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	M	Outer_Full	NV	-30	14.000000	0.003646	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	M	Outer_Full	NV	-20	5.000000	0.001302	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	M	Outer_Full	NV	-10	14.000000	0.003646	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	M	Outer_Full	NV	0	8.000000	0.002083	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	M	Outer_Full	NV	10	10.000000	0.002604	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	M	Outer_Full	NV	20	11.000000	0.002865	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	M	Outer_Full	NV	30	13.000000	0.003385	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	M	Outer_Full	NV	40	4.000000	0.001042	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	M	Outer_Full	NV	50	16.000000	0.004167	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Outer_Full	NV	-30	18.000000	0.004688	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Outer_Full	NV	-20	1.000000	0.000260	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Outer_Full	NV	-10	11.000000	0.002865	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Outer_Full	NV	0	8.000000	0.002083	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Outer_Full	NV	10	2.000000	0.000521	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Outer_Full	NV	20	14.000000	0.003646	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Outer_Full	NV	30	5.000000	0.001302	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Outer_Full	NV	40	14.000000	0.003646	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Outer_Full	NV	50	5.000000	0.001302	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	H	Outer_Full	NV	-30	3.000000	0.000763	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	H	Outer_Full	NV	-20	16.000000	0.004071	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	H	Outer_Full	NV	-10	18.000000	0.004580	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	H	Outer_Full	NV	0	12.000000	0.003053	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	H	Outer_Full	NV	10	16.000000	0.004071	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	H	Outer_Full	NV	20	19.000000	0.004835	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	H	Outer_Full	NV	30	9.000000	0.002290	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	H	Outer_Full	NV	40	3.000000	0.000763	PASS



N77-3700-3980	30	100	DFT-PI2BPSK	H	Outer_Full	NV	50	12.000000	0.003053	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Outer_Full	NV	-30	2.000000	0.000509	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Outer_Full	NV	-20	18.000000	0.004580	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Outer_Full	NV	-10	19.000000	0.004835	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Outer_Full	NV	0	3.000000	0.000763	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Outer_Full	NV	10	12.000000	0.003053	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Outer_Full	NV	20	12.000000	0.003053	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Outer_Full	NV	30	3.000000	0.000763	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Outer_Full	NV	40	15.000000	0.003817	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Outer_Full	NV	50	18.000000	0.004580	PASS

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