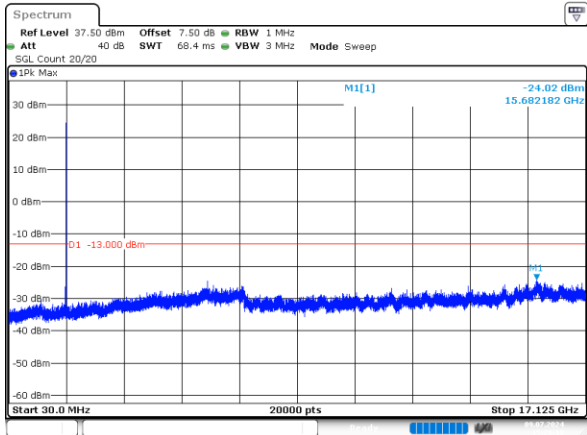
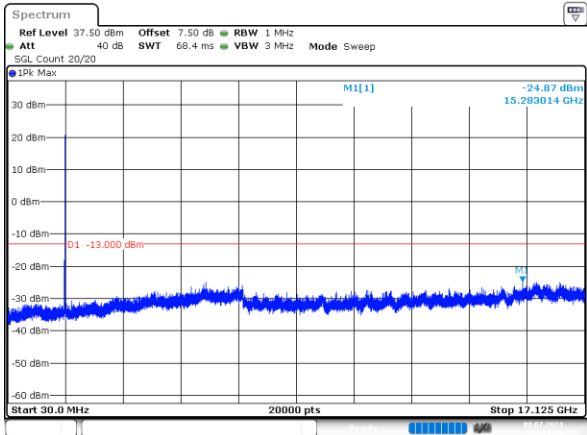


5MHz_Low_QPSK_1@0 -24.02dBm



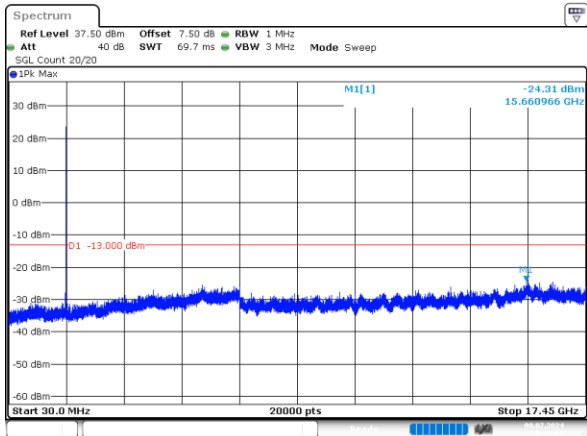
ProjectNo.:2402V84480E-RF Tester:Loge Long
 Date: 9.JUL.2024 15:29:18

5MHz_Low_QPSK_25@0 -24.87dBm



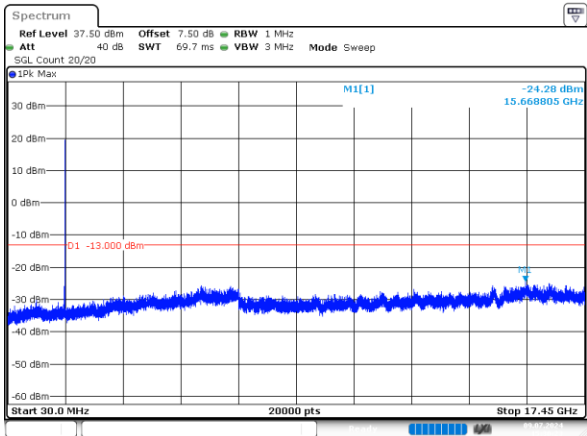
ProjectNo.:2402V84480E-RF Tester:Loge Long
 Date: 9.JUL.2024 15:29:40

5MHz_Middle_QPSK_1@0 -24.31dBm



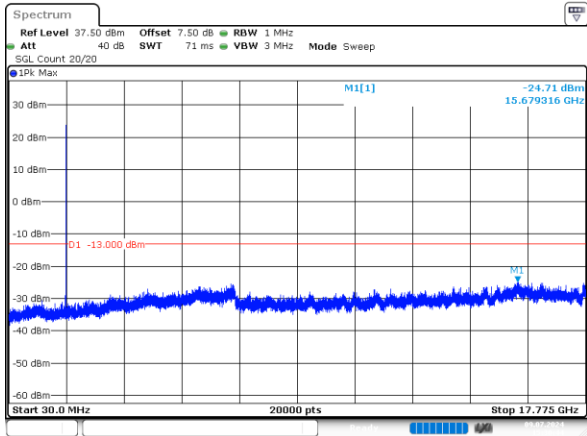
ProjectNo.:2402V84480E-RF Tester:Loge Long
 Date: 9.JUL.2024 15:30:01

5MHz_Middle_QPSK_25@0 -24.28dBm



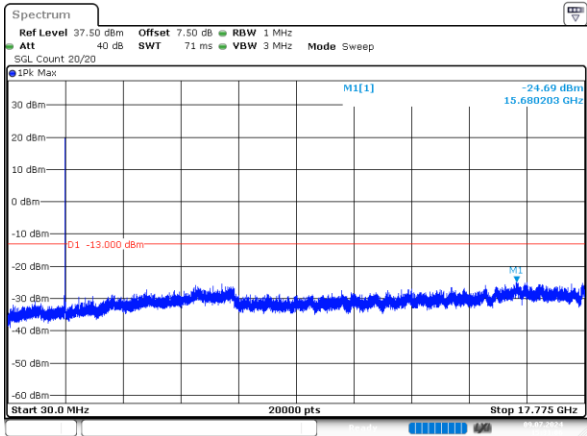
ProjectNo.:2402V84480E-RF Tester:Loge Long
 Date: 9.JUL.2024 15:30:23

5MHz_High_QPSK_1@0 -24.71dBm



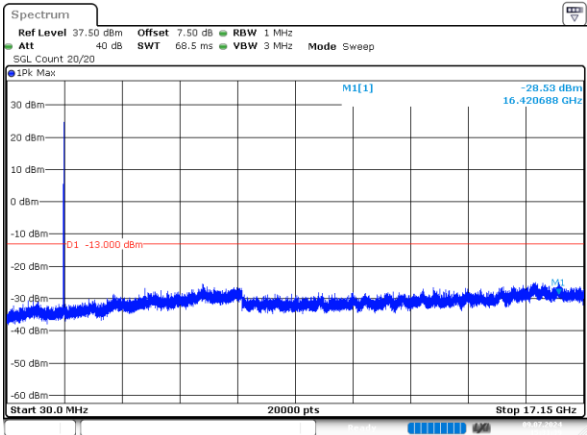
ProjectNo.:2402V84480E-RF Tester:Loge Long
 Date: 9.JUL.2024 15:30:44

5MHz_High_QPSK_25@0 -24.69dBm



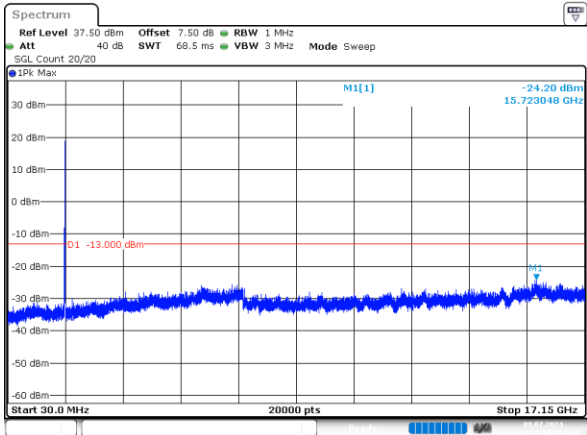
ProjectNo.:2402V84480E-RF Tester:Loge Long
 Date: 9.JUL.2024 15:31:05

10MHz_Low_QPSK_1@0 -28.53dBm



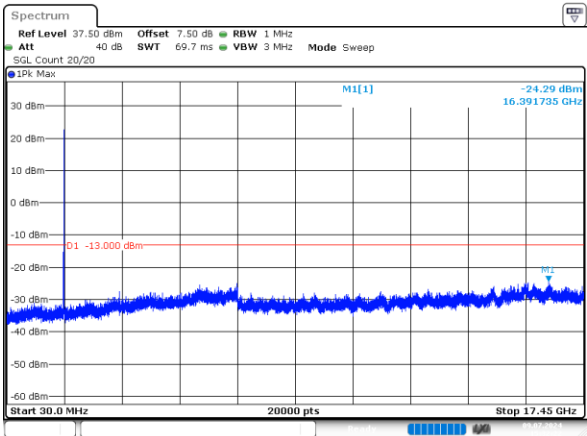
ProjectNo.:2402V84480E-RF Tester:Loge Long
Date: 9.JUL.2024 15:31:39

10MHz_Low_QPSK_50@0 -24.20dBm



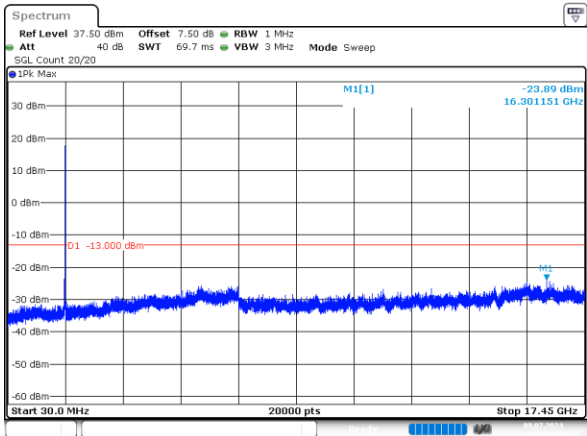
ProjectNo.:2402V84480E-RF Tester:Loge Long
Date: 9.JUL.2024 15:32:01

10MHz_Middle_QPSK_1@0 -24.29dBm



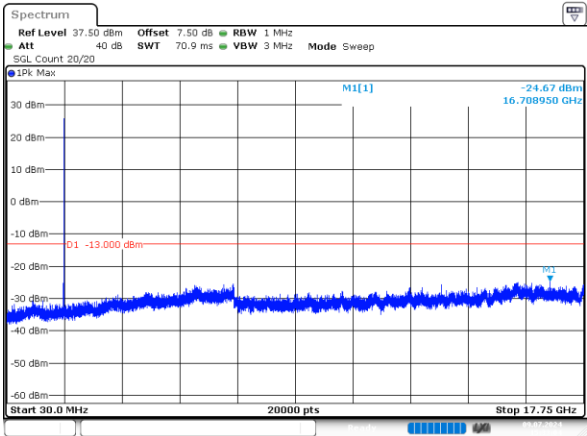
ProjectNo.:2402V84480E-RF Tester:Loge Long
Date: 9.JUL.2024 15:32:22

10MHz_Middle_QPSK_50@0 -23.89dBm



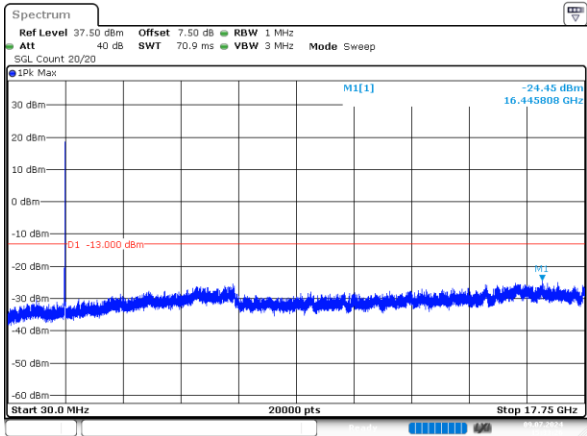
ProjectNo.:2402V84480E-RF Tester:Loge Long
Date: 9.JUL.2024 15:32:43

10MHz_High_QPSK_1@0 -24.67dBm



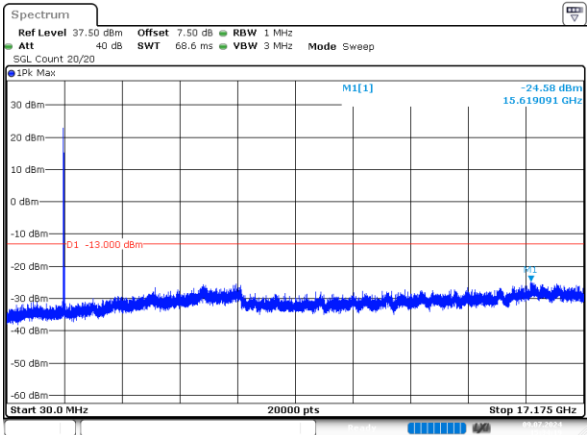
ProjectNo.:2402V84480E-RF Tester:Loge Long
Date: 9.JUL.2024 15:33:04

10MHz_High_QPSK_50@0 -24.45dBm



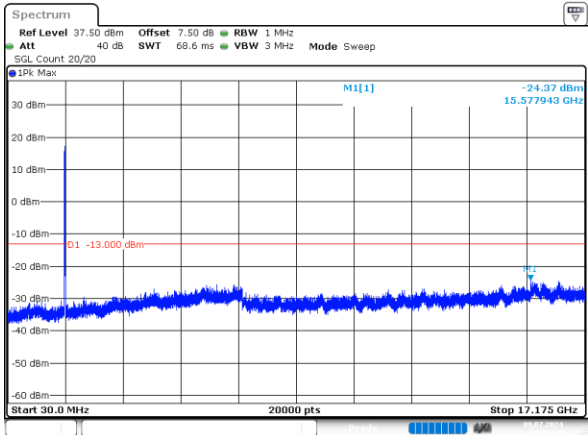
ProjectNo.:2402V84480E-RF Tester:Loge Long
Date: 9.JUL.2024 15:33:26

15MHz_Low_QPSK_1@0 -24.58dBm



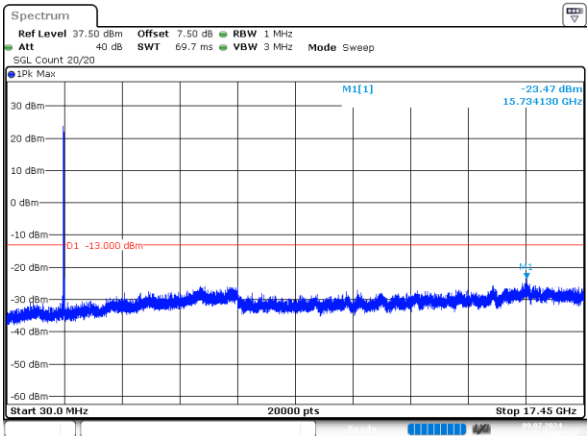
ProjectNo.:2402V84480E-RF Tester:Loge Long
Date: 9.JUL.2024 15:34:19

15MHz_Low_QPSK_75@0 -24.37dBm



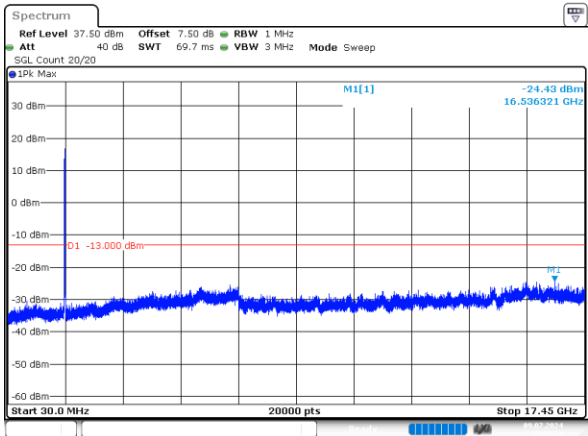
ProjectNo.:2402V84480E-RF Tester:Loge Long
Date: 9.JUL.2024 15:34:40

15MHz_Middle_QPSK_1@0 -23.47dBm



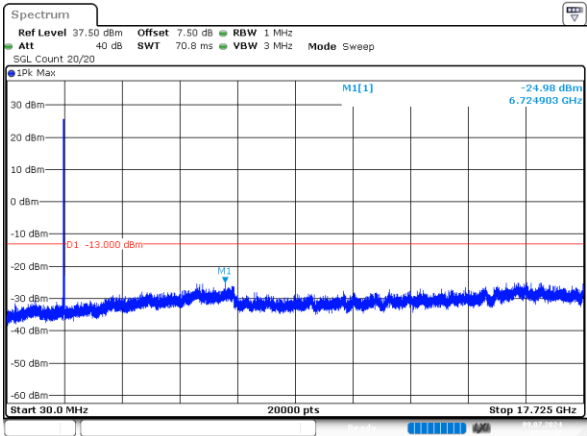
ProjectNo.:2402V84480E-RF Tester:Loge Long
Date: 9.JUL.2024 15:35:02

15MHz_Middle_QPSK_75@0 -24.43dBm



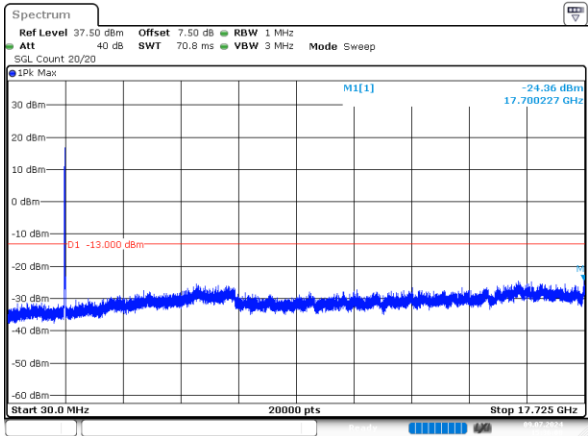
ProjectNo.:2402V84480E-RF Tester:Loge Long
Date: 9.JUL.2024 15:35:24

15MHz_High_QPSK_1@0 -24.98dBm



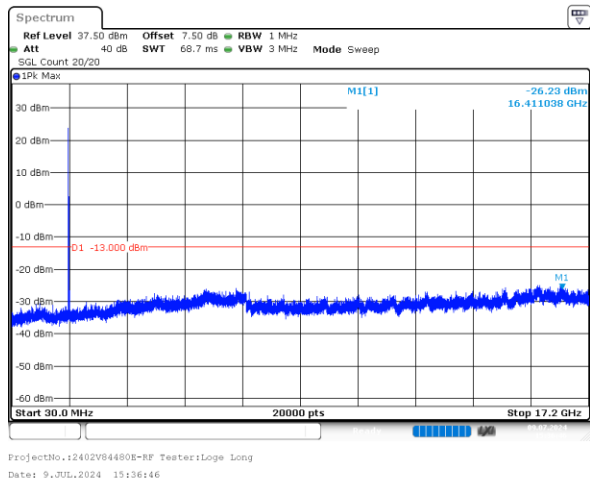
ProjectNo.:2402V84480E-RF Tester:Loge Long
Date: 9.JUL.2024 15:35:46

15MHz_High_QPSK_75@0 -24.36dBm

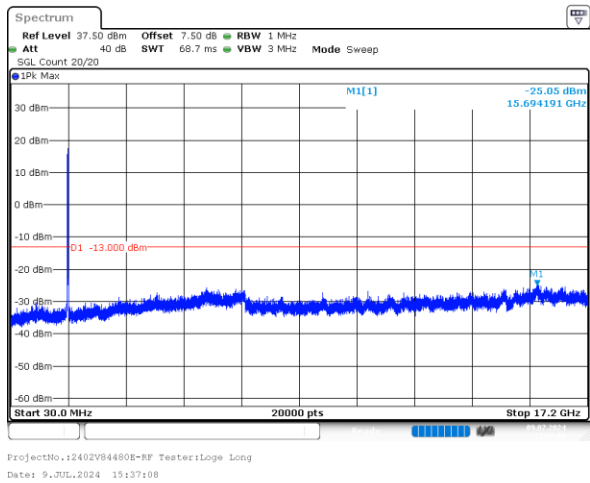


ProjectNo.:2402V84480E-RF Tester:Loge Long
Date: 9.JUL.2024 15:36:08

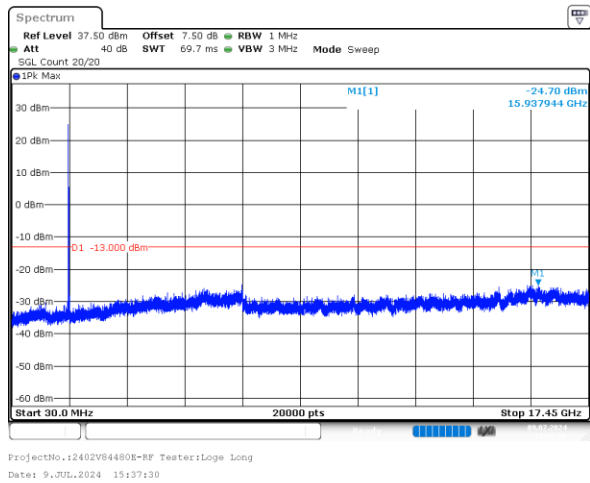
20MHz_Low_QPSK_1@0 -26.23dBm



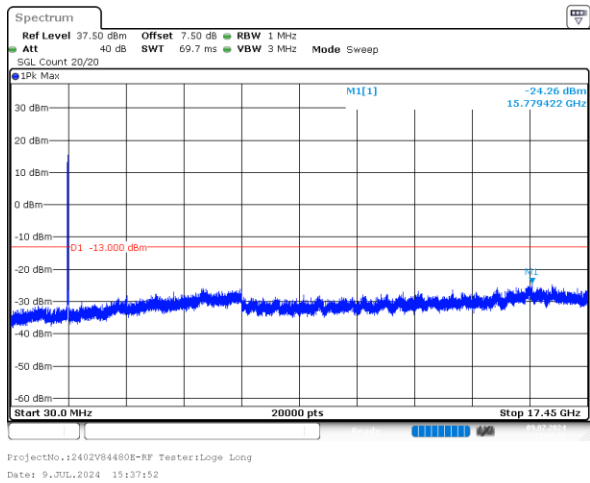
20MHz_Low_QPSK_100@0 -25.05dBm



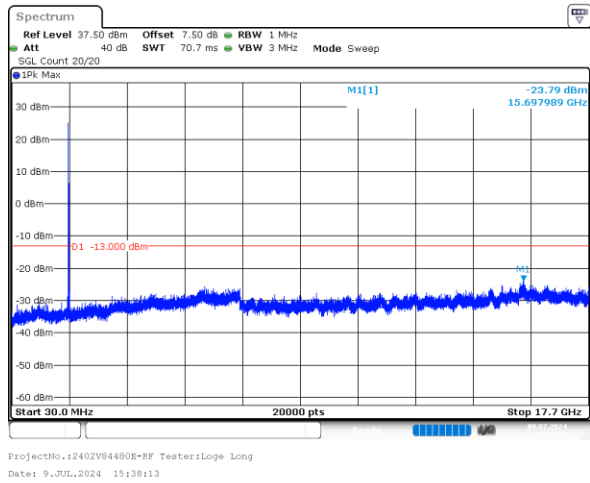
20MHz_Middle_QPSK_1@0 -24.70dBm



20MHz_Middle_QPSK_100@0 -24.26dBm



20MHz_High_QPSK_1@0 -23.79dBm



20MHz_High_QPSK_100@0 -24.43dBm

