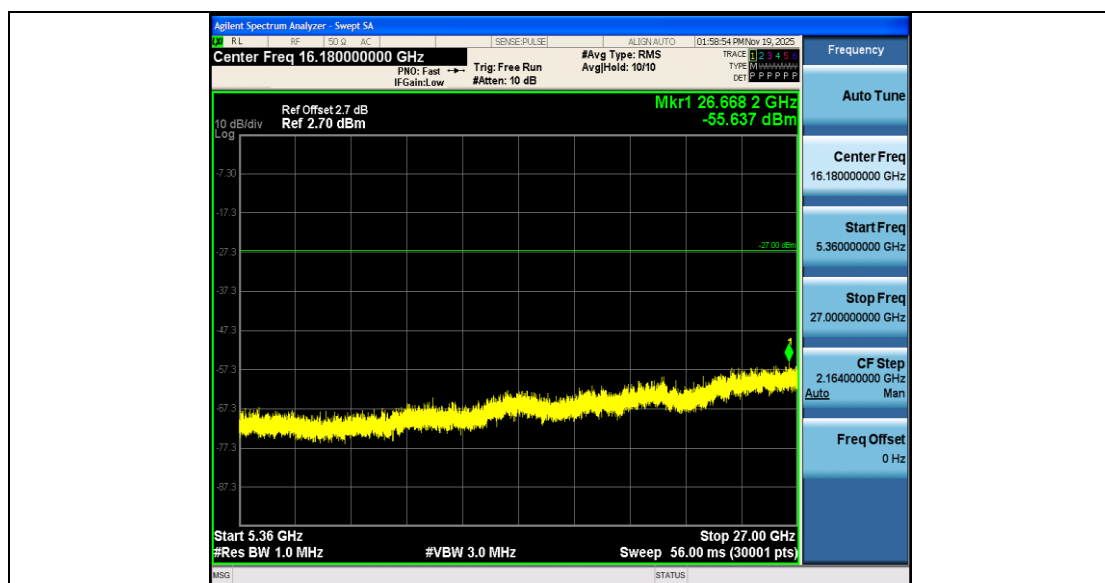
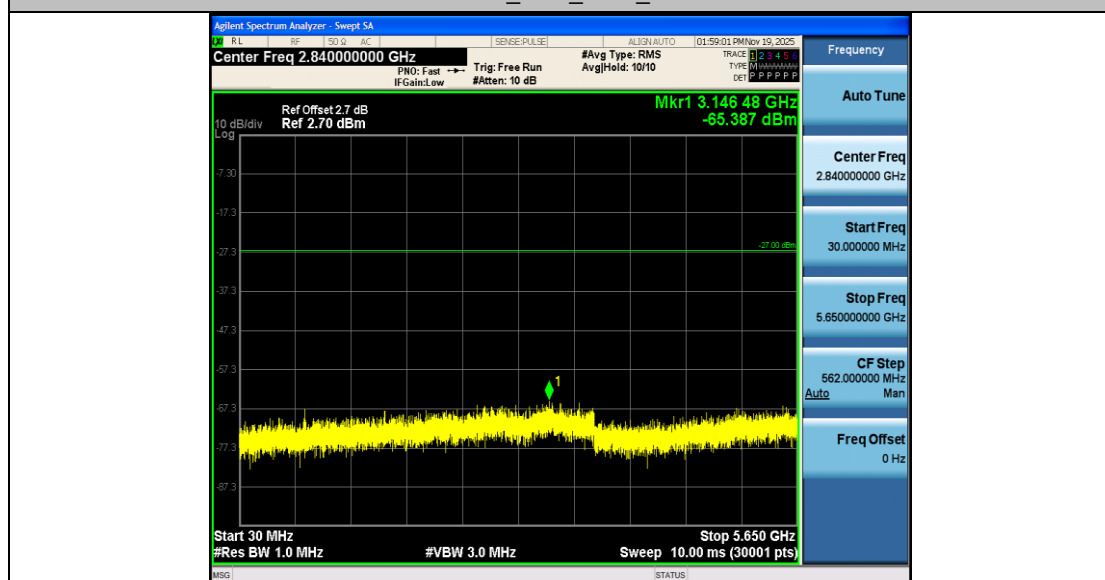
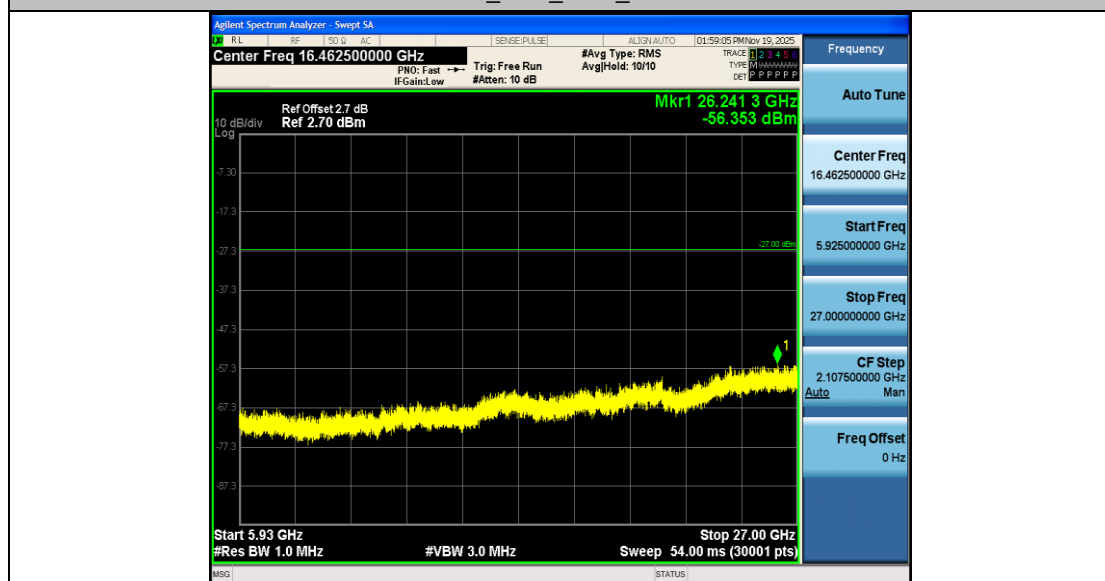




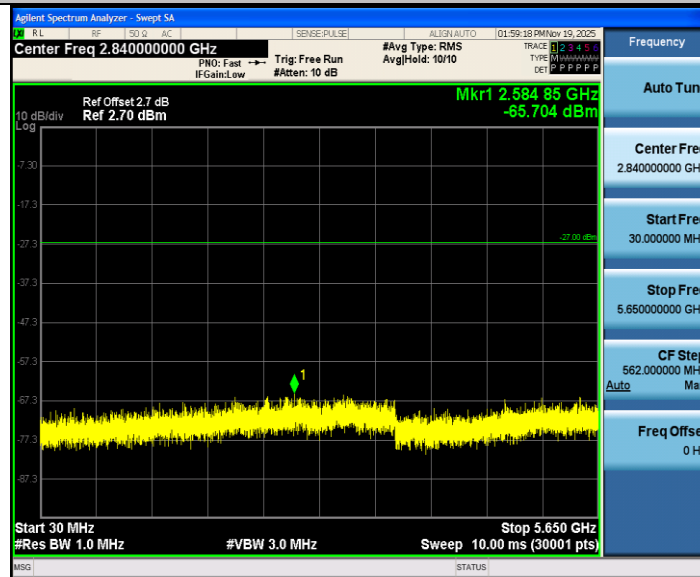
11N40SISO_Ant2_5755_30~5650



11N40SISO_Ant2_5755_5925~40000



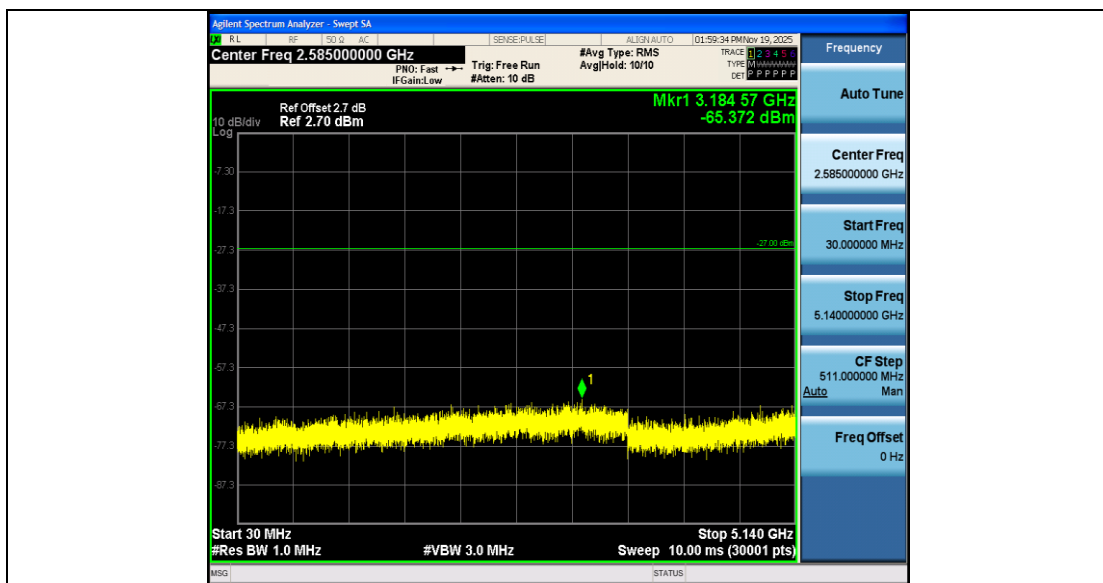
11N40SISO_Ant2_5795_30~5650



11N40SISO_Ant2_5795_5925~40000



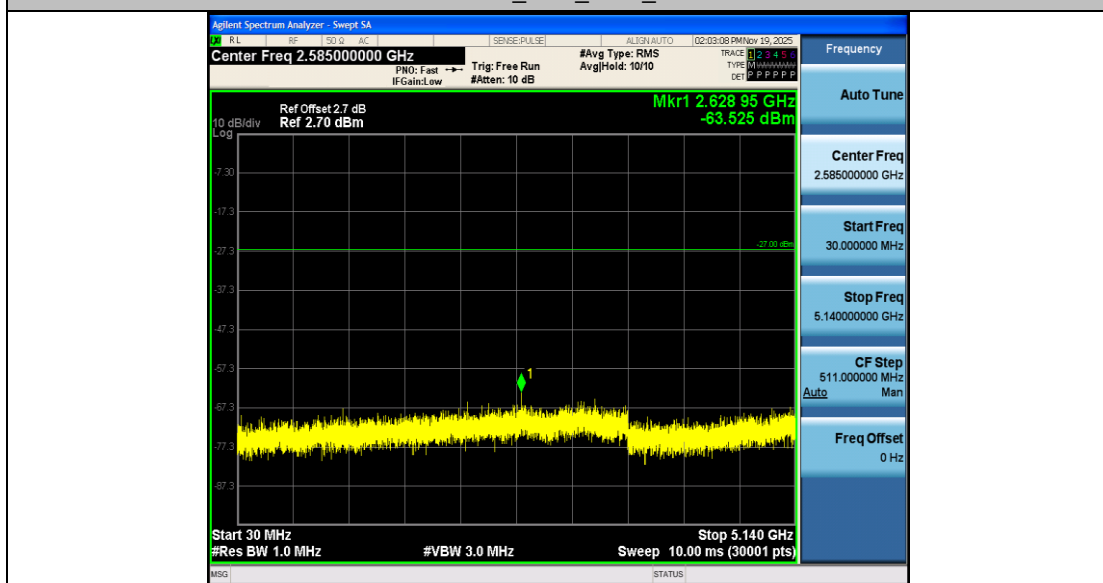
11AC20SISO_Ant2_5180_30~5140



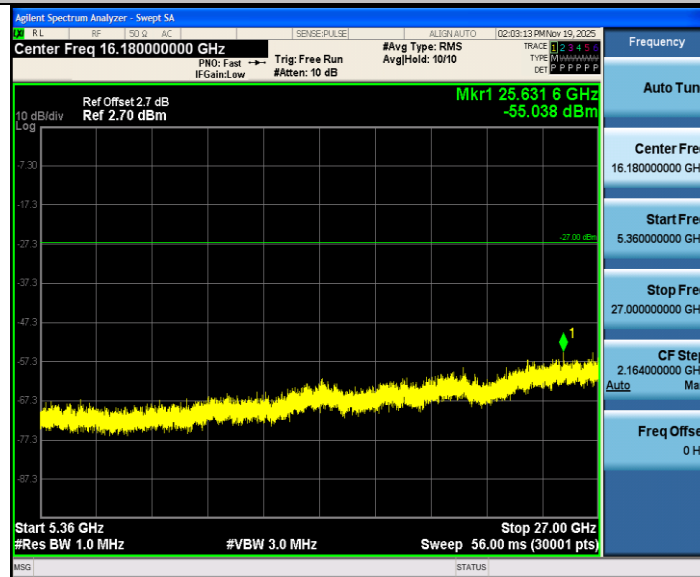
11AC20SISO_Ant2_5180_5360~40000



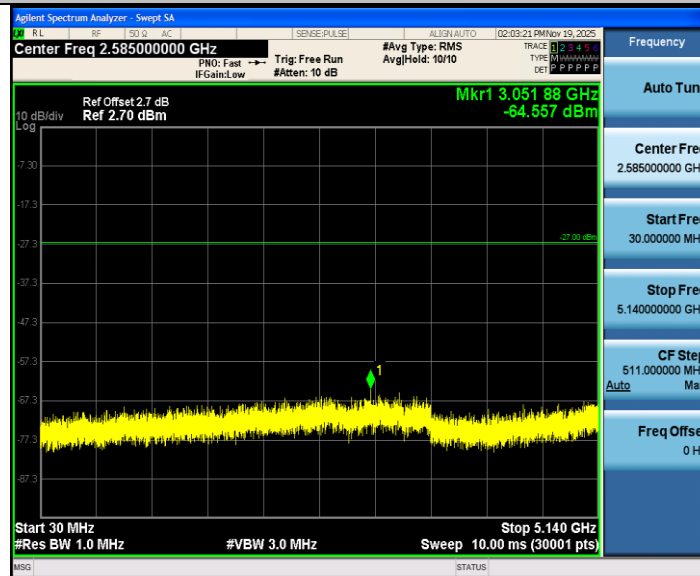
11AC20SISO_Ant2_5220_30~5140



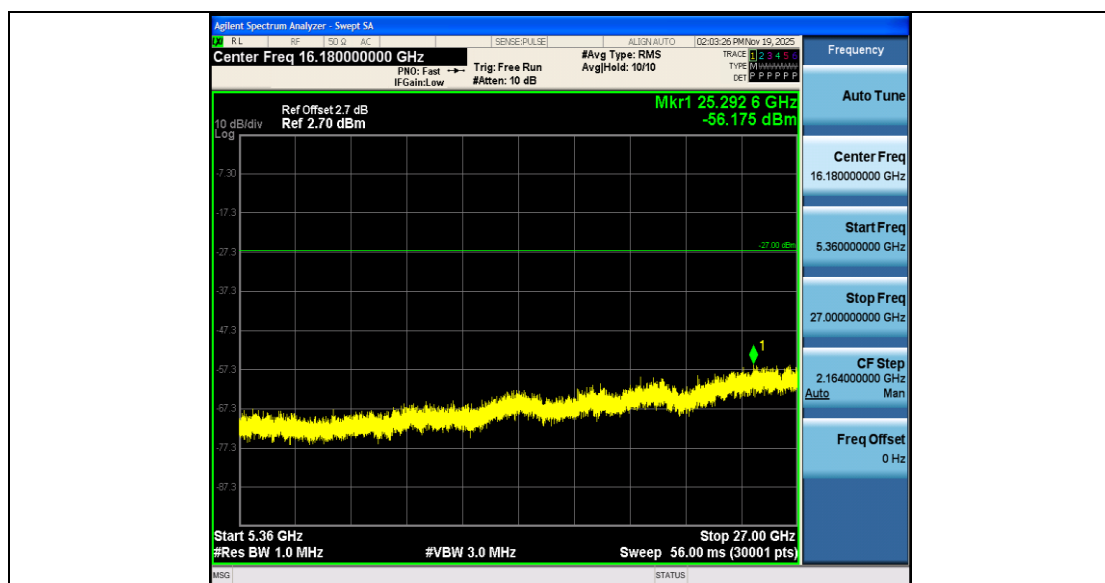
11AC20SISO_Ant2_5220_5360~40000



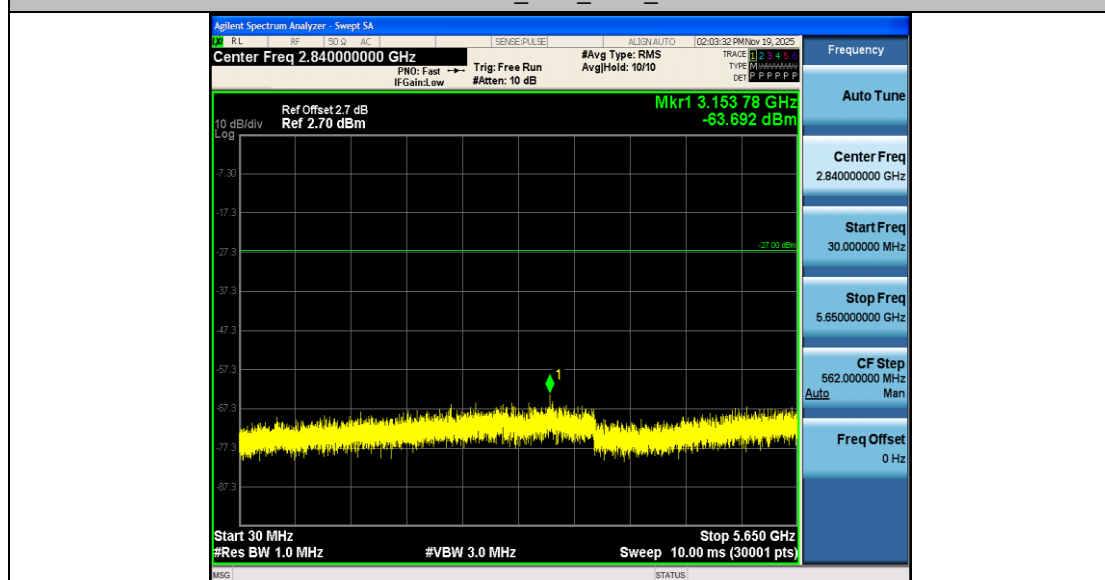
11AC20SISO_Ant2_5240_30~5140



11AC20SISO_Ant2_5240_5360~40000



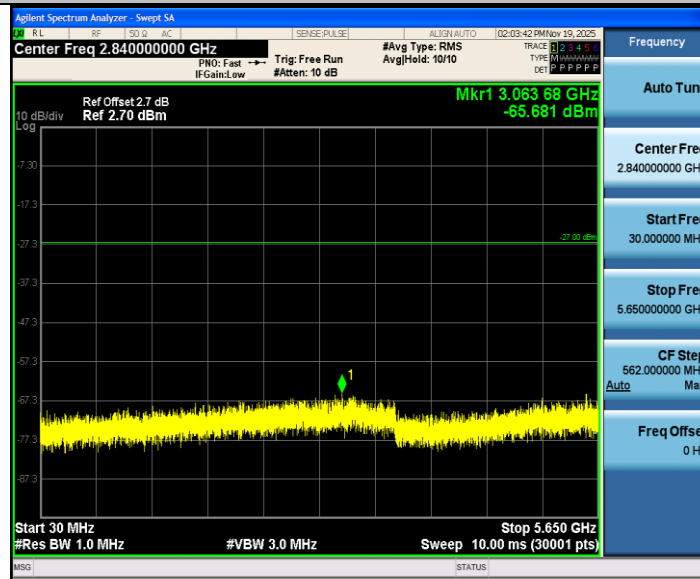
11AC20SISO_Ant2_5745_30~5650



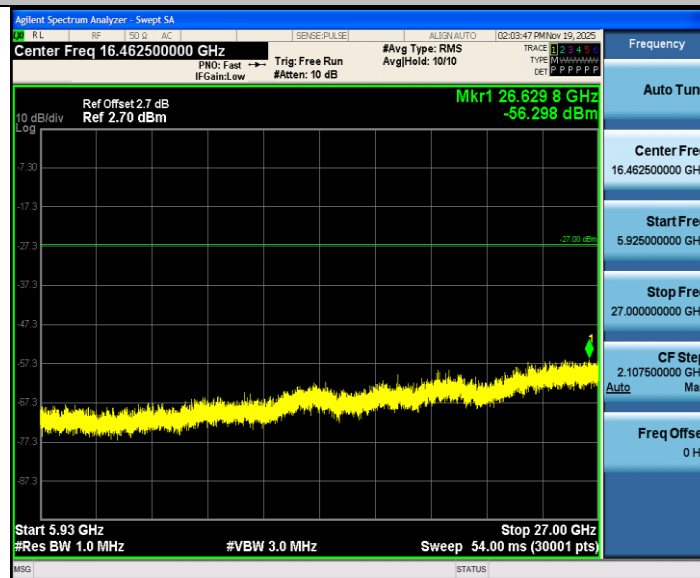
11AC20SISO_Ant2_5745_5925~40000



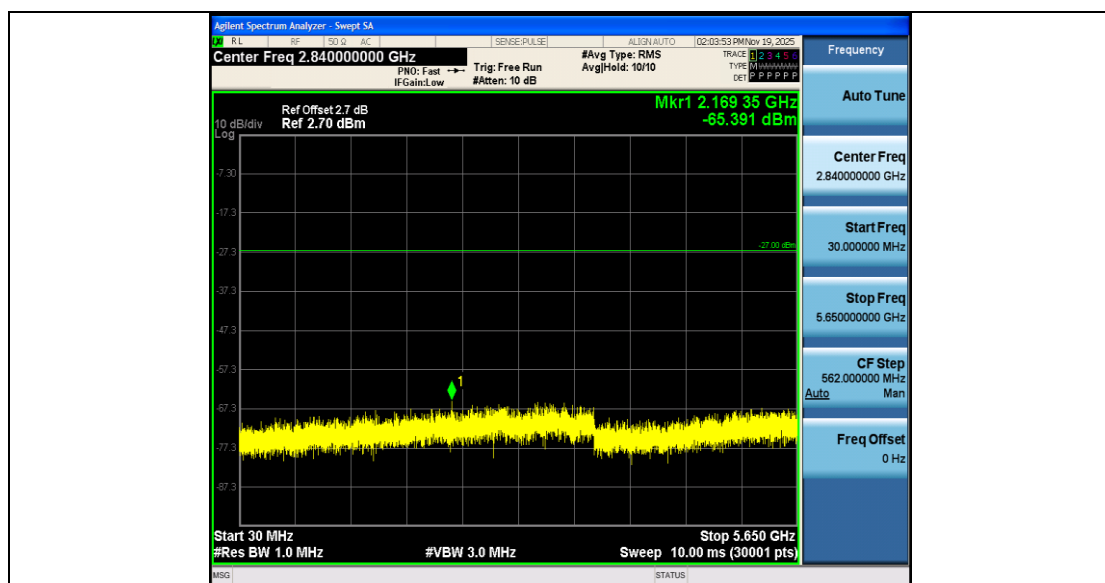
11AC20SISO_Ant2_5785_30~5650



11AC20SISO_Ant2_5785_5925~40000



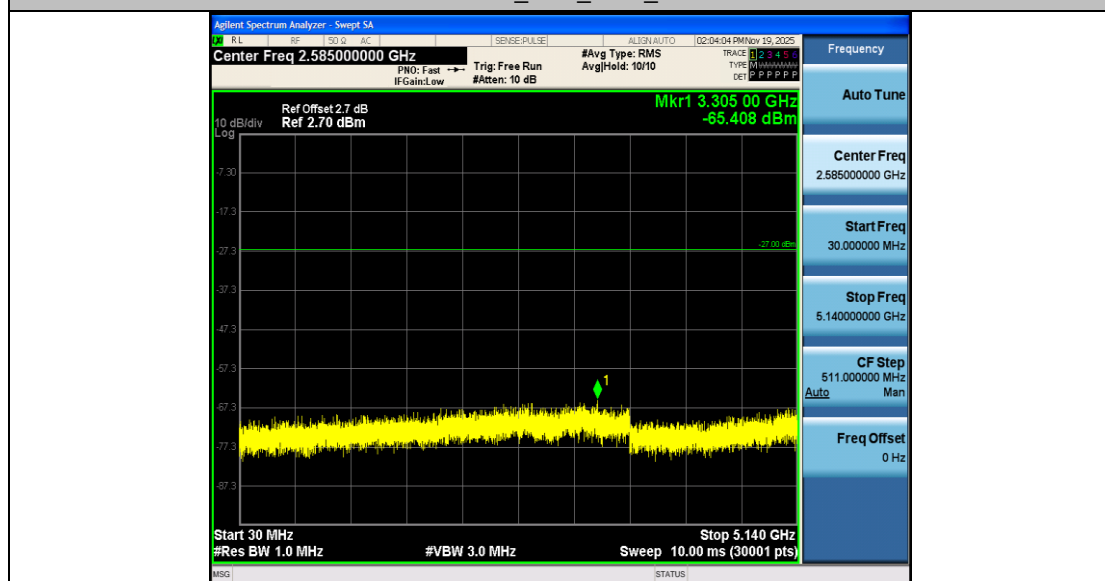
11AC20SISO_Ant2_5825_30~5650



11AC20SISO_Ant2_5825_5925~40000



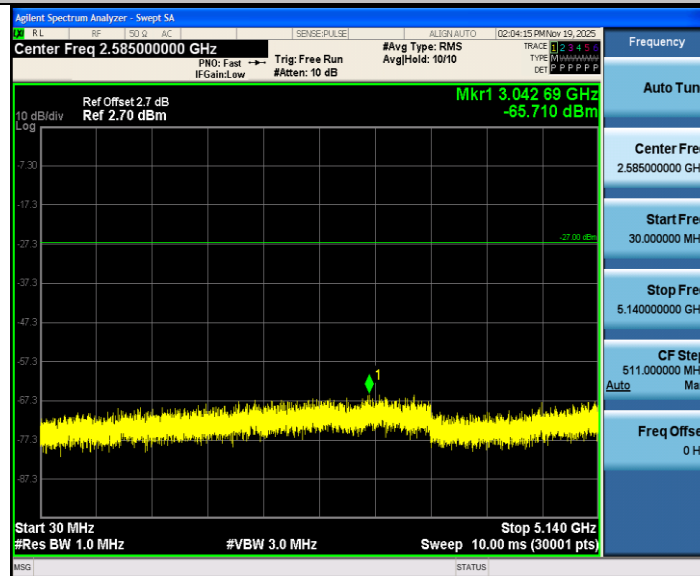
11AC40SISO_Ant2_5190_30~5140



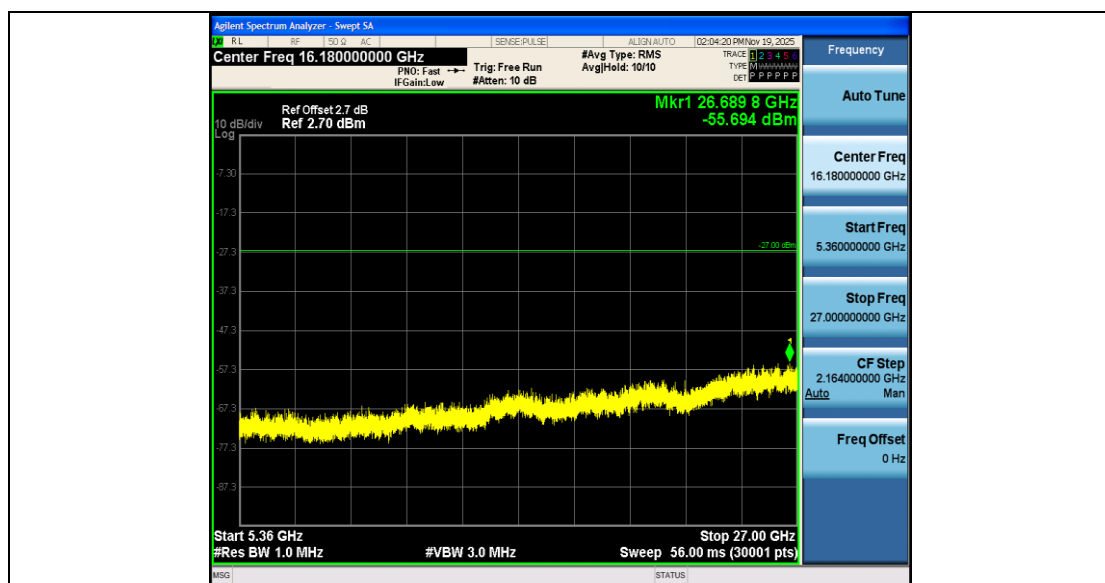
11AC40SISO_Ant2_5190_5360~40000



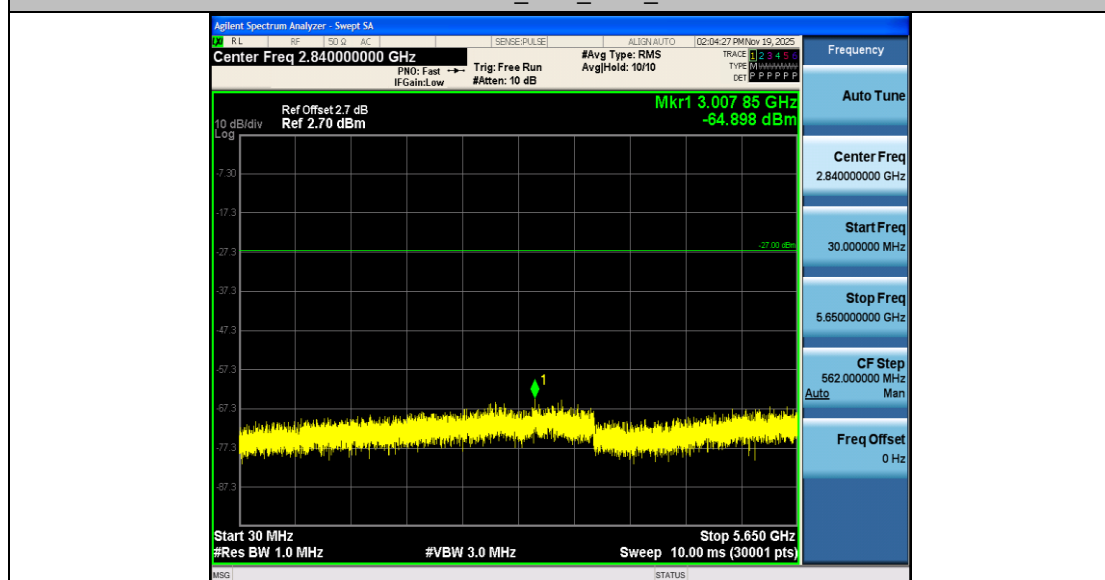
11AC40SISO_Ant2_5230_30~5140



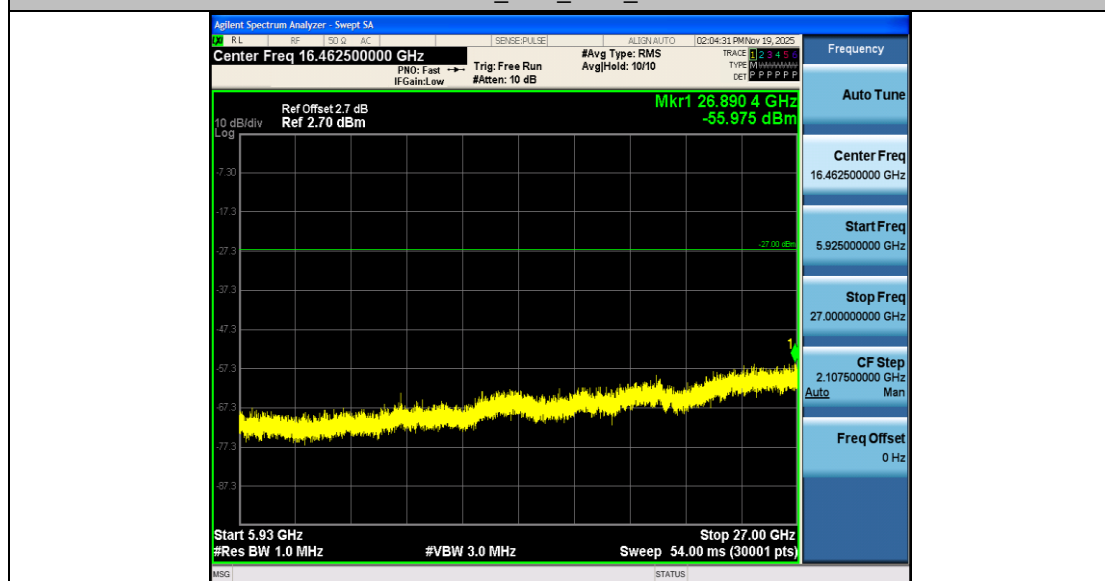
11AC40SISO_Ant2_5230_5360~40000



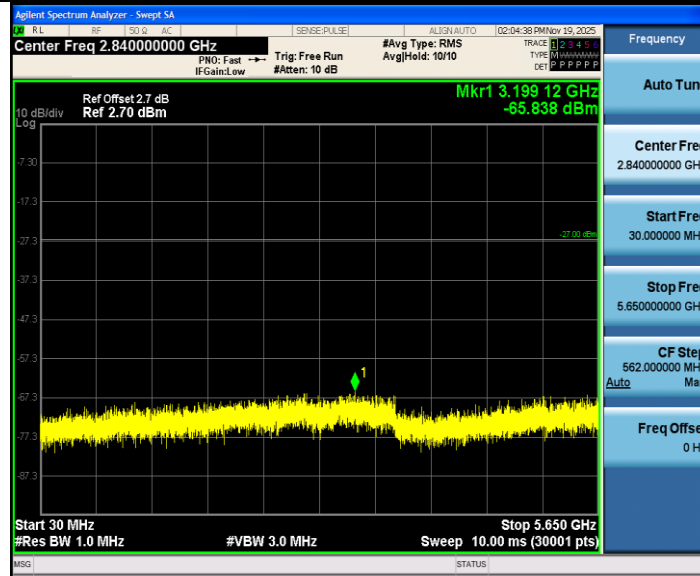
11AC40SISO_Ant2_5755_30~5650



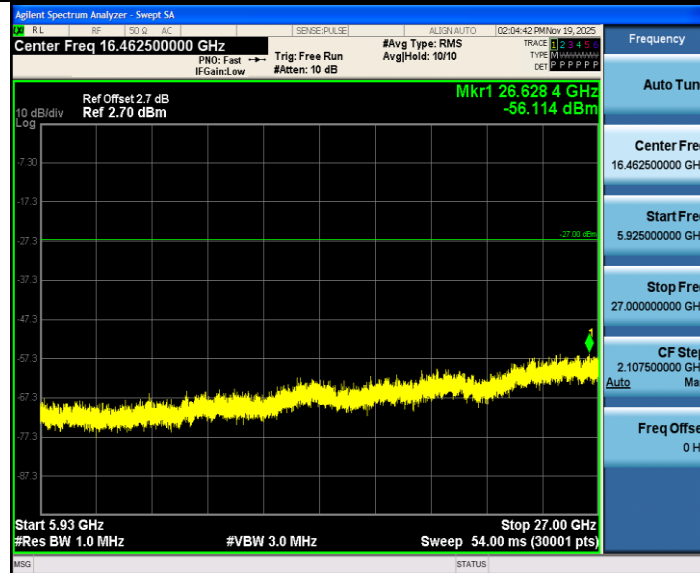
11AC40SISO_Ant2_5755_5925~40000



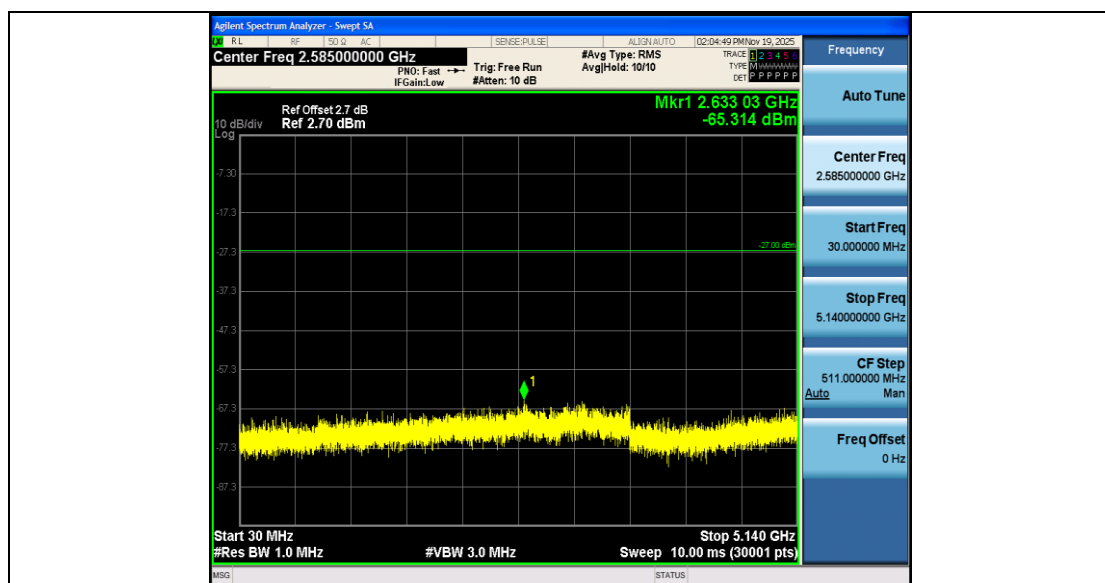
11AC40SISO_Ant2_5795_30~5650



11AC40SISO_Ant2_5795_5925~40000



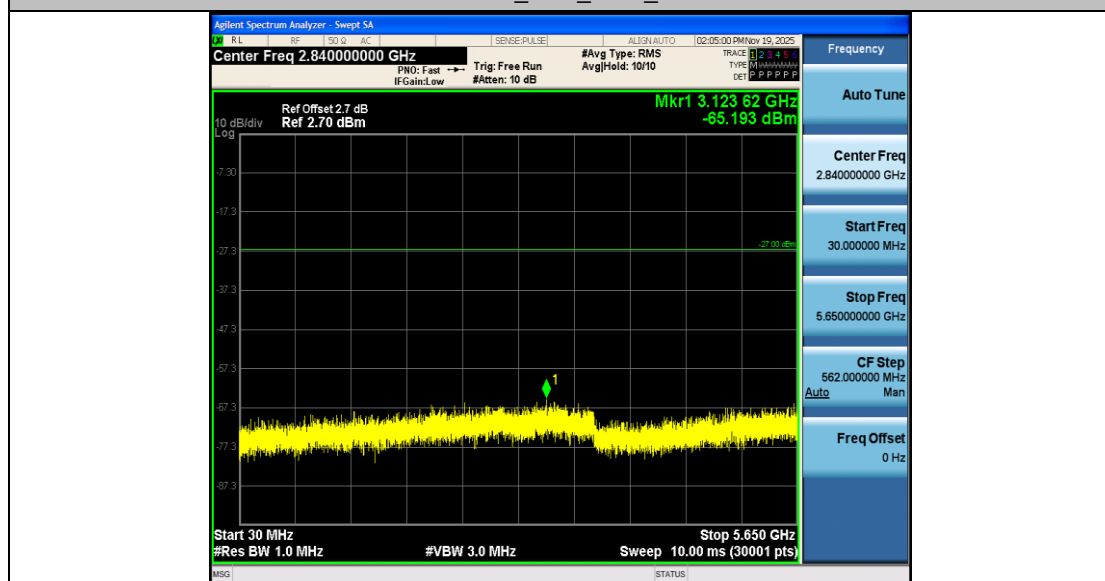
11AC80SISO_Ant2_5210_30~5140



11AC80SISO_Ant2_5210_5360~40000



11AC80SISO_Ant2_5775_30~5650



11AC80SISO_Ant2_5775_5925~40000

