

Appendix for DC_2A_n41A

Product Name: Rugged handheld

Model No: UA60

Appendix A: Average Power Output Data for NSA

Test Result

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_2A_n41A	30	5+20	DFT-QPSK	M+L	Outer_Full	24.94	PC3	PASS
DC_2A_n41A	30	5+20	DFT-QPSK	M+L	Inner_Full	25.00	PC3	PASS
DC_2A_n41A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	24.98	PC3	PASS
DC_2A_n41A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	24.73	PC3	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+L	Outer_Full	23.96	PC3	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+L	Inner_Full	24.77	PC3	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	23.98	PC3	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	24.10	PC3	PASS
DC_2A_n41A	30	5+20	DFT-QPSK	M+M	Outer_Full	24.81	PC3	PASS
DC_2A_n41A	30	5+20	DFT-QPSK	M+M	Inner_Full	24.78	PC3	PASS
DC_2A_n41A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	24.68	PC3	PASS
DC_2A_n41A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	24.77	PC3	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+M	Outer_Full	23.92	PC3	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+M	Inner_Full	24.90	PC3	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	23.96	PC3	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	23.71	PC3	PASS
DC_2A_n41A	30	5+20	DFT-QPSK	M+H	Outer_Full	24.23	PC3	PASS
DC_2A_n41A	30	5+20	DFT-QPSK	M+H	Inner_Full	24.24	PC3	PASS
DC_2A_n41A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	24.37	PC3	PASS
DC_2A_n41A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	24.22	PC3	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+H	Outer_Full	23.23	PC3	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+H	Inner_Full	24.22	PC3	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	23.50	PC3	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	23.32	PC3	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+L	Outer_Full	24.79	PC3	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+L	Inner_Full	24.82	PC3	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	24.88	PC3	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	24.68	PC3	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+L	Outer_Full	23.82	PC3	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+L	Inner_Full	24.82	PC3	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	23.66	PC3	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	23.90	PC3	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+M	Outer_Full	24.87	PC3	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+M	Inner_Full	24.77	PC3	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	24.68	PC3	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	24.78	PC3	PASS

DC_2A_n41A	30	5+30	DFT-16QAM	M+M	Outer_Full	23.88	PC3	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+M	Inner_Full	24.93	PC3	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	23.86	PC3	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	23.82	PC3	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+H	Outer_Full	24.30	PC3	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+H	Inner_Full	24.42	PC3	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	24.38	PC3	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	24.32	PC3	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+H	Outer_Full	23.30	PC3	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+H	Inner_Full	24.35	PC3	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	23.39	PC3	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	23.23	PC3	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+L	Outer_Full	24.78	PC3	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+L	Inner_Full	24.85	PC3	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	24.77	PC3	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	24.60	PC3	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+L	Outer_Full	23.79	PC3	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+L	Inner_Full	24.88	PC3	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	23.87	PC3	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	23.77	PC3	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+M	Outer_Full	24.88	PC3	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+M	Inner_Full	24.82	PC3	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	24.81	PC3	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	24.88	PC3	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+M	Outer_Full	23.90	PC3	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+M	Inner_Full	24.88	PC3	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	23.77	PC3	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	23.97	PC3	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+H	Outer_Full	24.45	PC3	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+H	Inner_Full	24.41	PC3	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	24.62	PC3	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	24.23	PC3	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+H	Outer_Full	23.37	PC3	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+H	Inner_Full	24.36	PC3	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	23.68	PC3	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	23.46	PC3	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+L	Outer_Full	24.71	PC3	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+L	Inner_Full	24.85	PC3	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	24.78	PC3	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	24.72	PC3	PASS
DC_2A_n41A	30	5+50	DFT-16QAM	M+L	Outer_Full	23.70	PC3	PASS
DC_2A_n41A	30	5+50	DFT-16QAM	M+L	Inner_Full	24.82	PC3	PASS

DC_2A_n41A	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	23.73	PC3	PASS
DC_2A_n41A	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	23.65	PC3	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+M	Outer_Full	24.64	PC3	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+M	Inner_Full	24.75	PC3	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	24.35	PC3	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	24.29	PC3	PASS
DC_2A_n41A	30	5+50	DFT-16QAM	M+M	Outer_Full	23.64	PC3	PASS
DC_2A_n41A	30	5+50	DFT-16QAM	M+M	Inner_Full	24.71	PC3	PASS
DC_2A_n41A	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	23.22	PC3	PASS
DC_2A_n41A	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	23.29	PC3	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+H	Outer_Full	24.26	PC3	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+H	Inner_Full	24.40	PC3	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	24.21	PC3	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	23.88	PC3	PASS
DC_2A_n41A	30	5+50	DFT-16QAM	M+H	Outer_Full	23.26	PC3	PASS
DC_2A_n41A	30	5+50	DFT-16QAM	M+H	Inner_Full	24.36	PC3	PASS
DC_2A_n41A	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	23.27	PC3	PASS
DC_2A_n41A	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	22.97	PC3	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+L	Outer_Full	24.73	PC3	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+L	Inner_Full	24.79	PC3	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	24.24	PC3	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	24.18	PC3	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+L	Outer_Full	23.64	PC3	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+L	Inner_Full	24.74	PC3	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	23.29	PC3	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	23.21	PC3	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+M	Outer_Full	24.55	PC3	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+M	Inner_Full	24.66	PC3	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	24.14	PC3	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	24.13	PC3	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+M	Outer_Full	23.54	PC3	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+M	Inner_Full	24.80	PC3	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	23.06	PC3	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	23.06	PC3	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+H	Outer_Full	24.25	PC3	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+H	Inner_Full	24.38	PC3	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	24.10	PC3	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	23.75	PC3	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+H	Outer_Full	23.31	PC3	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+H	Inner_Full	24.39	PC3	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	23.15	PC3	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	22.63	PC3	PASS

DC_2A_n41A	30	5+70	DFT-QPSK	M+L	Outer_Full	24.74	PC3	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+L	Inner_Full	24.80	PC3	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	24.78	PC3	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	24.58	PC3	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+L	Outer_Full	23.84	PC3	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+L	Inner_Full	24.82	PC3	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	23.82	PC3	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	23.49	PC3	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+M	Outer_Full	24.71	PC3	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+M	Inner_Full	24.74	PC3	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	24.50	PC3	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	24.55	PC3	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+M	Outer_Full	23.70	PC3	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+M	Inner_Full	24.68	PC3	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	23.75	PC3	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	23.61	PC3	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+H	Outer_Full	24.46	PC3	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+H	Inner_Full	24.33	PC3	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	24.47	PC3	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	23.92	PC3	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+H	Outer_Full	23.40	PC3	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+H	Inner_Full	24.37	PC3	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	23.52	PC3	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	22.91	PC3	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+L	Outer_Full	24.72	PC3	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+L	Inner_Full	24.70	PC3	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	24.69	PC3	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	24.55	PC3	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+L	Outer_Full	23.73	PC3	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+L	Inner_Full	24.65	PC3	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	23.64	PC3	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	23.57	PC3	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+M	Outer_Full	24.65	PC3	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+M	Inner_Full	24.71	PC3	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	24.48	PC3	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	24.42	PC3	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+M	Outer_Full	23.66	PC3	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+M	Inner_Full	24.72	PC3	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	23.56	PC3	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	23.56	PC3	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+H	Outer_Full	24.43	PC3	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+H	Inner_Full	24.43	PC3	PASS

DC_2A_n41A	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	24.50	PC3	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	23.93	PC3	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+H	Outer_Full	23.52	PC3	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+H	Inner_Full	24.39	PC3	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	23.65	PC3	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	22.96	PC3	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+L	Outer_Full	24.84	PC3	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+L	Inner_Full	24.72	PC3	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	24.68	PC3	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	24.73	PC3	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+L	Outer_Full	23.65	PC3	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+L	Inner_Full	24.66	PC3	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	23.62	PC3	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	23.66	PC3	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+M	Outer_Full	24.67	PC3	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+M	Inner_Full	24.80	PC3	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	24.45	PC3	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	24.53	PC3	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+M	Outer_Full	23.67	PC3	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+M	Inner_Full	24.83	PC3	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	23.54	PC3	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	23.52	PC3	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+H	Outer_Full	24.46	PC3	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+H	Inner_Full	24.51	PC3	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	24.54	PC3	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	24.08	PC3	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+H	Outer_Full	23.44	PC3	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+H	Inner_Full	24.52	PC3	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	23.58	PC3	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	23.02	PC3	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+L	Outer_Full	24.72	PC3	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+L	Inner_Full	24.74	PC3	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	24.60	PC3	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	24.53	PC3	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+L	Outer_Full	23.76	PC3	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+L	Inner_Full	24.73	PC3	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	23.79	PC3	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	23.69	PC3	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+M	Outer_Full	24.64	PC3	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+M	Inner_Full	24.73	PC3	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	24.38	PC3	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	24.34	PC3	PASS

DC_2A_n41A	30	5+100	DFT-16QAM	M+M	Outer_Full	23.64	PC3	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+M	Inner_Full	25.02	PC3	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	23.40	PC3	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	23.25	PC3	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+H	Outer_Full	24.54	PC3	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+H	Inner_Full	24.54	PC3	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	24.43	PC3	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	23.90	PC3	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+H	Outer_Full	23.49	PC3	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+H	Inner_Full	24.54	PC3	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	23.44	PC3	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	22.79	PC3	PASS

Appendix B: Peak-to-Average Ratio for NSA

Peak-to-Average Ratio(CCDF)

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_2A_n4 1A	30	5+20	DFT-QPSK	M+L	Outer_Full	4.54	≤13	PASS
DC_2A_n4 1A	30	5+20	DFT-16QAM	M+L	Outer_Full	5.42	≤13	PASS
DC_2A_n4 1A	30	5+20	DFT-QPSK	M+M	Outer_Full	4.83	≤13	PASS
DC_2A_n4 1A	30	5+20	DFT-16QAM	M+M	Outer_Full	5.69	≤13	PASS
DC_2A_n4 1A	30	5+20	DFT-QPSK	M+H	Outer_Full	5.02	≤13	PASS
DC_2A_n4 1A	30	5+20	DFT-16QAM	M+H	Outer_Full	5.83	≤13	PASS
DC_2A_n4 1A	30	5+30	DFT-QPSK	M+L	Outer_Full	4.80	≤13	PASS
DC_2A_n4 1A	30	5+30	DFT-16QAM	M+L	Outer_Full	5.61	≤13	PASS
DC_2A_n4 1A	30	5+30	DFT-QPSK	M+M	Outer_Full	4.72	≤13	PASS
DC_2A_n4 1A	30	5+30	DFT-16QAM	M+M	Outer_Full	5.69	≤13	PASS
DC_2A_n4 1A	30	5+30	DFT-QPSK	M+H	Outer_Full	5.10	≤13	PASS
DC_2A_n4 1A	30	5+30	DFT-16QAM	M+H	Outer_Full	6.02	≤13	PASS
DC_2A_n4 1A	30	5+40	DFT-QPSK	M+L	Outer_Full	4.99	≤13	PASS
DC_2A_n4 1A	30	5+40	DFT-16QAM	M+L	Outer_Full	5.80	≤13	PASS
DC_2A_n4 1A	30	5+40	DFT-QPSK	M+M	Outer_Full	4.88	≤13	PASS
DC_2A_n4 1A	30	5+40	DFT-16QAM	M+M	Outer_Full	5.85	≤13	PASS
DC_2A_n4 1A	30	5+40	DFT-QPSK	M+H	Outer_Full	5.03	≤13	PASS
DC_2A_n4	30	5+40	DFT-16QAM	M+H	Outer_Full	5.88	≤13	PASS

1A								
DC_2A_n4 1A	30	5+50	DFT-QPSK	M+L	Outer_Full	4.96	≤13	PASS
DC_2A_n4 1A	30	5+50	DFT-16QAM	M+L	Outer_Full	5.91	≤13	PASS
DC_2A_n4 1A	30	5+50	DFT-QPSK	M+M	Outer_Full	5.09	≤13	PASS
DC_2A_n4 1A	30	5+50	DFT-16QAM	M+M	Outer_Full	5.93	≤13	PASS
DC_2A_n4 1A	30	5+50	DFT-QPSK	M+H	Outer_Full	4.85	≤13	PASS
DC_2A_n4 1A	30	5+50	DFT-16QAM	M+H	Outer_Full	5.93	≤13	PASS
DC_2A_n4 1A	30	5+60	DFT-QPSK	M+L	Outer_Full	5.10	≤13	PASS
DC_2A_n4 1A	30	5+60	DFT-16QAM	M+L	Outer_Full	6.04	≤13	PASS
DC_2A_n4 1A	30	5+60	DFT-QPSK	M+M	Outer_Full	5.16	≤13	PASS
DC_2A_n4 1A	30	5+60	DFT-16QAM	M+M	Outer_Full	6.01	≤13	PASS
DC_2A_n4 1A	30	5+60	DFT-QPSK	M+H	Outer_Full	4.94	≤13	PASS
DC_2A_n4 1A	30	5+60	DFT-16QAM	M+H	Outer_Full	5.75	≤13	PASS
DC_2A_n4 1A	30	5+70	DFT-QPSK	M+L	Outer_Full	5.17	≤13	PASS
DC_2A_n4 1A	30	5+70	DFT-16QAM	M+L	Outer_Full	6.01	≤13	PASS
DC_2A_n4 1A	30	5+70	DFT-QPSK	M+M	Outer_Full	5.23	≤13	PASS
DC_2A_n4 1A	30	5+70	DFT-16QAM	M+M	Outer_Full	6.01	≤13	PASS
DC_2A_n4 1A	30	5+70	DFT-QPSK	M+H	Outer_Full	4.81	≤13	PASS
DC_2A_n4 1A	30	5+70	DFT-16QAM	M+H	Outer_Full	5.72	≤13	PASS
DC_2A_n4 1A	30	5+80	DFT-QPSK	M+L	Outer_Full	5.20	≤13	PASS
DC_2A_n4 1A	30	5+80	DFT-16QAM	M+L	Outer_Full	6.09	≤13	PASS
DC_2A_n4 1A	30	5+80	DFT-QPSK	M+M	Outer_Full	5.31	≤13	PASS

DC_2A_n4 1A	30	5+80	DFT-16QAM	M+M	Outer_Full	6.07	≤13	PASS
DC_2A_n4 1A	30	5+80	DFT-QPSK	M+H	Outer_Full	4.85	≤13	PASS
DC_2A_n4 1A	30	5+80	DFT-16QAM	M+H	Outer_Full	5.74	≤13	PASS
DC_2A_n4 1A	30	5+90	DFT-QPSK	M+L	Outer_Full	5.22	≤13	PASS
DC_2A_n4 1A	30	5+90	DFT-16QAM	M+L	Outer_Full	6.09	≤13	PASS
DC_2A_n4 1A	30	5+90	DFT-QPSK	M+M	Outer_Full	5.37	≤13	PASS
DC_2A_n4 1A	30	5+90	DFT-16QAM	M+M	Outer_Full	6.09	≤13	PASS
DC_2A_n4 1A	30	5+90	DFT-QPSK	M+H	Outer_Full	4.83	≤13	PASS
DC_2A_n4 1A	30	5+90	DFT-16QAM	M+H	Outer_Full	5.77	≤13	PASS
DC_2A_n4 1A	30	5+100	DFT-QPSK	M+L	Outer_Full	5.28	≤13	PASS
DC_2A_n4 1A	30	5+100	DFT-16QAM	M+L	Outer_Full	6.13	≤13	PASS
DC_2A_n4 1A	30	5+100	DFT-QPSK	M+M	Outer_Full	5.48	≤13	PASS
DC_2A_n4 1A	30	5+100	DFT-16QAM	M+M	Outer_Full	6.21	≤13	PASS
DC_2A_n4 1A	30	5+100	DFT-QPSK	M+H	Outer_Full	4.95	≤13	PASS
DC_2A_n4 1A	30	5+100	DFT-16QAM	M+H	Outer_Full	5.81	≤13	PASS

Test Graphs

















