Appendix for DC_12A_n77A(3700-3980MHz)

Product Name: Rugged handheld
Model No: UA60

Appendix A: Average Power Output Data for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_12A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	23.59	PC2	PASS
DC_12A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Inner_Full	24.35	PC2	PASS
DC_12A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	23.55	PC2	PASS
DC_12A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	23.24	PC2	PASS
DC_12A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	22.42	PC2	PASS
DC_12A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Inner_Full	23.58	PC2	PASS
DC_12A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	22.67	PC2	PASS
DC_12A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	22.35	PC2	PASS
DC_12A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	23.66	PC2	PASS
DC_12A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Inner_Full	24.61	PC2	PASS
DC_12A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	23.90	PC2	PASS
DC_12A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	23.42	PC2	PASS
DC_12A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	22.81	PC2	PASS
DC_12A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Inner_Full	23.94	PC2	PASS
DC_12A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	23.00	PC2	PASS
DC_12A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	22.43	PC2	PASS
DC_12A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	22.10	PC2	PASS
DC_12A_n77A-3	30	5+20	DFT-QPSK	M+H	Inner_Full	23.42	PC2	PASS

700-3980								
DC_12A_n77A-3 700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	22.33	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	22.15	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	21.12	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+20	DFT-16QAM	M+H	Inner_Full	22.42	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	21.64	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	21.10	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+30	DFT-QPSK	M+L	Outer_Full	23.53	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+30	DFT-QPSK	M+L	Inner_Full	24.73	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	23.66	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	23.87	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+30	DFT-16QAM	M+L	Outer_Full	22.55	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+30	DFT-16QAM	M+L	Inner_Full	23.73	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	22.62	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	22.96	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+30	DFT-QPSK	M+M	Outer_Full	23.75	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+30	DFT-QPSK	M+M	Inner_Full	24.71	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	24.10	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	23.72	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+30	DFT-16QAM	M+M	Outer_Full	22.98	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+30	DFT-16QAM	M+M	Inner_Full	23.71	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	23.07	PC2	PASS

DC_12A_n77A-3 700-3980	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	22.56	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+30	DFT-QPSK	M+H	Outer_Full	22.27	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+30	DFT-QPSK	M+H	Inner_Full	23.30	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	22.36	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	22.38	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+30	DFT-16QAM	M+H	Outer_Full	21.49	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+30	DFT-16QAM	M+H	Inner_Full	22.54	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	21.25	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	20.89	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	23.78	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+40	DFT-QPSK	M+L	Inner_Full	24.46	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	23.60	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	23.79	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	22.55	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+40	DFT-16QAM	M+L	Inner_Full	23.76	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	23.08	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	22.96	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	24.00	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+40	DFT-QPSK	M+M	Inner_Full	24.65	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	24.42	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	23.98	PC2	PASS
DC_12A_n77A-3	30	5+40	DFT-16QAM	M+M	Outer_Full	22.80	PC2	PASS

700-3980								
DC_12A_n77A-3 700-3980	30	5+40	DFT-16QAM	M+M	Inner_Full	23.93	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	23.17	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	22.55	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	22.16	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+40	DFT-QPSK	M+H	Inner_Full	23.15	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	22.29	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	22.07	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	21.40	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+40	DFT-16QAM	M+H	Inner_Full	22.42	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	21.15	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	20.86	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	23.61	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+60	DFT-QPSK	M+L	Inner_Full	24.64	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	23.28	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	23.64	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	22.63	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+60	DFT-16QAM	M+L	Inner_Full	23.45	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	22.62	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	22.69	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	23.53	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+60	DFT-QPSK	M+M	Inner_Full	24.53	PC2	PASS

DC_12A_n77A-3 700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	23.95	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	23.30	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	22.74	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+60	DFT-16QAM	M+M	Inner_Full	23.59	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	22.90	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	22.06	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	21.98	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+60	DFT-QPSK	M+H	Inner_Full	22.97	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	22.26	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	21.70	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	21.27	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+60	DFT-16QAM	M+H	Inner_Full	22.01	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	21.38	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	20.99	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	24.05	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+80	DFT-QPSK	M+L	Inner_Full	24.88	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	23.70	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	23.60	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	23.06	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+80	DFT-16QAM	M+L	Inner_Full	23.93	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	22.63	PC2	PASS
DC_12A_n77A-3	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	22.98	PC2	PASS

700-3980								
DC_12A_n77A-3 700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	23.50	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+80	DFT-QPSK	M+M	Inner_Full	24.57	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	23.70	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	22.99	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	22.72	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+80	DFT-16QAM	M+M	Inner_Full	23.63	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	22.91	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	21.82	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	22.21	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+80	DFT-QPSK	M+H	Inner_Full	23.18	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	22.59	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	22.00	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	21.25	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+80	DFT-16QAM	M+H	Inner_Full	22.12	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	21.66	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	21.22	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+L	Outer_Full	24.10	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+L	Inner_Full	24.99	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	23.33	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	23.63	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+100	DFT-16QAM	M+L	Outer_Full	22.91	PC2	PASS

DC_12A_n77A-3 700-3980	30	5+100	DFT-16QAM	M+L	Inner_Full	24.22	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	22.58	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	22.90	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+M	Outer_Full	23.69	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+M	Inner_Full	24.52	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	23.81	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	22.98	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+100	DFT-16QAM	M+M	Outer_Full	22.49	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+100	DFT-16QAM	M+M	Inner_Full	23.76	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	22.99	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	21.94	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+H	Outer_Full	22.58	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+H	Inner_Full	23.17	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	22.71	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	22.00	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+100	DFT-16QAM	M+H	Outer_Full	21.41	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+100	DFT-16QAM	M+H	Inner_Full	22.40	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	21.70	PC2	PASS
DC_12A_n77A-3 700-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	20.91	PC2	PASS

Appendix B: Peak-to-Average Ratio for NSA

Peak-to-Average Ratio(CCDF)

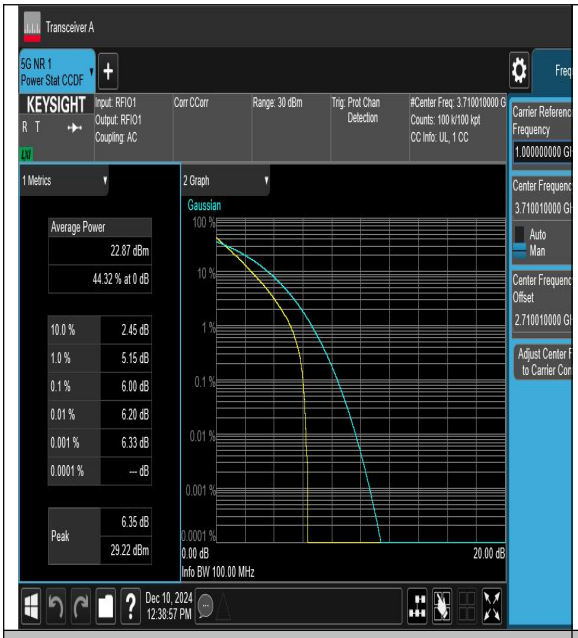
Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_12A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	6.00	≤13	PASS
DC_12A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	6.60	≤13	PASS
DC_12A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	6.05	≤13	PASS
DC_12A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	6.51	≤13	PASS
DC_12A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	5.72	≤13	PASS
DC_12A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	6.32	≤13	PASS
DC_12A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Outer_Full	6.04	≤13	PASS
DC_12A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Outer_Full	6.86	≤13	PASS
DC_12A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Outer_Full	5.76	≤13	PASS
DC_12A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Outer_Full	6.50	≤13	PASS
DC_12A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Outer_Full	5.65	≤13	PASS
DC_12A_n77A-3700-3	30	5+30	DFT-16QAM	M+H	Outer_Full	6.43	≤13	PASS

980								
DC_12A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	5.86	≤13	PASS
DC_12A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	6.45	≤13	PASS
DC_12A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	6.18	≤13	PASS
DC_12A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	6.64	≤13	PASS
DC_12A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	5.73	≤13	PASS
DC_12A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	6.13	≤13	PASS
DC_12A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	5.82	≤13	PASS
DC_12A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	6.59	≤13	PASS
DC_12A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	5.88	≤13	PASS
DC_12A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	6.50	≤13	PASS
DC_12A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	5.95	≤13	PASS
DC_12A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	6.59	≤13	PASS
DC_12A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	5.75	≤13	PASS
DC_12A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	6.61	≤13	PASS

DC_12A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	5.75	≤13	PASS
DC_12A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	6.52	≤13	PASS
DC_12A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	5.74	≤13	PASS
DC_12A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	6.41	≤13	PASS
DC_12A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Outer_Full	5.62	≤13	PASS
DC_12A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Outer_Full	6.57	≤13	PASS
DC_12A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Outer_Full	5.73	≤13	PASS
DC_12A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Outer_Full	6.30	≤13	PASS
DC_12A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Outer_Full	5.78	≤13	PASS
DC_12A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Outer_Full	6.38	≤13	PASS

Test Graphs



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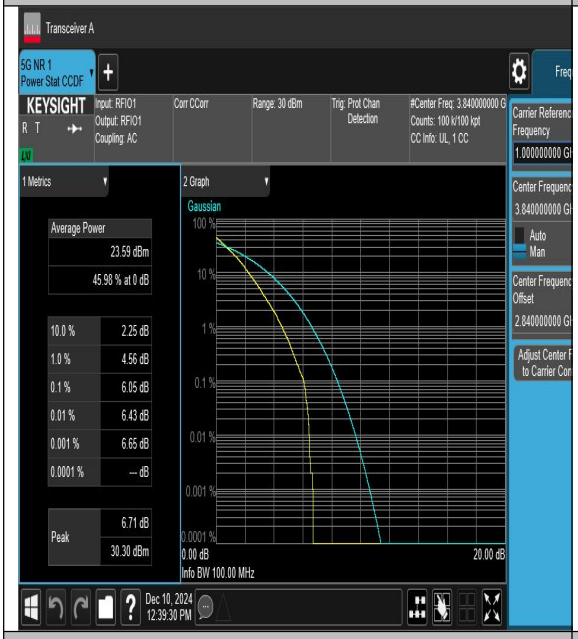

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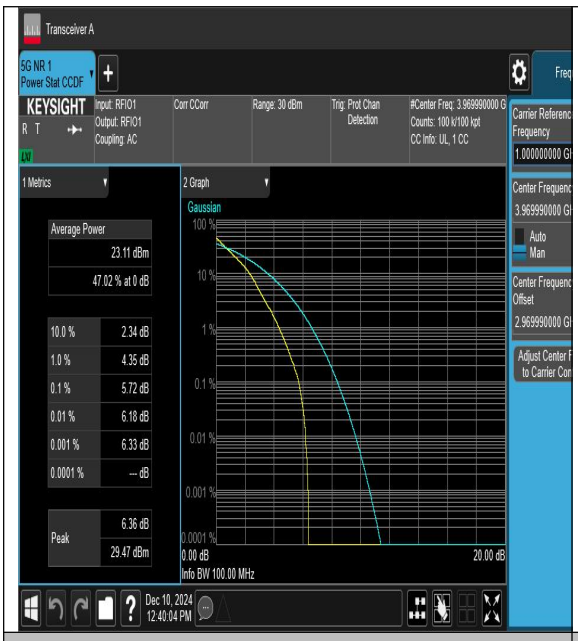

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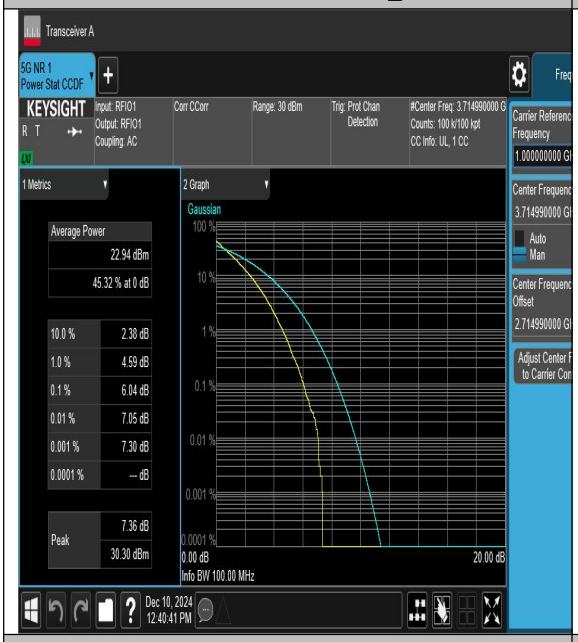

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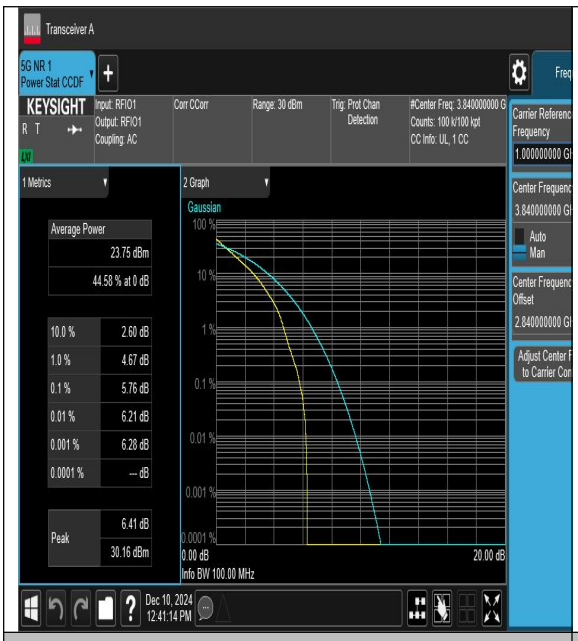

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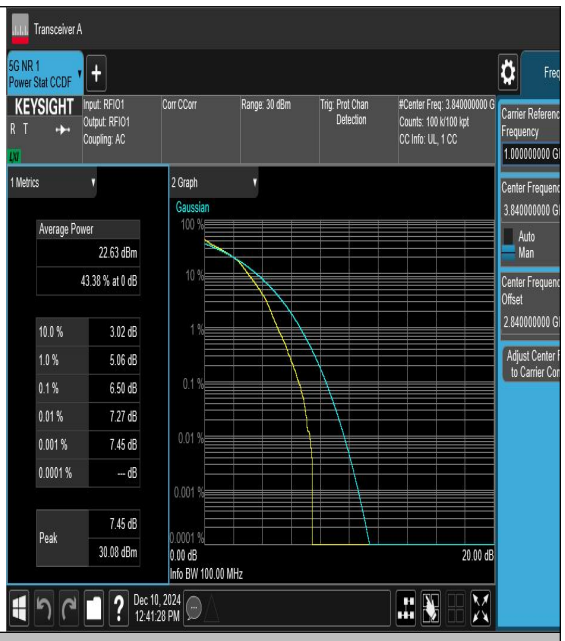

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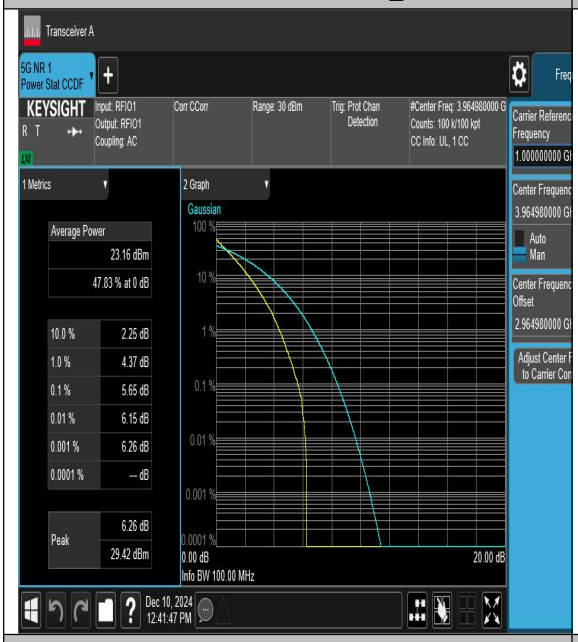

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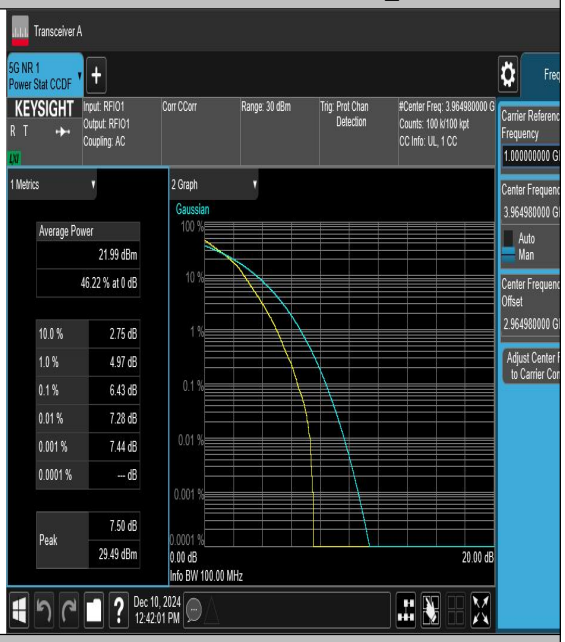

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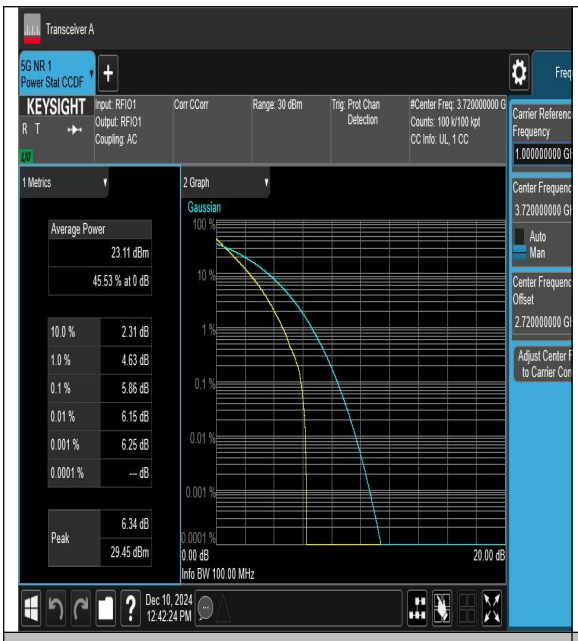

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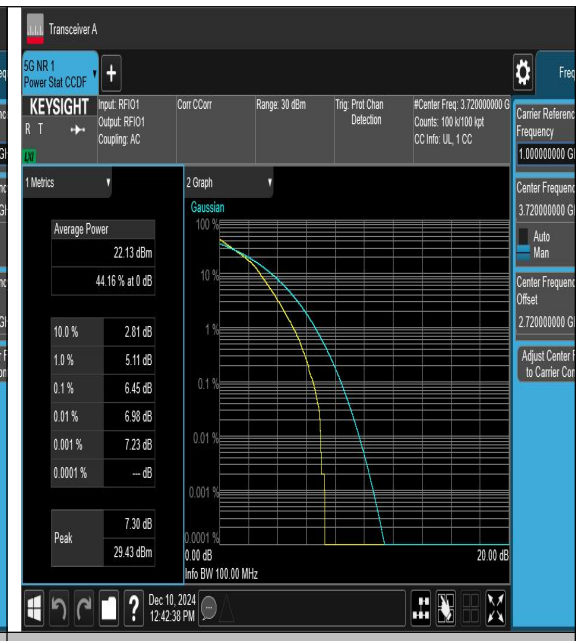

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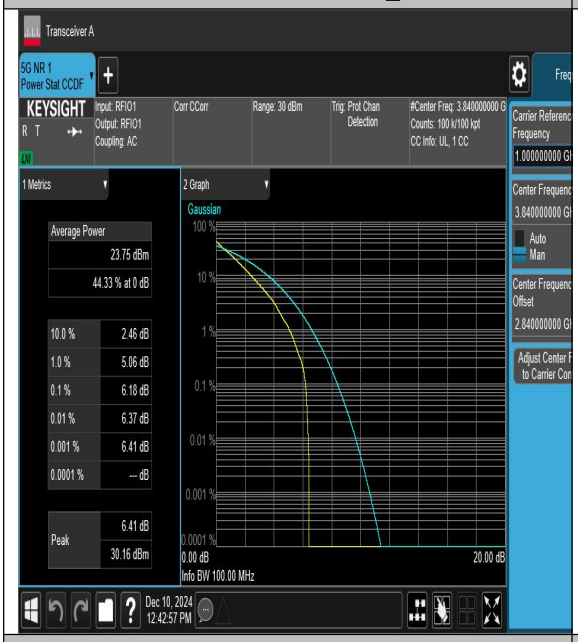

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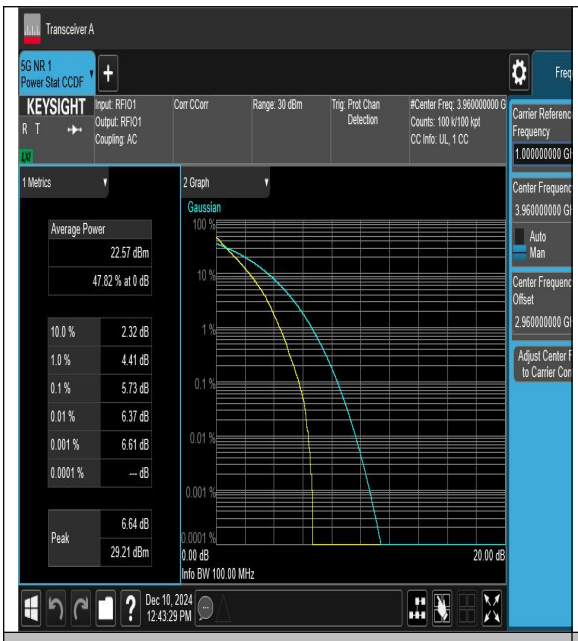

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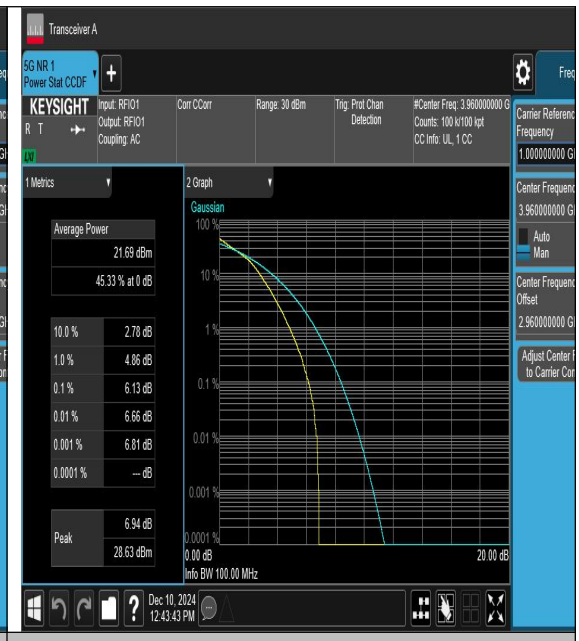

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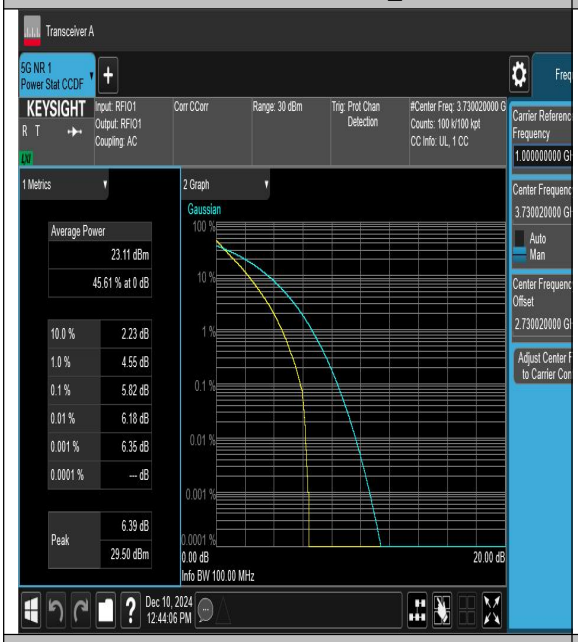

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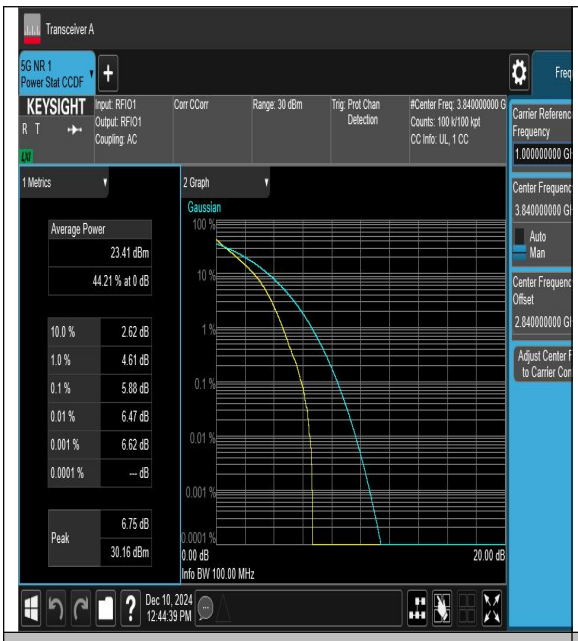

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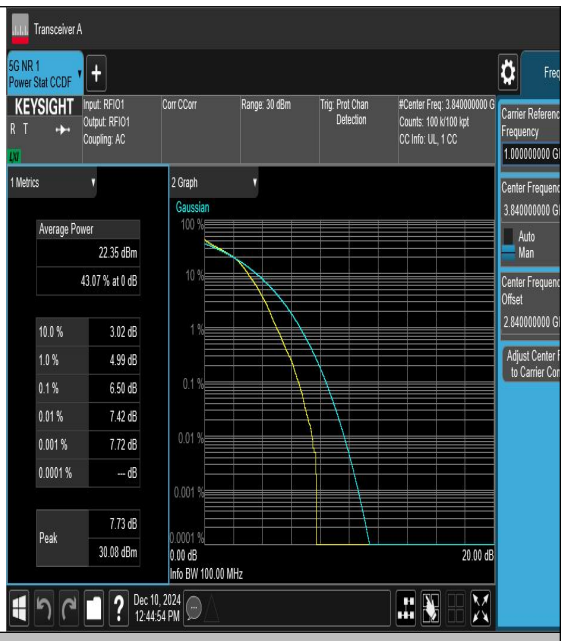

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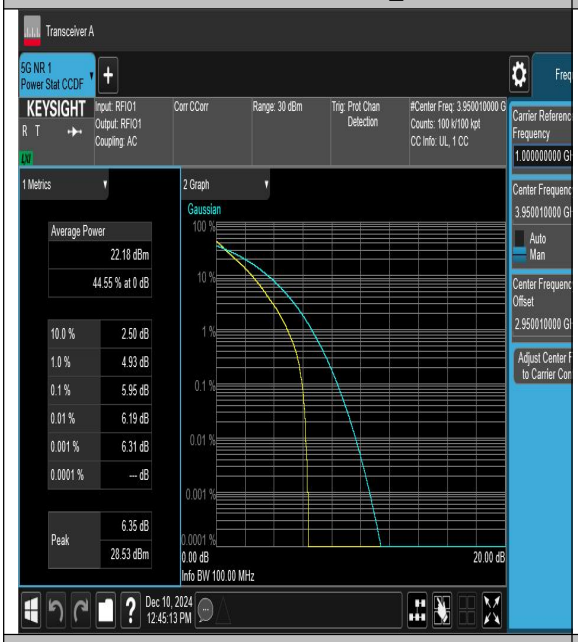

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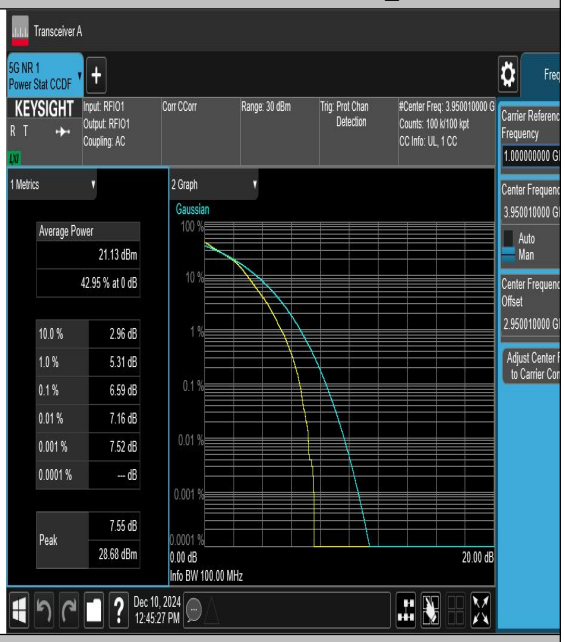

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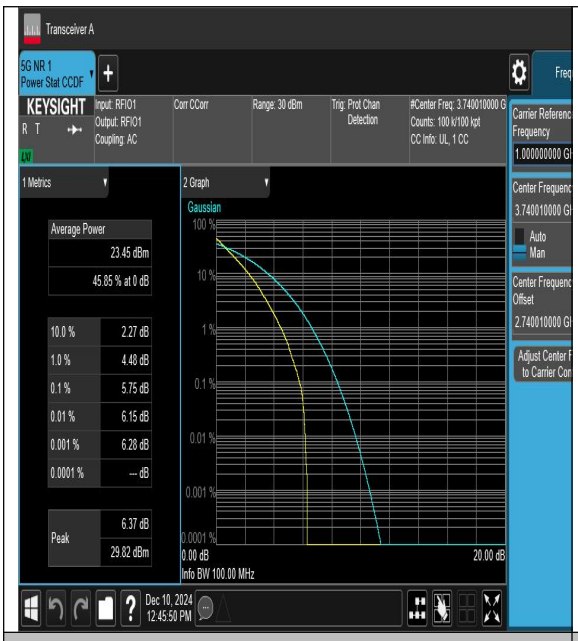

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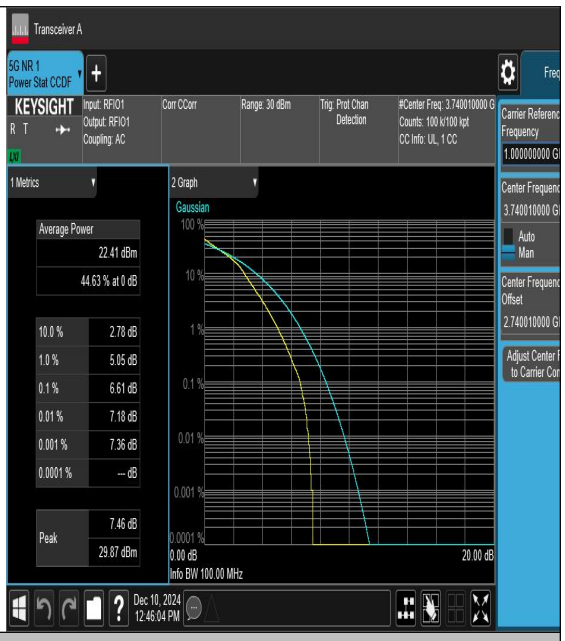

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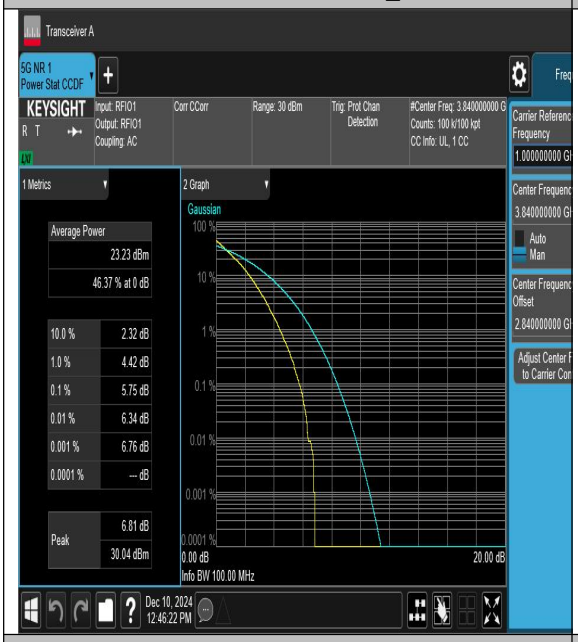

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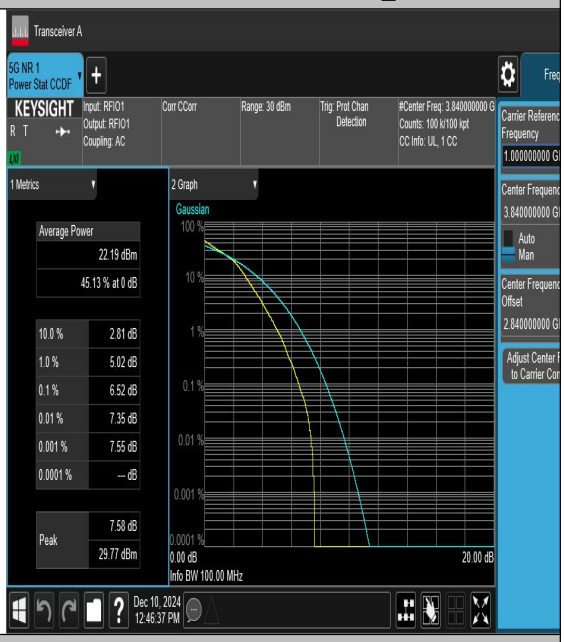

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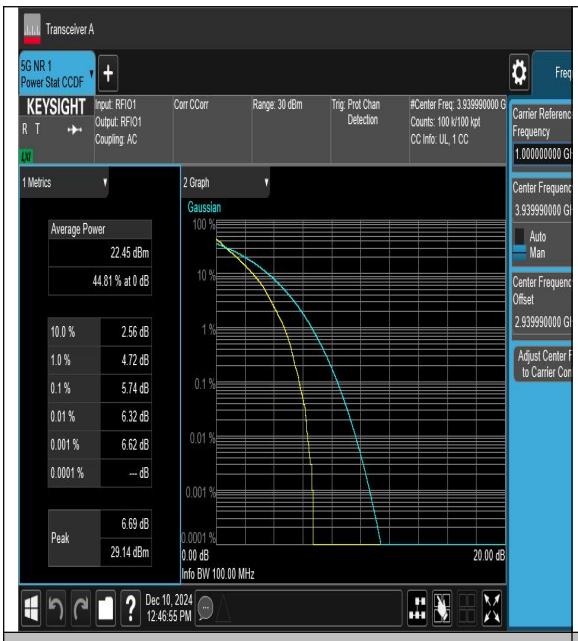

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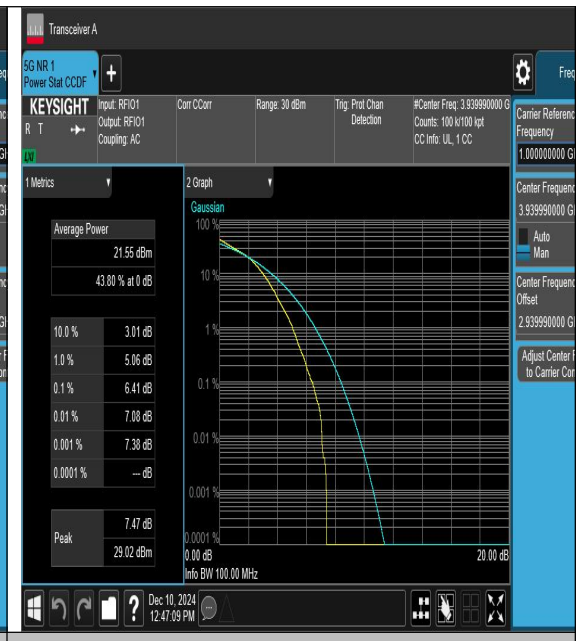

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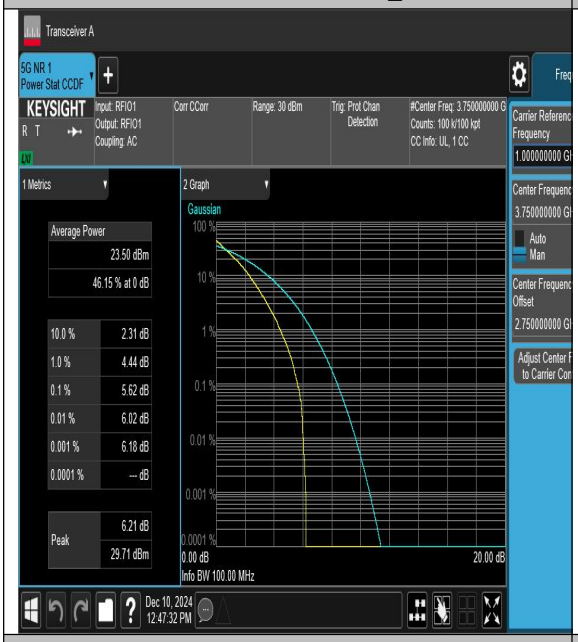

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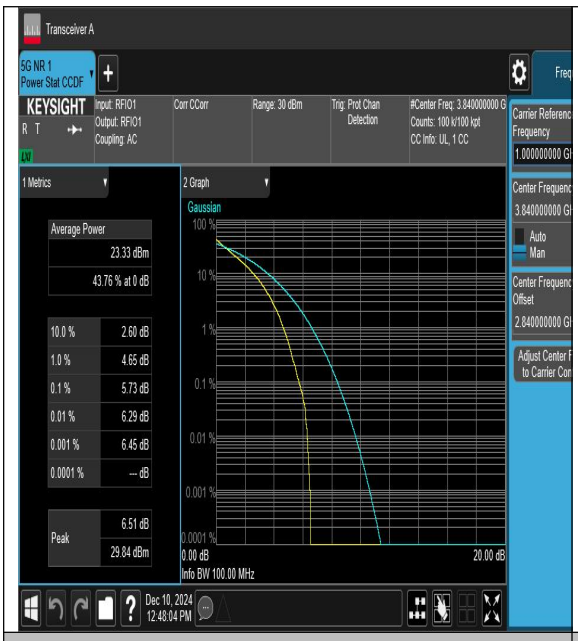

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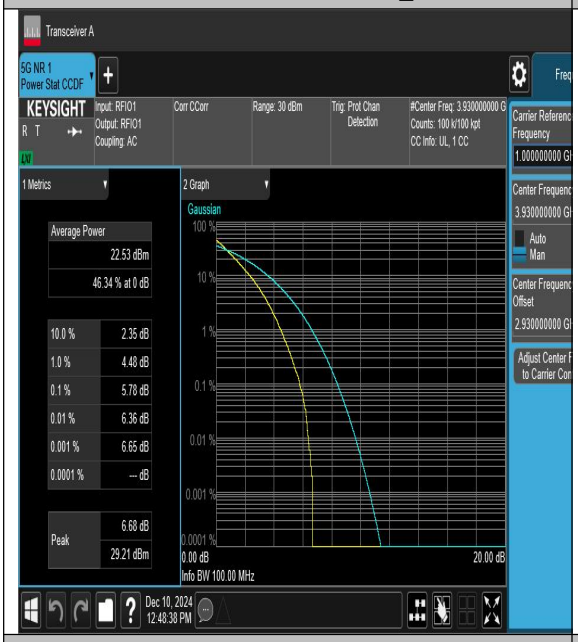

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NTNV-DC_12A_n77A-3700-3980-PC2-30-5+100-M+H-N/A-N/A-DFT-16QAM-Outer_Full-PASS

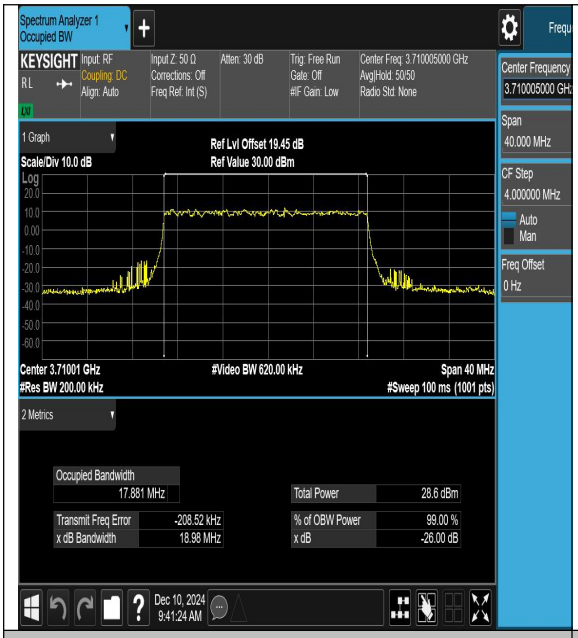
Appendix C: 26dB Bandwidth and Occupied Bandwidth for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_12A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	17.881	18.98	PASS
DC_12A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	17.879	19.63	PASS
DC_12A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	17.897	19.63	PASS
DC_12A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	17.847	19.71	PASS
DC_12A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	17.798	19.20	PASS
DC_12A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	17.811	19.34	PASS
DC_12A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Outer_Full	26.862	29.11	PASS
DC_12A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Outer_Full	26.821	29.58	PASS
DC_12A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Outer_Full	26.807	28.70	PASS
DC_12A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Outer_Full	26.721	28.46	PASS
DC_12A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Outer_Full	26.750	28.64	PASS
DC_12A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Outer_Full	26.844	28.67	PASS
DC_12A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	35.665	37.54	PASS
DC_12A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	35.644	37.67	PASS
DC_12A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	35.753	37.60	PASS
DC_12A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	35.698	37.65	PASS
DC_12A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	35.762	37.87	PASS

DC_12A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	35.685	37.59	PASS
DC_12A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	57.836	60.34	PASS
DC_12A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	57.850	60.44	PASS
DC_12A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	57.773	60.52	PASS
DC_12A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	57.827	60.11	PASS
DC_12A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	57.825	60.42	PASS
DC_12A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	57.851	60.40	PASS
DC_12A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	77.061	80.78	PASS
DC_12A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	76.994	80.34	PASS
DC_12A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	76.839	80.83	PASS
DC_12A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	77.213	80.59	PASS
DC_12A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	77.179	81.01	PASS
DC_12A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	77.110	80.49	PASS
DC_12A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Outer_Full	96.247	99.63	PASS
DC_12A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Outer_Full	96.333	101.2	PASS
DC_12A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Outer_Full	96.076	101.7	PASS
DC_12A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Outer_Full	96.446	99.99	PASS
DC_12A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Outer_Full	96.256	100.8	PASS
DC_12A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Outer_Full	96.564	101.5	PASS

Test Graphs



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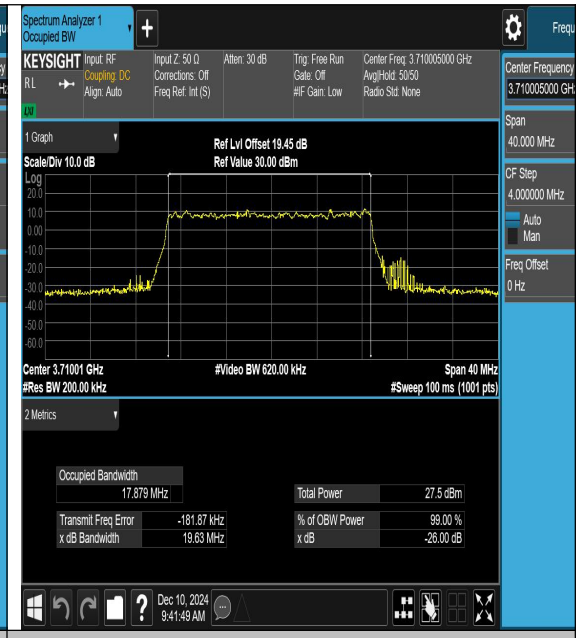

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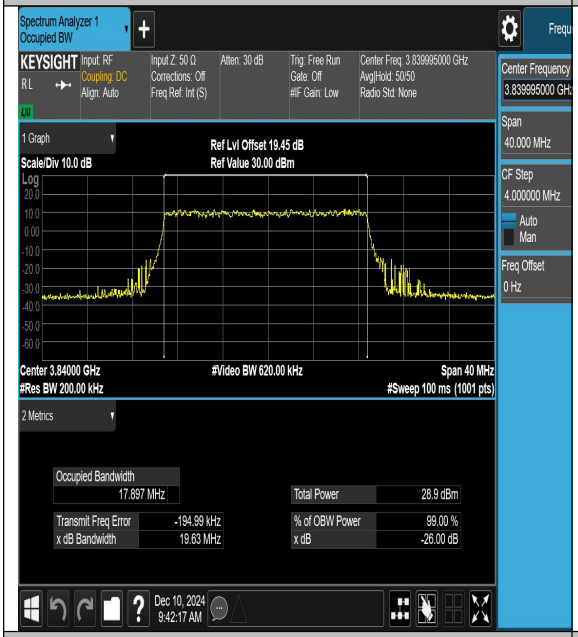

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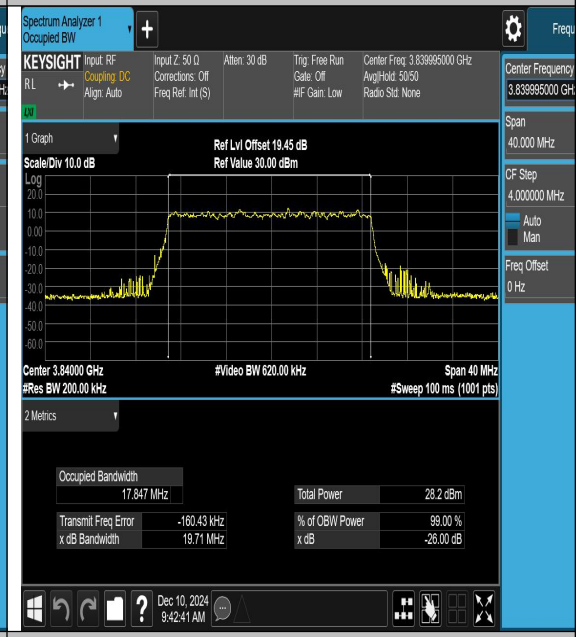

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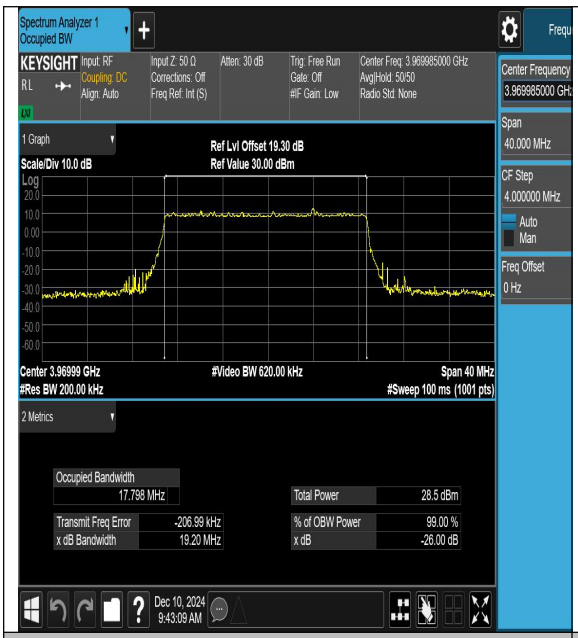

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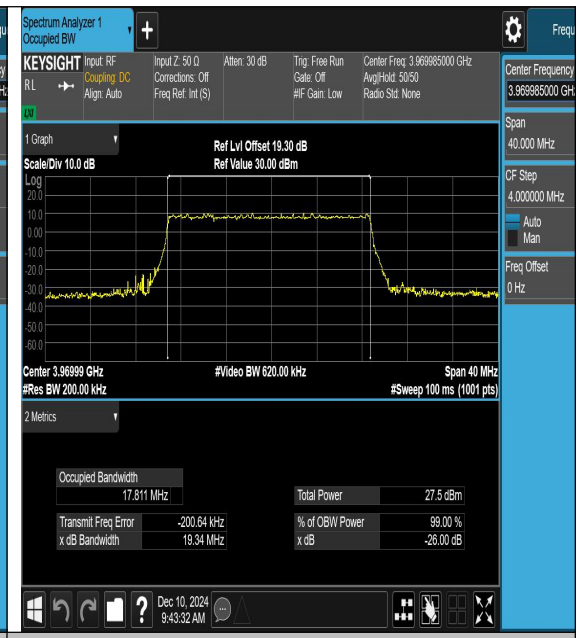

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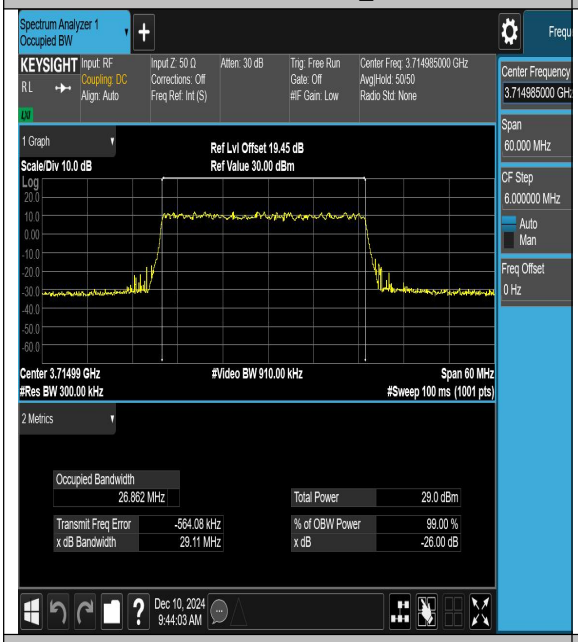

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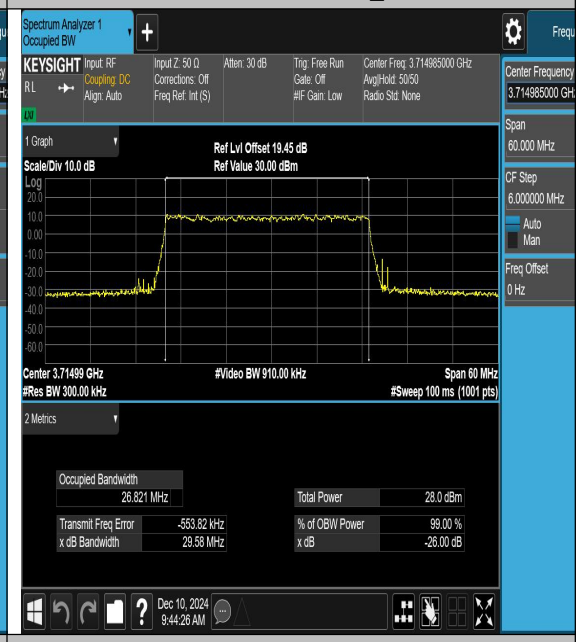

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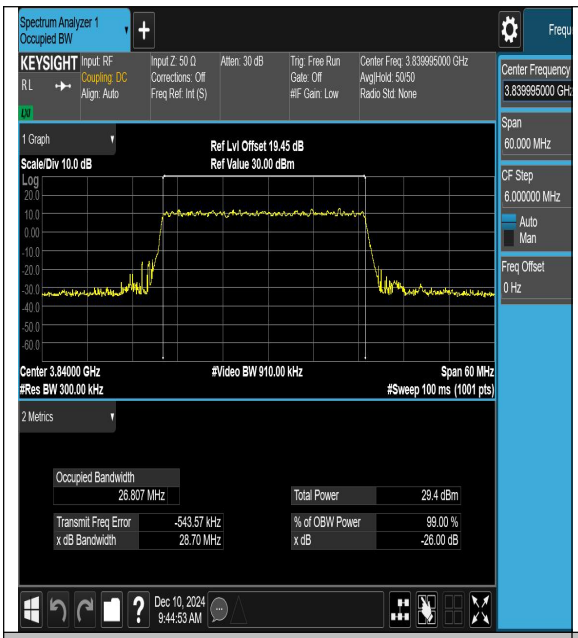

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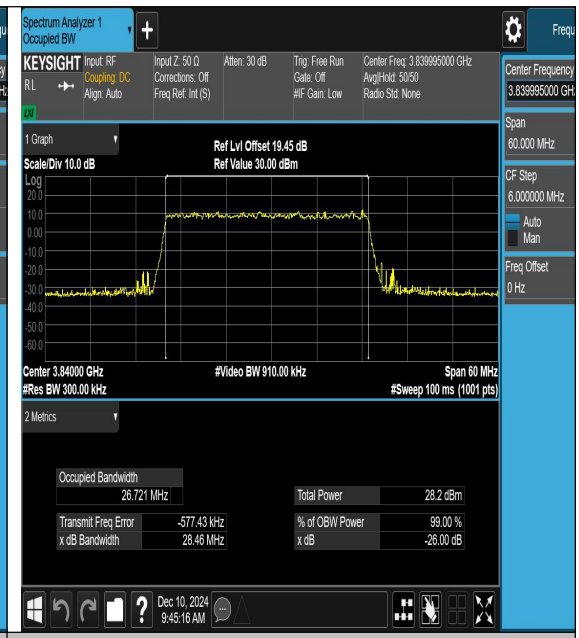

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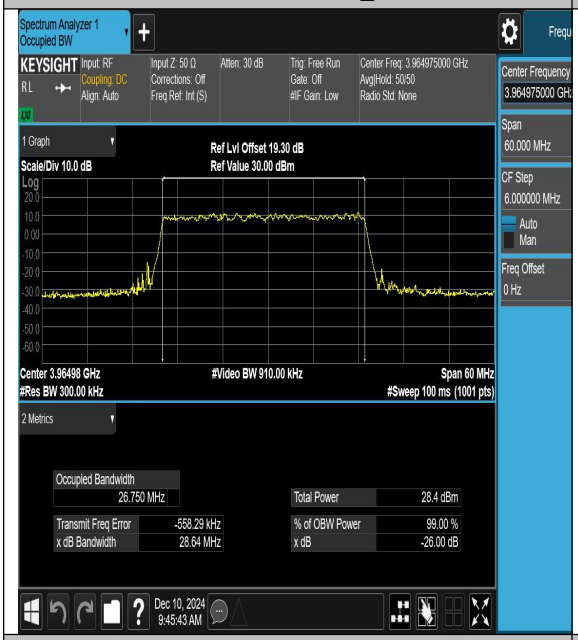

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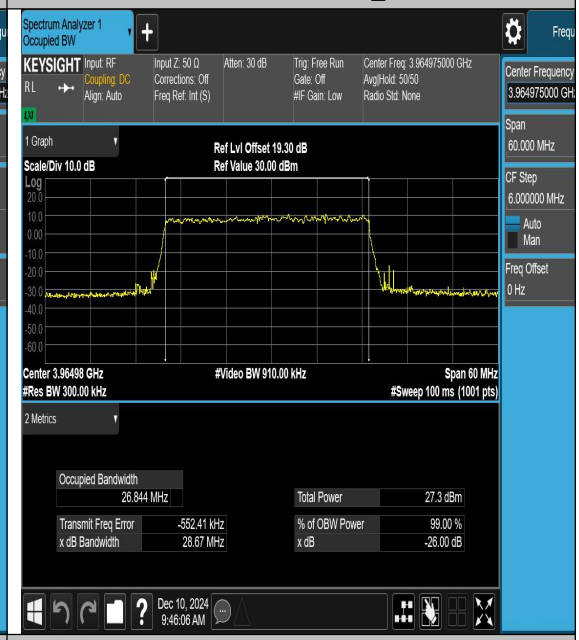

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