





Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SC S	Band width	Modulation	Chan nel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-65.25	-43	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	0.15	30	-76.67	-33	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	30	1000	-55.92	-23	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	1000	6000	-34.17	-13	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	6000	26000	-37.59	-13	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	26000	40000	-41.66	-13	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-67.17	-43	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-76.79	-33	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-56.17	-23	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-34.06	-13	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-37.56	-13	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-41.75	-13	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-66.57	-43	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-76.12	-33	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.99	-23	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-33.99	-13	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-37.55	-13	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-41.82	-13	PASS
DC_12A_n77A	30	5+20	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-66.74	-43	PASS

-3700-3980										
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	0.15	30	-77.28	-33	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	30	1000	-56.14	-23	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	1000	6000	-34.16	-13	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	6000	26000	-37.59	-13	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	26000	40000	-41.87	-13	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-66.40	-43	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-77.42	-33	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-56.06	-23	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-33.98	-13	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-37.42	-13	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-41.68	-13	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-65.80	-43	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-78.86	-33	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-56.19	-23	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-34.09	-13	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-37.35	-13	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-41.76	-13	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-65.97	-43	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	0.15	30	-75.99	-33	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	30	1000	-54.93	-23	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	1000	6000	-33.59	-13	PASS

DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	6000	26000	-37.63	-13	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	26000	40000	-41.81	-13	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-67.12	-43	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-76.46	-33	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-56.09	-23	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-34.05	-13	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-37.53	-13	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-41.83	-13	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-67.64	-43	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-75.65	-33	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-56.19	-23	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-33.98	-13	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-37.61	-13	PASS
DC_12A_n77A -3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-41.85	-13	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-66.70	-43	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+L	Outer_Full	0.15	30	-75.42	-33	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+L	Outer_Full	30	1000	-25.28	-23	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+L	Outer_Full	1000	6000	-34.07	-13	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+L	Outer_Full	6000	26000	-37.55	-13	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+L	Outer_Full	26000	40000	-41.65	-13	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-66.40	-43	PASS
DC_12A_n77A	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-75.75	-33	PASS

-3700-3980										
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-56.13	-23	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-34.07	-13	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-37.53	-13	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-41.82	-13	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-65.95	-43	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-76.64	-33	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-56.23	-23	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-34.02	-13	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-37.33	-13	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-41.80	-13	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-67.44	-43	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+M	Outer_Full	0.15	30	-77.18	-33	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+M	Outer_Full	30	1000	-56.13	-23	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+M	Outer_Full	1000	6000	-34.05	-13	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+M	Outer_Full	6000	26000	-37.60	-13	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+M	Outer_Full	26000	40000	-41.57	-13	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-66.91	-43	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-77.35	-33	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-55.58	-23	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-34.10	-13	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-37.46	-13	PASS

DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-41.72	-13	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.93	-43	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-77.09	-33	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.92	-23	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-34.00	-13	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-37.55	-13	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-41.63	-13	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-66.01	-43	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+H	Outer_Full	0.15	30	-76.29	-33	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+H	Outer_Full	30	1000	-49.86	-23	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+H	Outer_Full	1000	6000	-33.85	-13	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+H	Outer_Full	6000	26000	-37.59	-13	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+H	Outer_Full	26000	40000	-41.64	-13	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-66.73	-43	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.72	-33	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-56.05	-23	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-34.04	-13	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-37.61	-13	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-41.74	-13	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-66.87	-43	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-76.39	-33	PASS
DC_12A_n77A	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.79	-23	PASS

-3700-3980										
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-34.06	-13	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-37.64	-13	PASS
DC_12A_n77A -3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-41.53	-13	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-67.54	-43	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	0.15	30	-76.38	-33	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	30	1000	-54.40	-23	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	1000	6000	-34.10	-13	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	6000	26000	-37.57	-13	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	26000	40000	-41.70	-13	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-66.53	-43	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-76.23	-33	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-56.00	-23	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-33.95	-13	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-37.52	-13	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-41.73	-13	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-66.21	-43	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-77.08	-33	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-56.06	-23	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-33.96	-13	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-37.34	-13	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-41.62	-13	PASS

DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-67.10	-43	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	0.15	30	-77.85	-33	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	30	1000	-55.77	-23	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	1000	6000	-33.94	-13	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	6000	26000	-37.46	-13	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	26000	40000	-41.50	-13	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-66.51	-43	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-77.36	-33	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-56.13	-23	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-33.99	-13	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-37.41	-13	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-41.60	-13	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-67.00	-43	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-76.29	-33	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.93	-23	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-33.99	-13	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-37.37	-13	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-41.78	-13	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-66.49	-43	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	0.15	30	-77.45	-33	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	30	1000	-56.15	-23	PASS
DC_12A_n77A	30	5+40	DFT-QPSK	M+H	Outer_Full	1000	6000	-33.75	-13	PASS

-3700-3980										
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	6000	26000	-37.65	-13	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	26000	40000	-41.67	-13	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-66.95	-43	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-76.66	-33	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-55.53	-23	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-34.04	-13	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-37.50	-13	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-41.74	-13	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-65.01	-43	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-76.69	-33	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-56.27	-23	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-34.10	-13	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-37.47	-13	PASS
DC_12A_n77A -3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-41.63	-13	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-66.13	-43	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	0.15	30	-77.41	-33	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	30	1000	-55.99	-23	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	1000	6000	-33.97	-13	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	6000	26000	-37.46	-13	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	26000	40000	-41.29	-13	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-66.44	-43	PASS

DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-75.59	-33	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-42.29	-23	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-34.06	-13	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-37.57	-13	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-41.83	-13	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-66.73	-43	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-76.82	-33	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-56.18	-23	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-34.05	-13	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-37.58	-13	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-41.69	-13	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-66.32	-43	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	0.15	30	-70.59	-33	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	30	1000	-56.25	-23	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	1000	6000	-34.06	-13	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	6000	26000	-37.35	-13	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	26000	40000	-41.71	-13	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-67.34	-43	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-67.80	-33	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-56.28	-23	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-33.91	-13	PASS
DC_12A_n77A	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-37.48	-13	PASS

-3700-3980										
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-41.74	-13	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-66.59	-43	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-77.22	-33	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-56.06	-23	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-34.00	-13	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-37.55	-13	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-41.70	-13	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-67.12	-43	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	0.15	30	-77.28	-33	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	30	1000	-56.22	-23	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	1000	6000	-33.71	-13	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	6000	26000	-37.41	-13	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	26000	40000	-41.70	-13	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-66.42	-43	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.91	-33	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-56.40	-23	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-34.02	-13	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-37.50	-13	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-41.69	-13	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-66.04	-43	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-76.46	-33	PASS

DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-55.62	-23	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-33.95	-13	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-37.45	-13	PASS
DC_12A_n77A -3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-41.61	-13	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-66.73	-43	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	0.15	30	-76.81	-33	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	30	1000	-56.03	-23	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	1000	6000	-33.89	-13	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	6000	26000	-37.45	-13	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	26000	40000	-41.64	-13	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-67.09	-43	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-75.93	-33	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-55.95	-23	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-33.89	-13	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-37.43	-13	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-41.72	-13	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-66.26	-43	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-76.78	-33	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.79	-23	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-34.01	-13	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-37.43	-13	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-41.67	-13	PASS

-3700-3980										
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-66.24	-43	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	0.15	30	-76.90	-33	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	30	1000	-55.91	-23	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	1000	6000	-33.98	-13	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	6000	26000	-37.54	-13	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	26000	40000	-41.50	-13	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-66.31	-43	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-76.97	-33	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-56.03	-23	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-34.04	-13	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-37.49	-13	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-41.63	-13	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-66.87	-43	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-76.16	-33	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-56.20	-23	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-33.97	-13	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-37.43	-13	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-41.73	-13	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-66.79	-43	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	0.15	30	-77.33	-33	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	30	1000	-55.43	-23	PASS

DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	1000	6000	-33.88	-13	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	6000	26000	-37.57	-13	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	26000	40000	-41.69	-13	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-66.59	-43	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-77.92	-33	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-56.02	-23	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-33.95	-13	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-37.42	-13	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-41.75	-13	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-66.12	-43	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-76.73	-33	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-56.04	-23	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-33.80	-13	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-37.38	-13	PASS
DC_12A_n77A -3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-41.64	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-65.75	-43	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Outer_Full	0.15	30	-76.93	-33	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Outer_Full	30	1000	-42.61	-23	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Outer_Full	1000	6000	-34.01	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Outer_Full	6000	26000	-37.53	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Outer_Full	26000	40000	-41.71	-13	PASS
DC_12A_n77A	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-65.65	-43	PASS

-3700-3980		0								
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-76.87	-33	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-56.09	-23	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-33.79	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-37.59	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-41.76	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-66.09	-43	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-75.52	-33	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.75	-23	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-33.97	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-37.44	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-41.71	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-65.79	-43	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.15	30	-77.44	-33	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Outer_Full	30	1000	-55.04	-23	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Outer_Full	1000	6000	-34.00	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Outer_Full	6000	26000	-37.63	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Outer_Full	26000	40000	-41.72	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-67.11	-43	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-77.72	-33	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-55.93	-23	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-33.93	-13	PASS

DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-37.50	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-41.48	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-67.14	-43	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-77.33	-33	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-56.19	-23	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-33.93	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-37.48	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-41.80	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-66.40	-43	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.15	30	-76.58	-33	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Outer_Full	30	1000	-56.03	-23	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Outer_Full	1000	6000	-33.67	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Outer_Full	6000	26000	-37.47	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Outer_Full	26000	40000	-41.74	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-66.87	-43	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-77.25	-33	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-56.01	-23	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-33.98	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-37.41	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-41.77	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-66.47	-43	PASS
DC_12A_n77A	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-76.30	-33	PASS

-3700-3980		0								
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-31.57	-23	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-34.10	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-37.48	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-41.62	-13	PASS

Test Graphs













