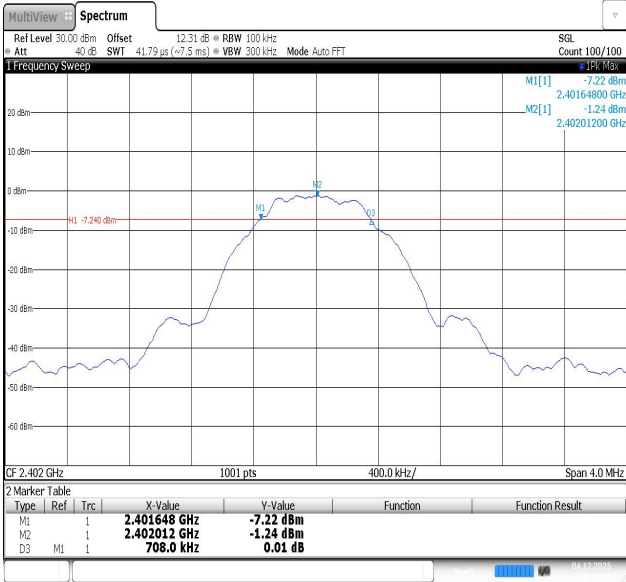


Appendix C: 6db Bandwidth

Test Result

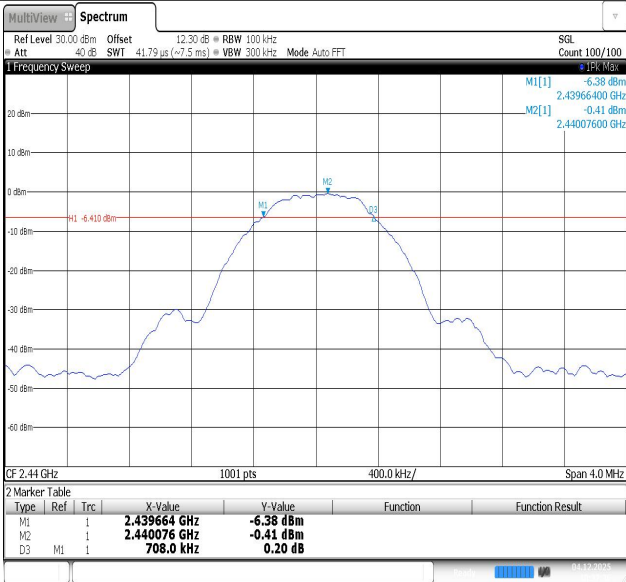
TestMode	Frequency[MHz]	6db Bandwidth [MHz]	Limit[MHz]	Verdict
BLE_1M	2402	0.71	0.5	PASS
BLE_1M	2440	0.71	0.5	PASS
BLE_1M	2480	0.69	0.5	PASS
BLE_2M	2402	1.36	0.5	PASS
BLE_2M	2440	1.29	0.5	PASS
BLE_2M	2480	1.29	0.5	PASS

Test Graphs



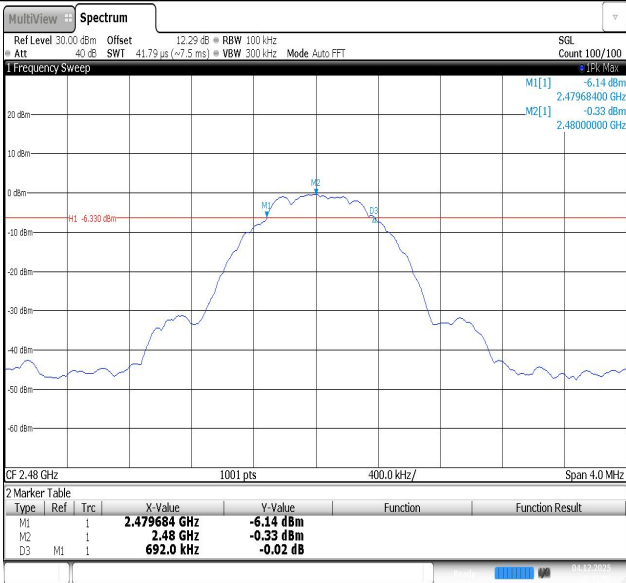
Date: 4 DEC 2025 10:36:04

BLE_1M-2402-PASS



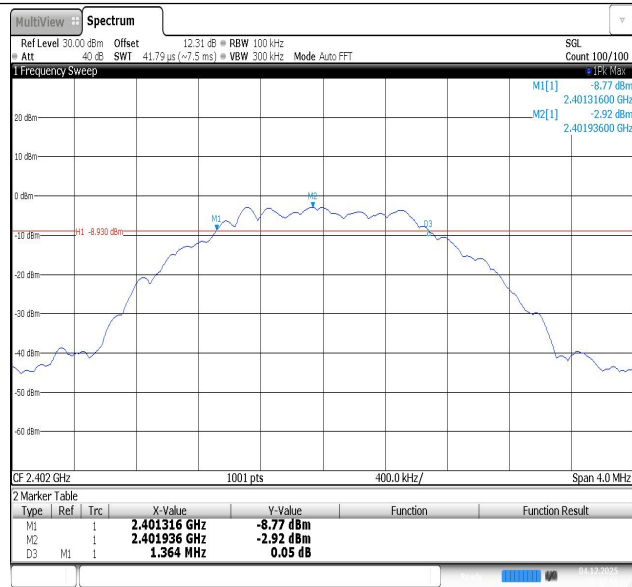
Date: 4 DEC 2025 10:37:37

BLE_1M-2440-PASS



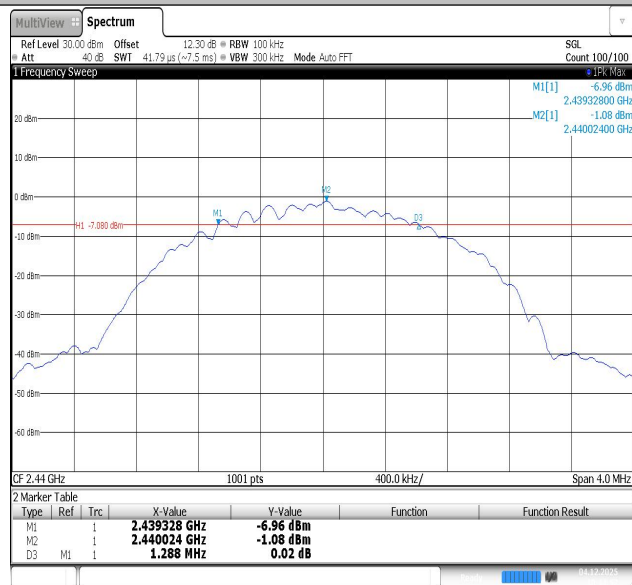
Date: 4 DEC 2025 10:41:06

BLE_1M-2480-PASS



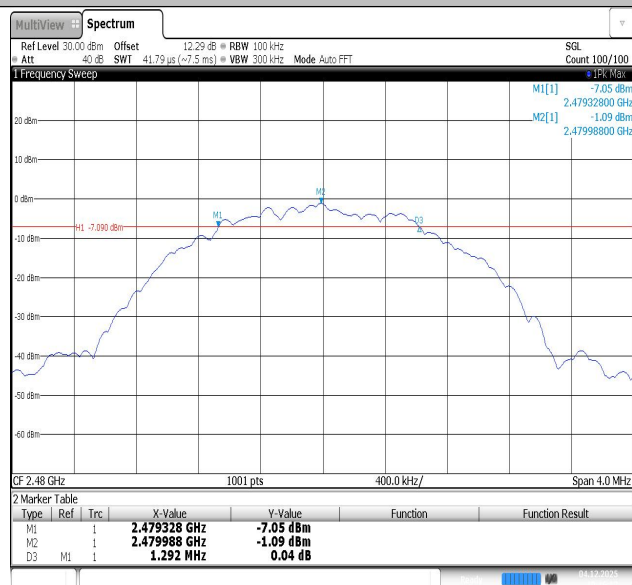
Date: 4 DEC 2025 10:42:51

BLE_2M-2402-PASS



Date: 4 DEC 2025 10:44:53

BLE_2M-2440-PASS



Date: 4 DEC 2025 10:46:12

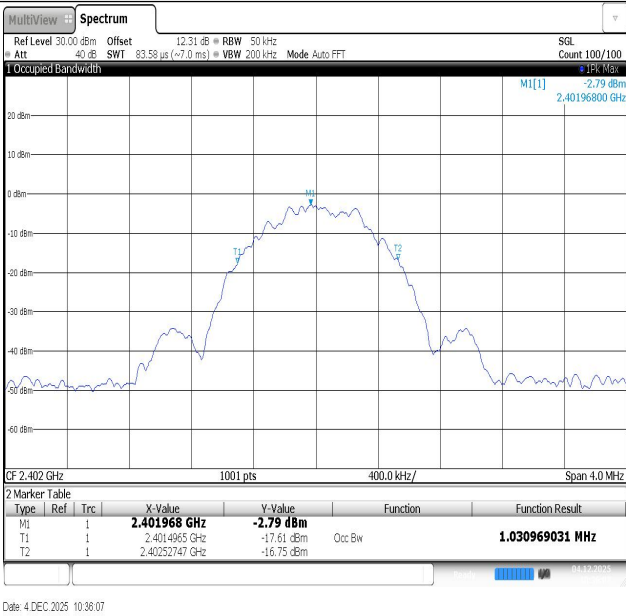
BLE_2M-2480-PASS

Appendix D: 99% Occupied Bandwidth

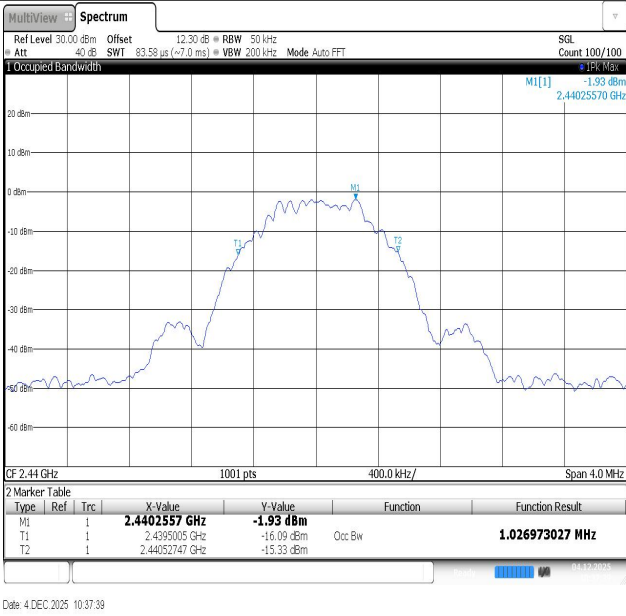
Test Result

TestMode	Frequency[MHz]	99% Occupied Bandwidth [MHz]	Limit[MHz]	Verdict
BLE_1M	2402	1.0310	---	---
BLE_1M	2440	1.0270	---	---
BLE_1M	2480	1.0310	---	---
BLE_2M	2402	2.0659	---	---
BLE_2M	2440	2.0340	---	---
BLE_2M	2480	2.0380	---	---

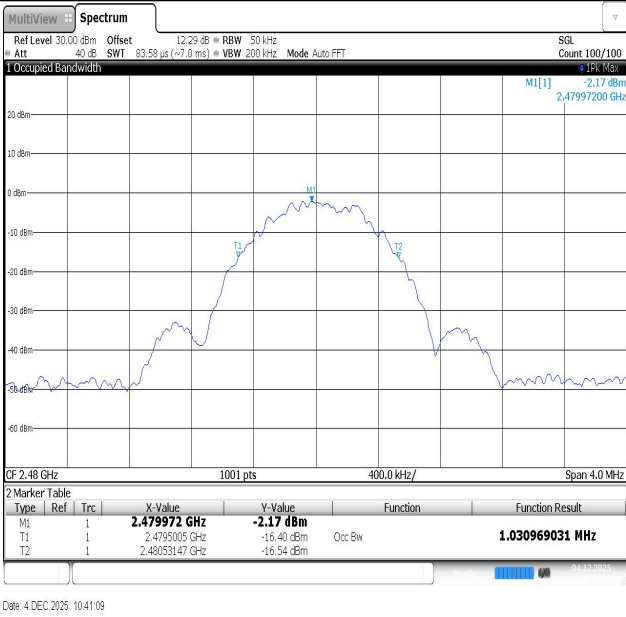
Test Graphs



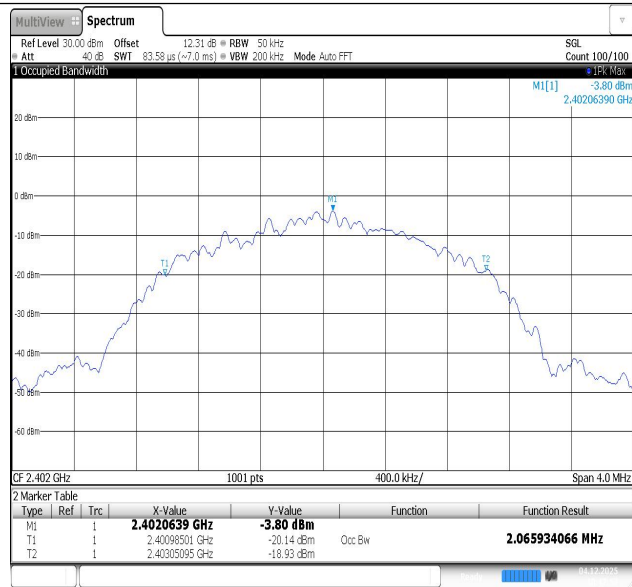
BLE_1M-2402



BLE_1M-2440



BLE_1M-2480



Date: 4 DEC 2025 10:42:53

BLE_2M-2402



Date: 4 DEC 2025 10:44:55

BLE_2M-2440



Date: 4 DEC 2025 10:46:15

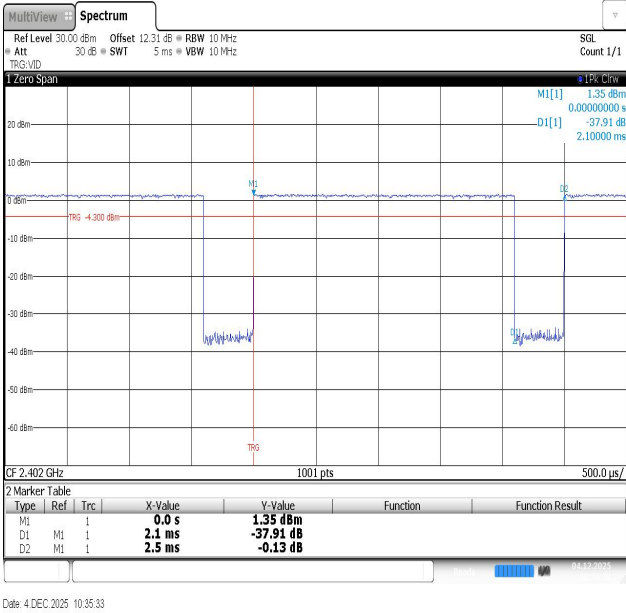
BLE_2M-2480

Appendix E: Duty Cycle

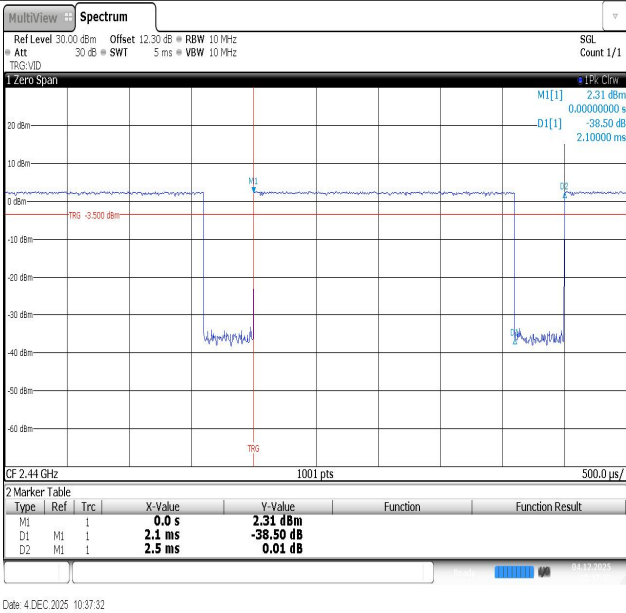
Test Result

TestMode	Frequency[MHz]	ON Time [ms]	Period [ms]	Duty Cycle [%]	Duty Cycle Factor[dB]	1/Ton time(kHz)
BLE_1M	2402	2.10	2.50	84.00	0.76	0.48
BLE_1M	2440	2.10	2.50	84.00	0.76	0.48
BLE_1M	2480	2.10	2.50	84.00	0.76	0.48
BLE_2M	2402	1.06	1.88	56.38	2.49	0.94
BLE_2M	2440	1.07	1.88	56.91	2.45	0.93
BLE_2M	2480	1.07	1.88	56.91	2.45	0.93

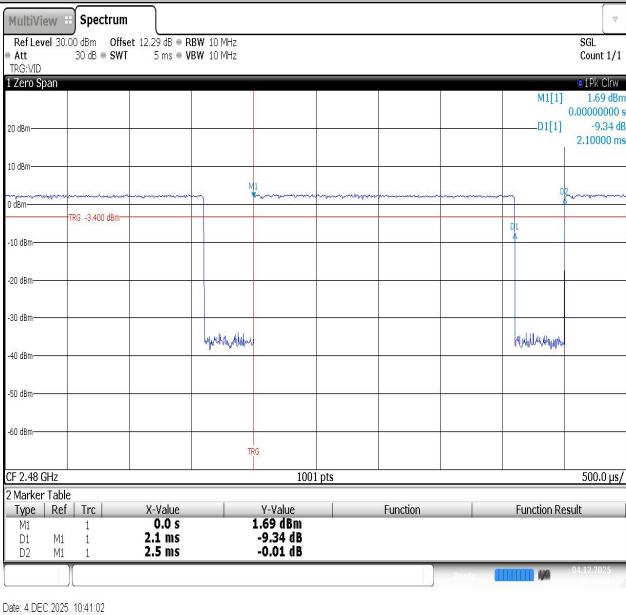
Test Graphs



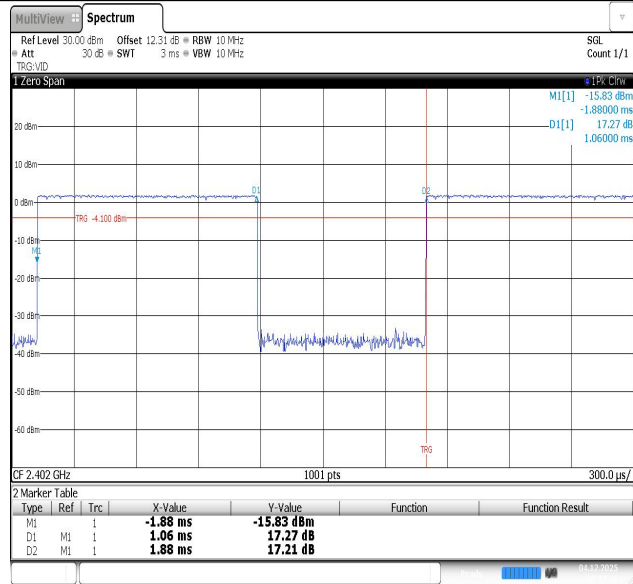
NTNV-BLE_1M-2402



NTNV-BLE_1M-2440

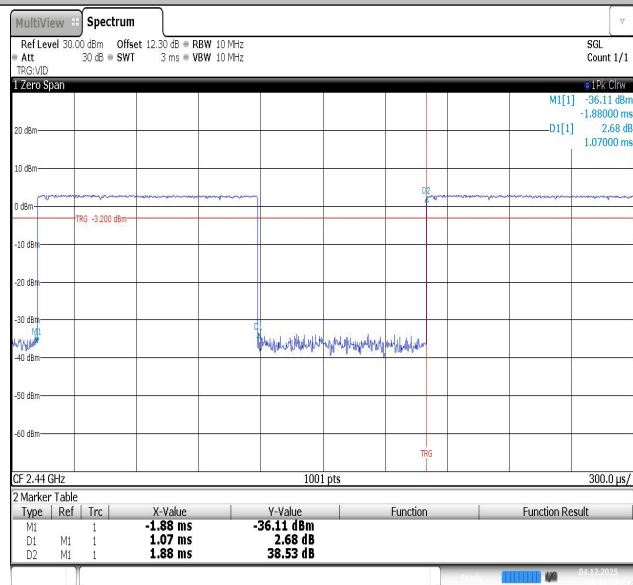


NTNV-BLE_1M-2480



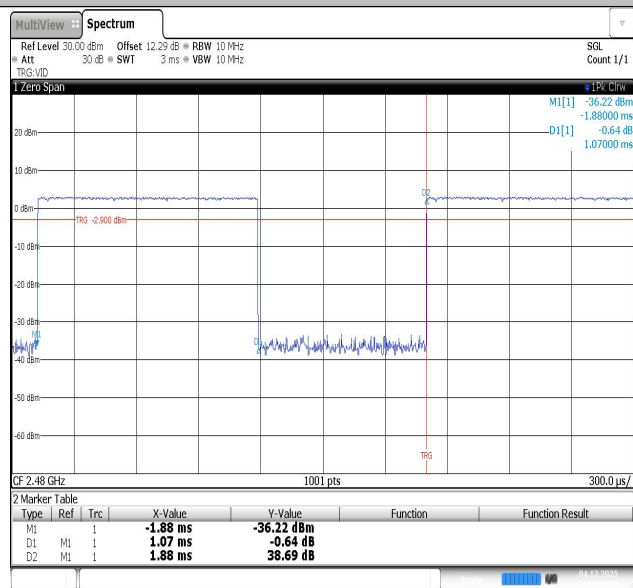
Date: 4 DEC 2025 10:42:46

NTNV-BLE_2M-2402



Date: 4 DEC 2025 10:44:48

NTNV-BLE_2M-2440

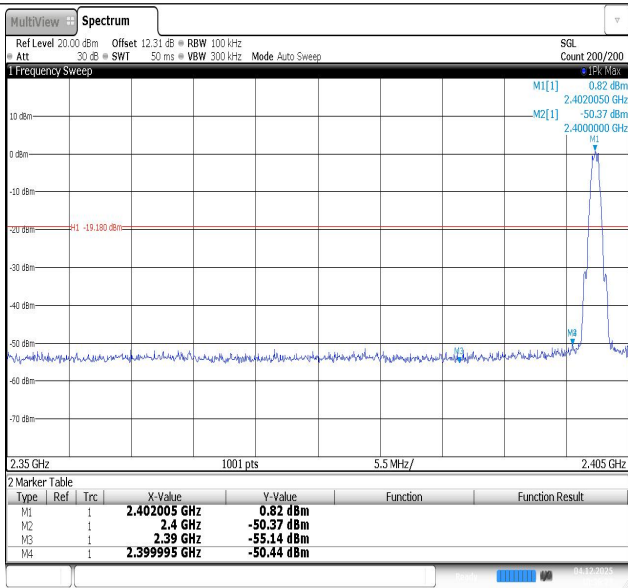


Date: 4 DEC 2025 10:46:08

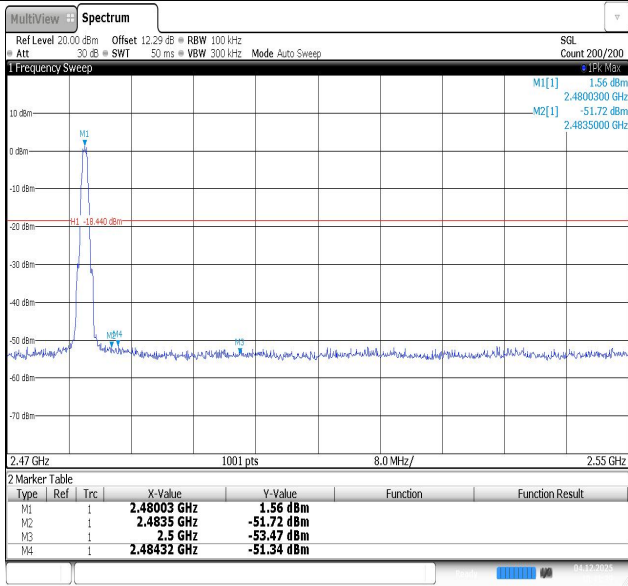
NTNV-BLE_2M-2480

Appendix F: Band edge and Spurious Emission(Conducted)

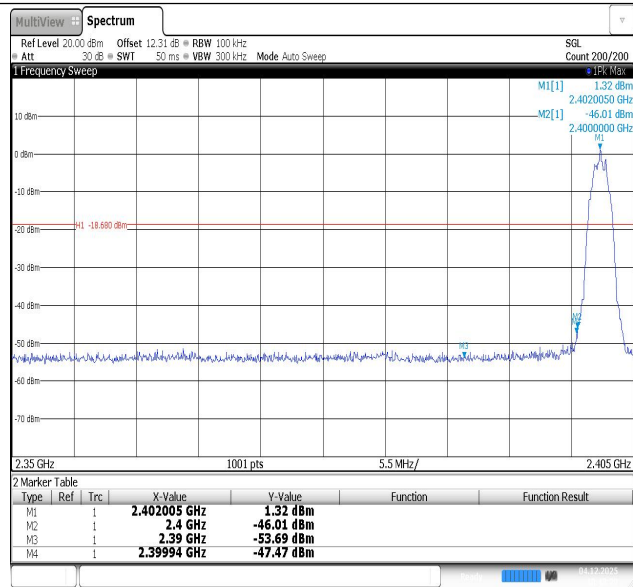
Test Graphs



BLE_1M-2402-PASS

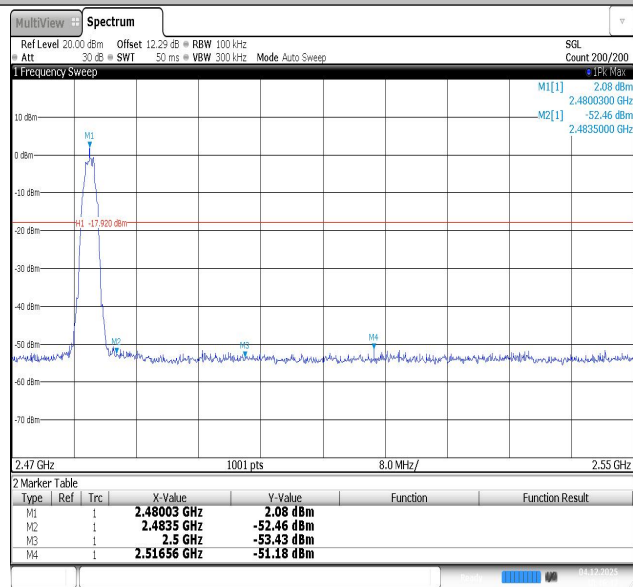


BLE_1M-2480-PASS



Date: 4 DEC 2025 10:43:21

BLE_2M-2402-PASS



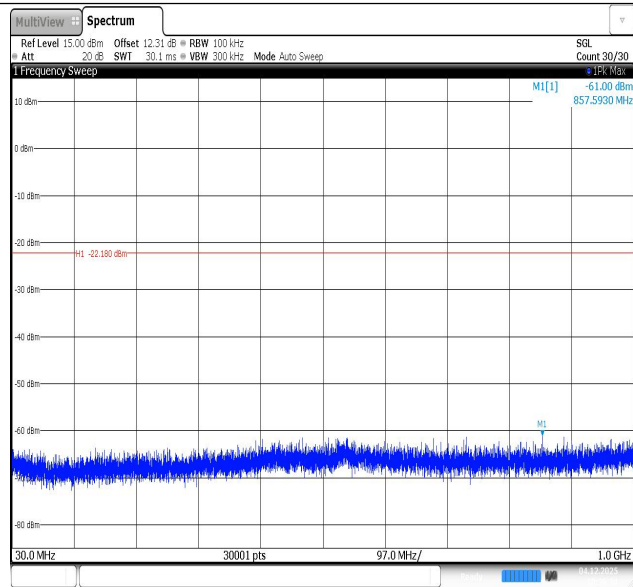
Date: 4 DEC 2025 10:46:43

BLE_2M-2480-PASS



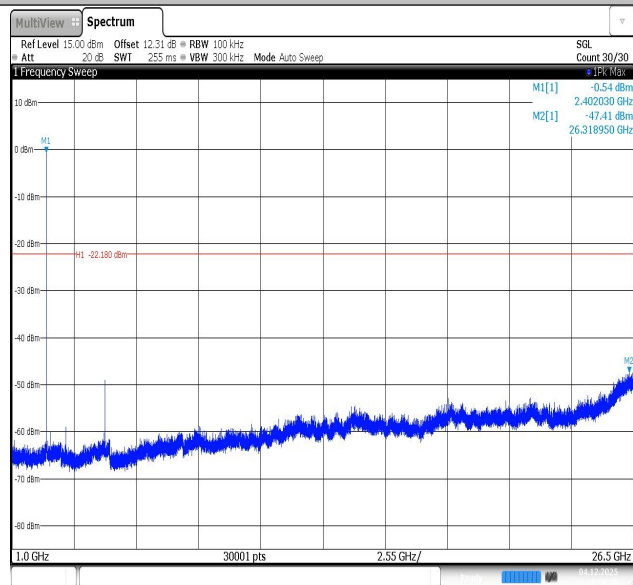
Date: 4 DEC 2025 10:36:30

BLE_1M-2402-0~Reference-PASS



Date: 4 DEC 2025 10:36:35

BLE_1M-2402-30~1000-PASS



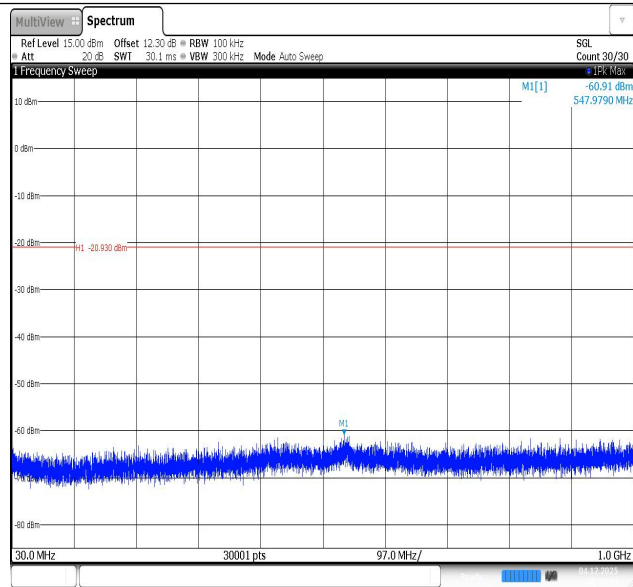
Date: 4 DEC 2025 10:36:49

BLE_1M-2402-1000~26500-PASS



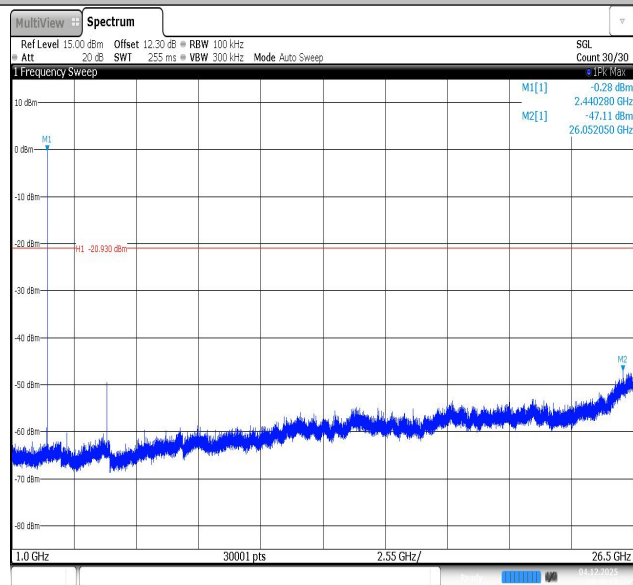
Date: 4 DEC 2025 10:37:58

BLE_1M-2440-0~Reference-PASS



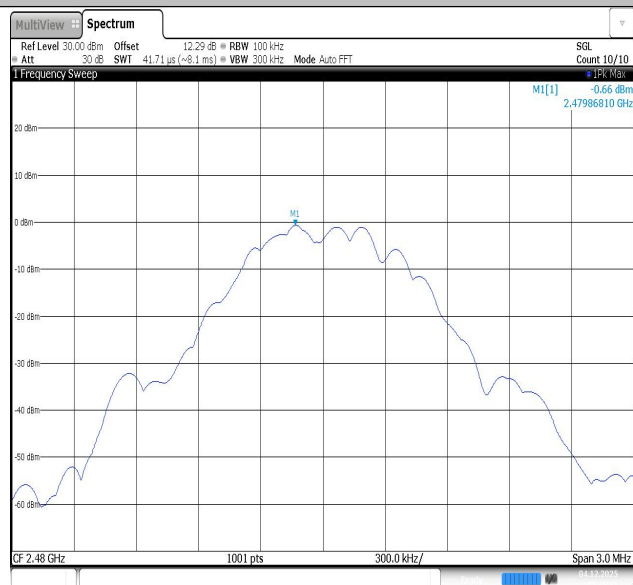
Date: 4 DEC 2025 10:38:03

BLE_1M-2440-30~1000-PASS



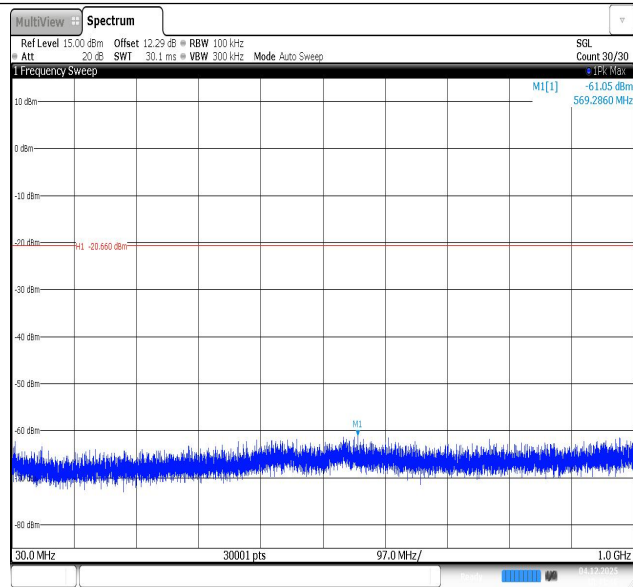
Date: 4 DEC 2025 10:38:17

BLE_1M-2440-1000~26500-PASS



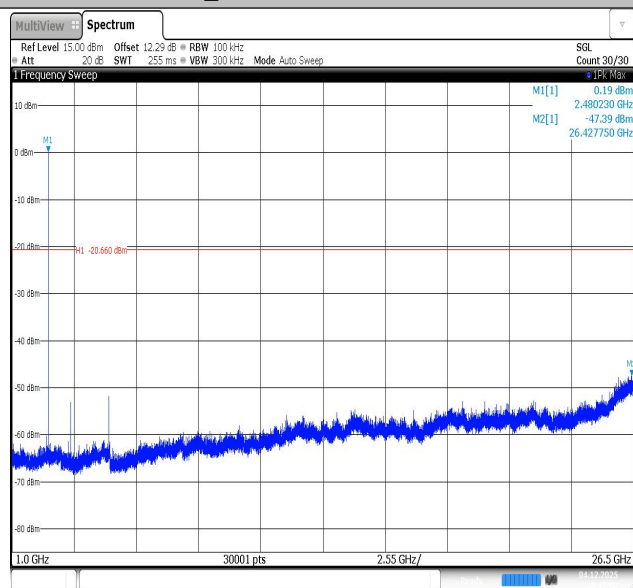
Date: 4 DEC 2025 10:41:41

BLE_1M-2480-0~Reference-PASS



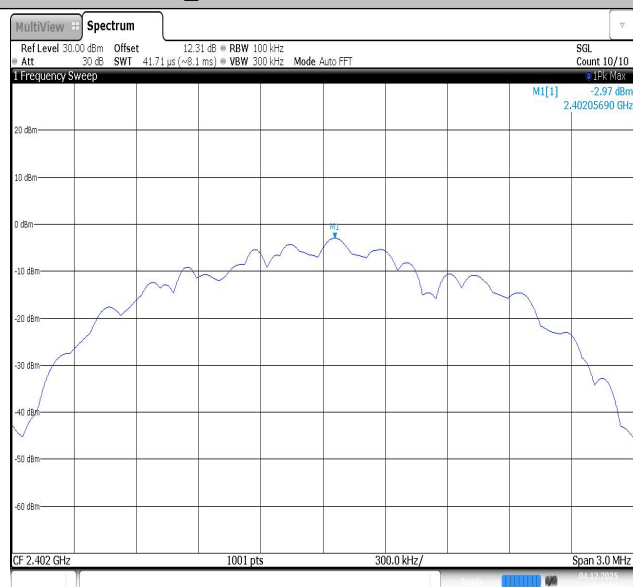
Date: 4 DEC 2025 10:41:46

BLE_1M-2480-30~1000-PASS



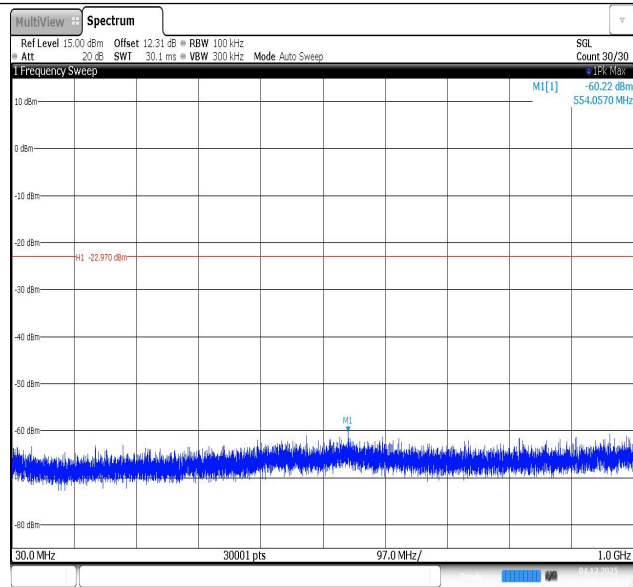
Date: 4 DEC 2025 10:42:00

BLE_1M-2480-1000~26500-PASS



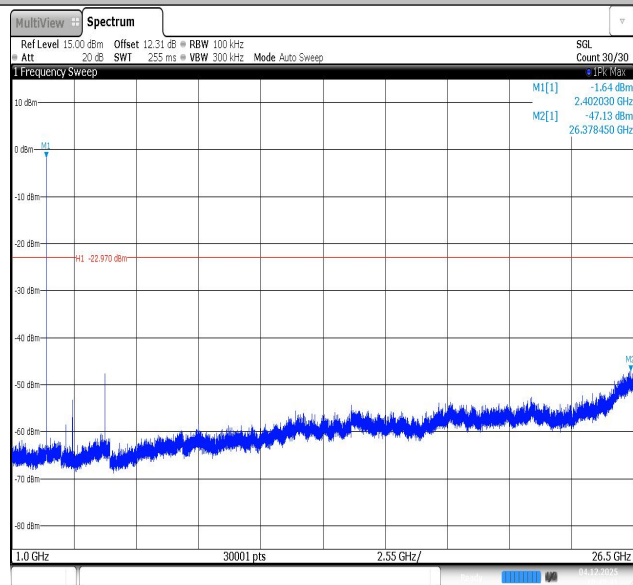
Date: 4 DEC 2025 10:43:23

BLE_2M-2402-0~Reference-PASS



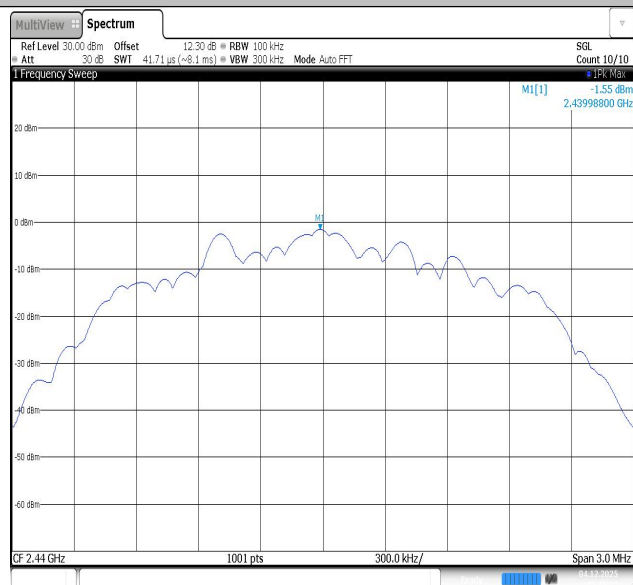
Date: 4 DEC 2025 10:43:28

BLE_2M-2402-30~1000-PASS



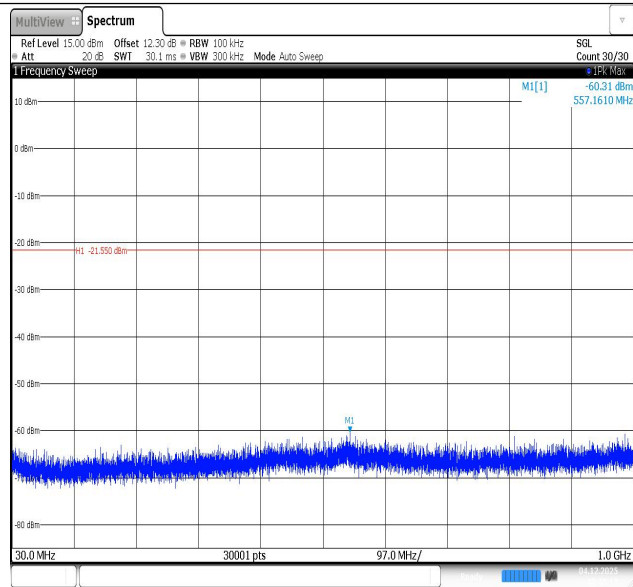
Date: 4 DEC 2025 10:43:42

BLE_2M-2402-1000~26500-PASS



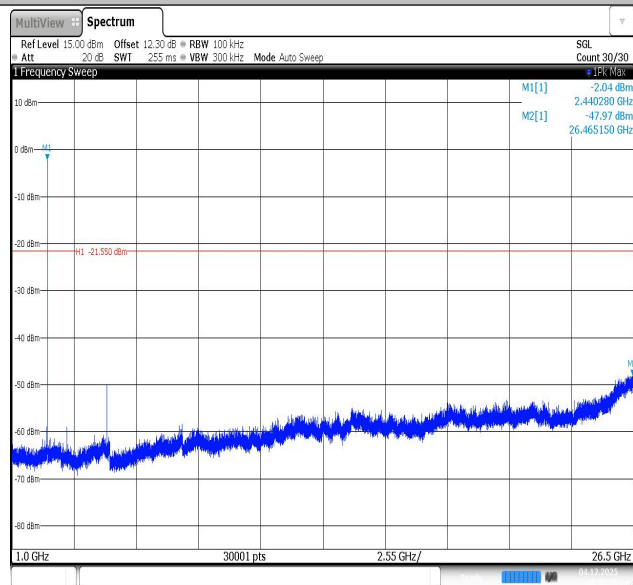
Date: 4 DEC 2025 10:45:12

BLE_2M-2440-0~Reference-PASS



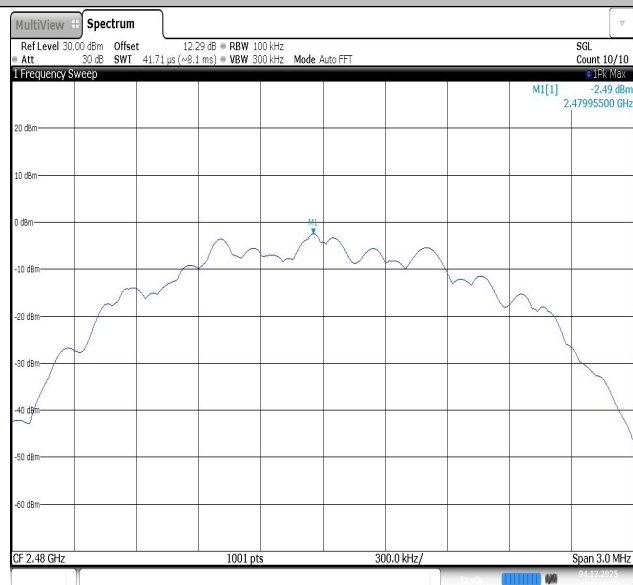
Date: 4 DEC 2025 10:45:17

BLE_2M-2440-30~1000-PASS



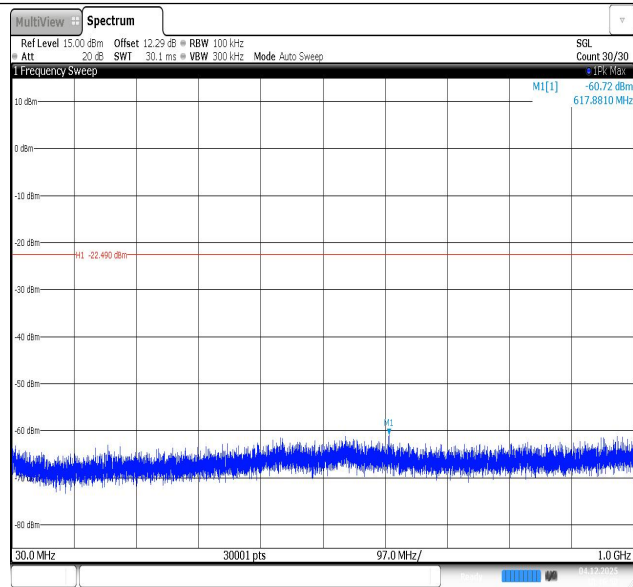
Date: 4 DEC 2025 10:45:31

BLE_2M-2440-1000~26500-PASS



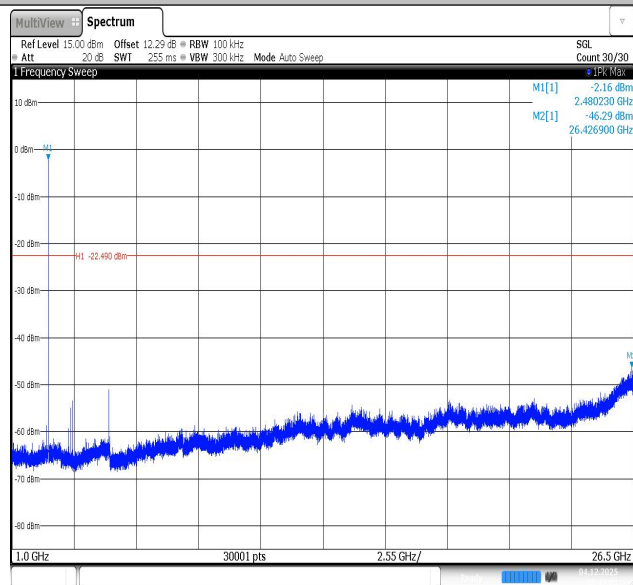
Date: 4 DEC 2025 10:48:45

BLE_2M-2480-0~Reference-PASS



Date: 4 DEC 2025 10:46:50

BLE_2M-2480-30~1000-PASS



Date: 4 DEC 2025 10:47:04

BLE_2M-2480-1000~26500-PASS

-----End of Report-----