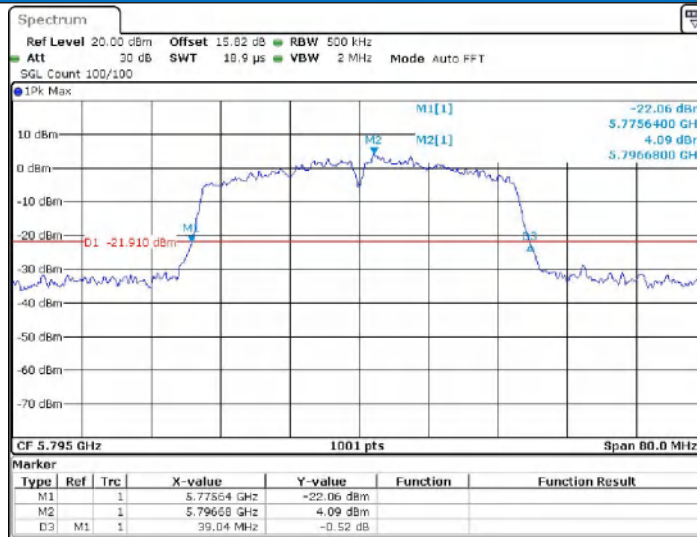
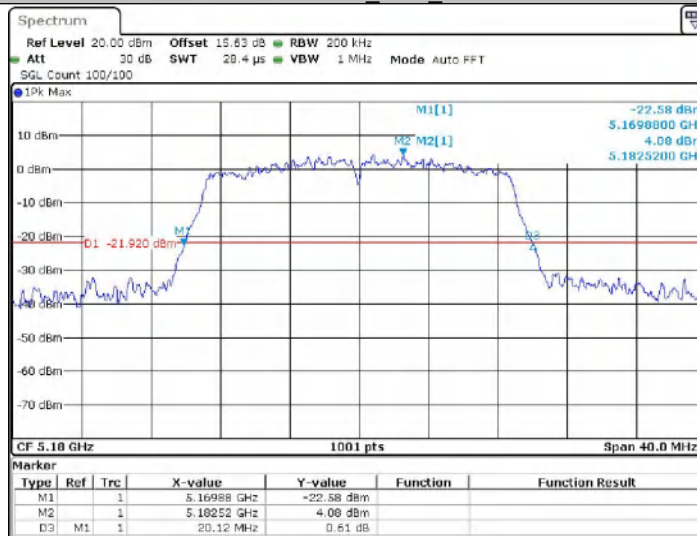




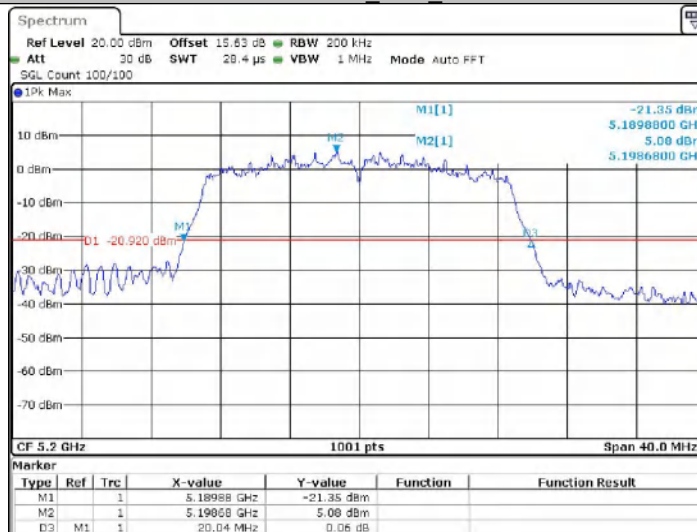
Date: 26 FEB 2026 20:27:33

11AC20SISO_Ant2_5180



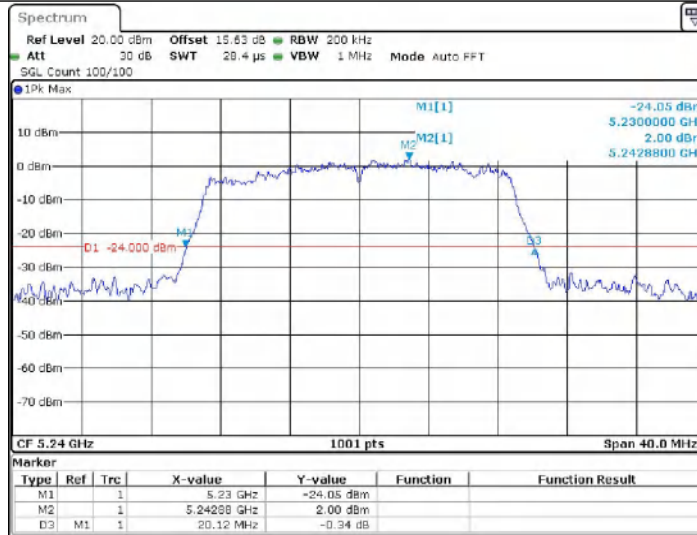
Date: 26 FEB 2026 20:31:40

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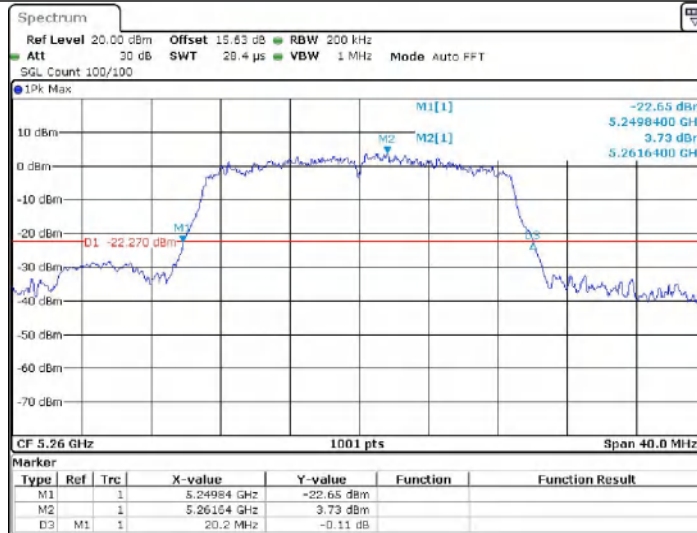
Date: 26 FEB 2026 20:33:44

11AC20SISO_Ant2_5240



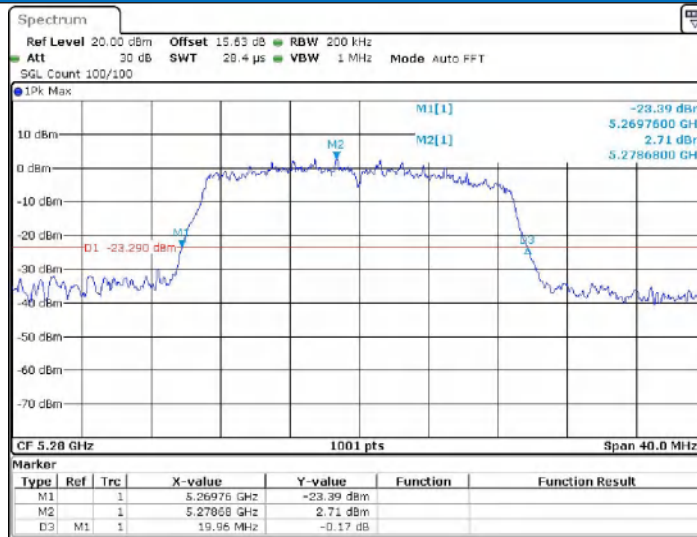
Date: 26 FEB 2026 20:38:18

11AC20SISO_Ant2_5260



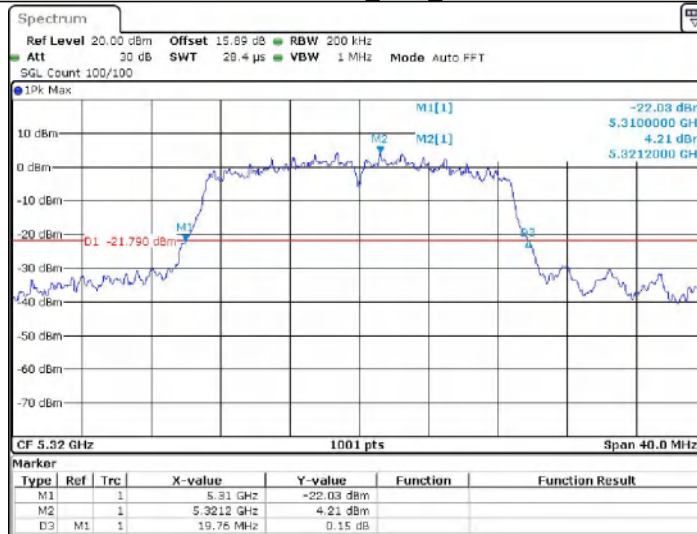
Date: 26 FEB 2026 20:37:19

11AC20SISO_Ant2_5280



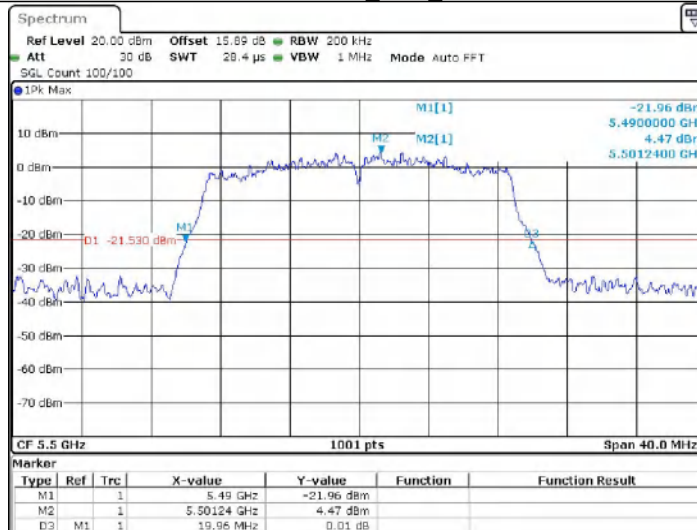
Date: 26 FEB 2026 20:38:25

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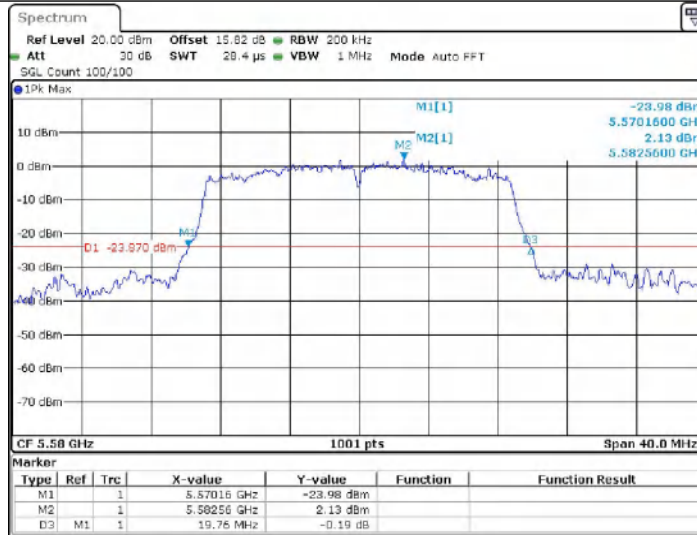
Date: 26 FEB 2026 20:38:35

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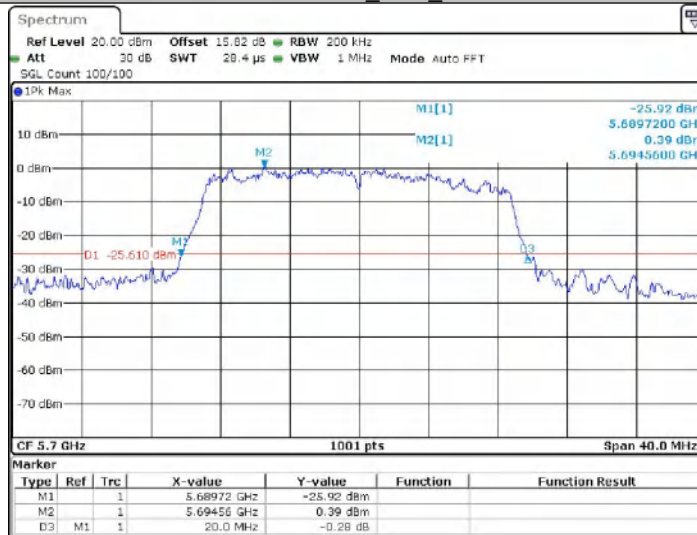
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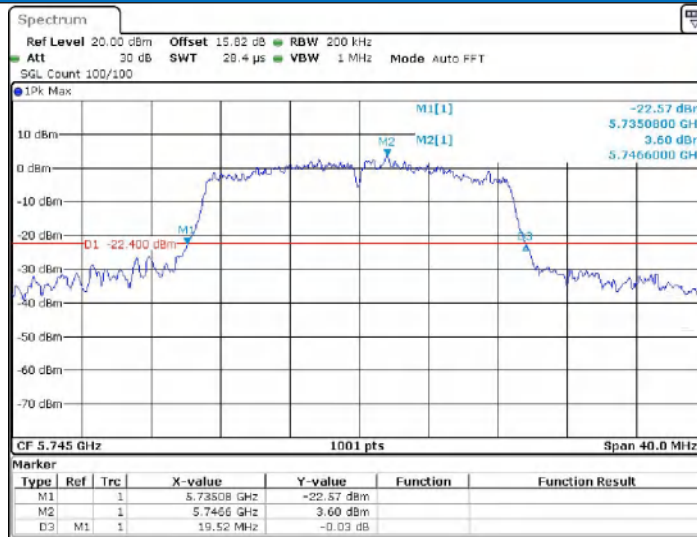
Date: 26 FEB 2026 20:43:04

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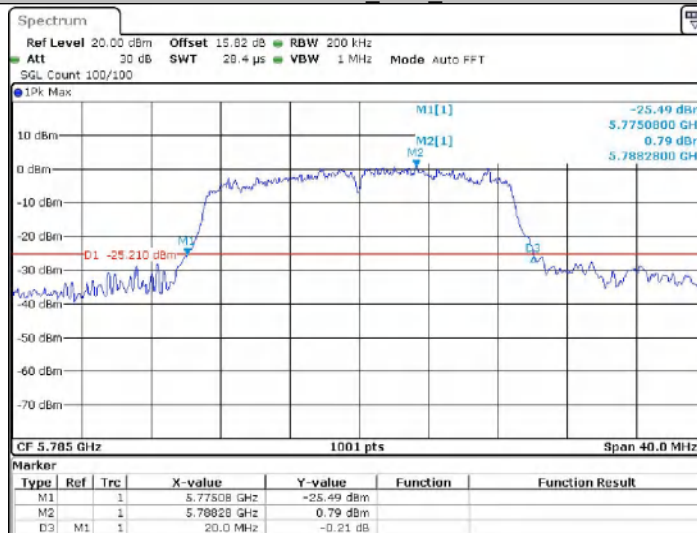
Date: 26 FEB 2026 20:44:15

11AC20SISO_Ant2_5745



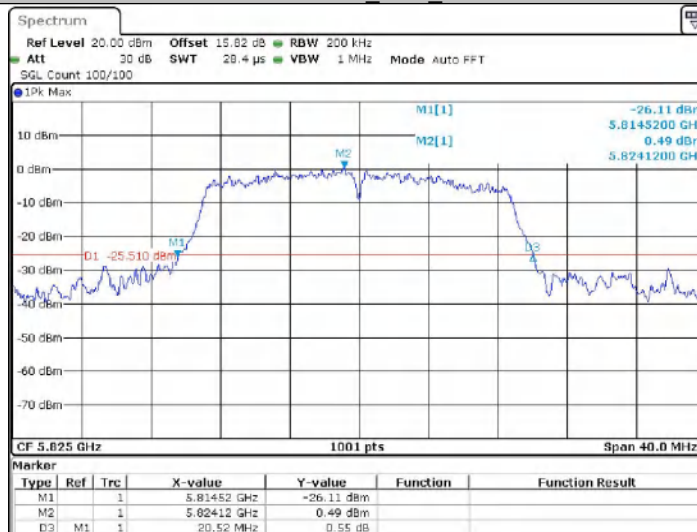
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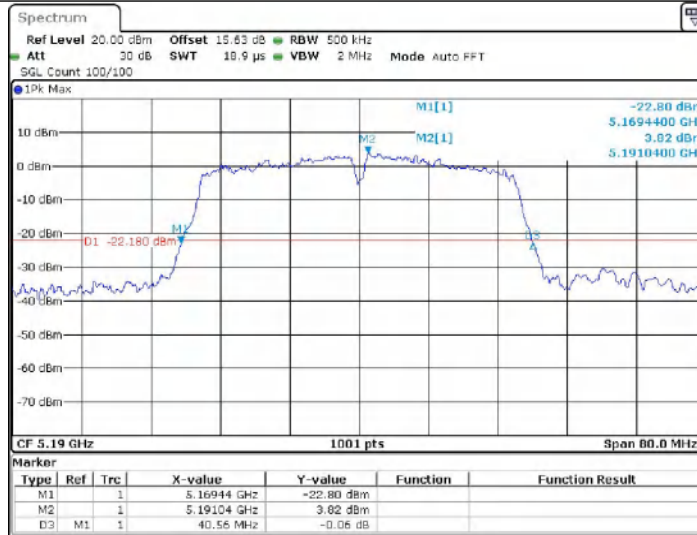
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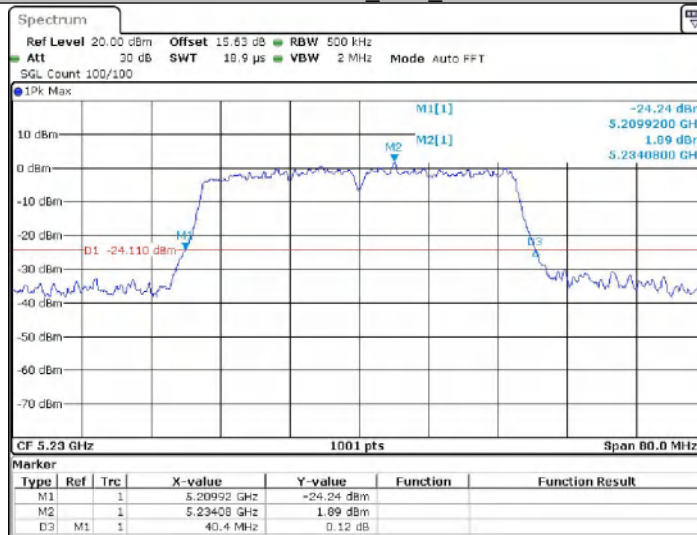
Date: 26 FEB 2026 20:48:23

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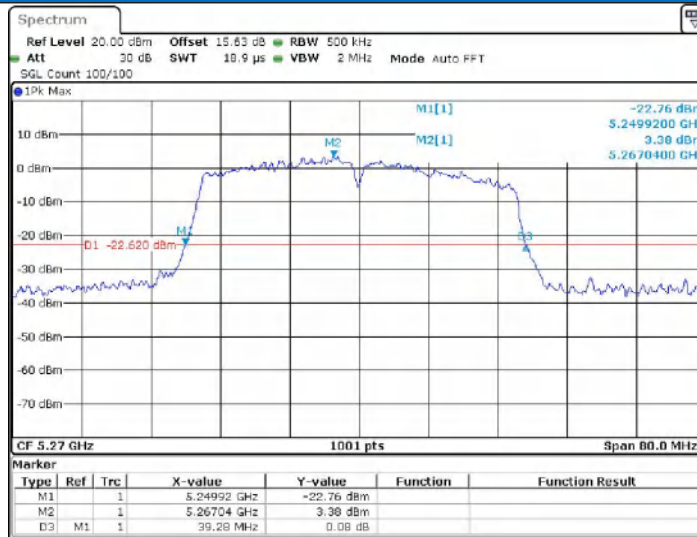
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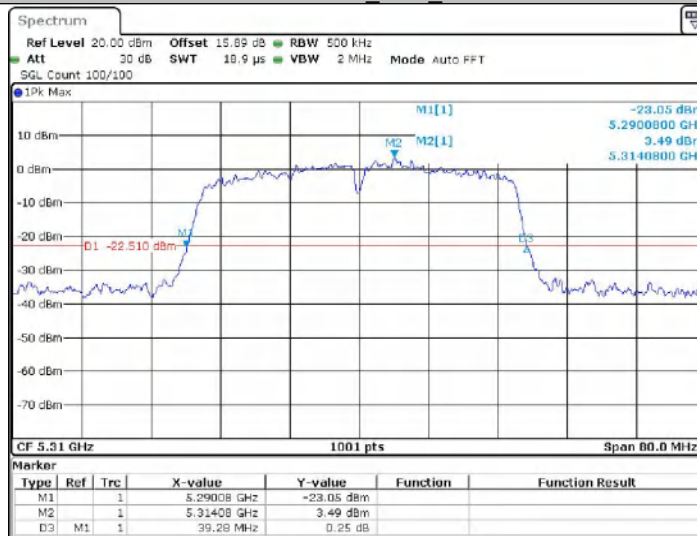


Date: 26 FEB 2026 20:55:03

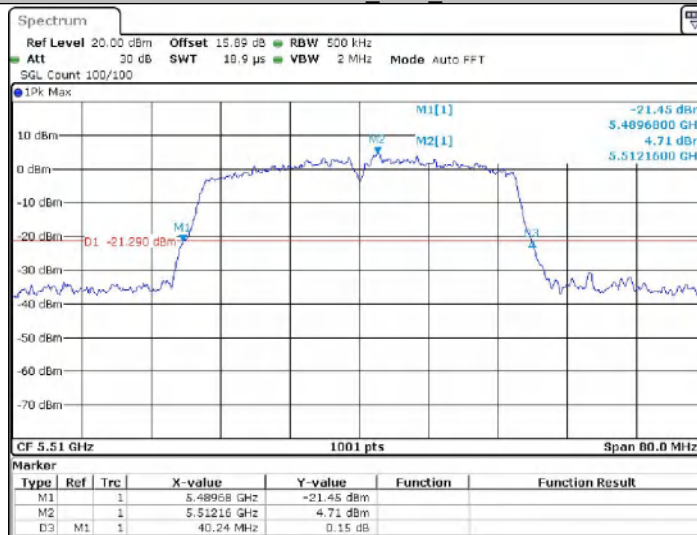
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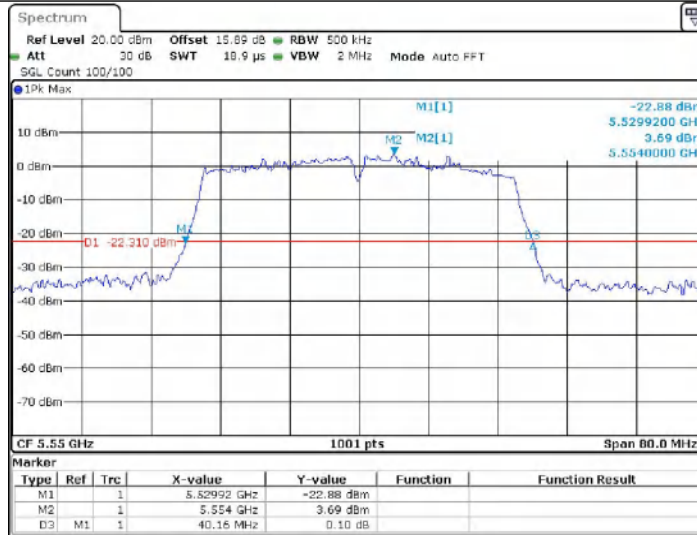
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11AC40SISO_Ant2_5510

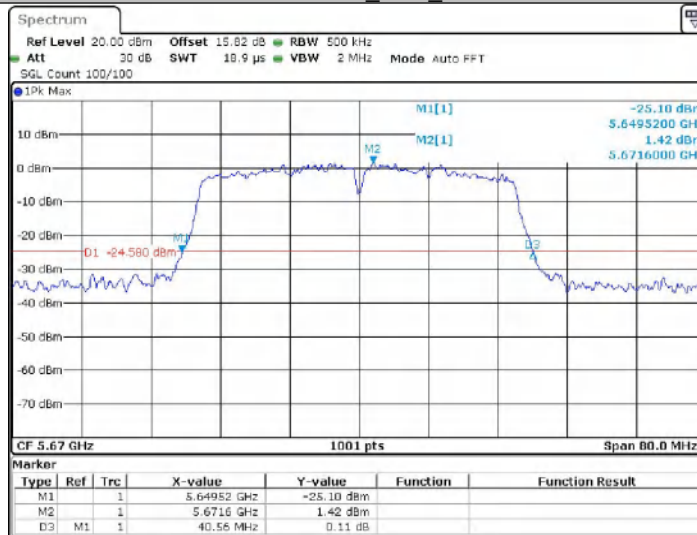


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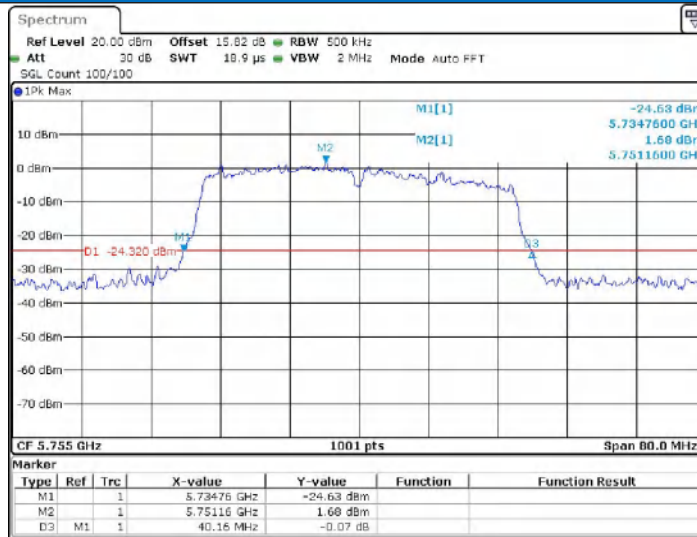
Date: 26 FEB 2026 21:04:08

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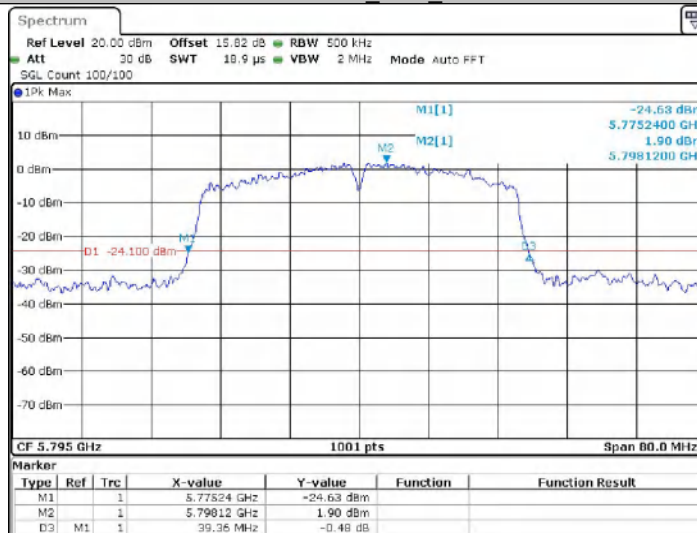
Date: 26 FEB 2026 21:08:25

11AC40SISO_Ant2_5755



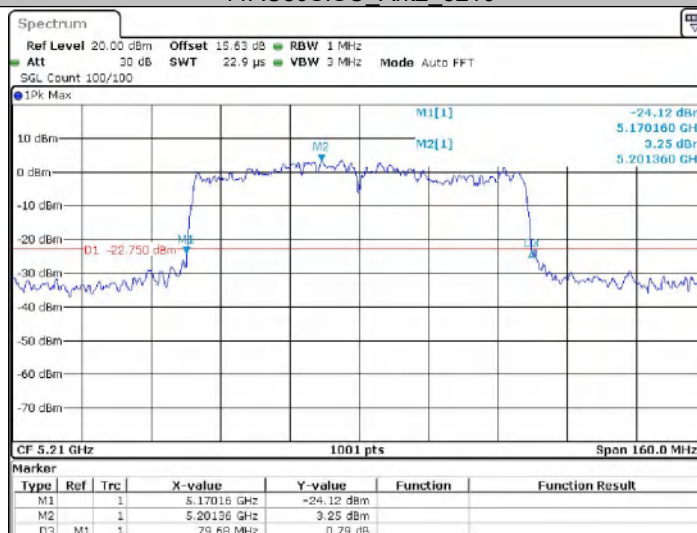
Date: 26 FEB 2026 21:14:59

11AC40SISO_Ant2_5795



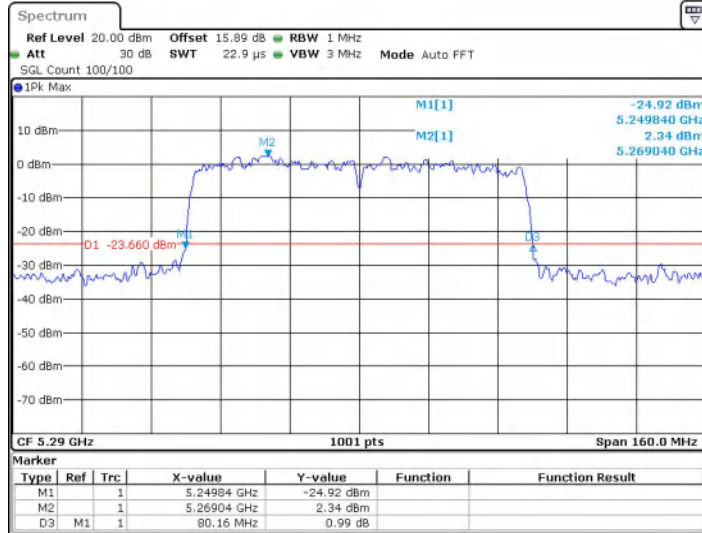
Date: 26 FEB 2026 21:17:14

11AC80SISO_Ant2_5210



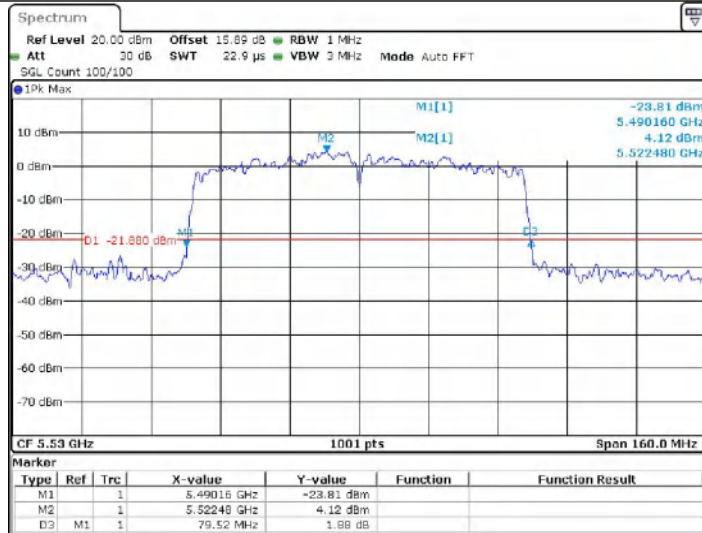
Date: 26 FEB 2026 21:19:58

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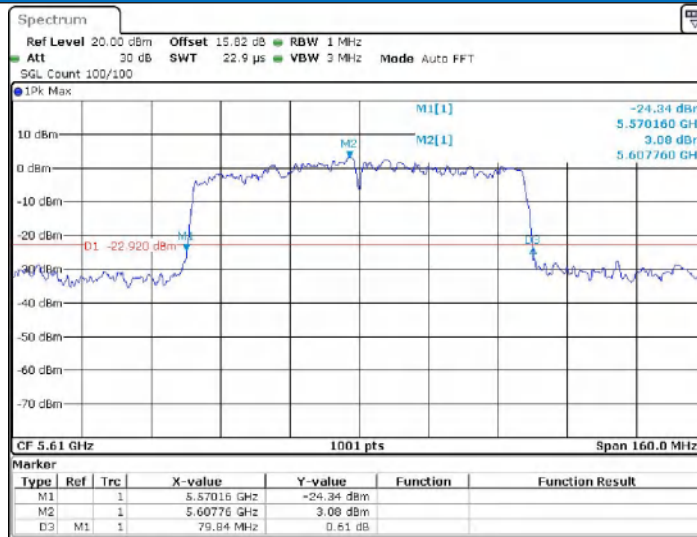
Date: 2 MAR 2026 10:13:29

11AC80SISO_Ant2_5530



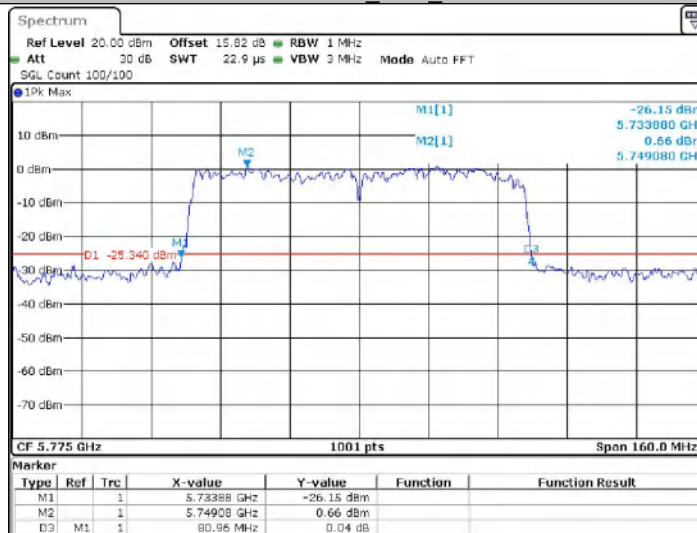
Date: 2 MAR 2026 10:18:44

11AC80SISO_Ant2_5610



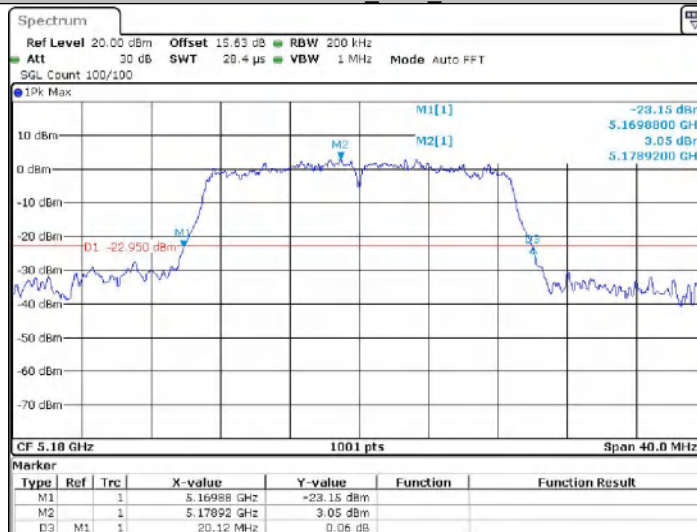
Date: 2 MAR 2026 10:20:30

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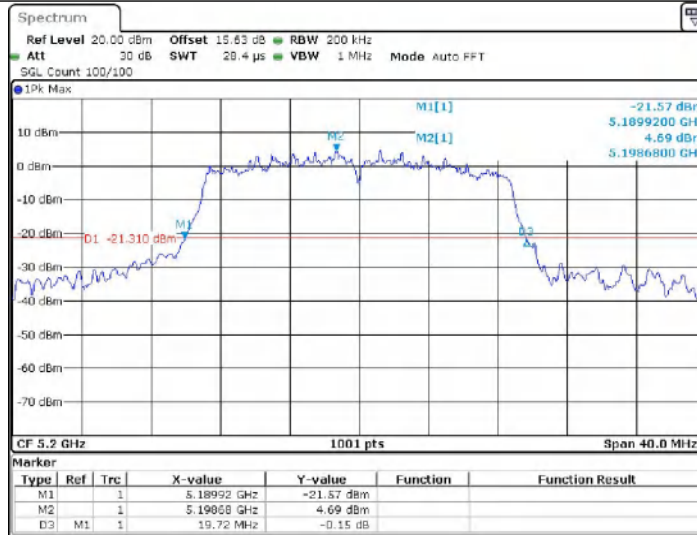
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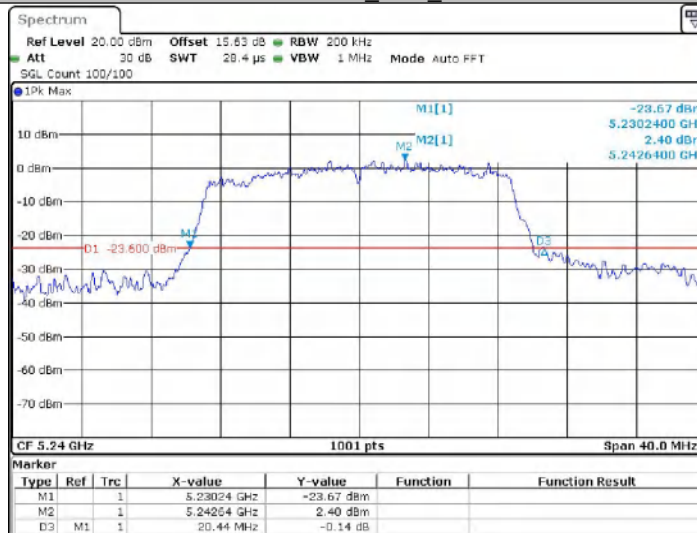
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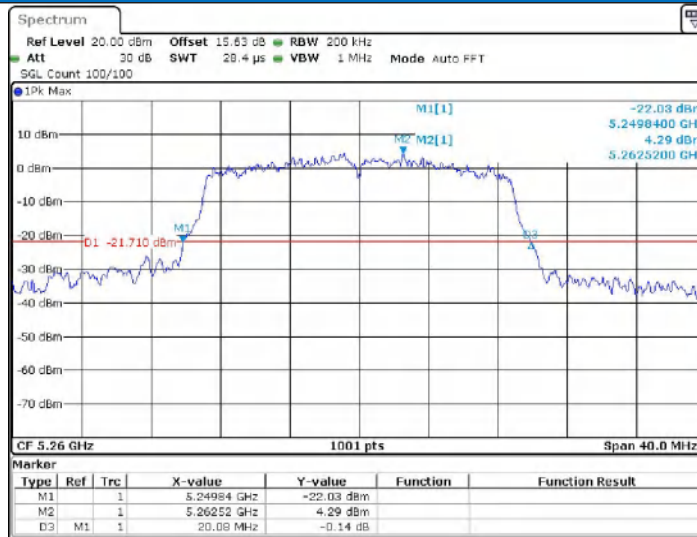
Date: 2 MAR 2026 10:31:05

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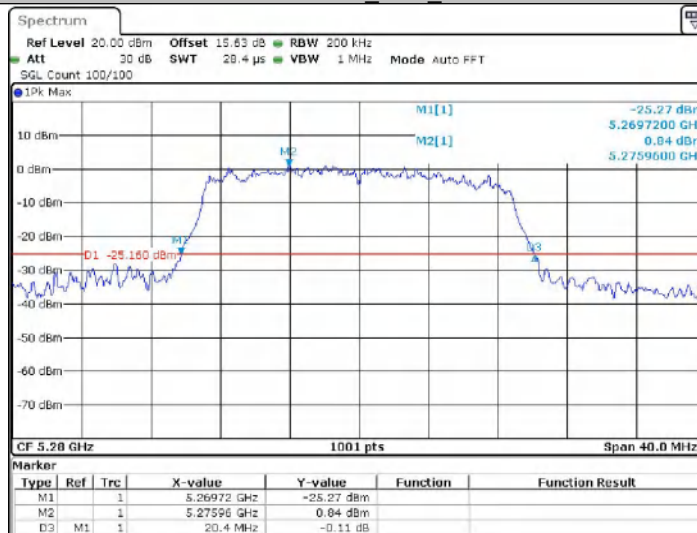
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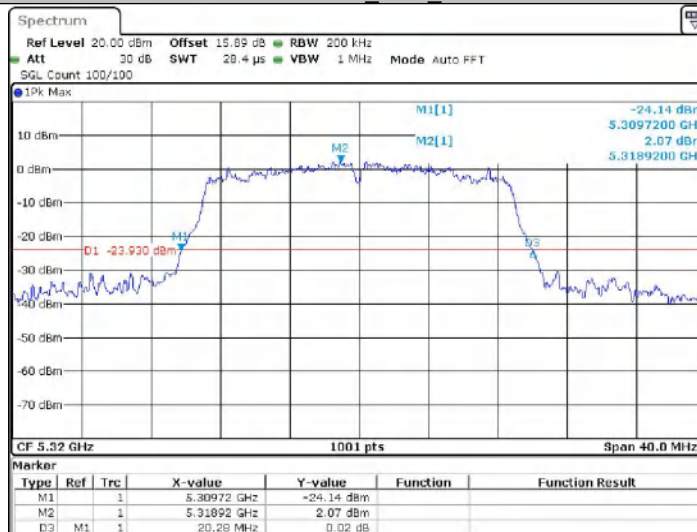
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11AX20SISO_Ant2_5280



Date: 2 MAR 2026 10:36:31

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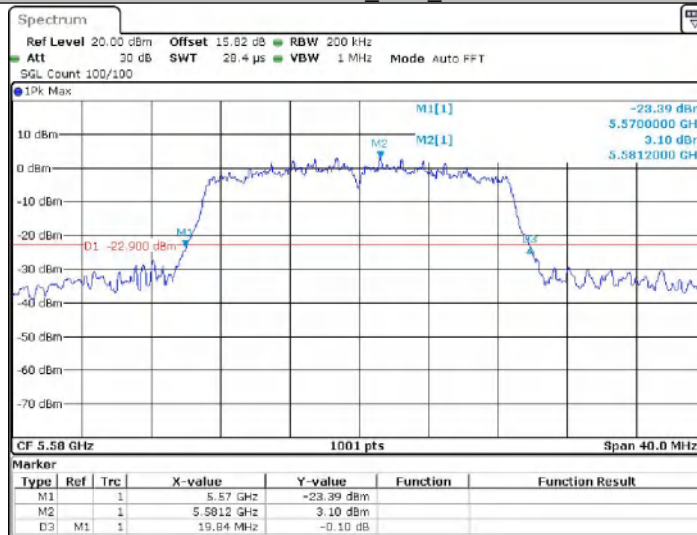
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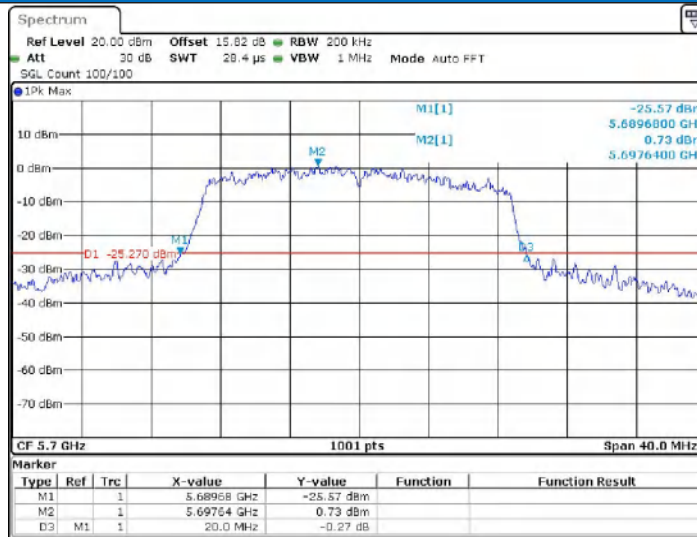
Date: 2 MAR 2026 10:42:43

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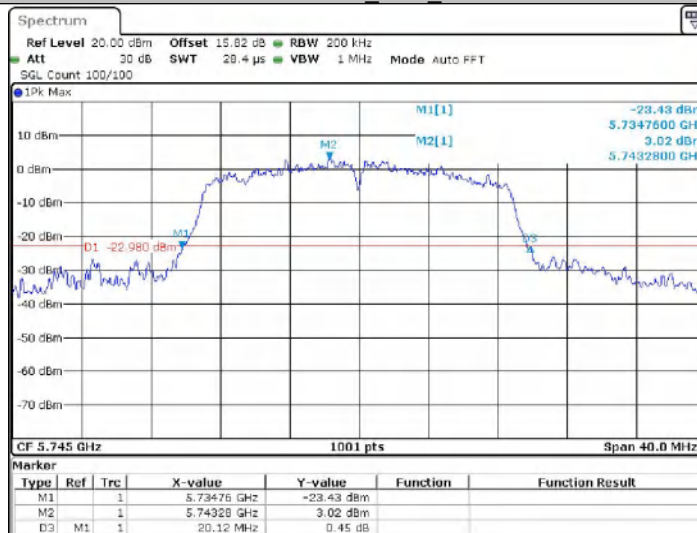
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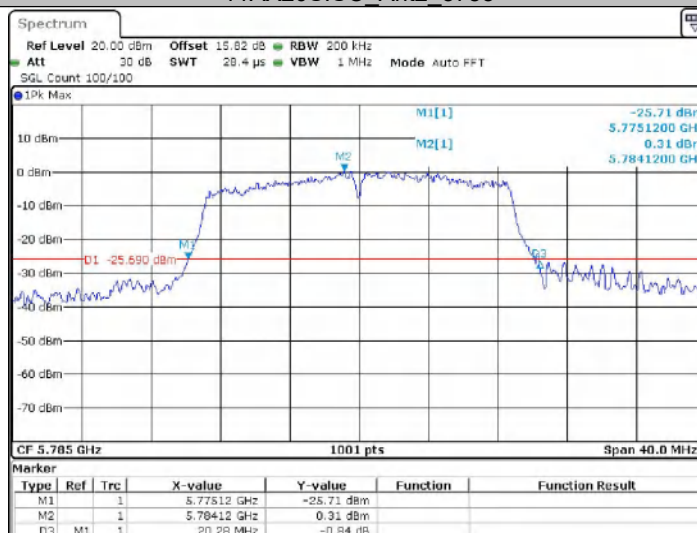
Date: 2 MAR 2026 10:49:40

11AX20SISO_Ant2_5745



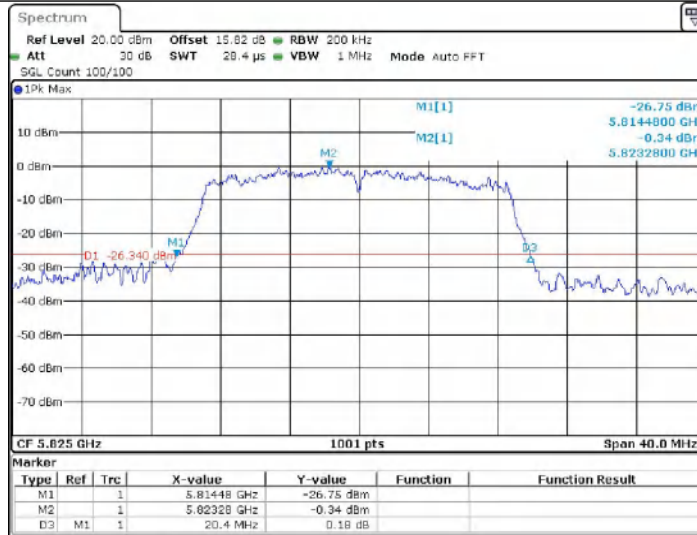
Date: 2 MAR 2026 10:52:59

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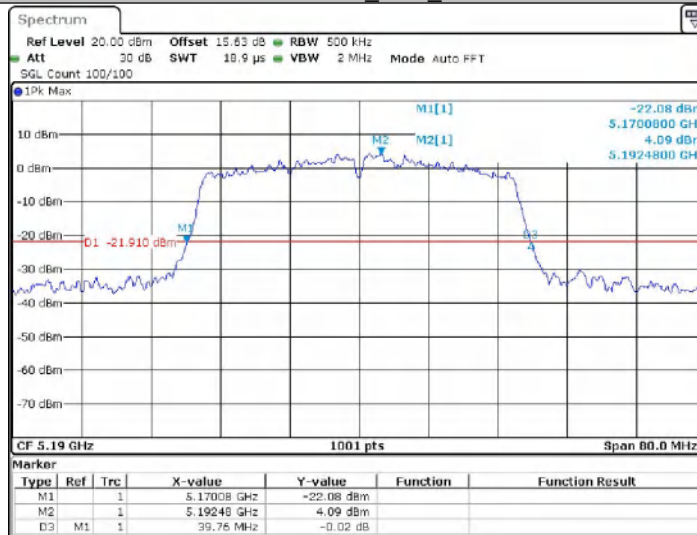
Date: 2 MAR 2026 10:56:41

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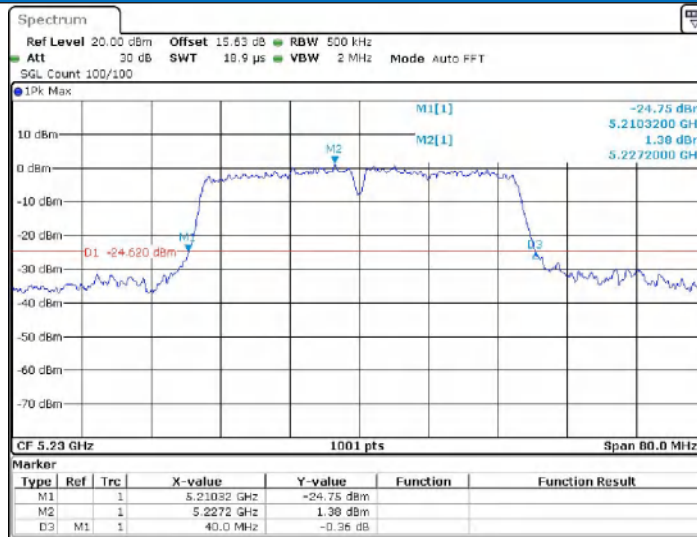
Date: 2 MAR 2026 10:58:27

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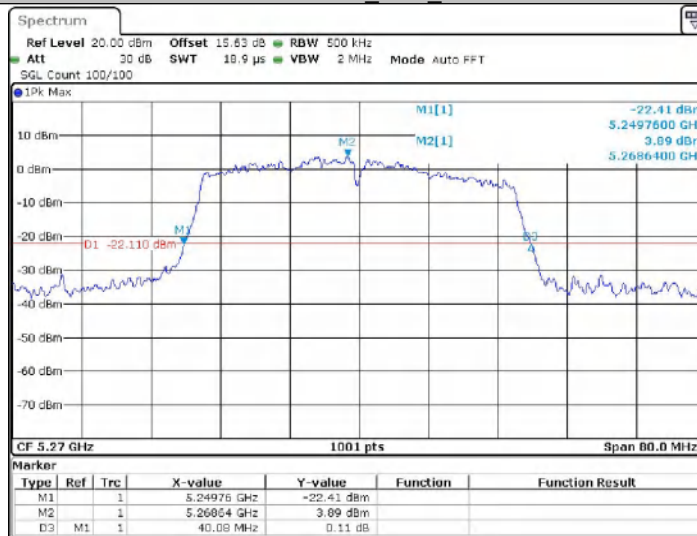


Date: 2 MAR 2026 11:08:58

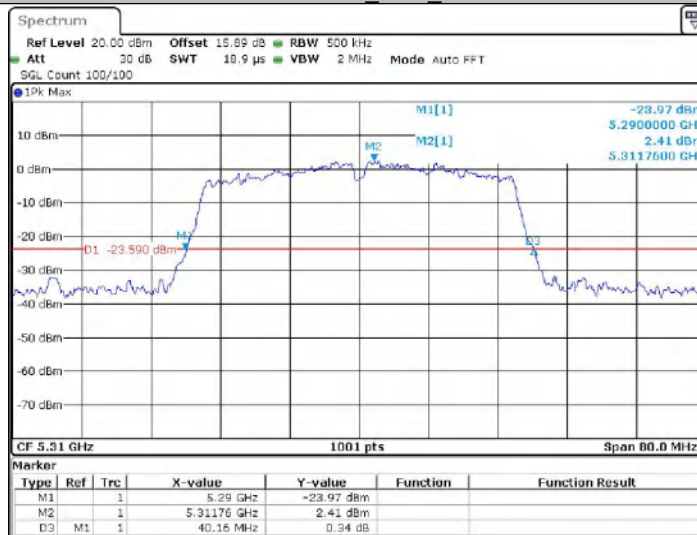
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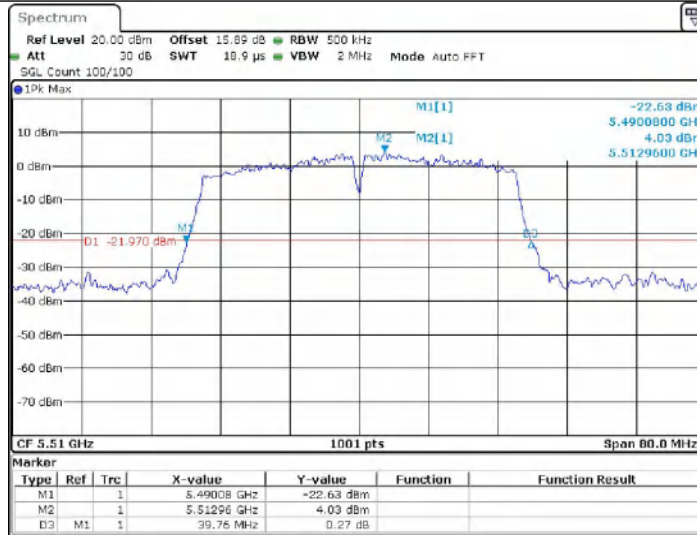
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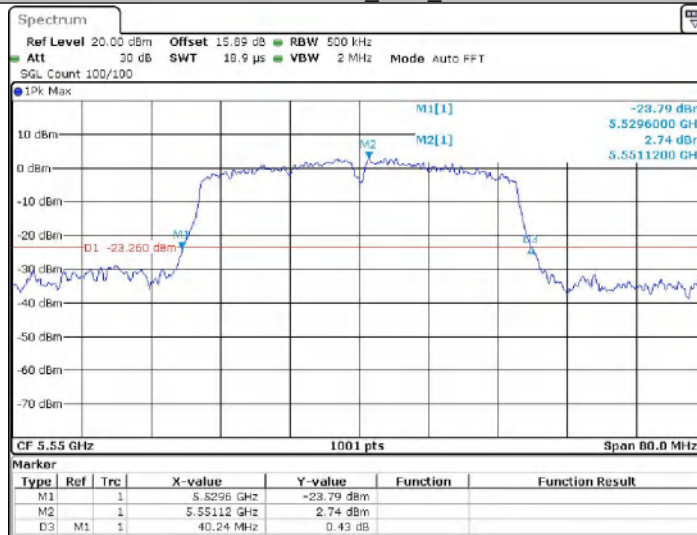


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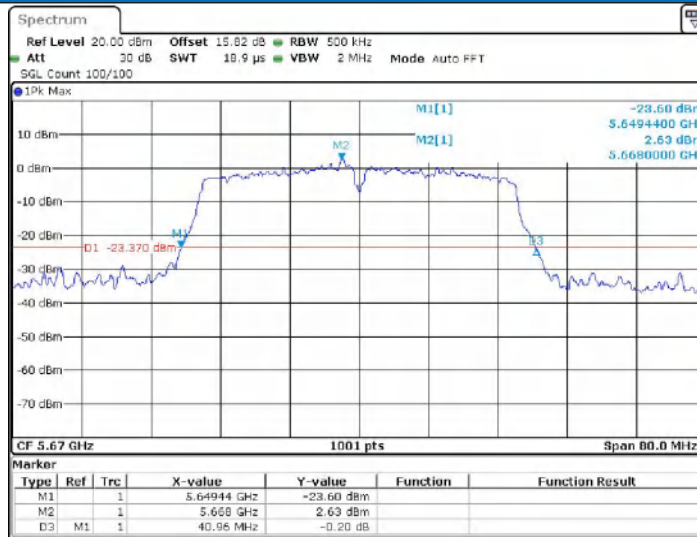
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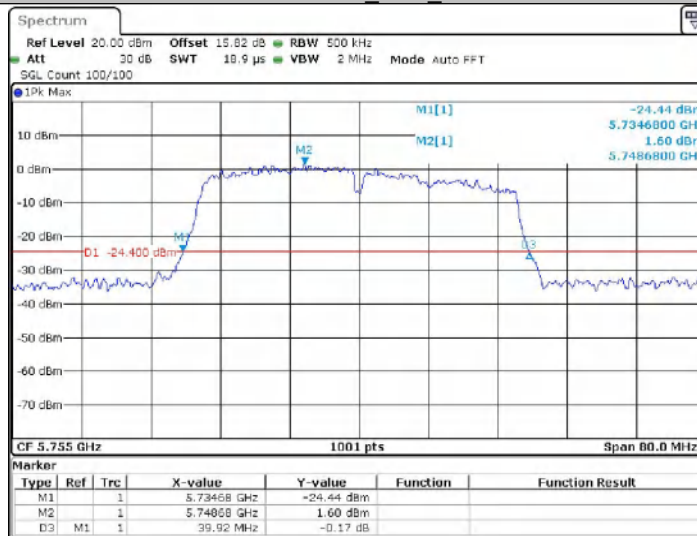


Date: 2 MAR 2026 11:44:02

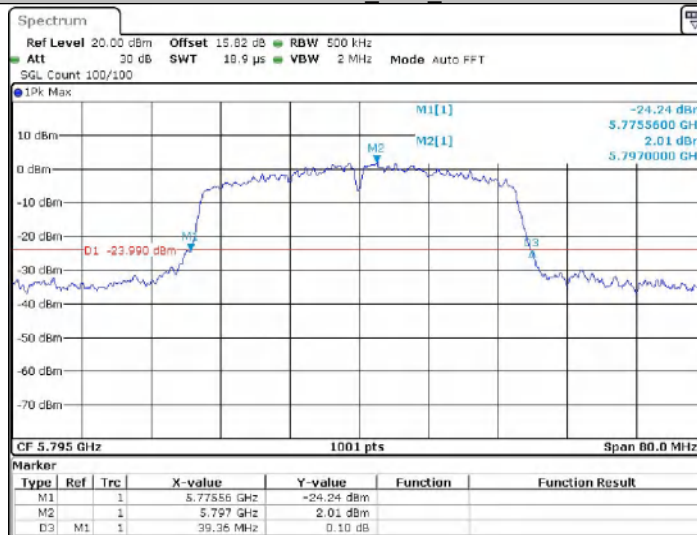
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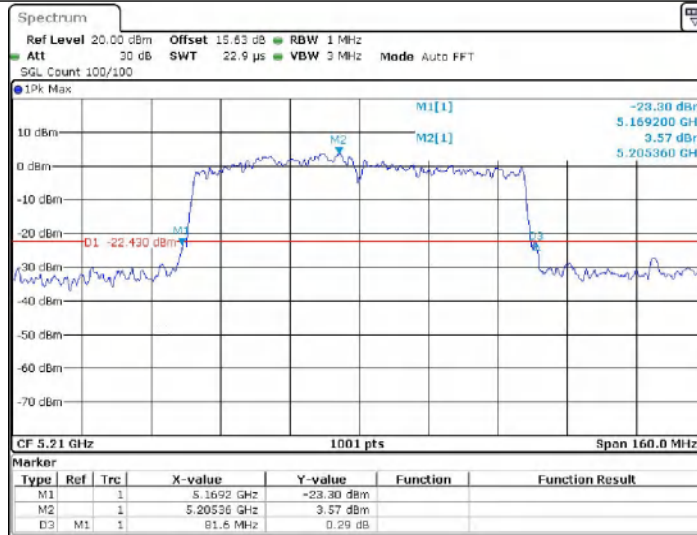
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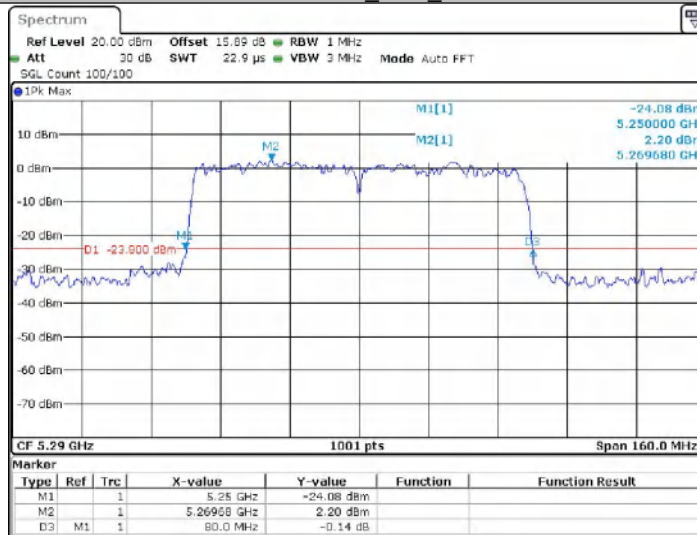


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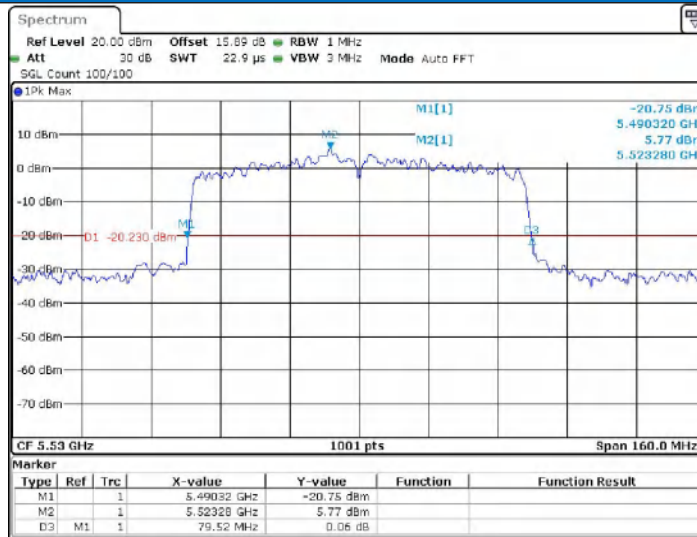
Date: 2 MAR 2026 11:54:41

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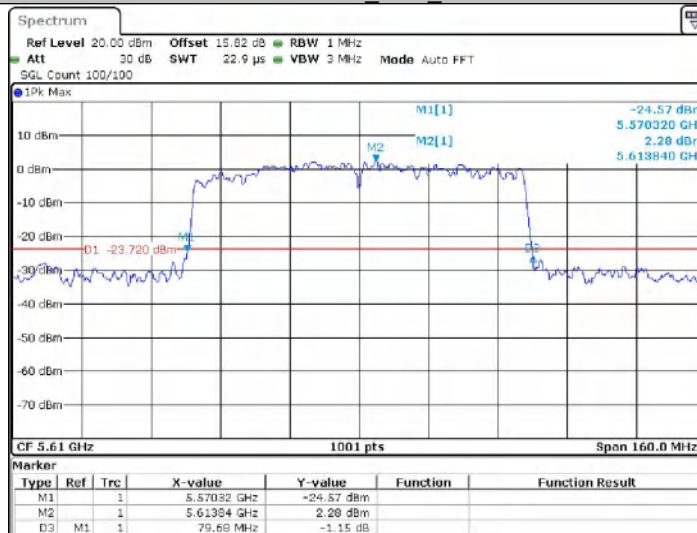
Date: 2 MAR 2026 11:58:47

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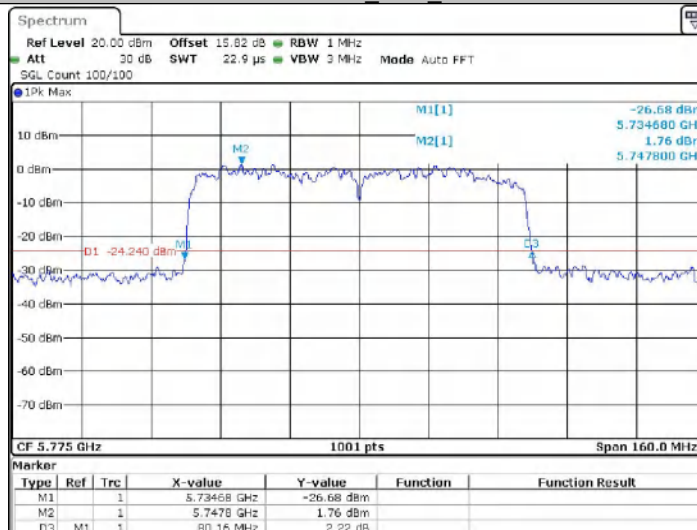
Date: 2 MAR 2026 12:01:11

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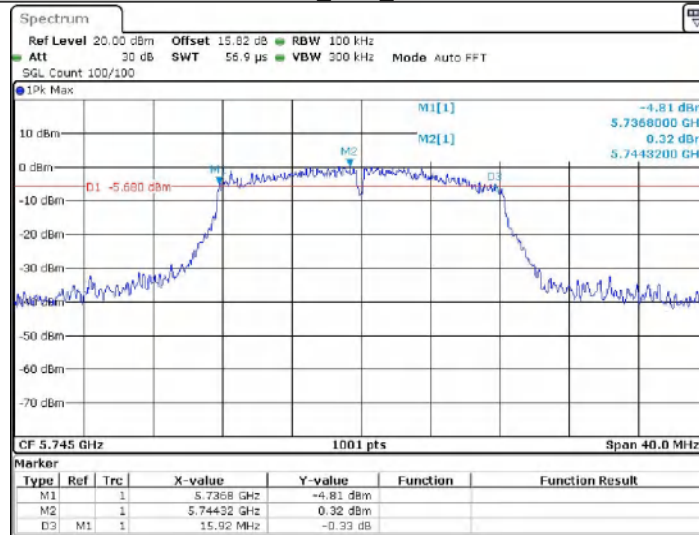
Date: 2 MAR 2026 12:03:04

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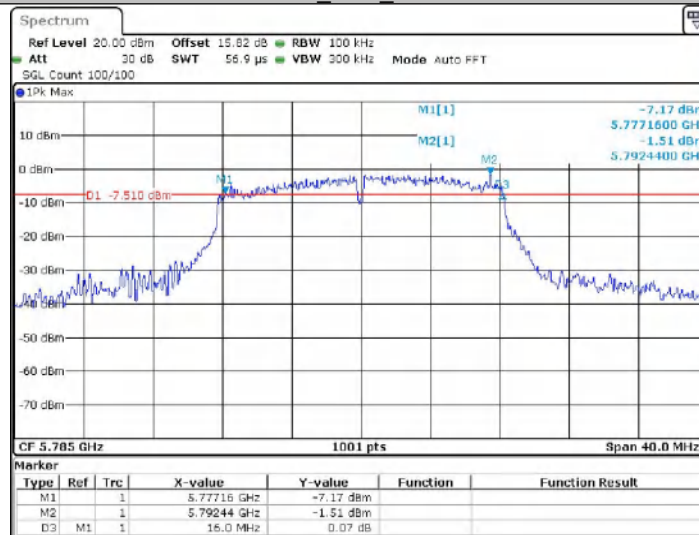
Date: 2 MAR 2026 13:38:13

11A_Ant2_5745



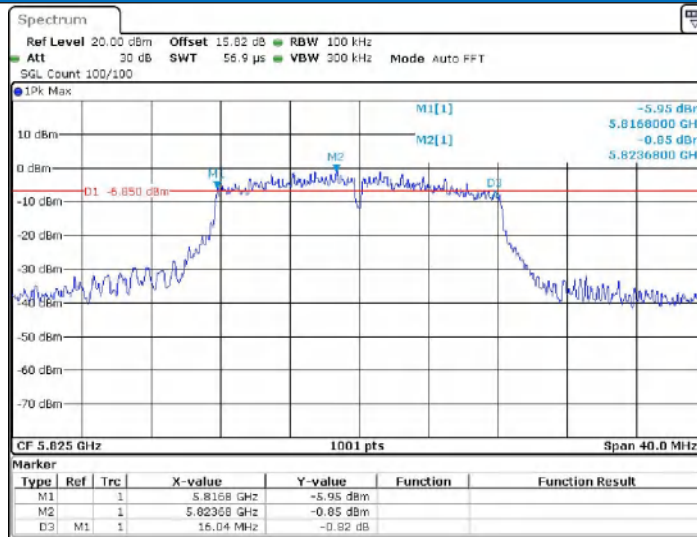
Date: 26 FEB 2026 19:43:59

11A_Ant2_5785



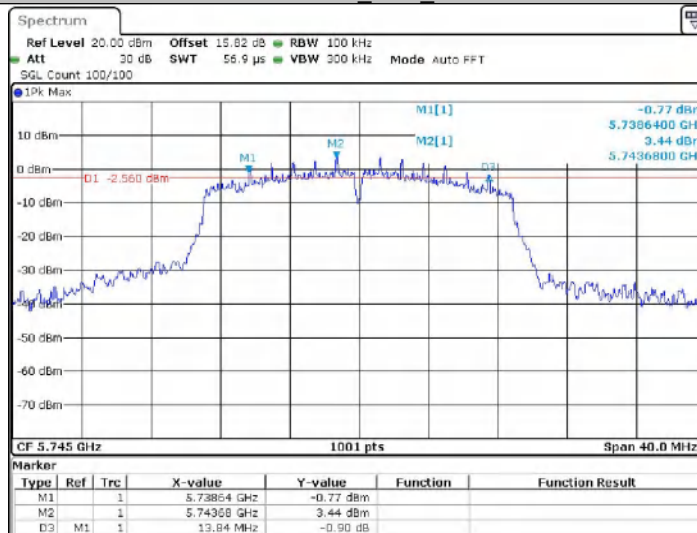
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11A_Ant2_5825



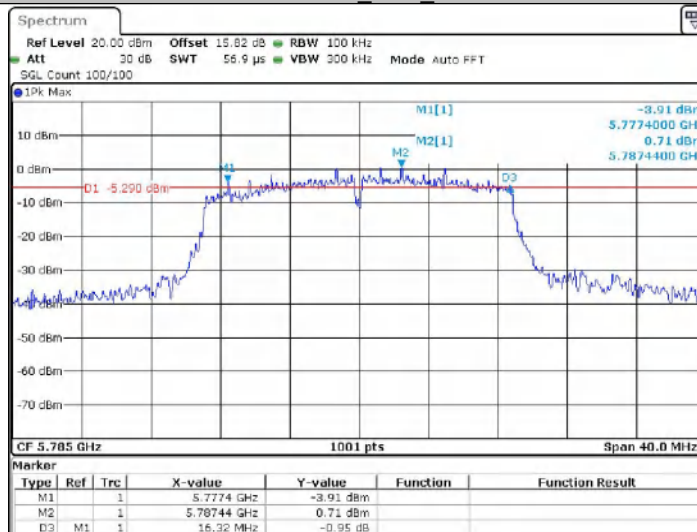
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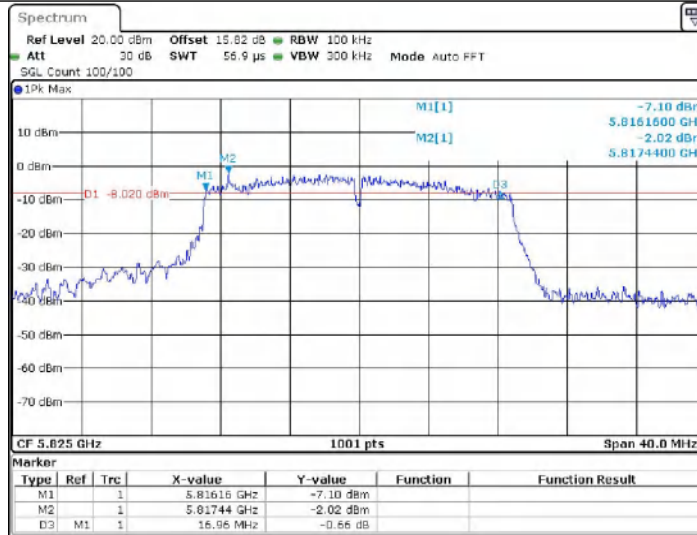
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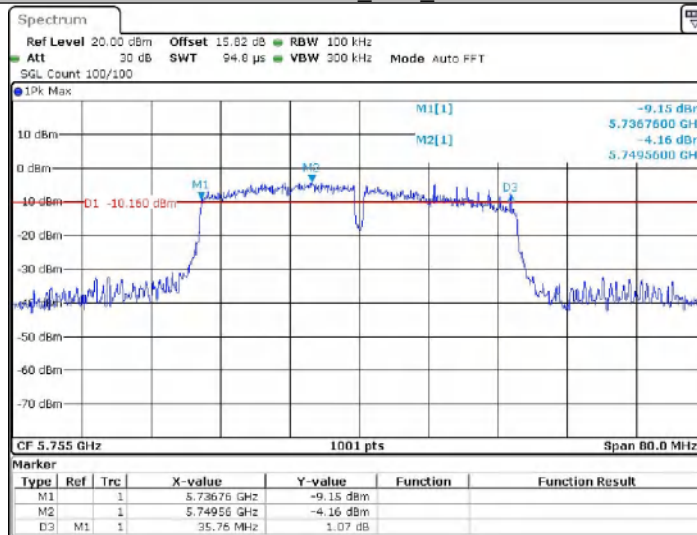
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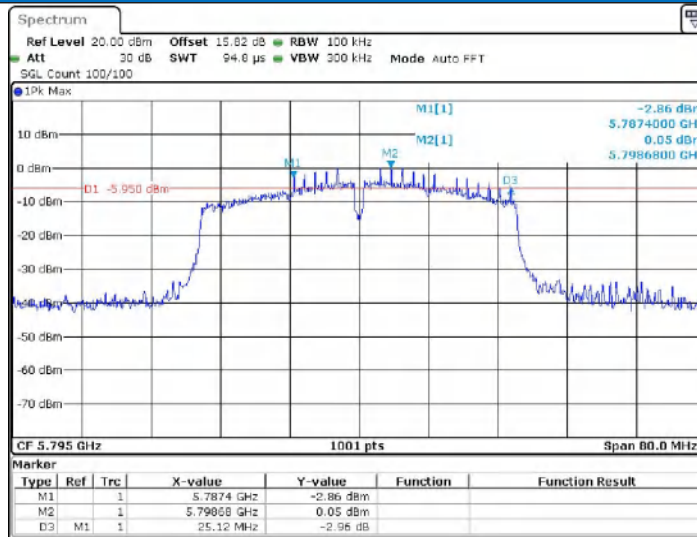
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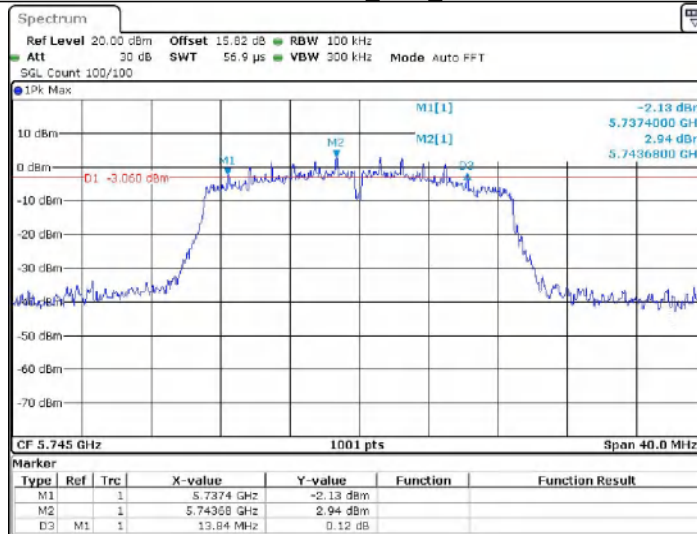
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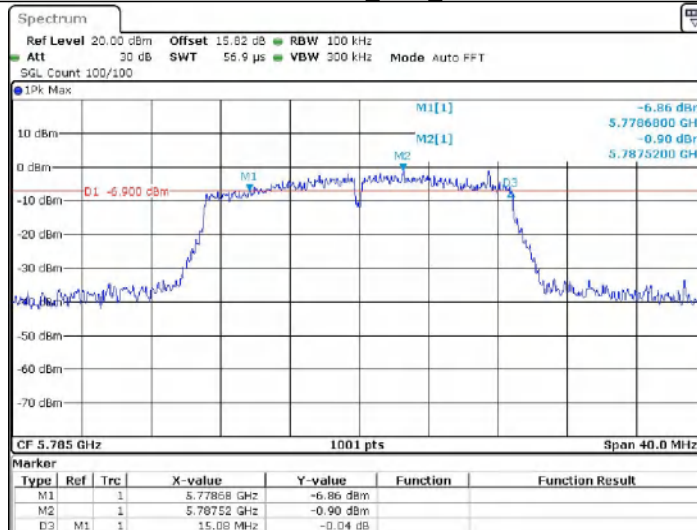
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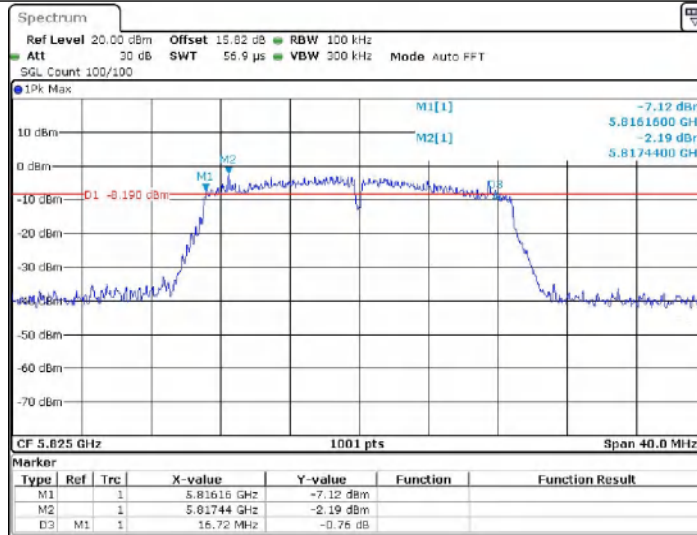
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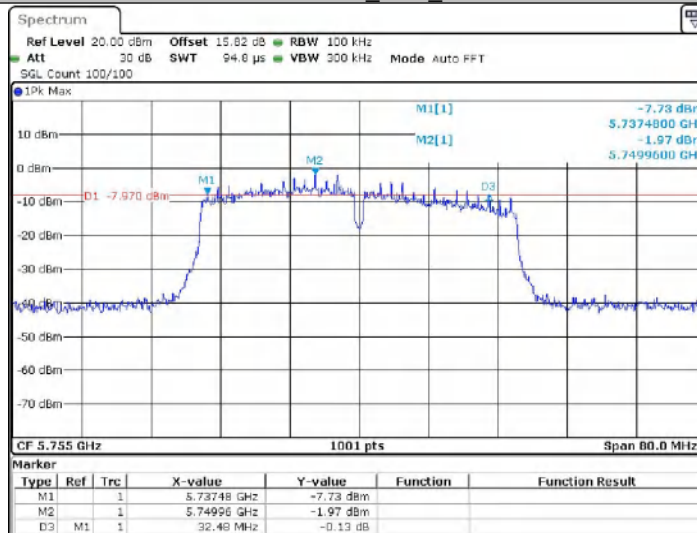
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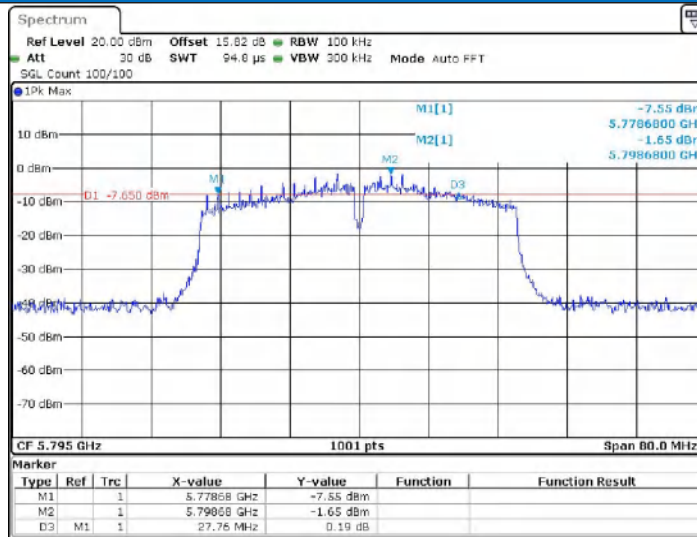
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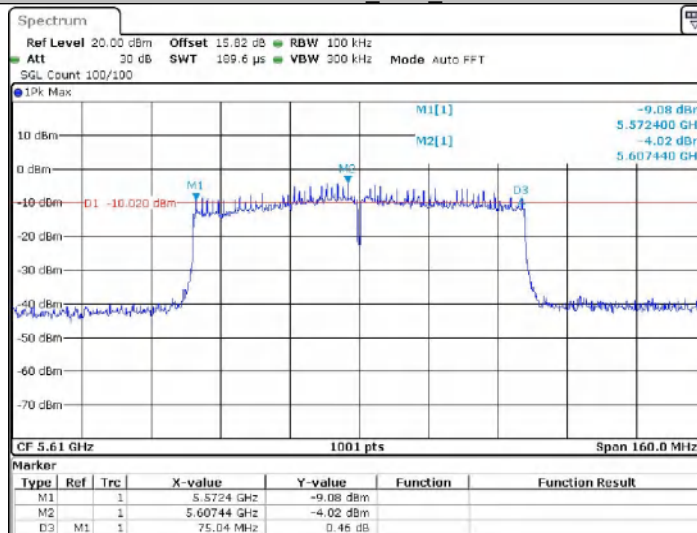
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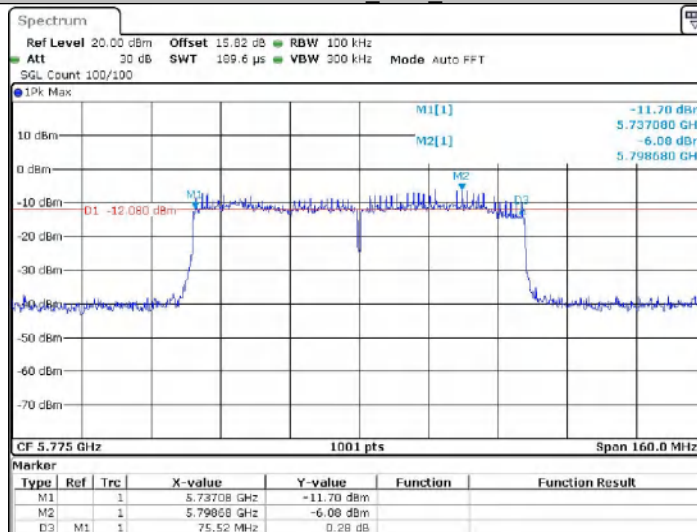
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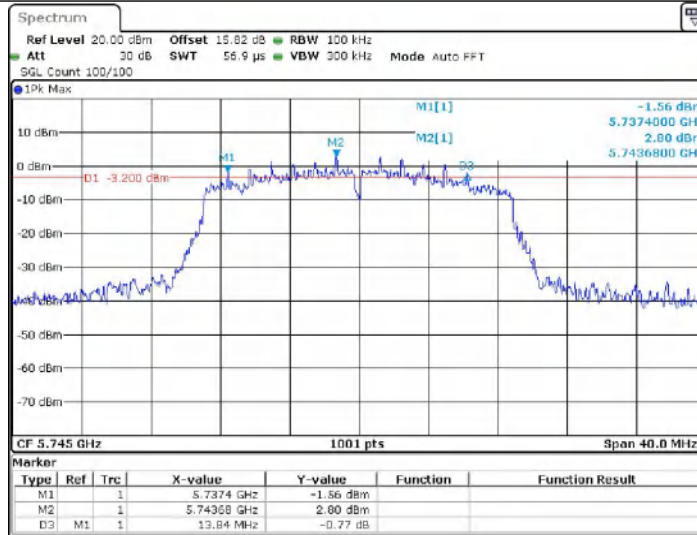
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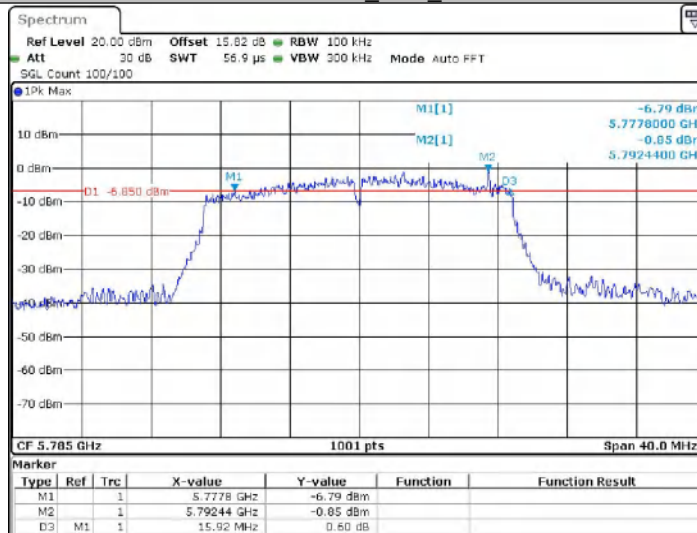
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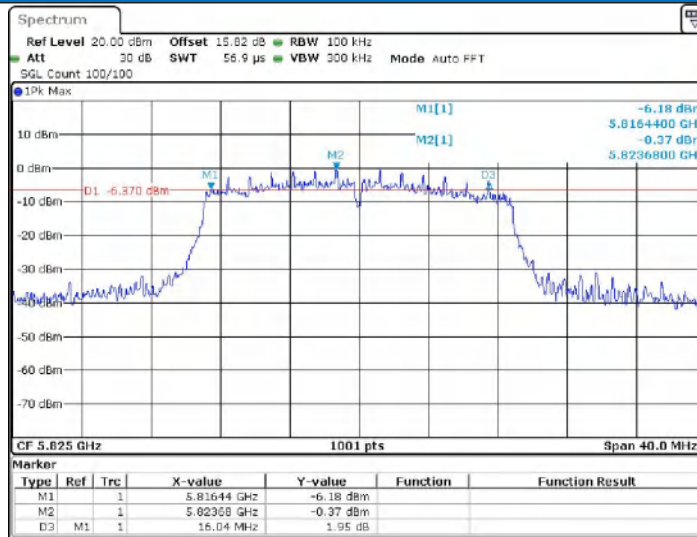
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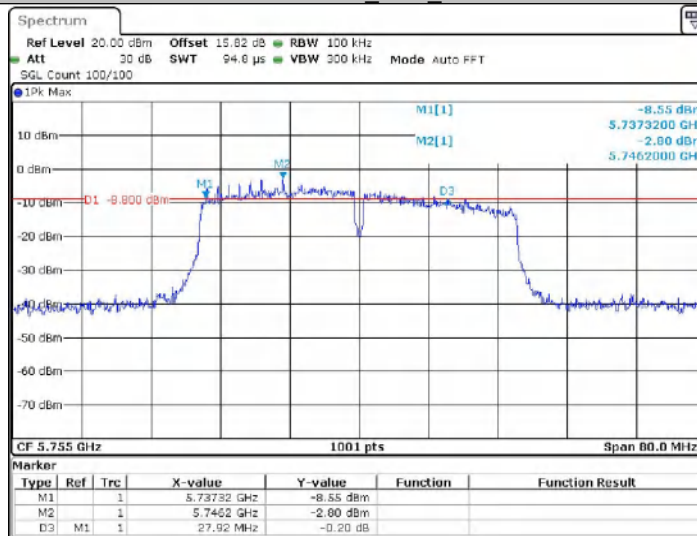
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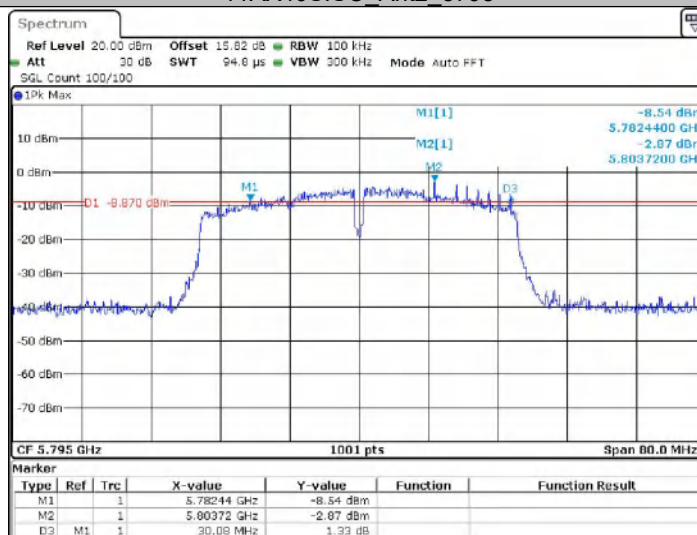
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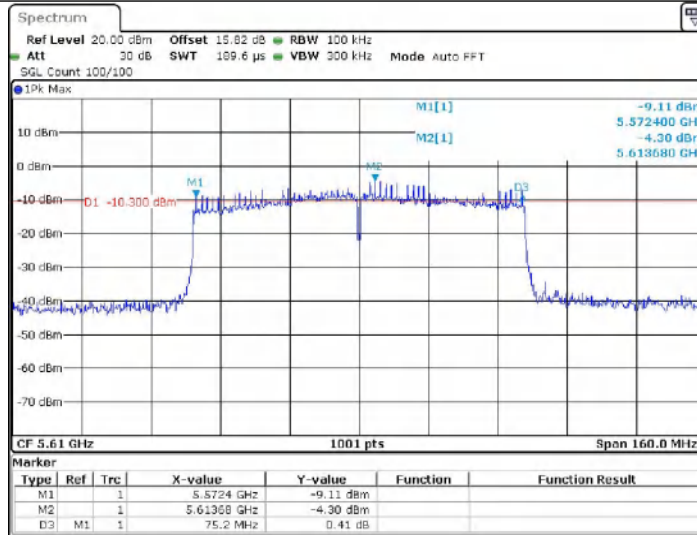
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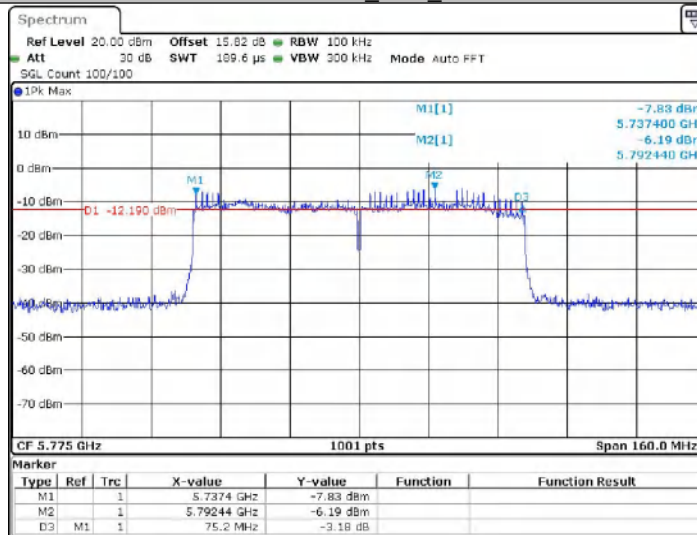
Date: 2 MAR 2026 11:53:10

11AX80SISO_Ant2_5610



Date: 2 MAR 2026 12:03:11

11AX80SISO_Ant2_5775



Date: 2 MAR 2026 13:38:20

Appendix B): Maximum Conduct Output Power

1.Duty Cycle (x)

Test Requirement KDB 789033 D02 II B 1

Test Method: KDB 789033 II B 1

Test Procedure:

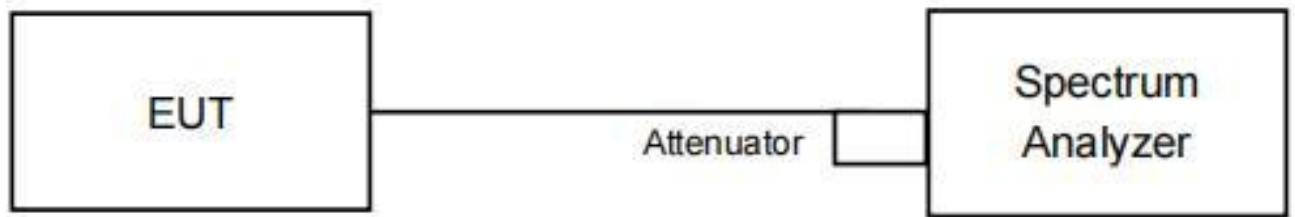
Set RBW = 20MHz

Set VBW = 40MHz

Set detector = peak.

Set span =0Hz

Test Setup Diagram

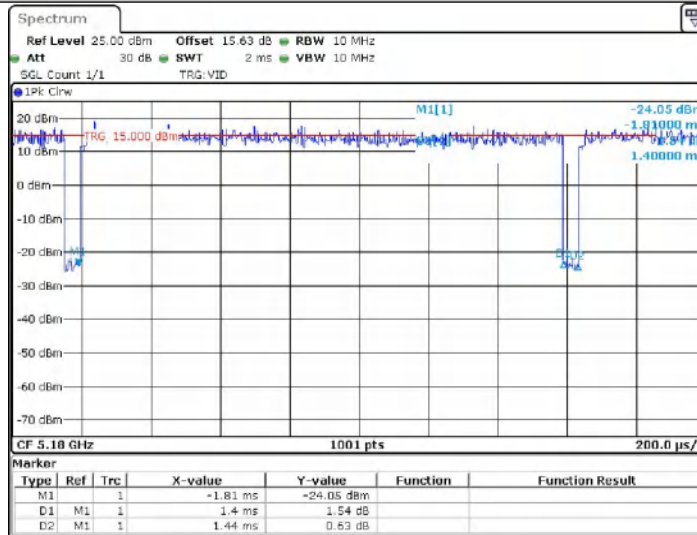


Measurement Data

Ant1:

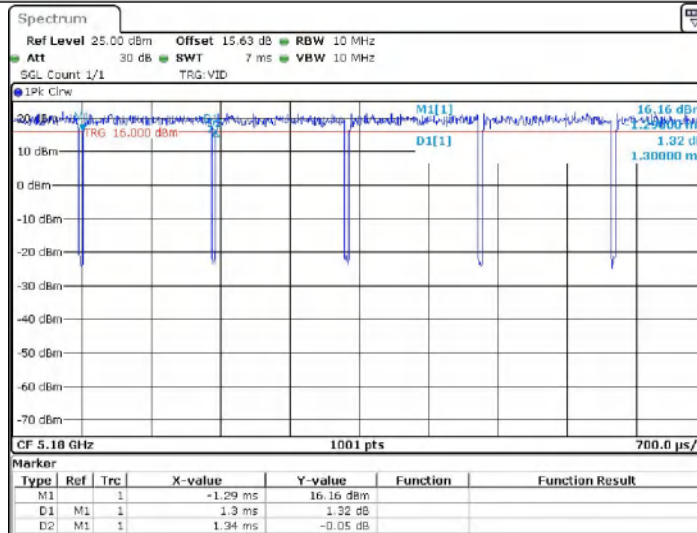
Test Mode	Antenna	Duty Cycle[%]	10log(1/x) Factor[dB]
11A	Ant1	97.22	0.12
11N20	Ant1	97.01	0.13
11N40	Ant1	94.20	0.26
11AC20	Ant1	96.32	0.16
11AC40	Ant1	94.20	0.26
11AC80	Ant1	86.49	0.63
11AX20	Ant1	97.04	0.13
11AX40	Ant1	92.86	0.32
11AX80	Ant1	89.19	0.50

11A



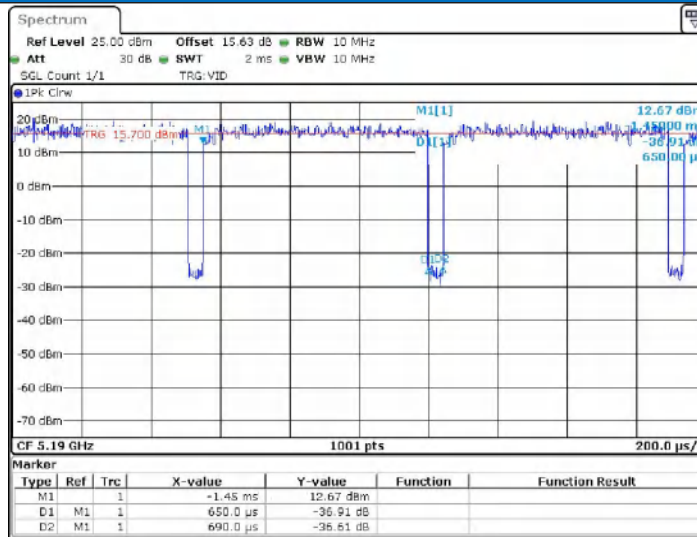
Date: 26 FEB 2026 10:51:50

11N20



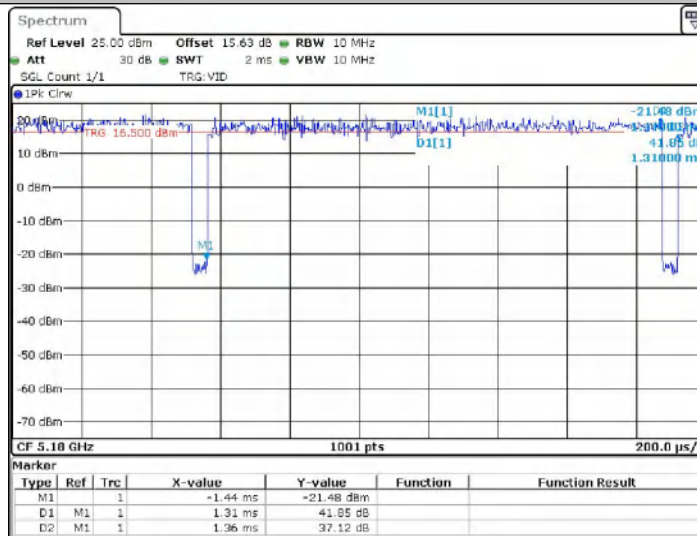
Date: 26 FEB 2026 11:18:02

11N40



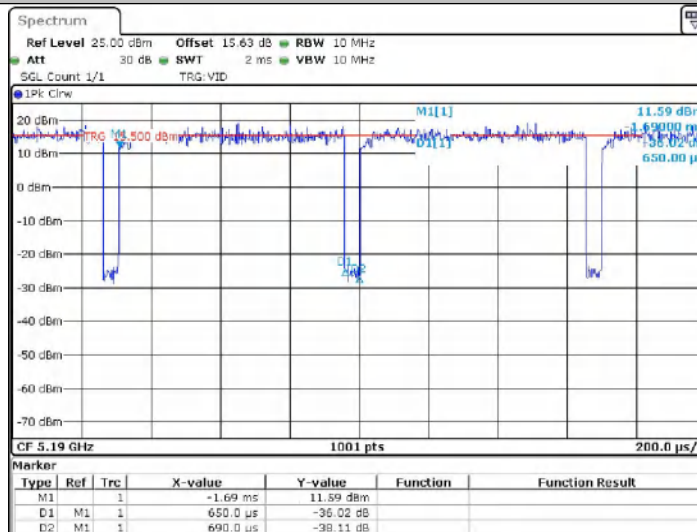
Date: 26 FEB 2026 14:25:10

11AC20



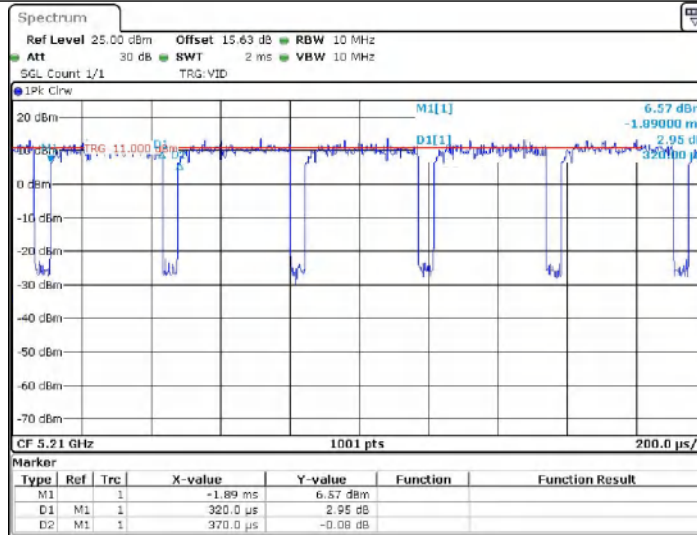
Date: 26 FEB 2026 14:57:02

11AC40



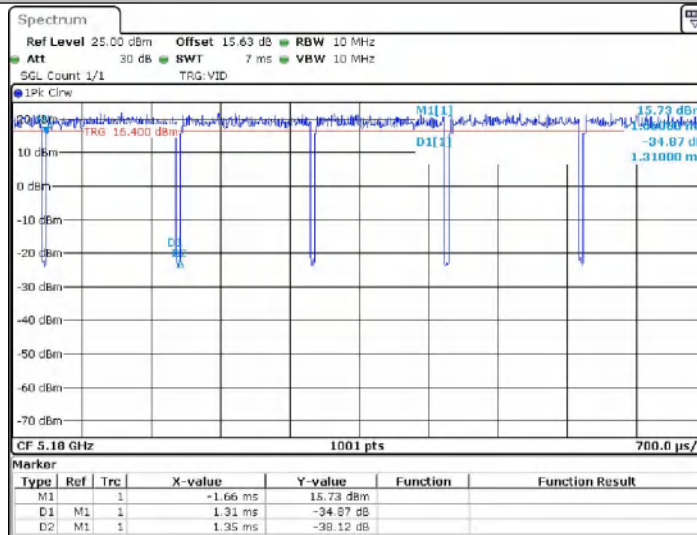
Date: 26 FEB 2026 16:01:24

11AC80



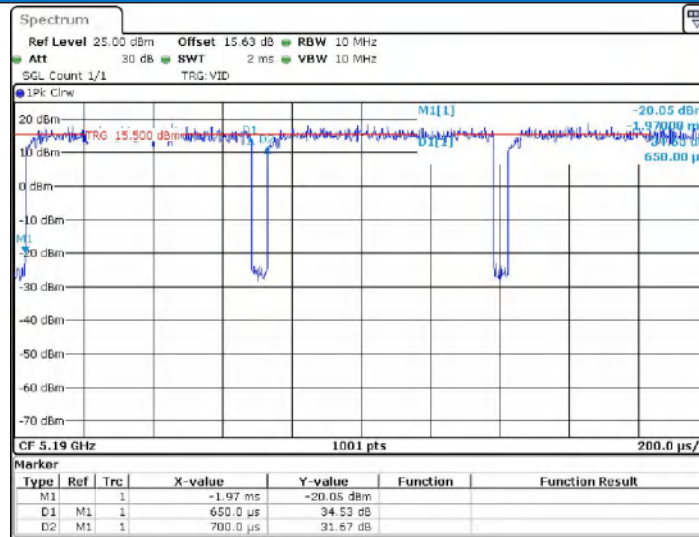
Date: 26 FEB 2026 16:21:20

11AX20



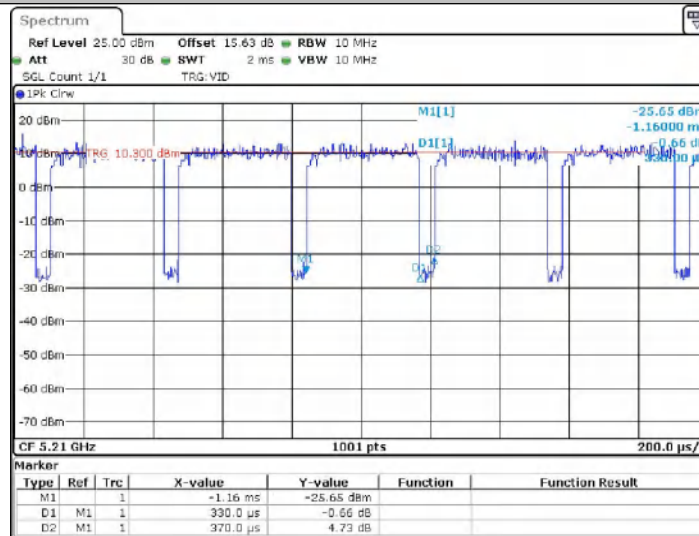
Date: 26 FEB 2026 16:35:20

11AX40



Date: 26 FEB 2026 16:55:53

11AX80

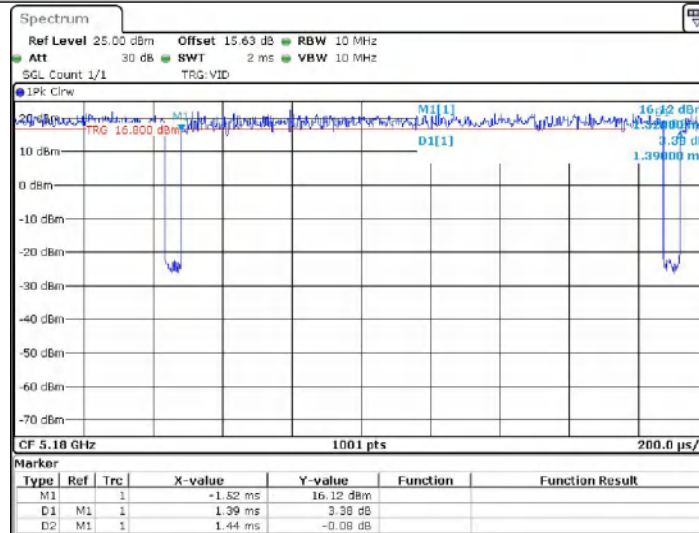


Date: 26 FEB 2026 17:14:50

Ant2:

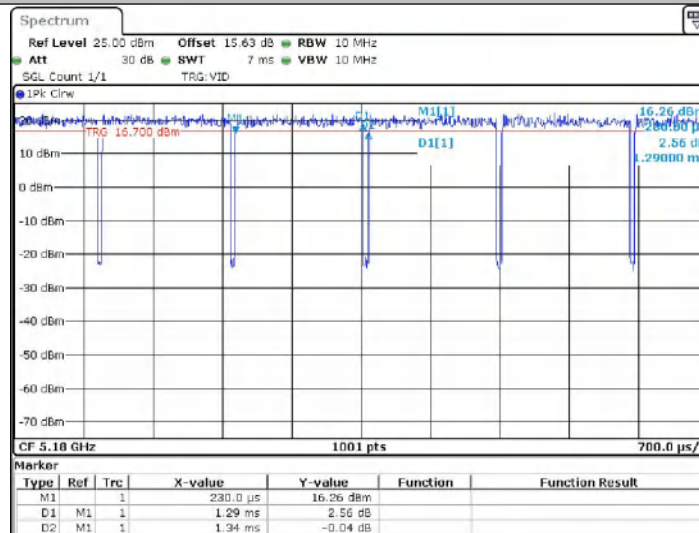
Test Mode	Antenna	Duty Cycle[%]	10log(1/x) Factor[dB]
11A	Ant1	96.53	0.15
11N20	Ant1	96.27	0.17
11N40	Ant1	94.20	0.26
11AC20	Ant1	97.06	0.13
11AC40	Ant1	92.86	0.32
11AC80	Ant1	89.19	0.50
11AX20	Ant1	97.06	0.13
11AX40	Ant1	92.86	0.32
11AX80	Ant1	86.49	0.63

11A



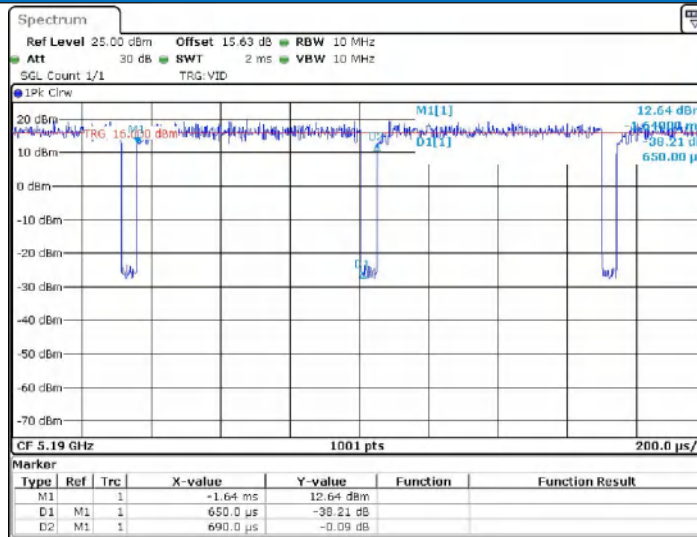
Date: 26 FEB 2026 19:10:56

11N20



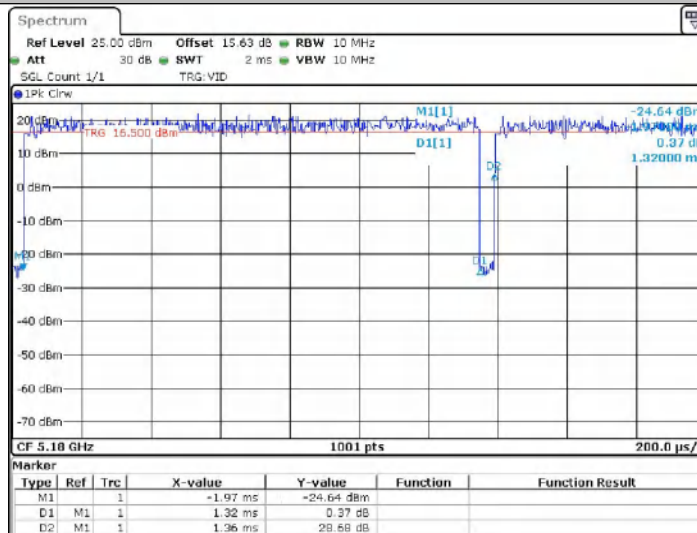
Date: 26 FEB 2026 19:49:17

11N40



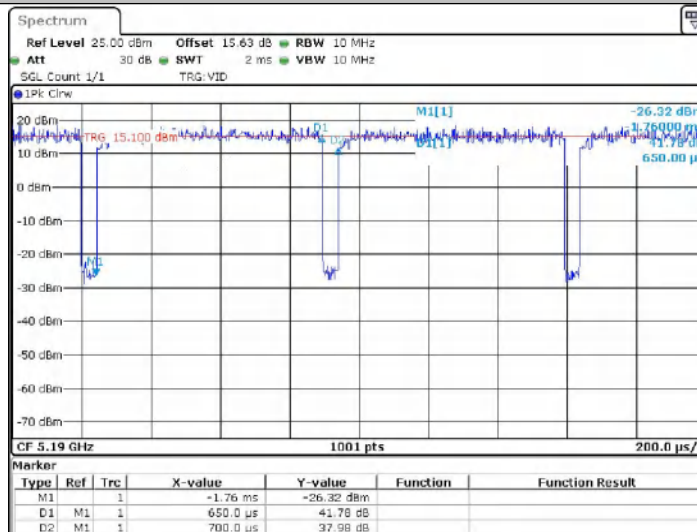
Date: 26 FEB 2026 20:13:10

11AC20



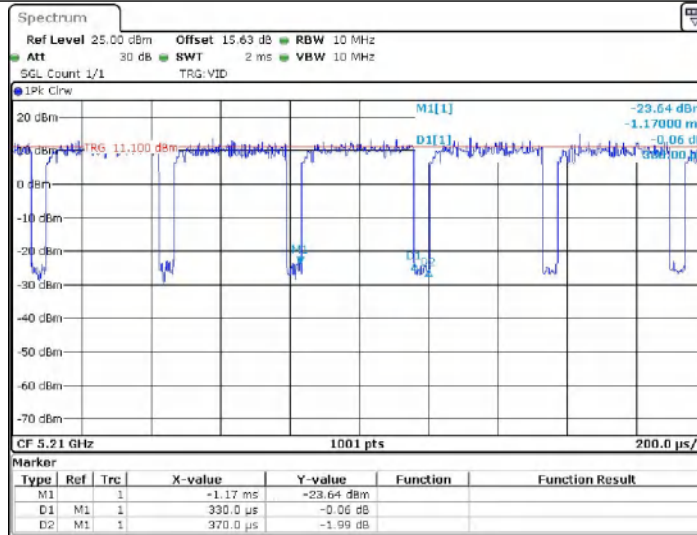
Date: 26 FEB 2026 20:31:59

11AC40



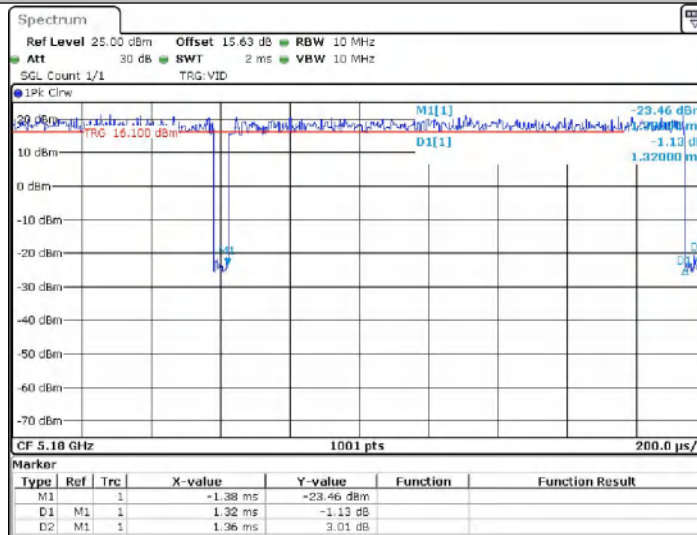
Date: 26 FEB 2026 20:50:23

11AC80



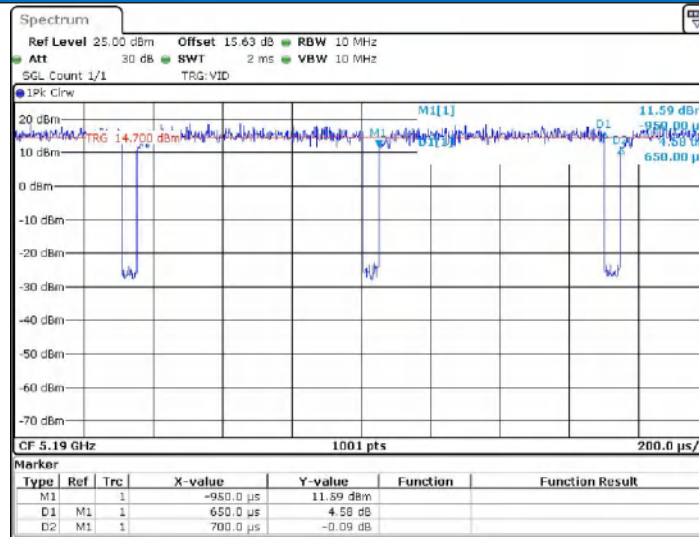
Date: 26 FEB 2026 21:20:30

11AX20



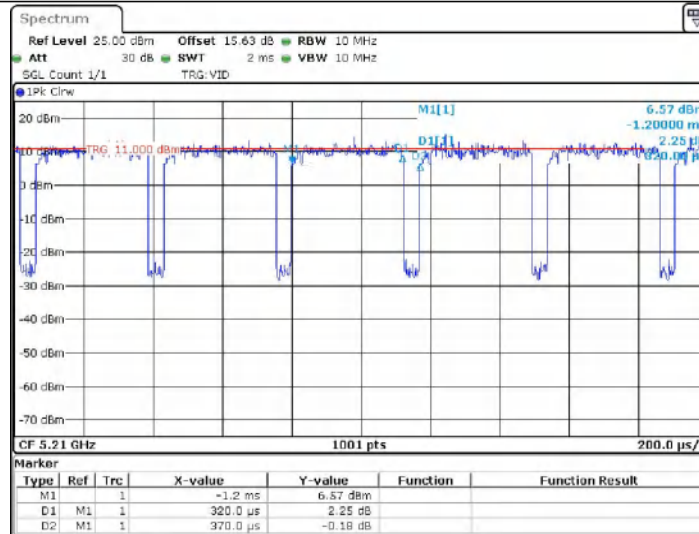
Date: 2 MAR 2026 10:29:05

11AX40



Date: 2 MAR 2026 11:07:20

11AX80



Date: 2 MAR 2026 11:55:12

2. Maximum Conducted Output Power

Test Requirement 47 CFR Part 15, Subpart C 15.407 (a)

Test Method: KDB 789033 D02 II E

Limit:

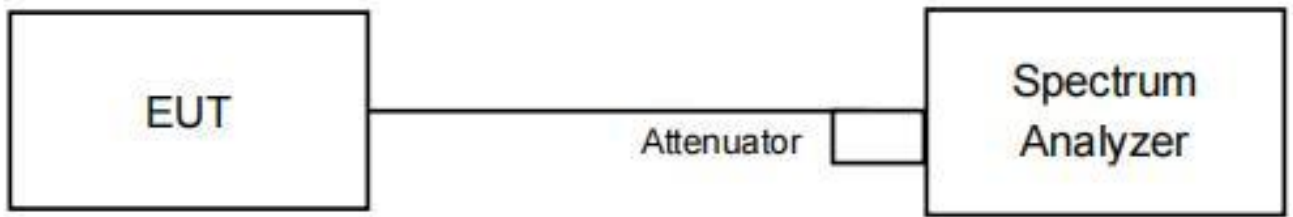
Frequency band(MHz)	Limit
5150-5250	$\leq 1\text{W}(30\text{dBm})$ for master device
	$\leq 250\text{mW}(24\text{dBm})$ for client device
5250-5350	$\leq 250\text{mW}(24\text{dBm})$ for client device or $11\text{dBm}+10\log B^*$
5470-5725	$\leq 250\text{mW}(24\text{dBm})$ for client device or $11\text{dBm}+10\log B^*$
5725-5850	$\leq 1\text{W}(30\text{dBm})$
Remark:	* Where B is the 26dB emission bandwidth in MHz. The maximum conducted output power must be measured over any interval of continuous transmission using instrumentation calibrated in terms of an rms-equivalent voltage.

Test Procedure:

Method SA-2 (trace averaging across on and off times of the EUT transmissions, followed by duty cycle correction).

- (1) Set RBW = 1 MHz.
- (2) Set VBW $\geq$ 3 MHz.
- (3) Detector = power average
- (4) Sweep time = auto.
- (5) Add duty cycle to the measured average power.

Test Setup Diagram



Measurement Data

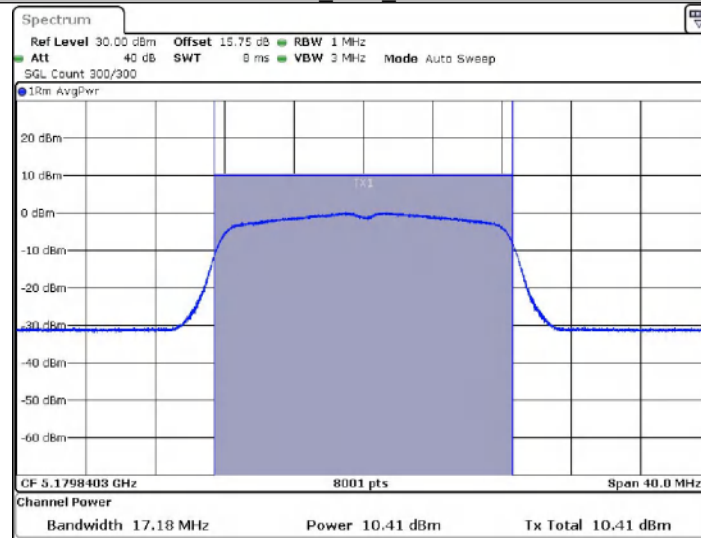
Ant1:

Test Mode	Antenna	Freq(MHz)	Result [dBm]	Limit [dBm]	Verdict
11A	Ant1	5180	10.41	≤23.98	PASS
		5200	10.09	≤23.98	PASS
		5240	8.57	≤23.98	PASS
		5260	10.60	≤23.86	PASS
		5280	8.16	≤23.94	PASS
		5320	9.59	≤23.91	PASS
		5500	9.70	≤23.87	PASS
		5580	8.38	≤23.98	PASS
		5700	7.27	≤23.82	PASS
		5745	9.13	≤30.00	PASS
		5785	7.45	≤30.00	PASS
		5825	6.68	≤30.00	PASS
11N20SISO	Ant1	5180	14.94	≤23.98	PASS
		5200	14.70	≤23.98	PASS
		5240	13.21	≤23.98	PASS
		5260	15.19	≤23.98	PASS
		5280	12.78	≤23.98	PASS
		5320	13.93	≤23.98	PASS
		5500	14.30	≤23.98	PASS
		5580	12.95	≤23.98	PASS
		5700	11.60	≤23.98	PASS
		5745	13.53	≤30.00	PASS
		5785	11.90	≤30.00	PASS
		5825	11.06	≤30.00	PASS
11N40SISO	Ant1	5190	14.43	≤23.98	PASS
		5230	12.08	≤23.98	PASS
		5270	13.41	≤23.98	PASS
		5310	12.96	≤23.98	PASS
		5510	14.33	≤23.98	PASS
		5550	13.88	≤23.98	PASS
		5670	12.62	≤23.98	PASS
		5755	11.79	≤30.00	PASS
		5795	12.24	≤30.00	PASS

	11AC20SIS O	Ant1	5180	14.49	≤23.98	PASS
			5200	14.23	≤23.98	PASS
			5240	12.61	≤23.98	PASS
			5260	14.44	≤23.98	PASS
			5280	12.10	≤23.98	PASS
			5320	13.30	≤23.89	PASS
			5500	13.67	≤23.96	PASS
			5580	12.50	≤23.98	PASS
			5700	11.07	≤23.98	PASS
			5745	13.03	≤30.00	PASS
			5785	11.23	≤30.00	PASS
			5825	10.47	≤30.00	PASS
	11AC40SIS O	Ant1	5190	13.42	≤23.98	PASS
			5230	11.08	≤23.98	PASS
			5270	12.54	≤23.98	PASS
			5310	11.99	≤23.98	PASS
			5510	13.45	≤23.98	PASS
			5550	12.86	≤23.98	PASS
			5670	11.66	≤23.98	PASS
			5755	10.84	≤30.00	PASS
			5795	11.48	≤30.00	PASS
	11AC80SIS O	Ant1	5210	12.45	≤23.98	PASS
			5290	12.08	≤23.98	PASS
			5530	13.28	≤23.98	PASS
			5610	11.94	≤23.98	PASS
			5775	10.93	≤30.00	PASS
	11AX20SIS O	Ant1	5180	14.50	≤23.98	PASS
			5200	14.12	≤23.98	PASS
			5240	12.71	≤23.98	PASS
			5260	14.42	≤23.96	PASS
			5280	12.33	≤23.98	PASS
			5320	13.55	≤23.98	PASS
			5500	13.79	≤23.98	PASS
			5580	12.48	≤23.98	PASS
			5700	11.14	≤23.98	PASS
			5745	13.09	≤30.00	PASS
			5785	11.45	≤30.00	PASS

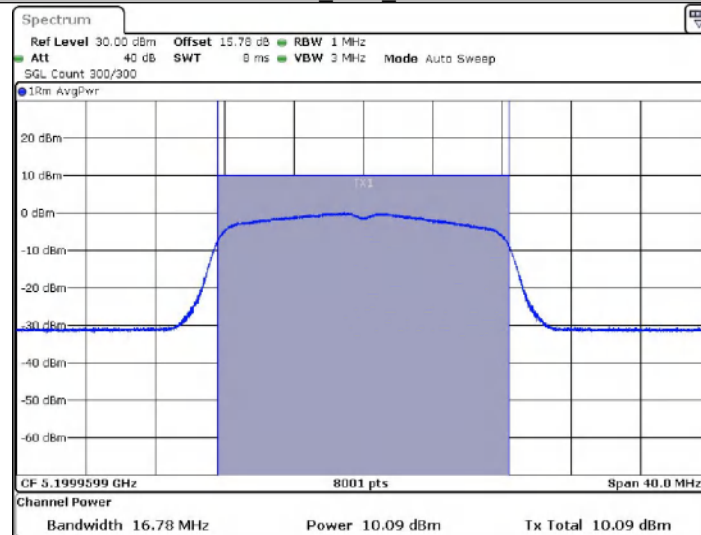
		5825	10.49	≤30.00	PASS
11AX40SIS O	Ant1	5190	13.60	≤23.98	PASS
		5230	11.04	≤23.98	PASS
		5270	12.61	≤23.98	PASS
		5310	11.91	≤23.98	PASS
		5510	13.31	≤23.98	PASS
		5550	13.08	≤23.98	PASS
		5670	11.50	≤23.98	PASS
		5755	10.92	≤30.00	PASS
		5795	11.45	≤30.00	PASS
11AX80SIS O	Ant1	5210	12.25	≤23.98	PASS
		5290	12.20	≤23.98	PASS
		5530	13.27	≤23.98	PASS
		5610	11.86	≤23.98	PASS
		5775	10.79	≤30.00	PASS

11A_Ant1_5180



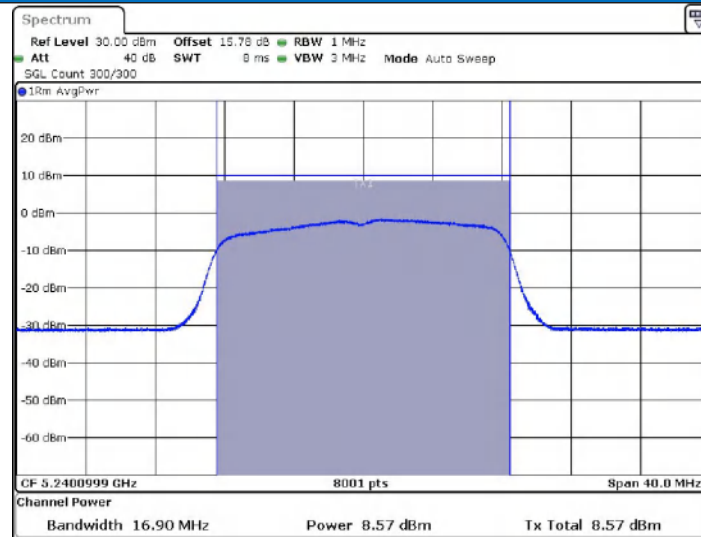
Date: 26 FEB. 2026 10:52:00

11A_Ant1_5200



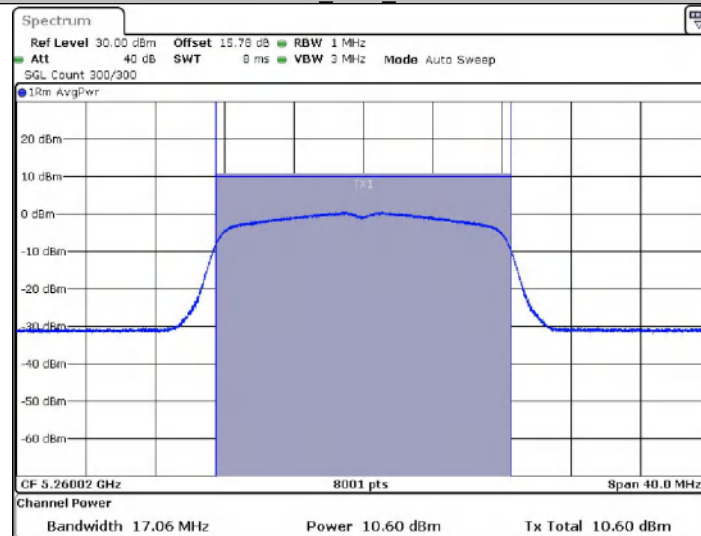
Date: 26 FEB. 2026 10:55:03

11A_Ant1_5240



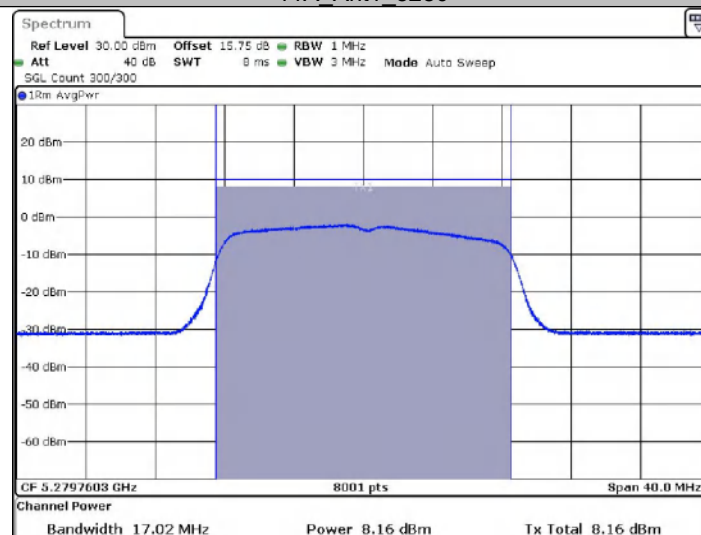
Date: 26 FEB.2026 10:56:17

11A_Ant1_5260



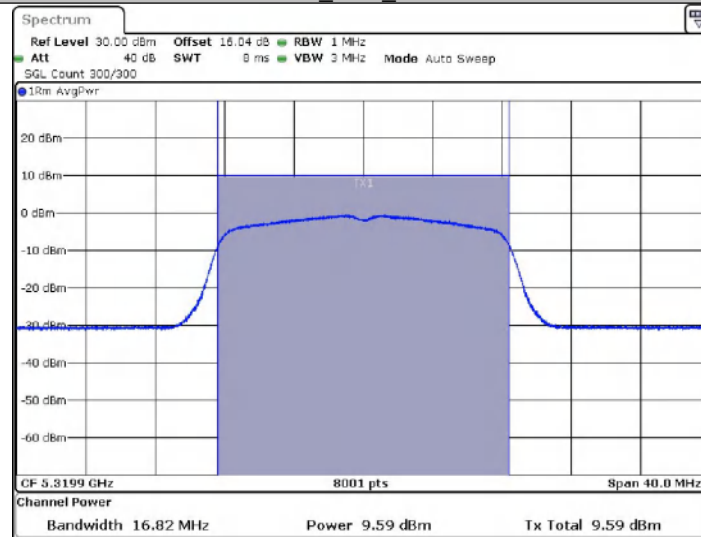
Date: 26 FEB.2026 10:57:24

11A_Ant1_5280



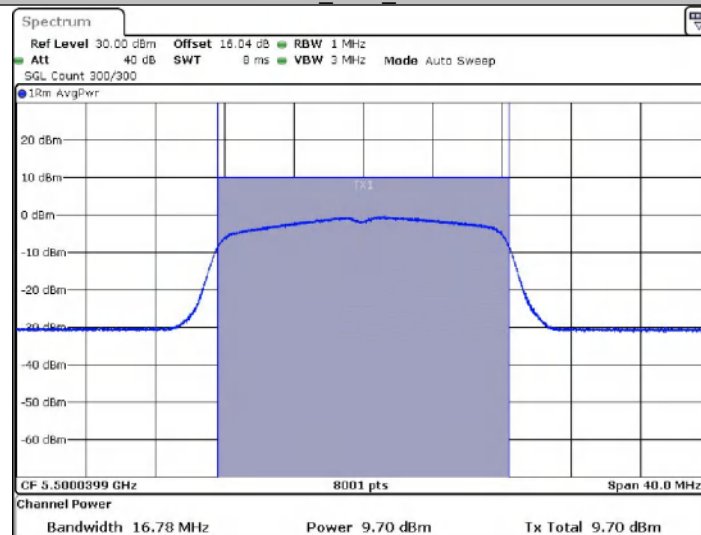
Date: 26 FEB.2026 10:58:30

11A_Ant1_5320



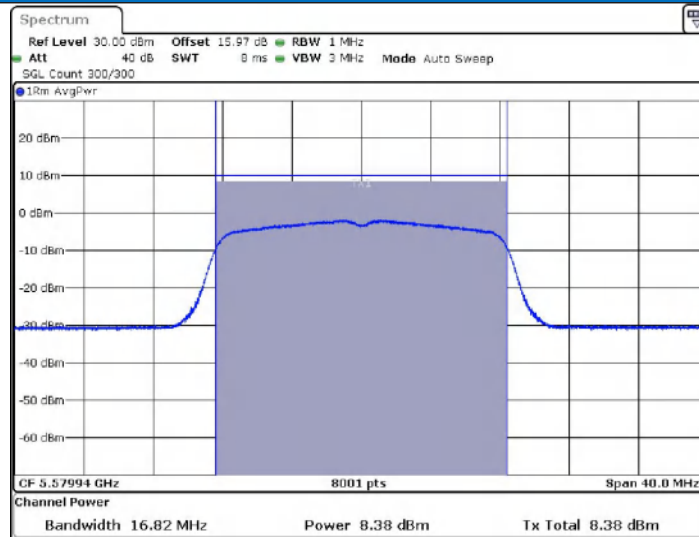
Date: 26 FEB.2026 11:02:05

11A_Ant1_5500



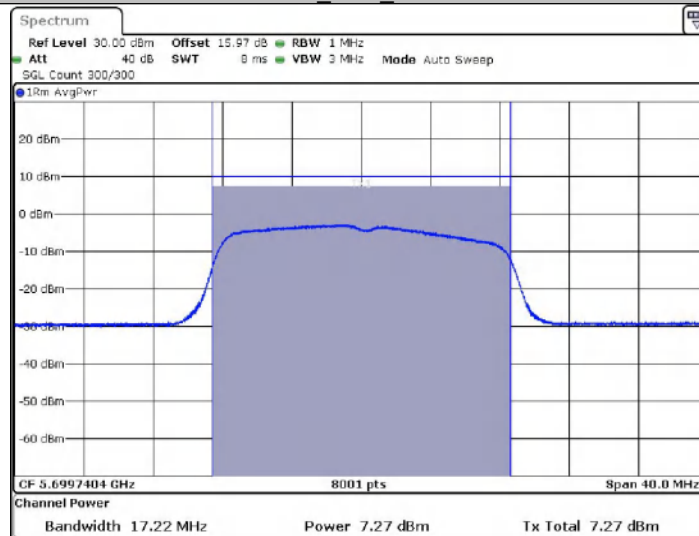
Date: 26 FEB.2026 11:02:01

11A_Ant1_5580



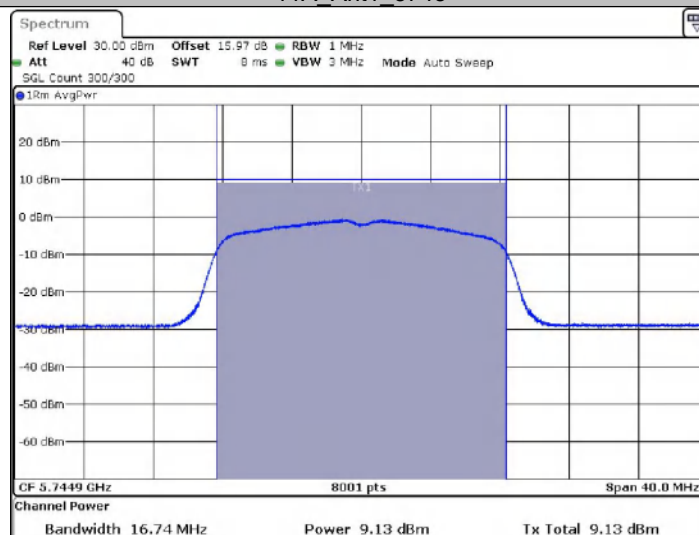
Date: 26 FEB.2026 11:03:45

11A_Ant1_5700



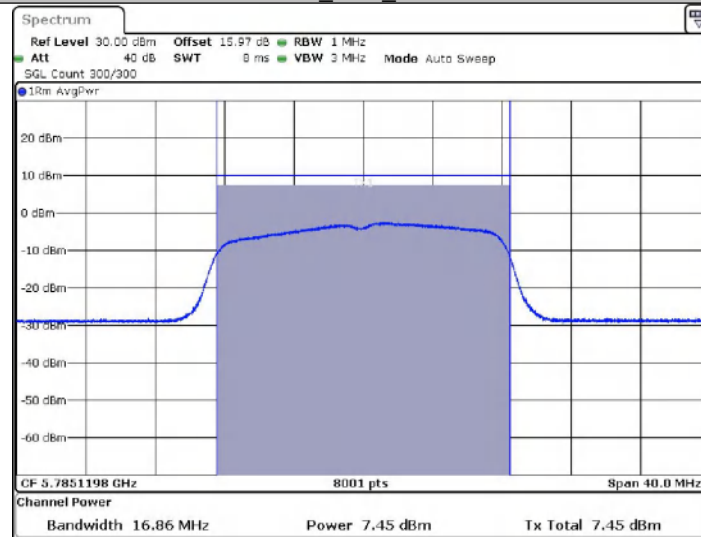
Date: 26 FEB.2026 11:04:58

11A_Ant1_5745



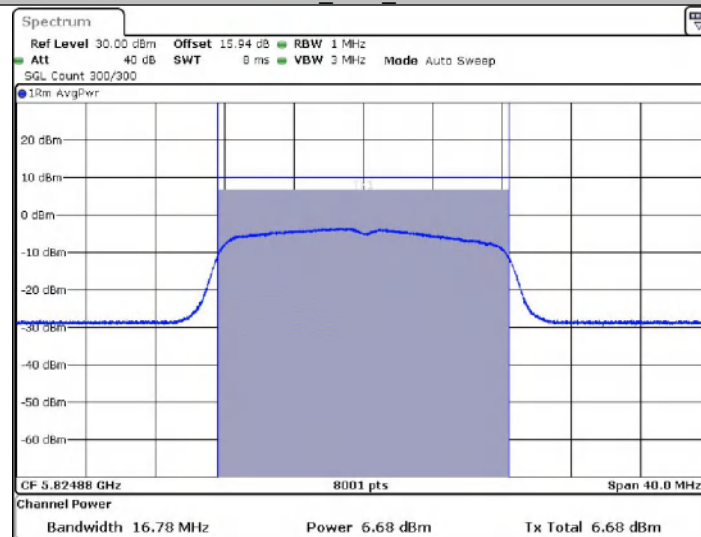
Date: 26 FEB.2026 11:07:20

11A_Ant1_5785



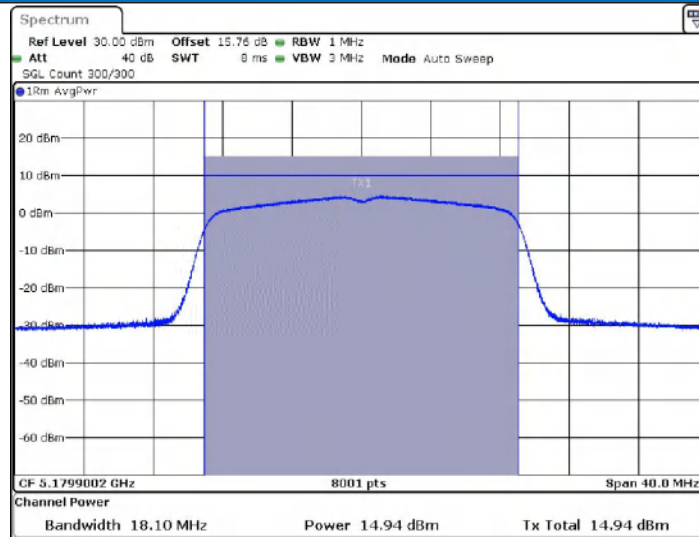
Date: 26 FEB. 2026 11:12:47

11A_Ant1_5825



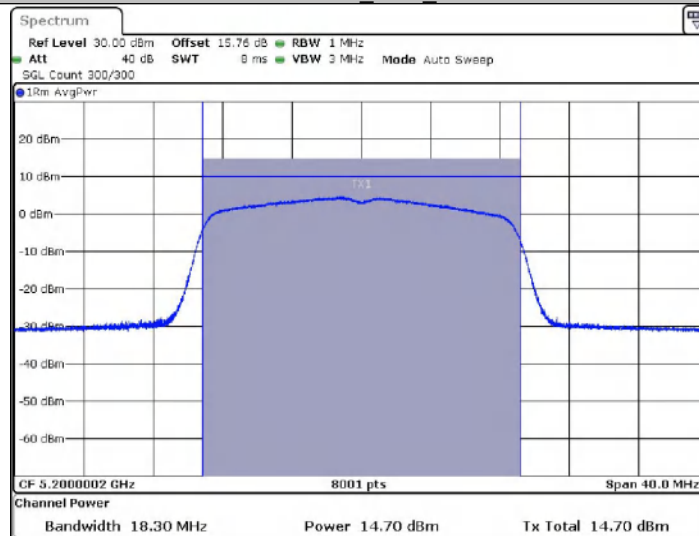
Date: 26 FEB. 2026 11:15:06

11N20SISO_Ant1_5180



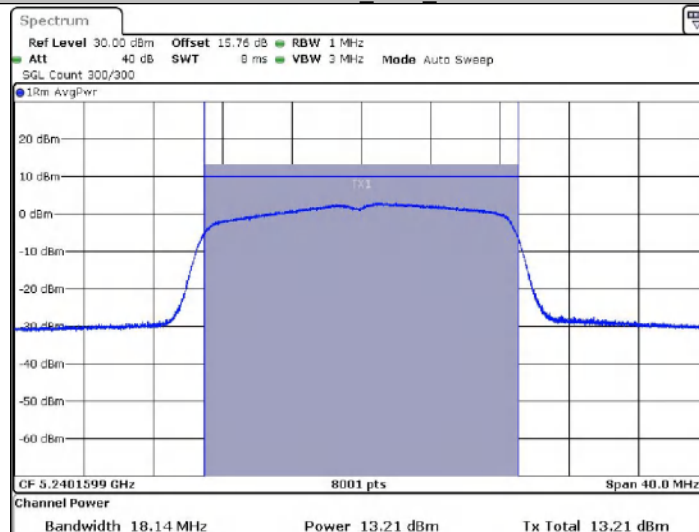
Date: 26 FEB.2026 11:18:12

11N20SISO_Ant1_5200



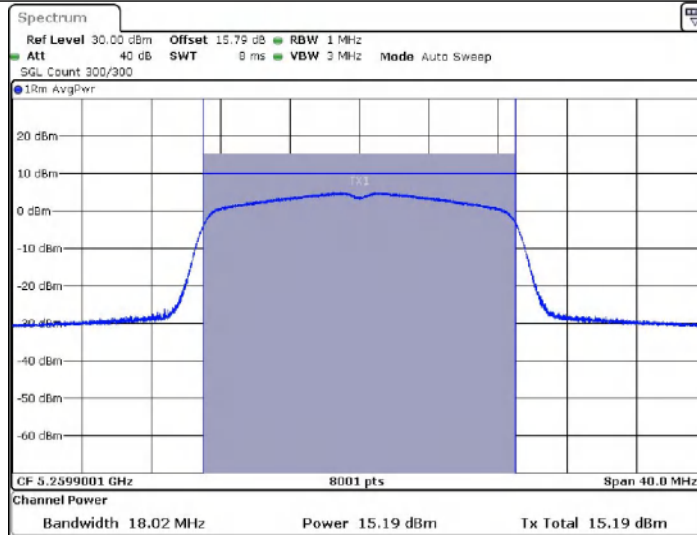
Date: 26 FEB.2026 11:20:07

11N20SISO_Ant1_5240



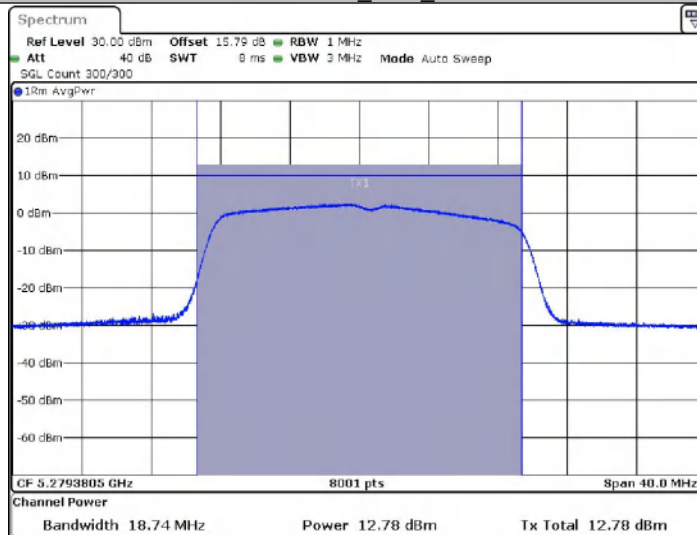
Date: 26 FEB.2026 13:36:48

11N20SISO_Ant1_5260



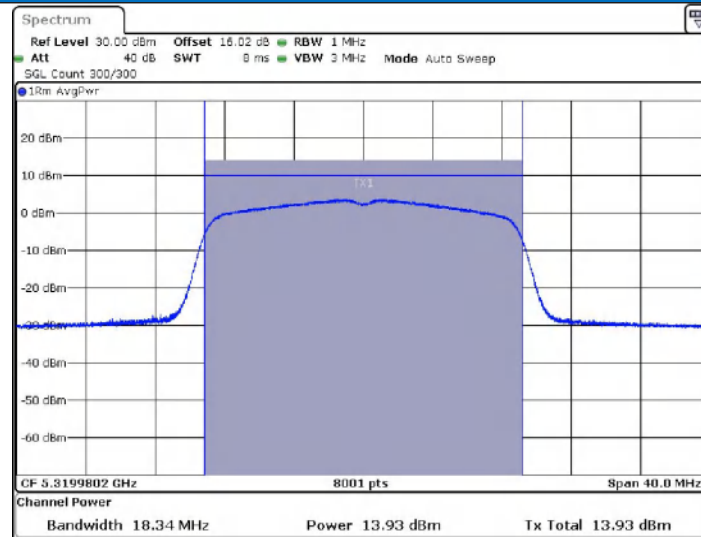
Date: 26 FEB. 2026 13:38:32

11N20SISO_Ant1_5280



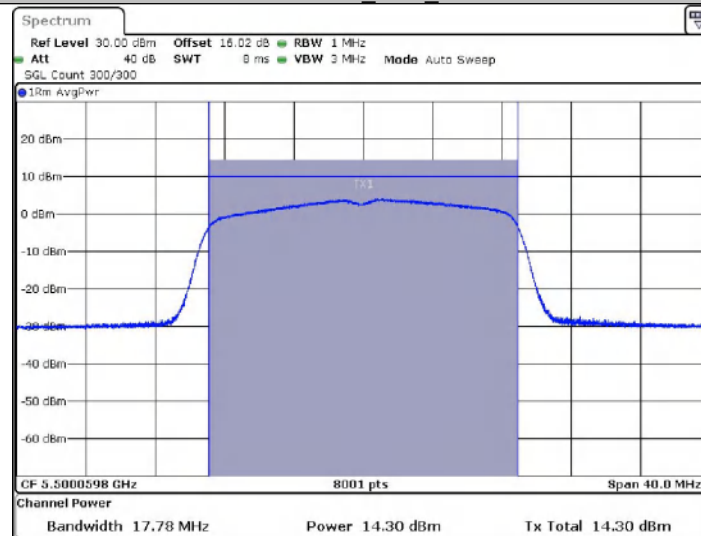
Date: 26 FEB. 2026 13:38:34

11N20SISO_Ant1_5320



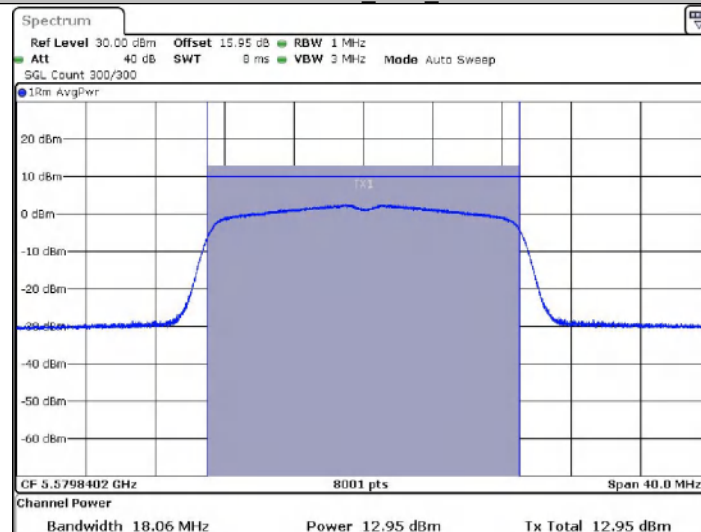
Date: 26 FEB.2026 13:41:04

11N20SISO_Ant1_5500



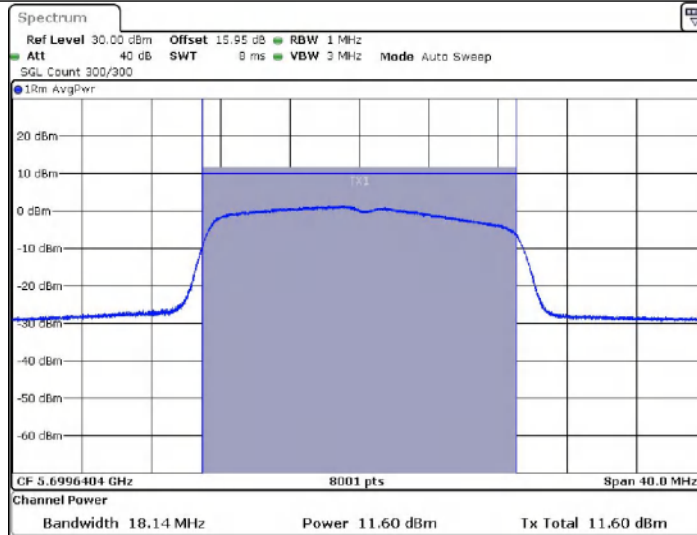
Date: 26 FEB.2026 13:42:42

11N20SISO_Ant1_5580



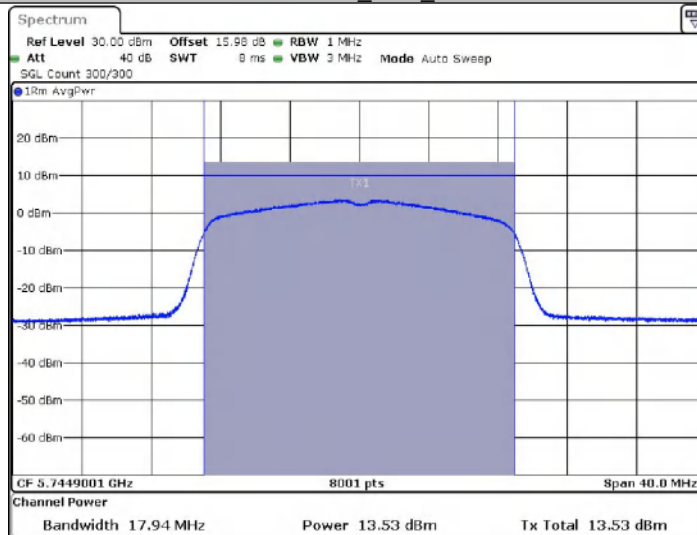
Date: 26 FEB.2026 13:44:57

11N20SISO_Ant1_5700



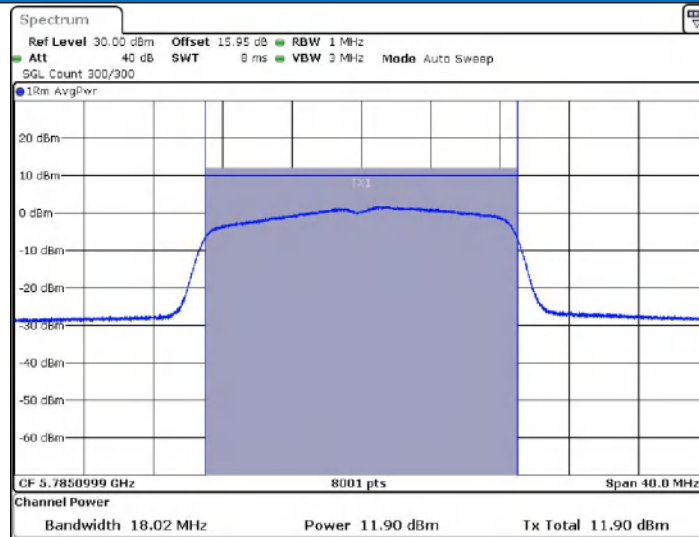
Date: 26 FEB. 2026 13:50:42

11N20SISO_Ant1_5745



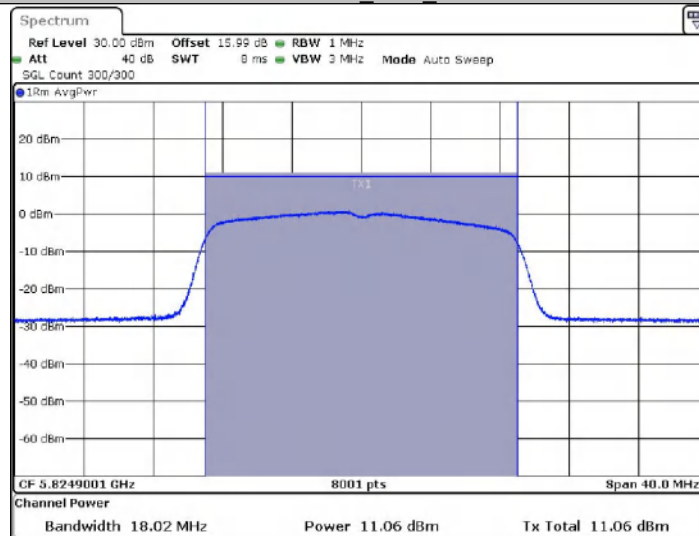
Date: 26 FEB. 2026 13:53:12

11N20SISO_Ant1_5785



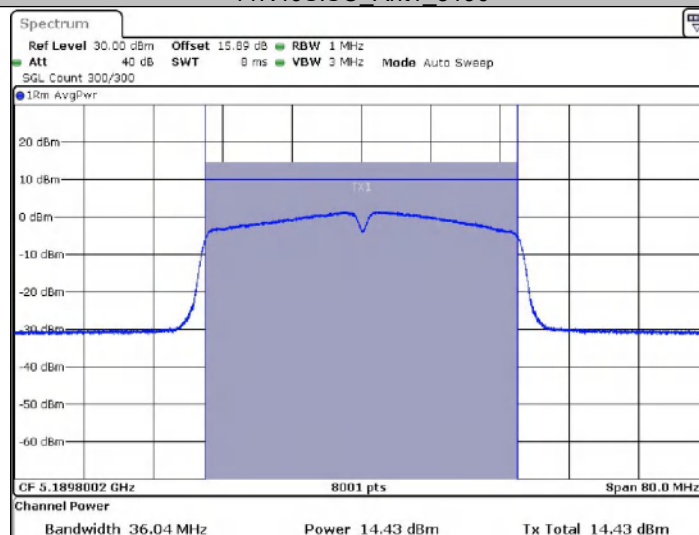
Date: 26 FEB.2026 13:55:04

11N20SISO_Ant1_5825



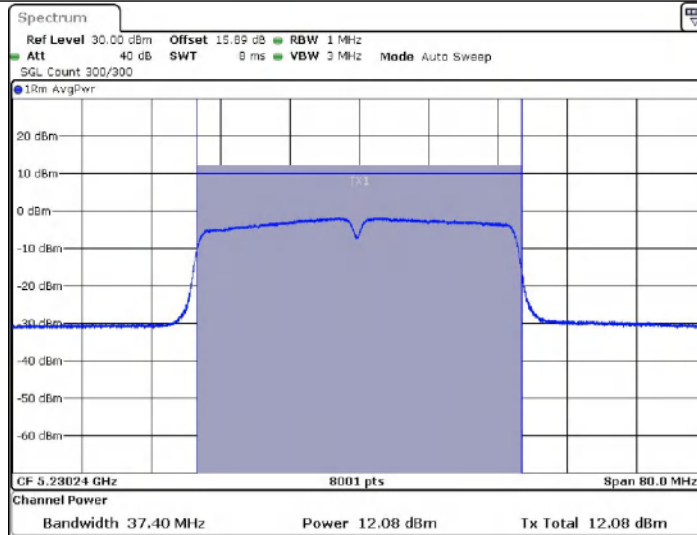
Date: 26 FEB.2026 14:21:52

11N40SISO_Ant1_5190



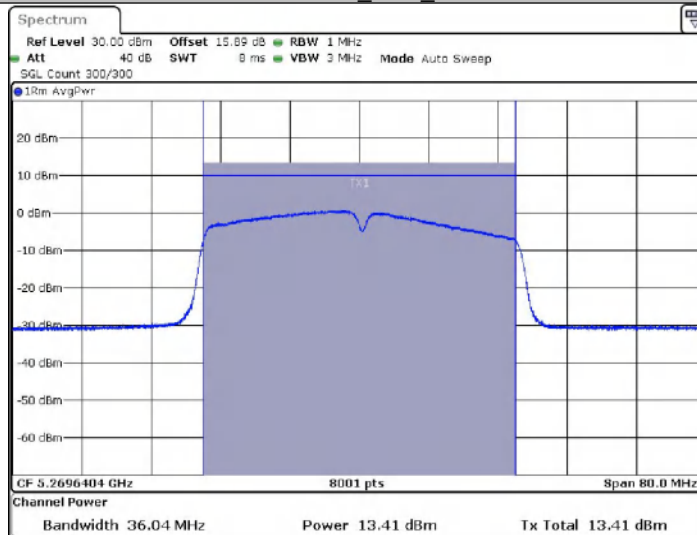
Date: 26 FEB.2026 14:25:19

11N40SISO_Ant1_5230



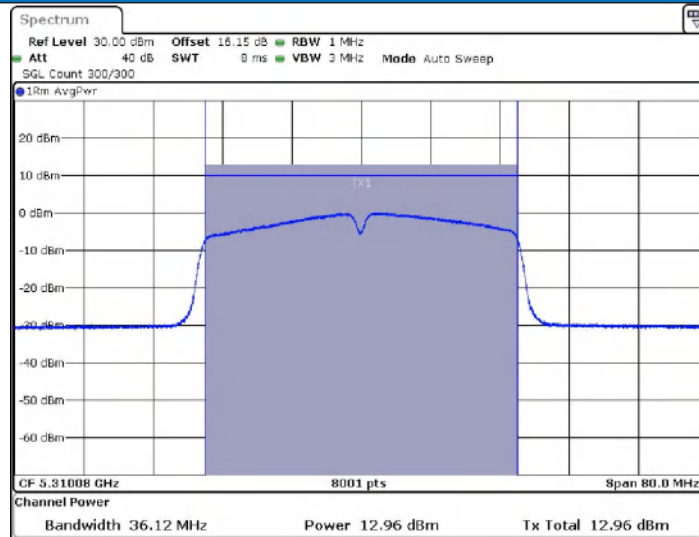
Date: 26 FEB. 2026 14:30:52

11N40SISO_Ant1_5270



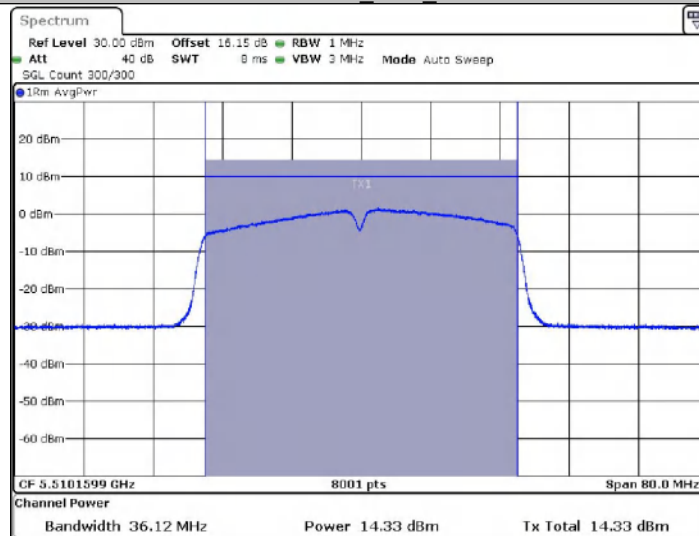
Date: 26 FEB. 2026 14:33:06

11N40SISO_Ant1_5310



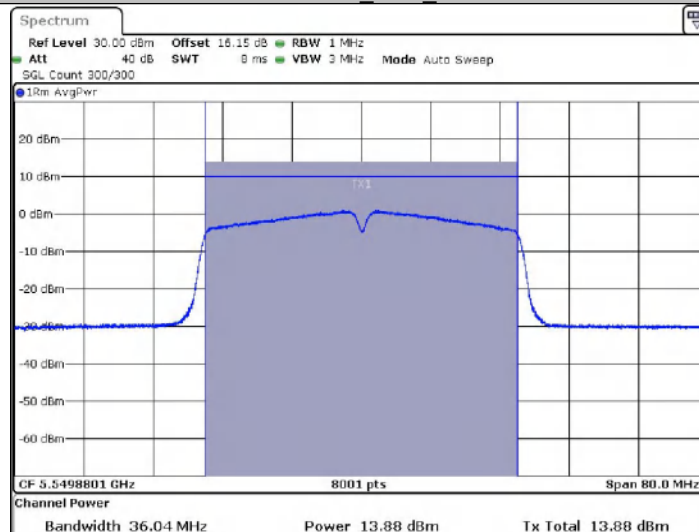
Date: 26 FEB.2026 14:34:46

11N40SISO_Ant1_5510



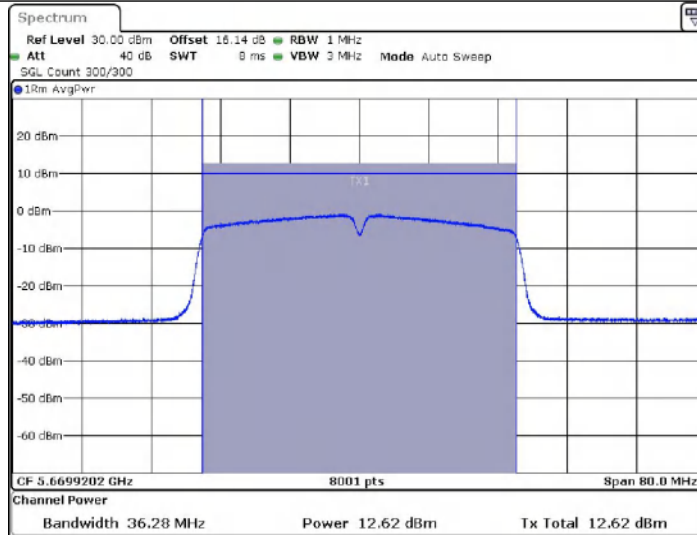
Date: 26 FEB.2026 14:36:16

11N40SISO_Ant1_5550



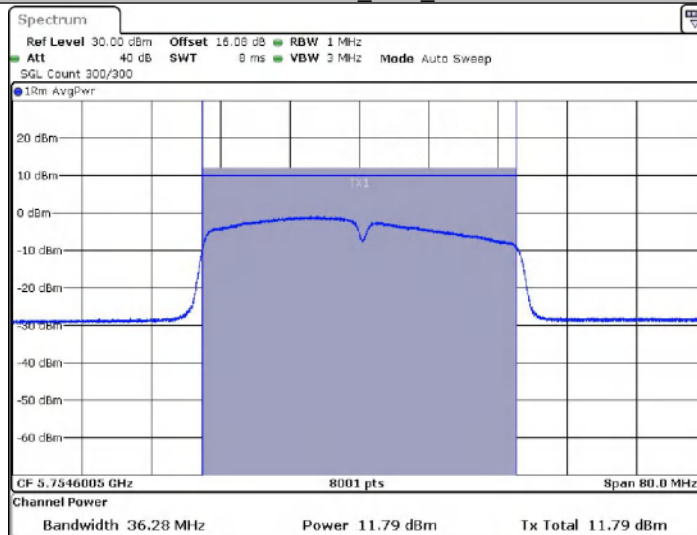
Date: 26 FEB.2026 14:40:40

11N40SISO_Ant1_5670



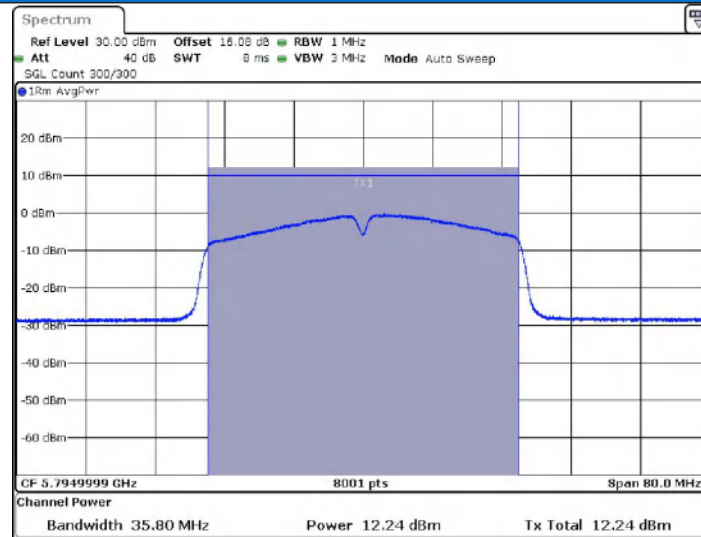
Date: 26 FEB. 2026 14:42:14

11N40SISO_Ant1_5755



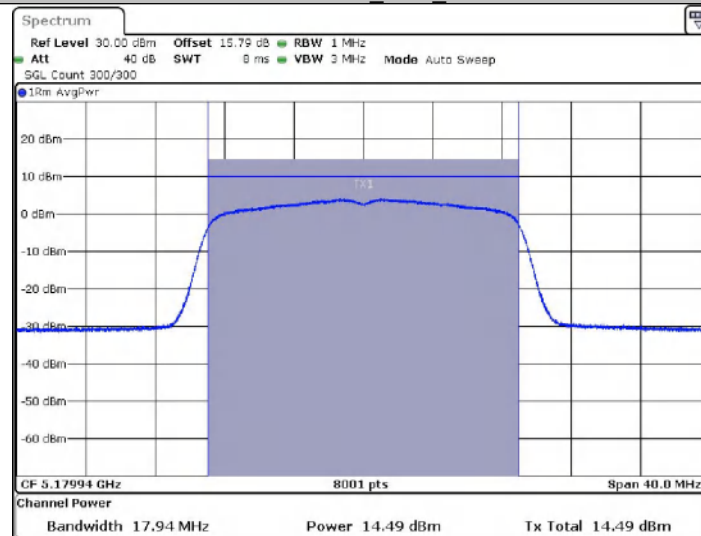
Date: 26 FEB. 2026 14:44:05

11N40SISO_Ant1_5795



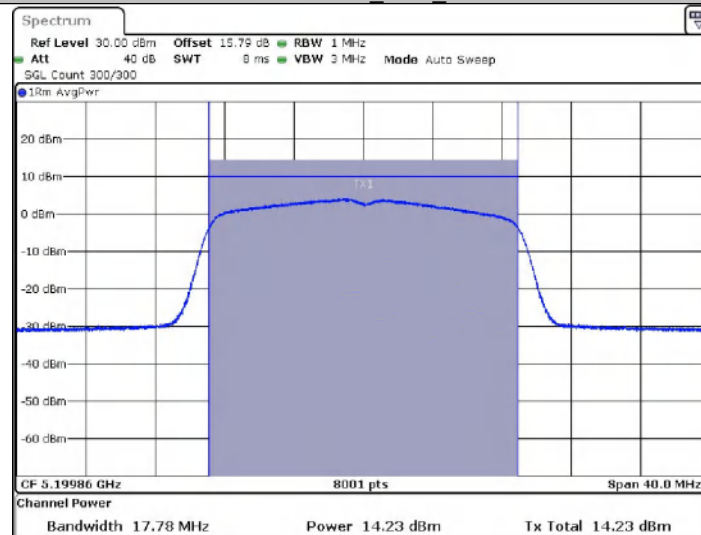
Date: 26 FEB.2026 14:46:47

11AC20SISO_Ant1_5180



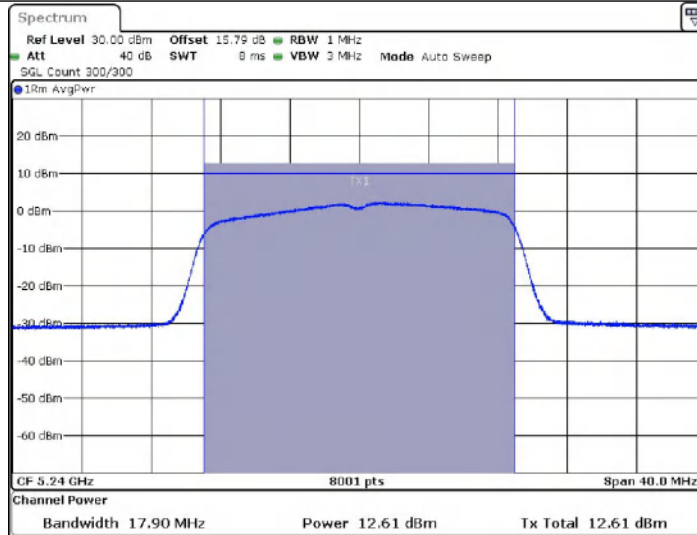
Date: 26 FEB.2026 14:57:11

11AC20SISO_Ant1_5200



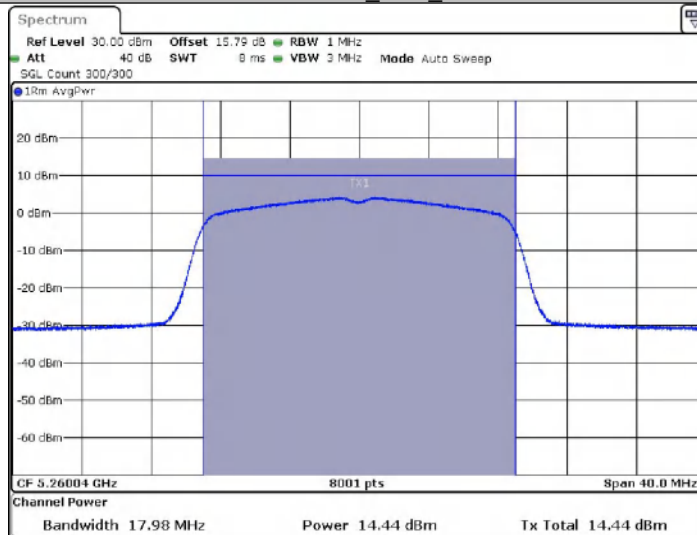
Date: 26 FEB.2026 15:03:53

11AC20SISO_Ant1_5240



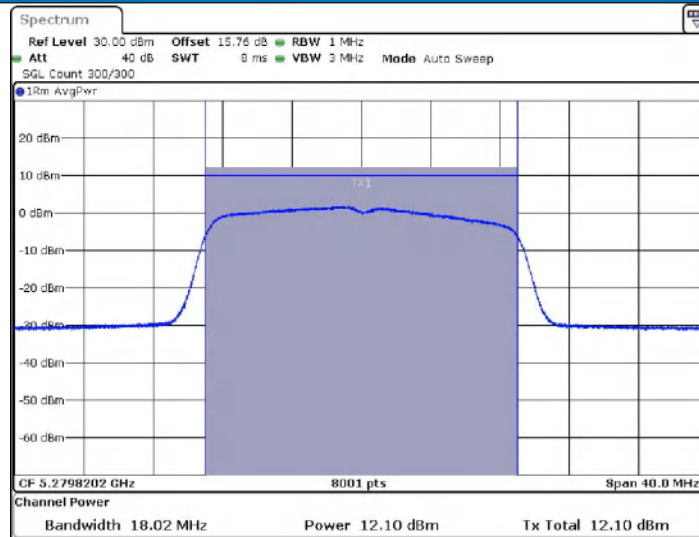
Date: 26 FEB.2026 15:23:03

11AC20SISO_Ant1_5260



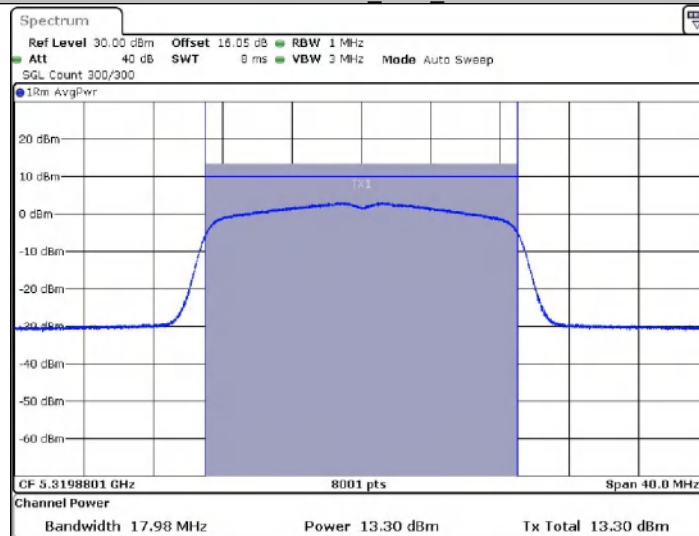
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11AC20SISO_Ant1_5280



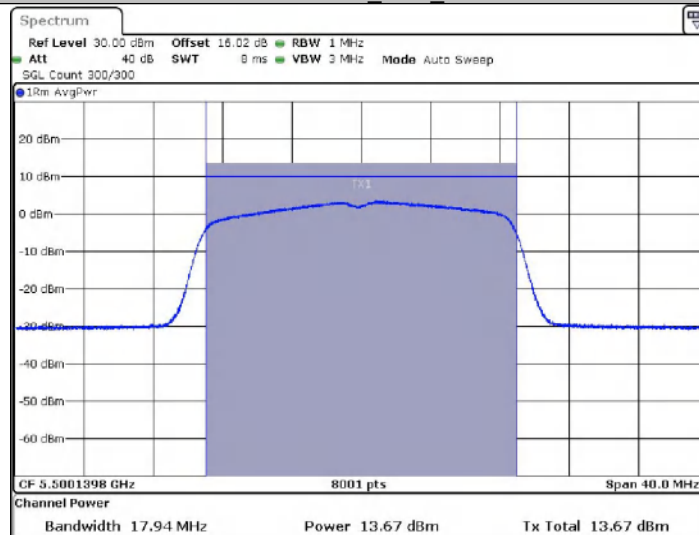
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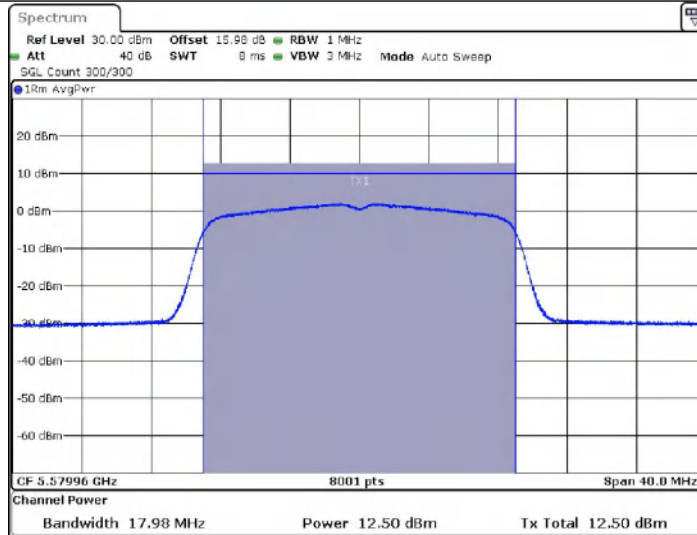
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11AC20SISO_Ant1_5500



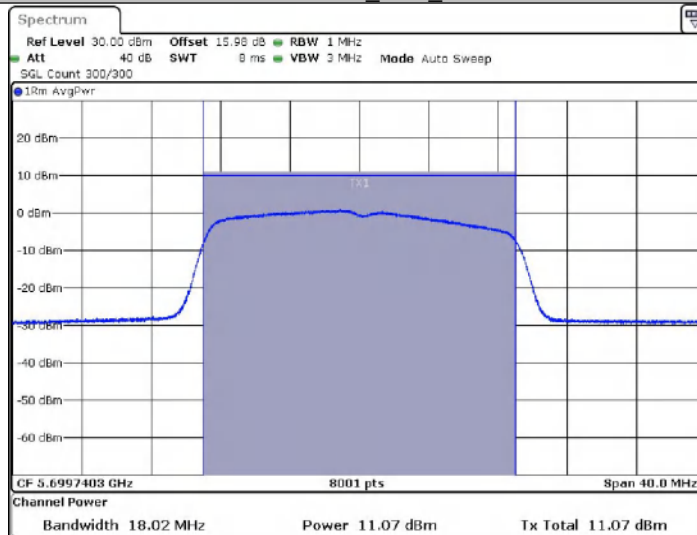
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11AC20SISO_Ant1_5580



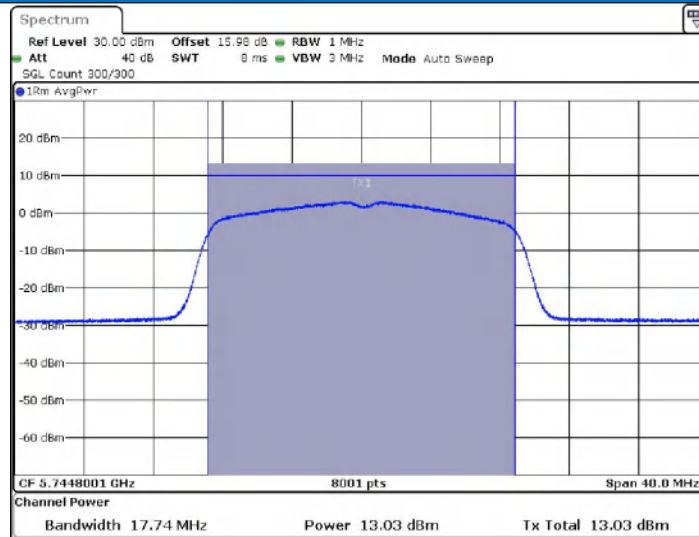
Date: 26 FEB. 2026 15:34:52

11AC20SISO_Ant1_5700



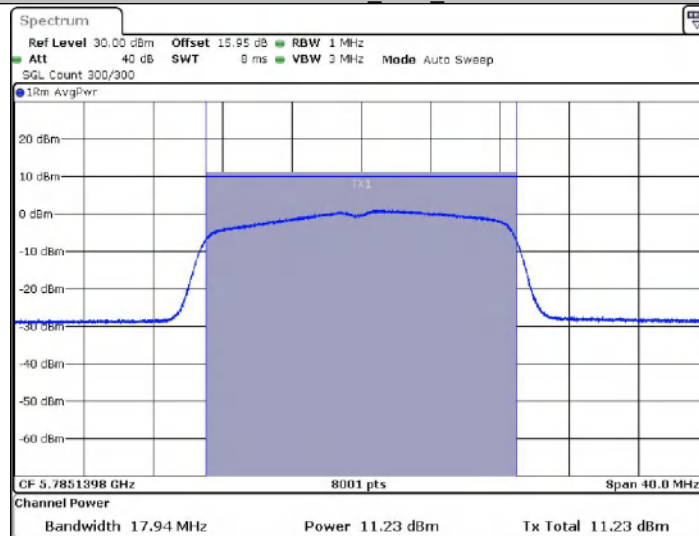
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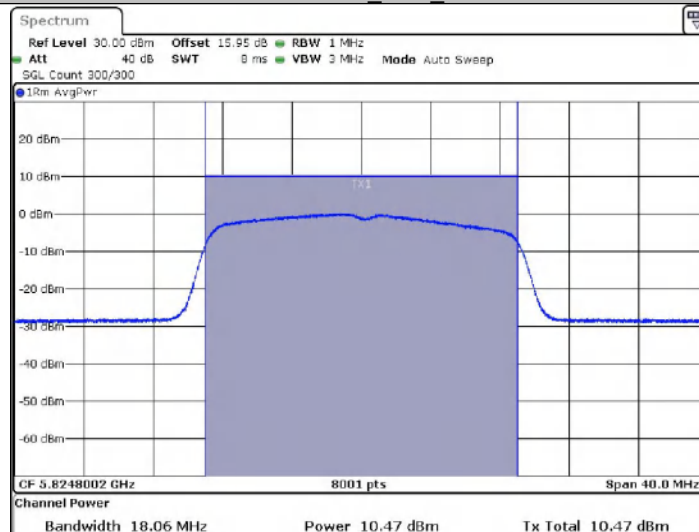
Date: 26 FEB.2026 15:41:34

11AC20SISO_Ant1_5785



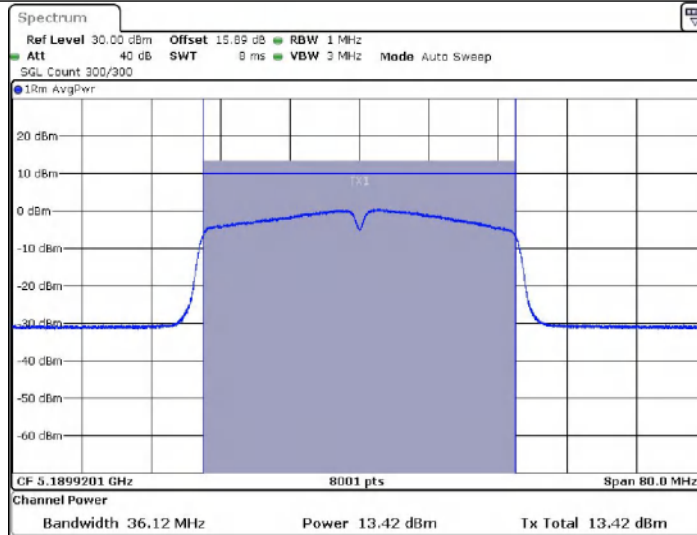
Date: 26 FEB.2026 15:56:54

11AC20SISO_Ant1_5825



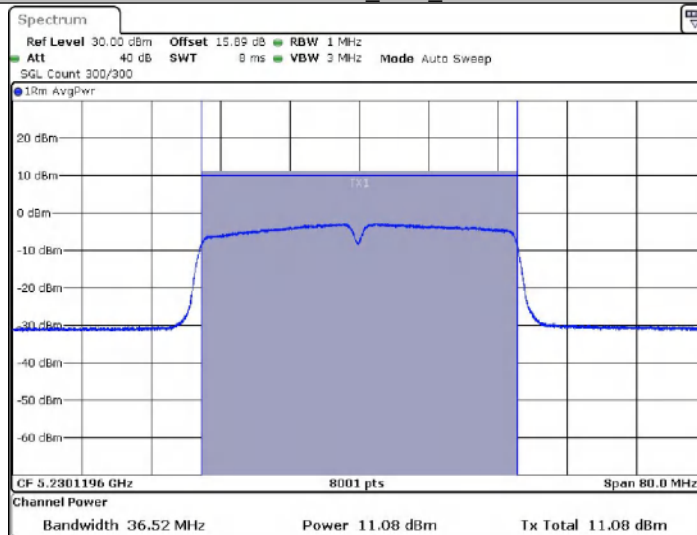
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11AC40SISO_Ant1_5190



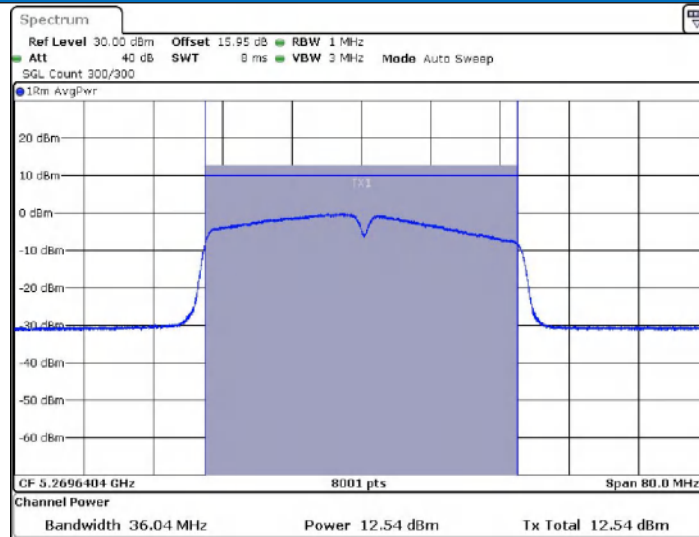
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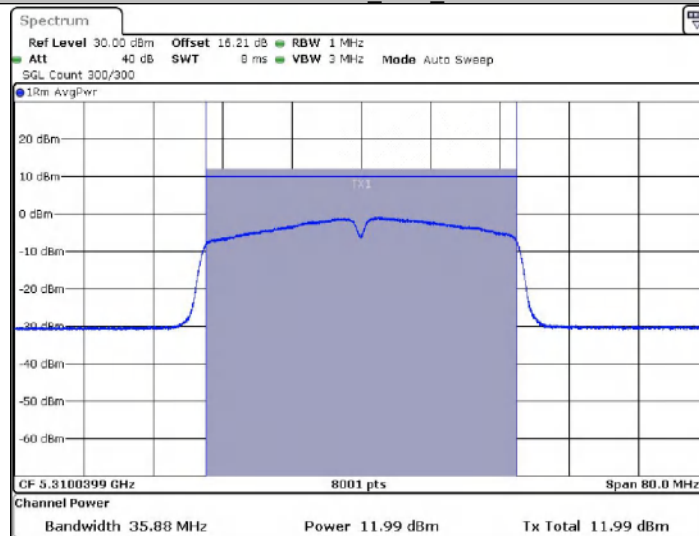
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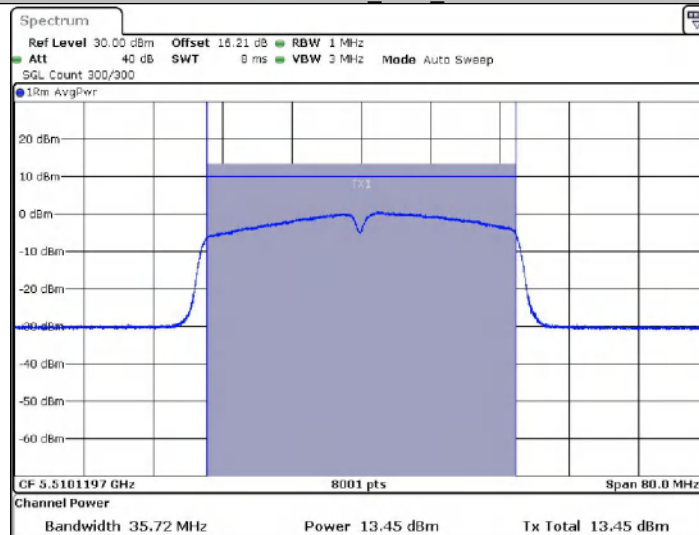
Date: 26 FEB. 2026 16:08:14

11AC40SISO_Ant1_5310



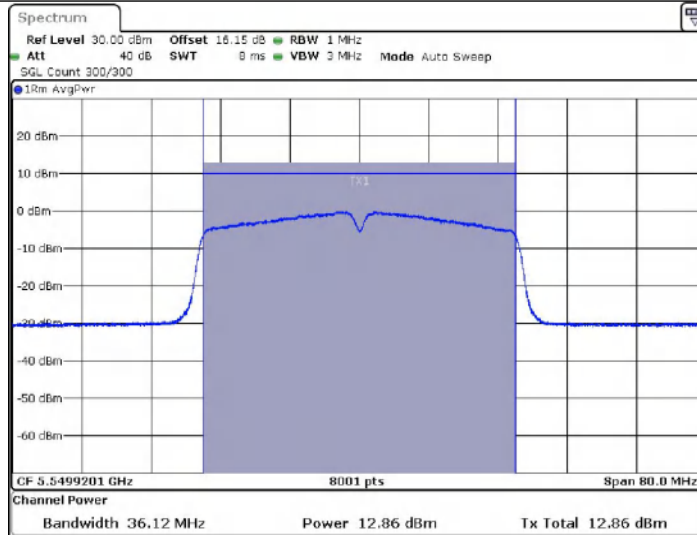
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11AC40SISO_Ant1_5510

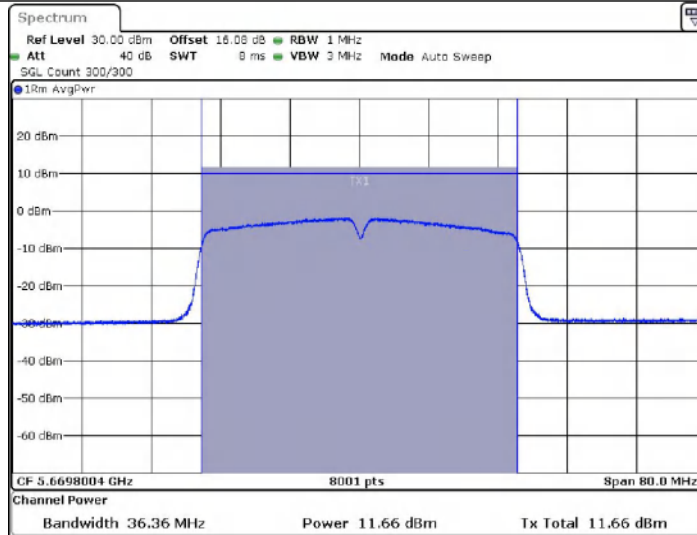


Date: 26 FEB. 2026 16:11:21

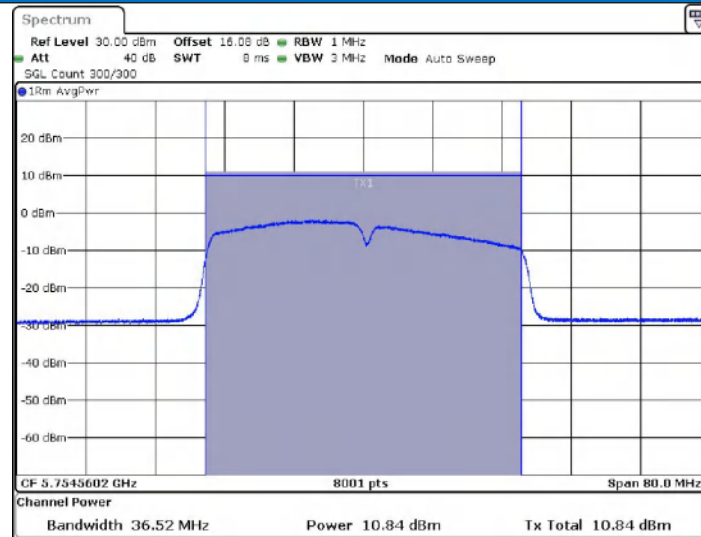
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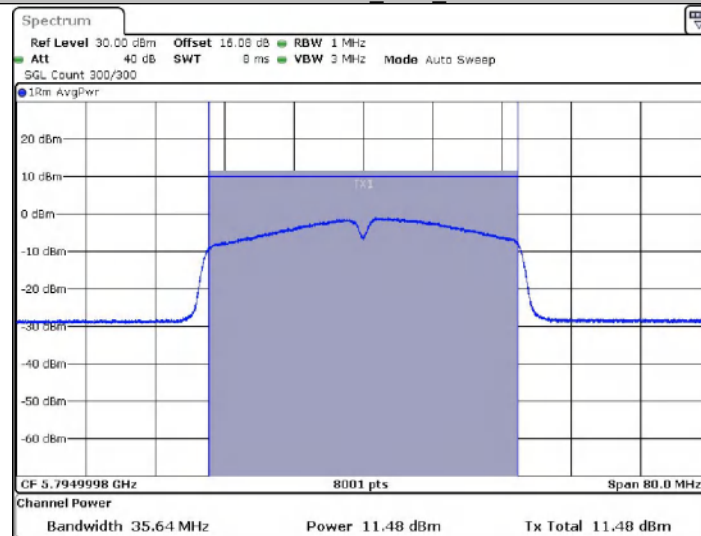


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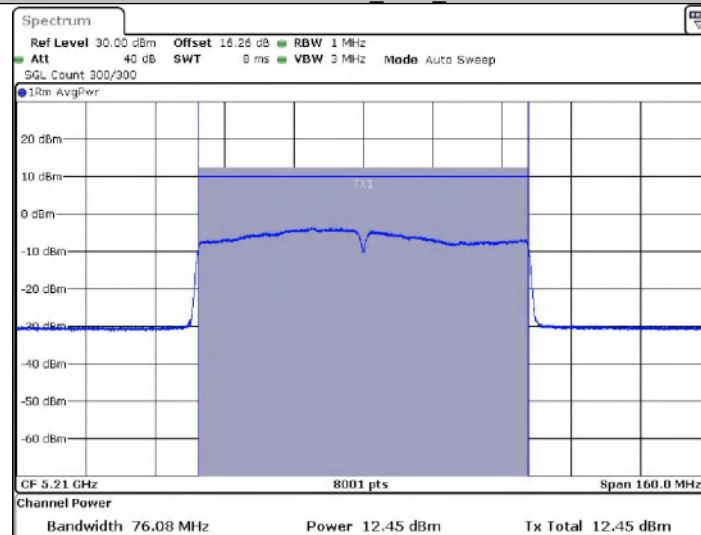
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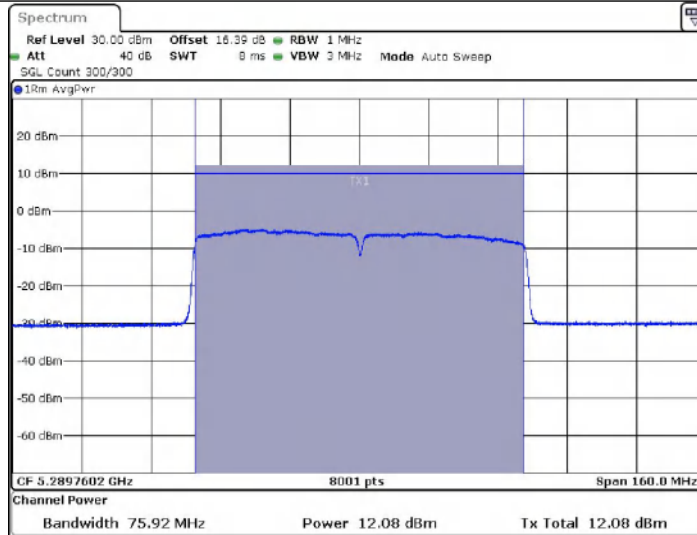
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11AC80SISO_Ant1_5210



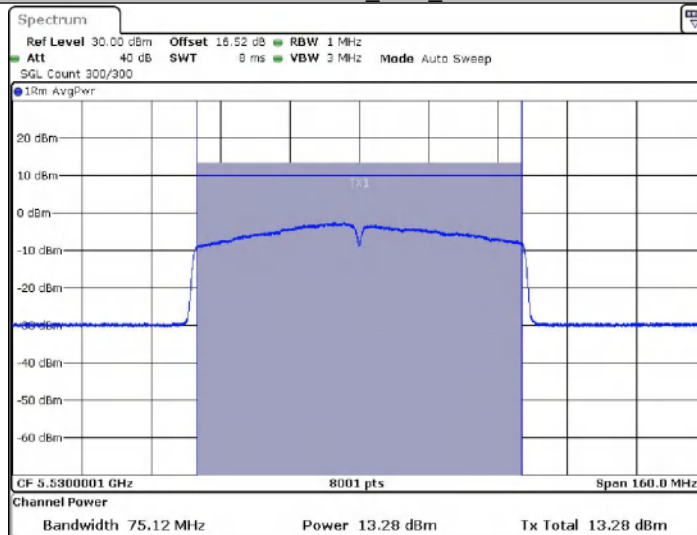
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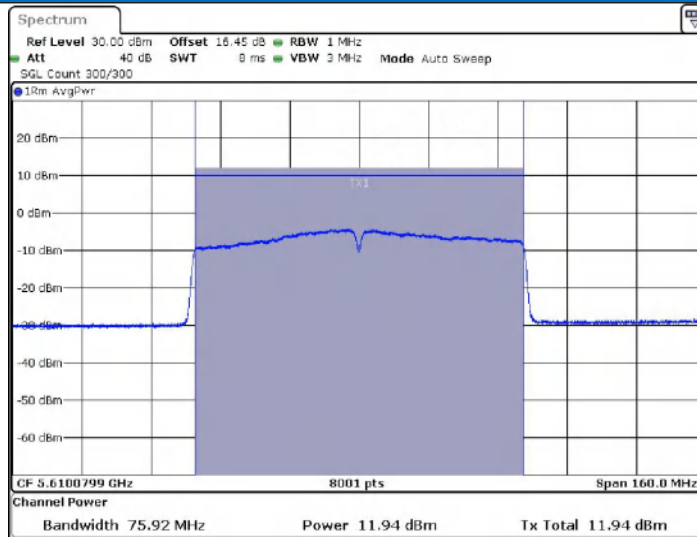
Date: 26 FEB. 2026 16:28:40

11AC80SISO_Ant1_5530



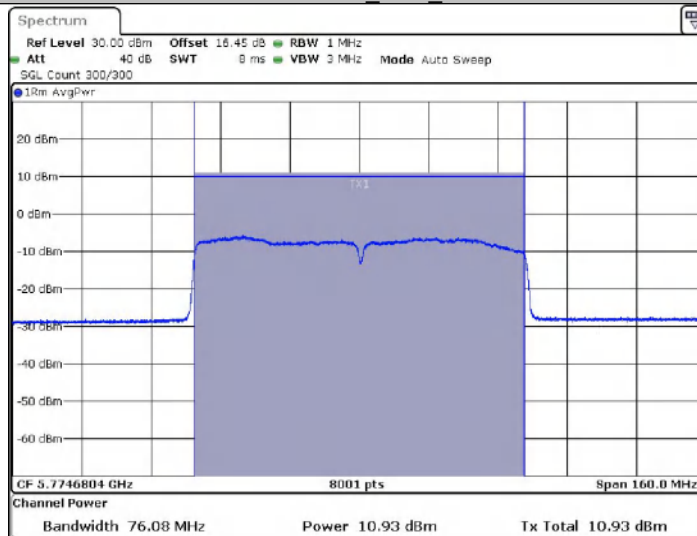
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11AC80SISO_Ant1_5610



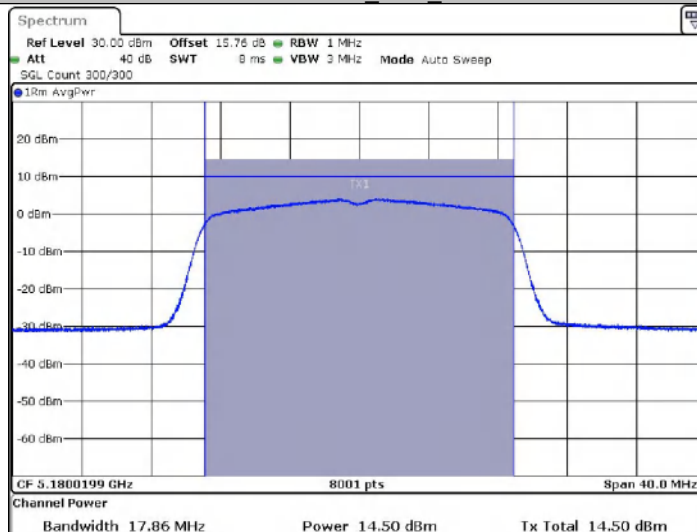
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11AC80SISO_Ant1_5775



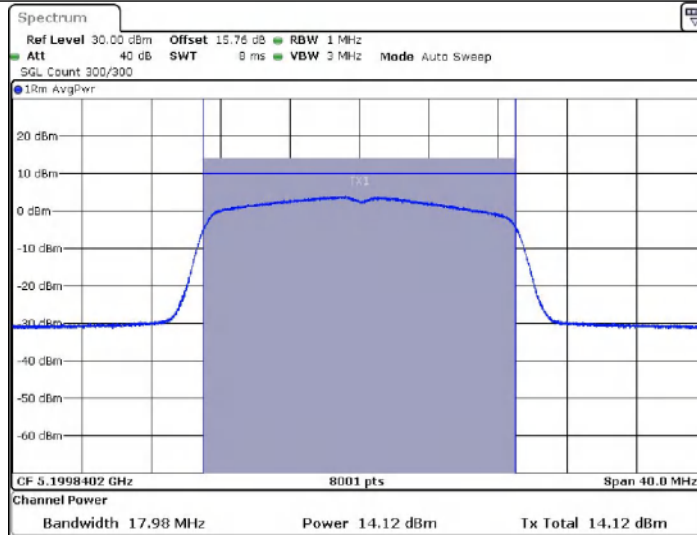
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11AX20SISO_Ant1_5180



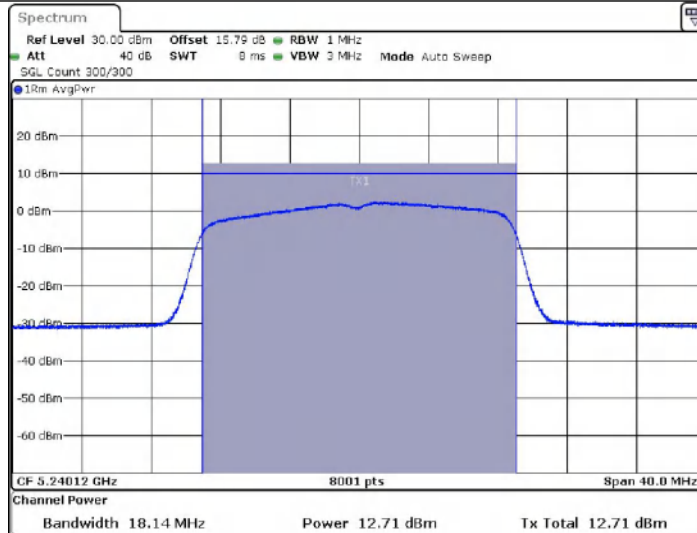
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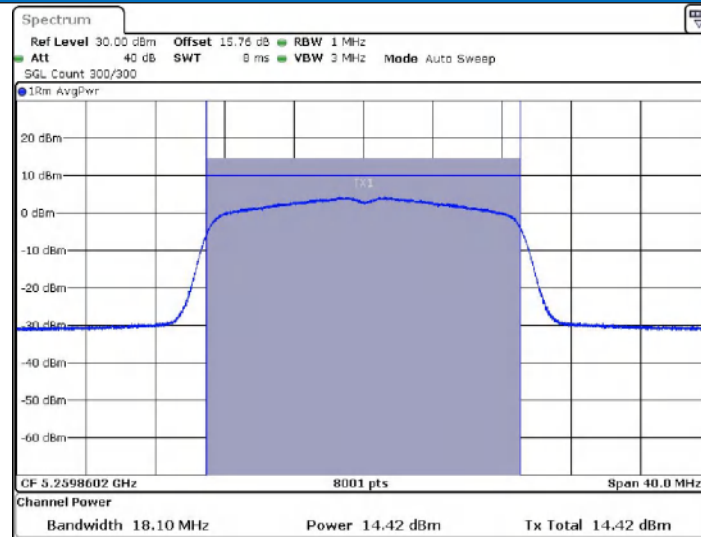
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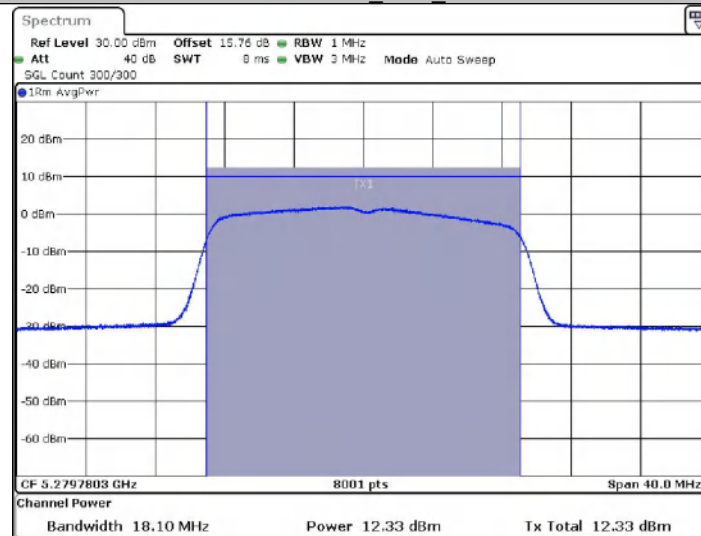
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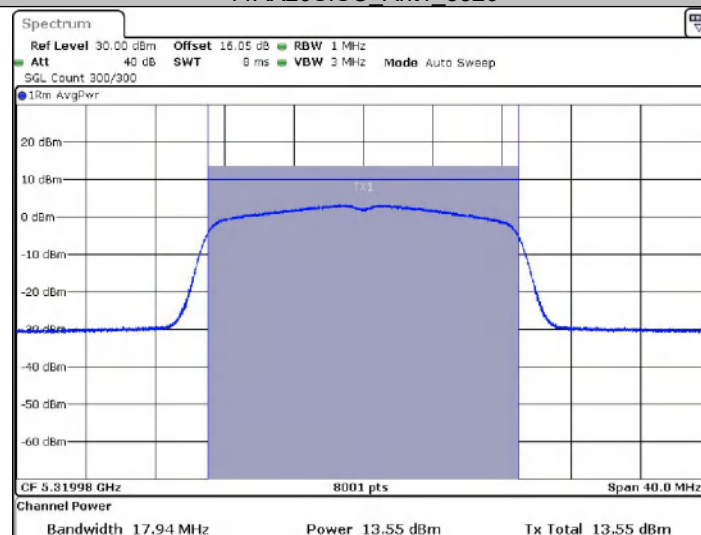
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11AX20SISO_Ant1_5280



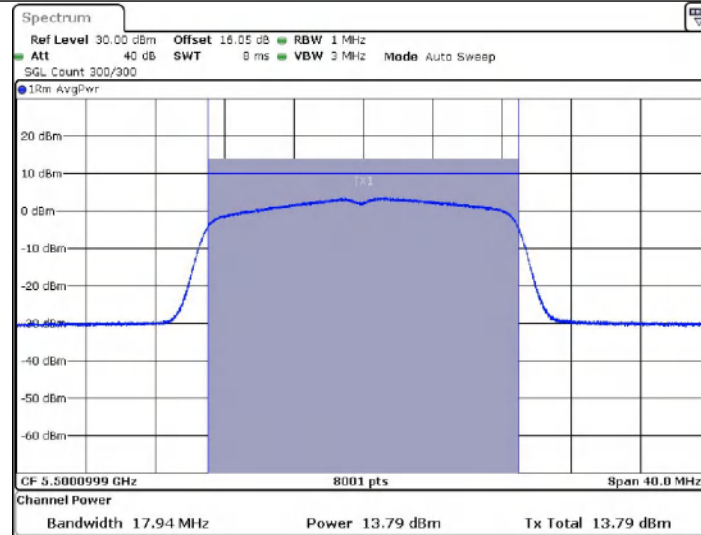
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11AX20SISO_Ant1_5320



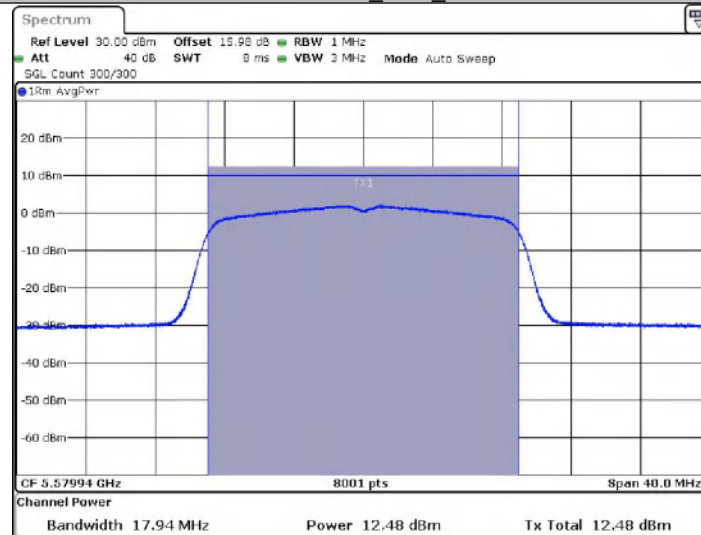
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11AX20SISO_Ant1_5500



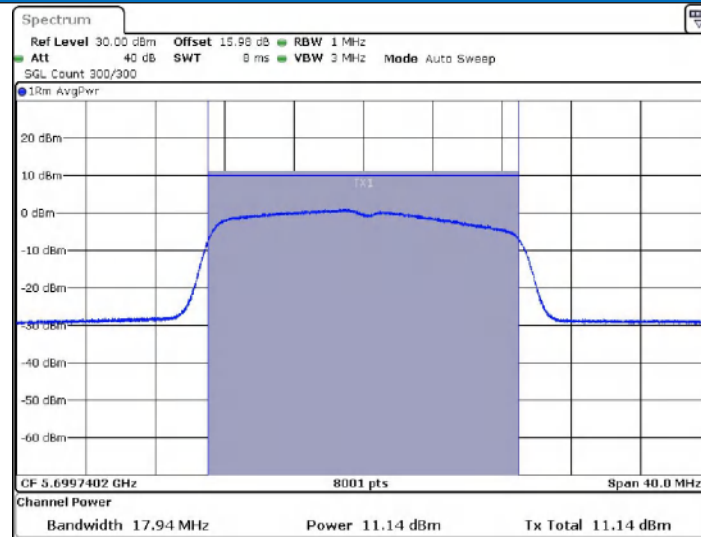
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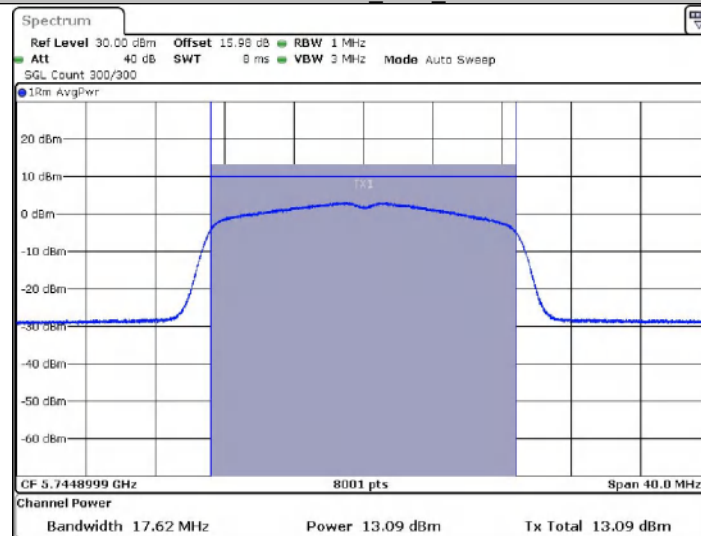
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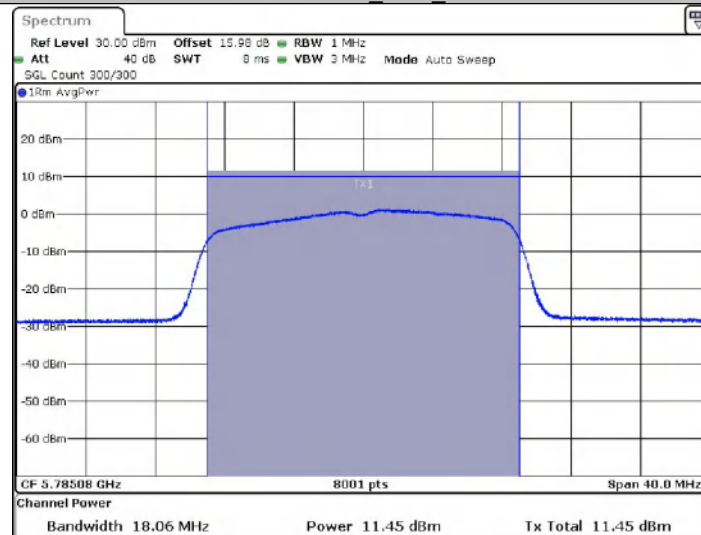
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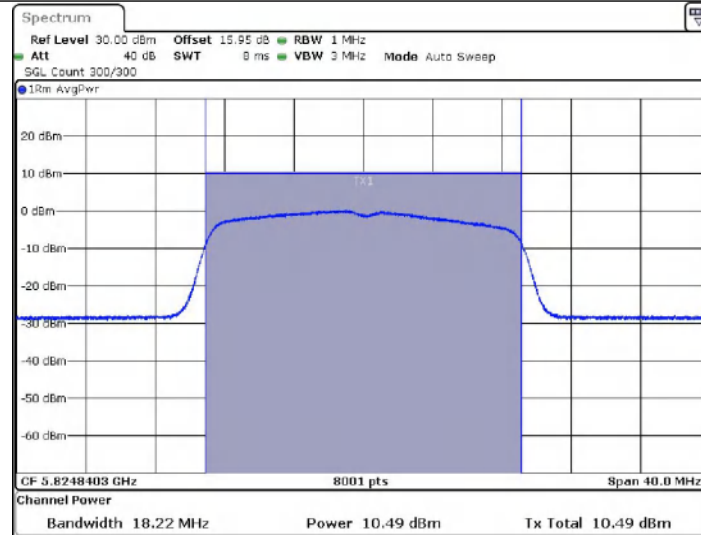
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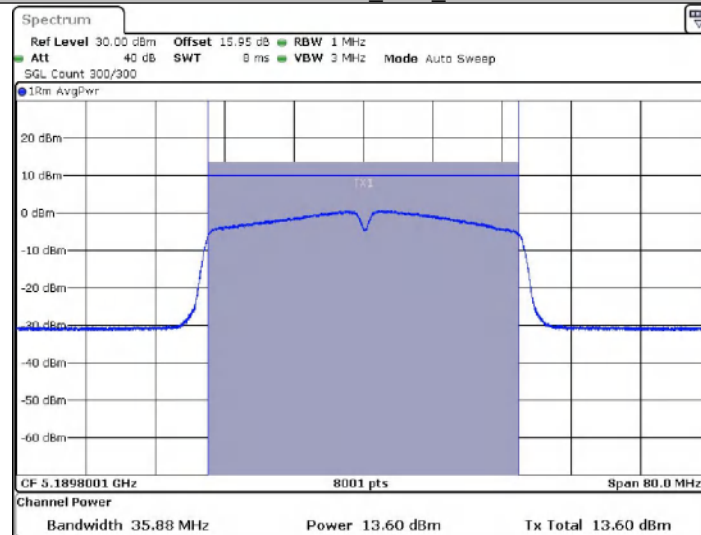
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11AX20SISO_Ant1_5825



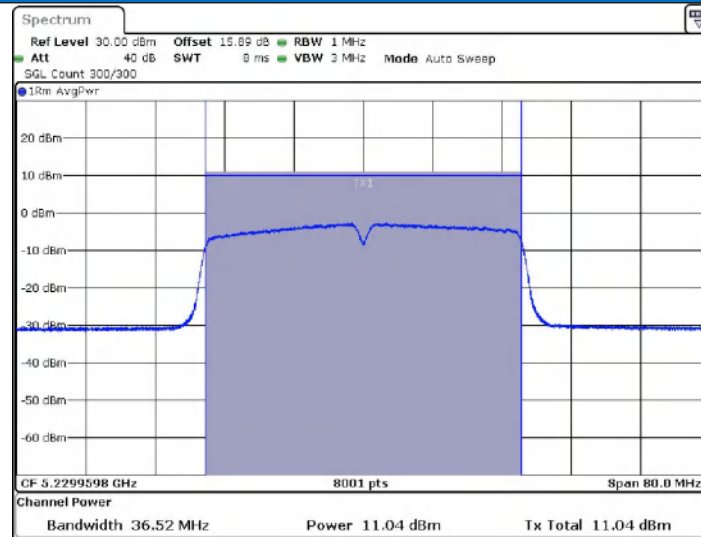
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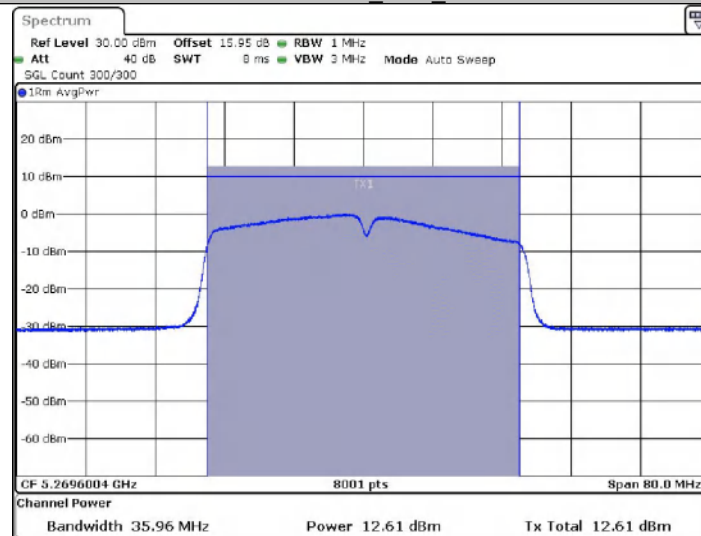
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11AX40SISO_Ant1_5230



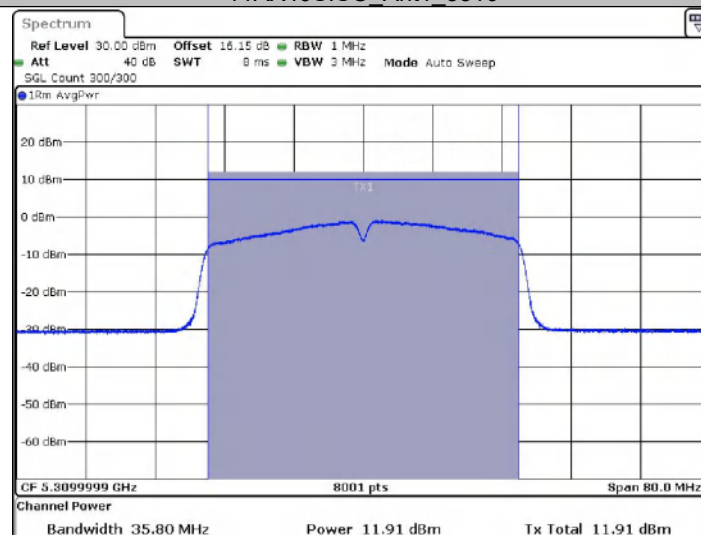
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11AX40SISO_Ant1_5270



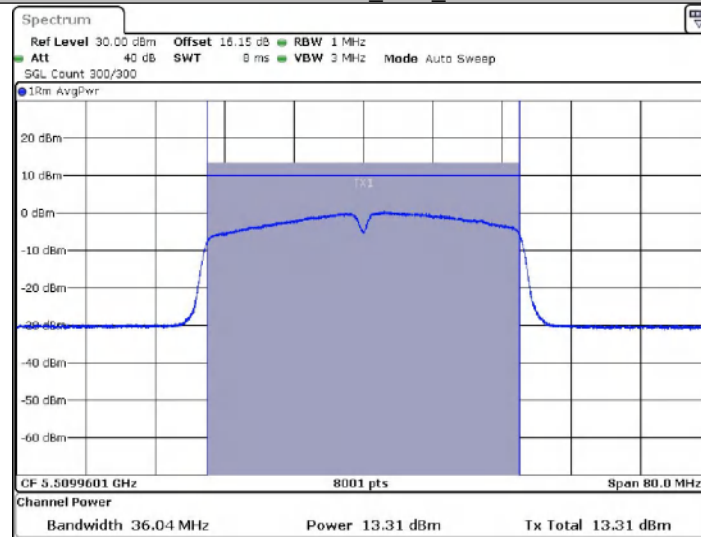
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11AX40SISO_Ant1_5310

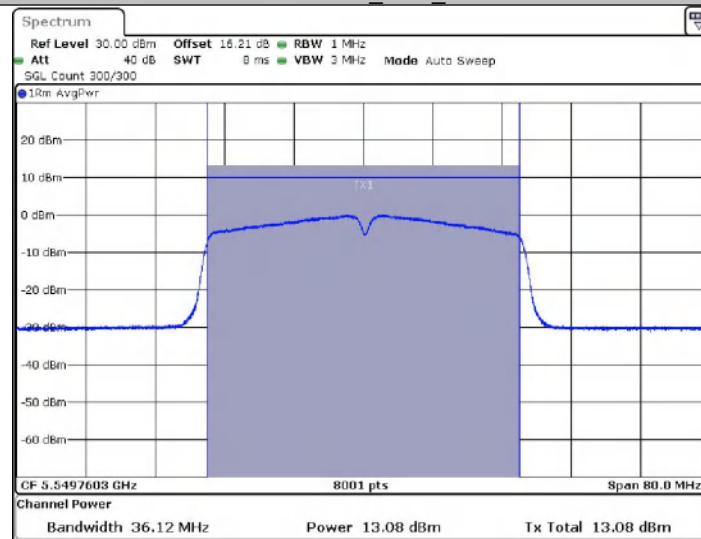


Date: 26 FEB.2026 17:02:02

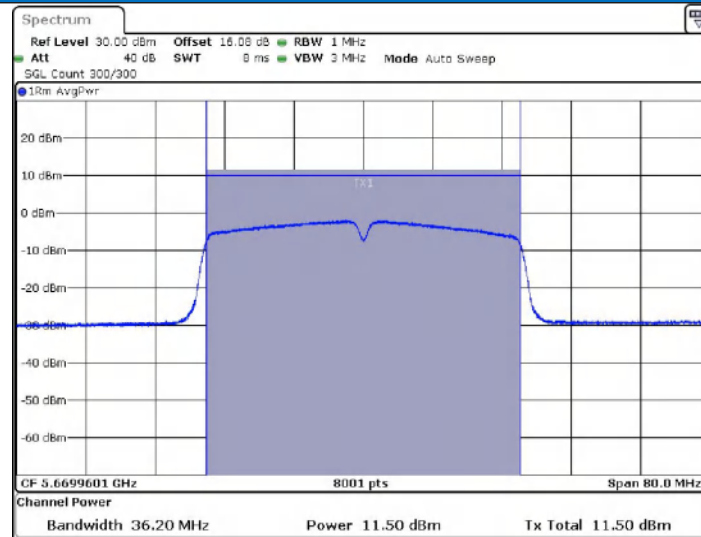
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11AX40SISO_Ant1_5550



11AX40SISO_Ant1_5670



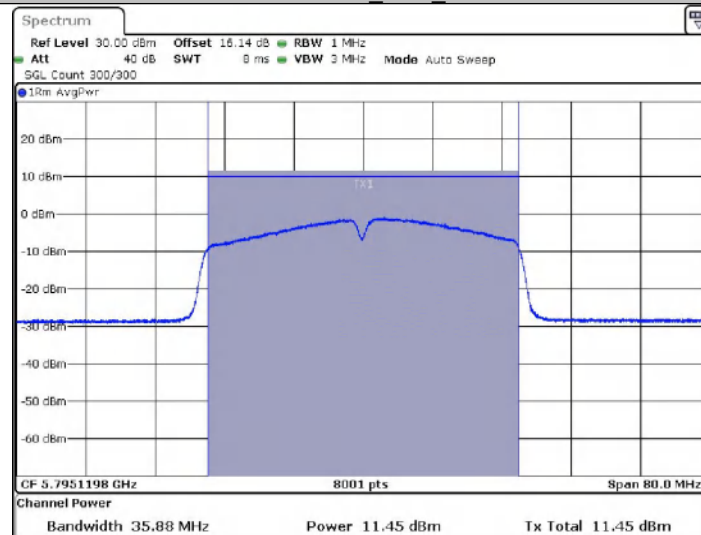
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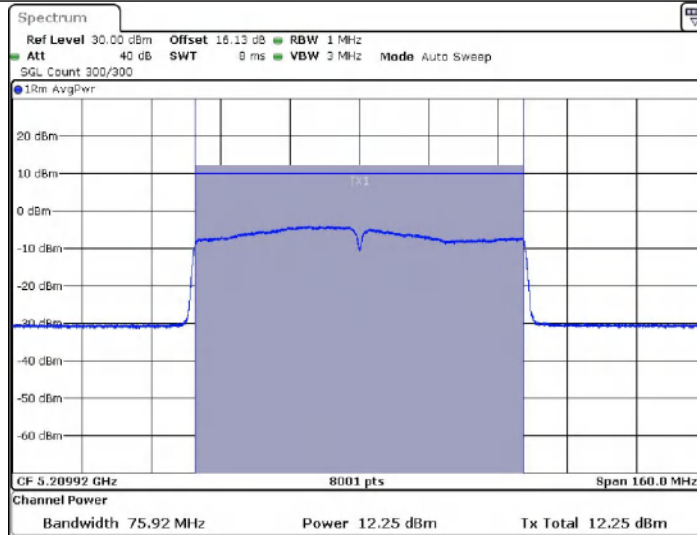
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11AX40SISO_Ant1_5795



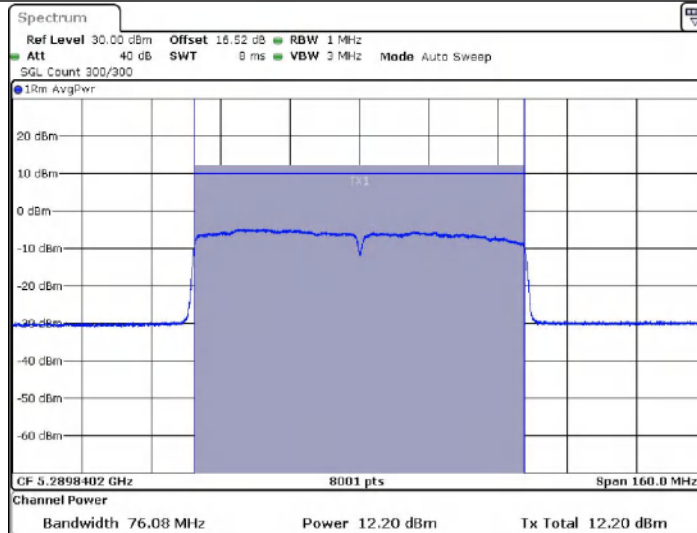
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11AX80SISO_Ant1_5210



Date: 26 FEB.2026 17:15:00

11AX80SISO_Ant1_5290



Date: 26 FEB.2026 17:17:37

11AX80SISO_Ant1_5530