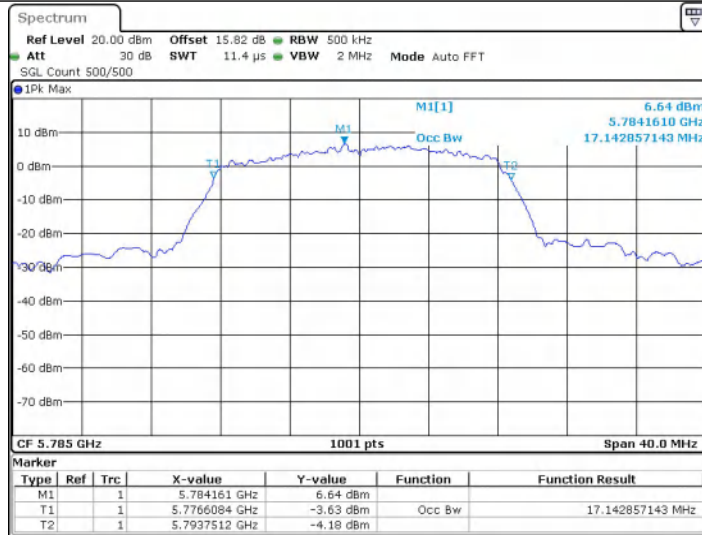
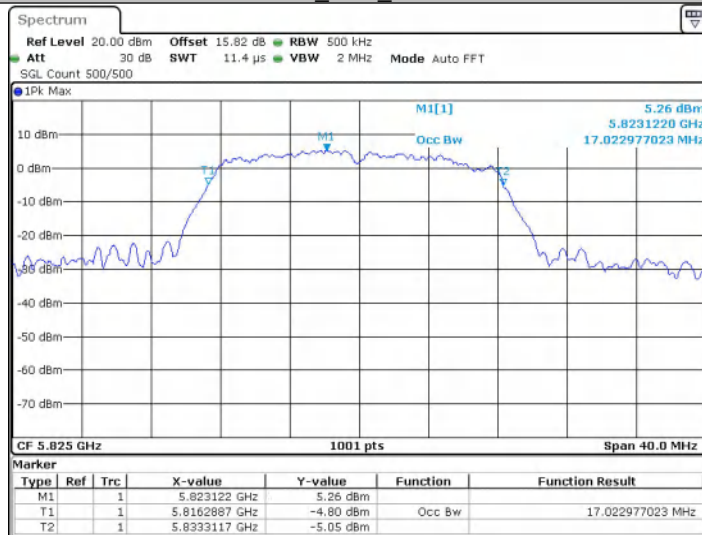


11A_Ant2_5785



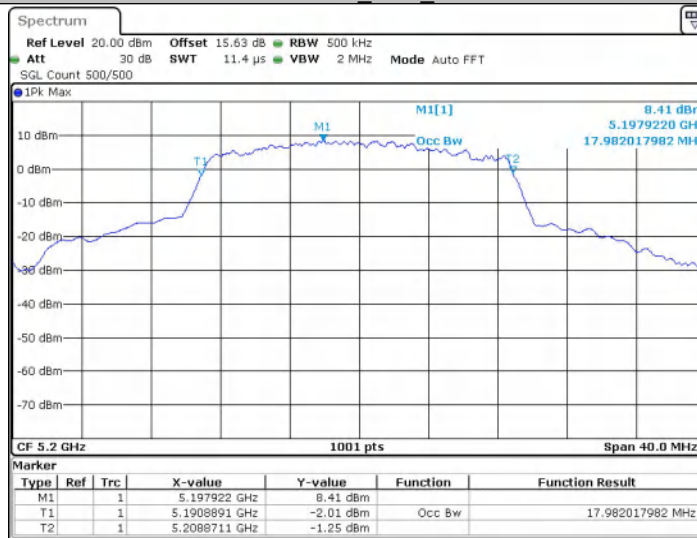
11A_Ant2_5825



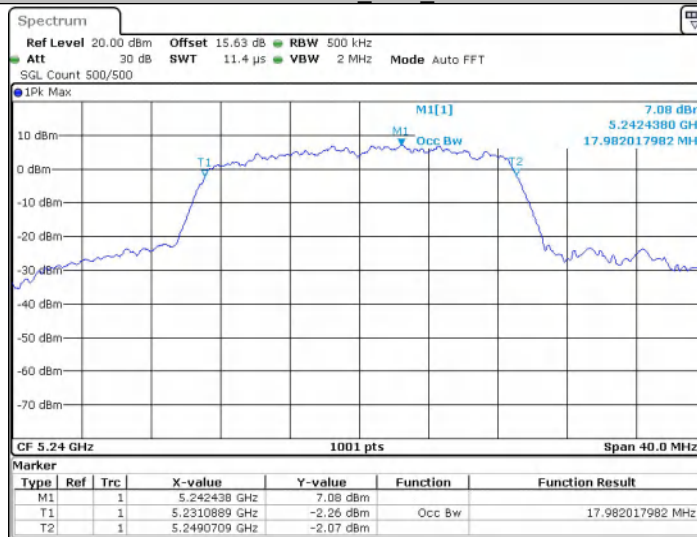
11N20SISO_Ant2_5180



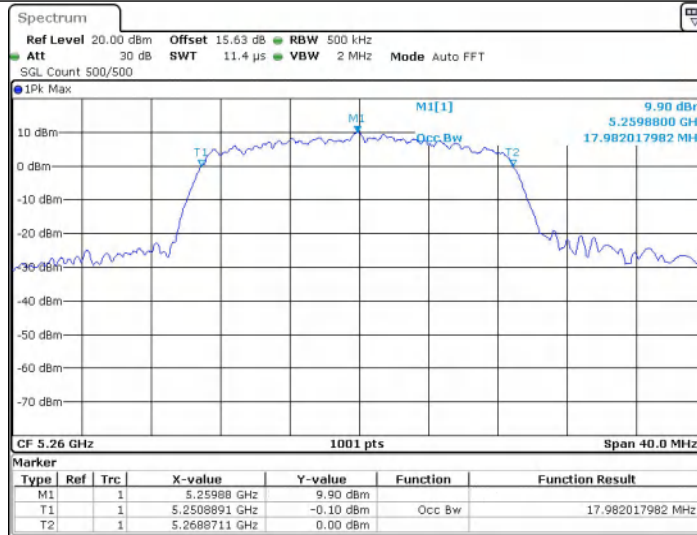
11N20SISO_Ant2_5200



11N20SISO_Ant2_5240

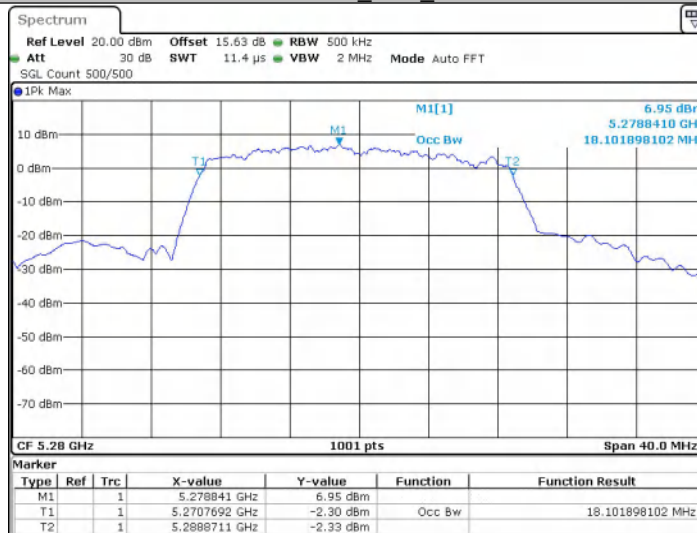


11N20SISO_Ant2_5260



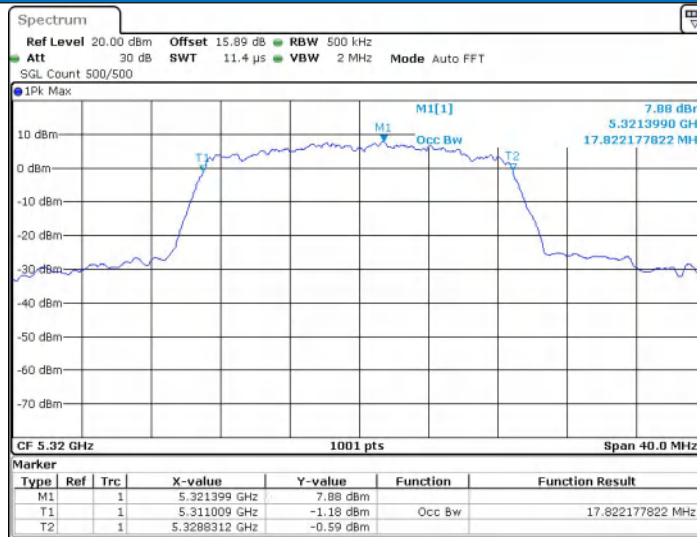
Date: 4 MAR 2026 10:42:08

11N20SISO_Ant2_5280



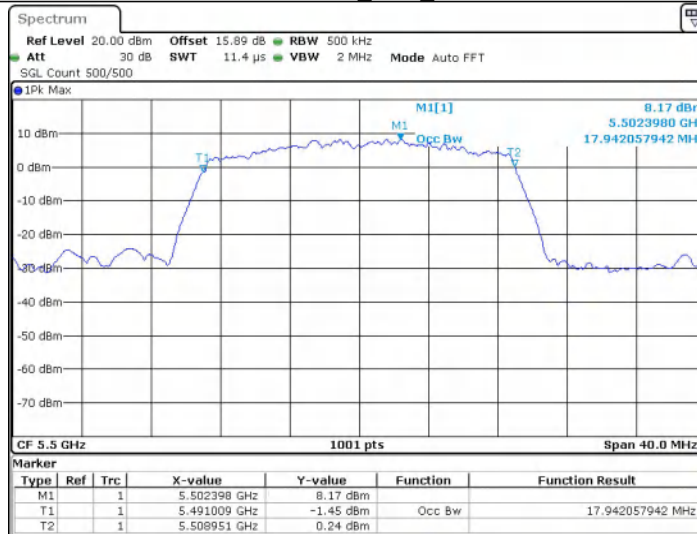
Date: 4 MAR 2026 10:44:20

11N20SISO_Ant2_5320



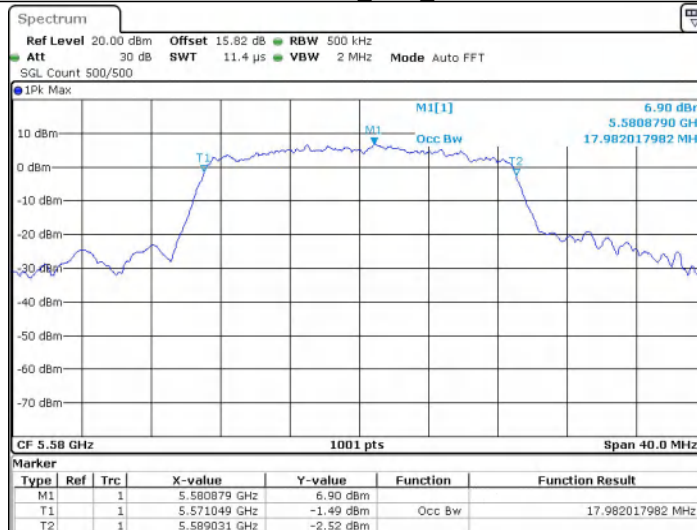
Date: 4 MAR 2026 10:45:31

11N20SISO_Ant2_5500



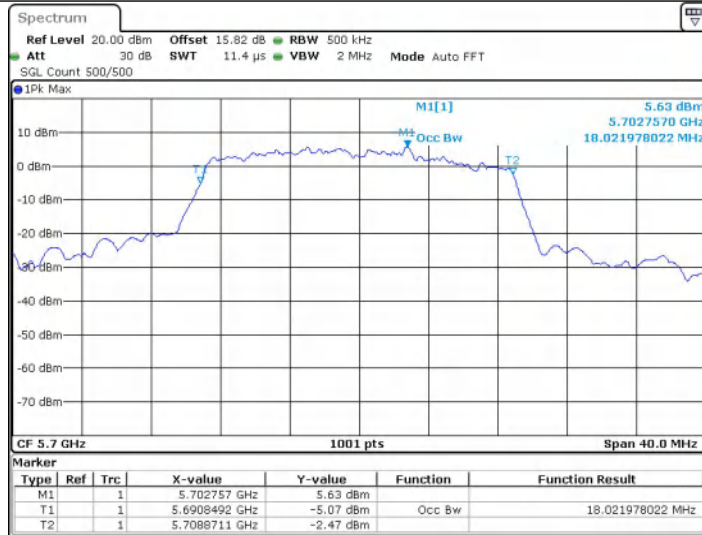
Date: 4 MAR 2026 10:50:34

11N20SISO_Ant2_5580

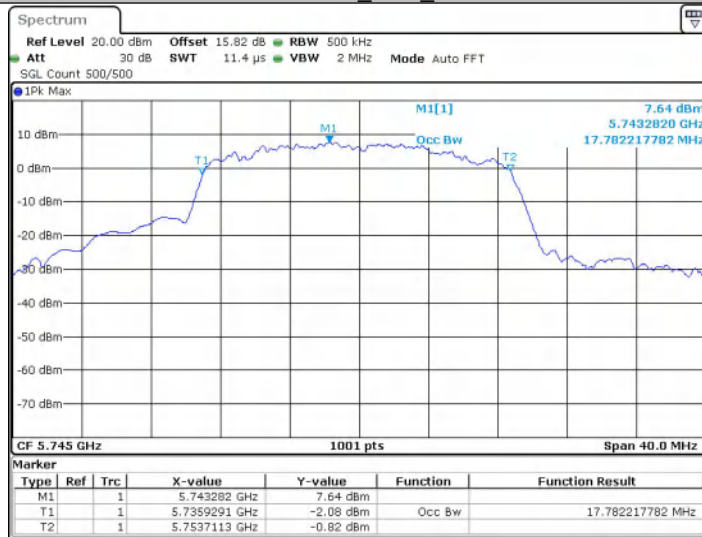


Date: 4 MAR 2026 10:52:57

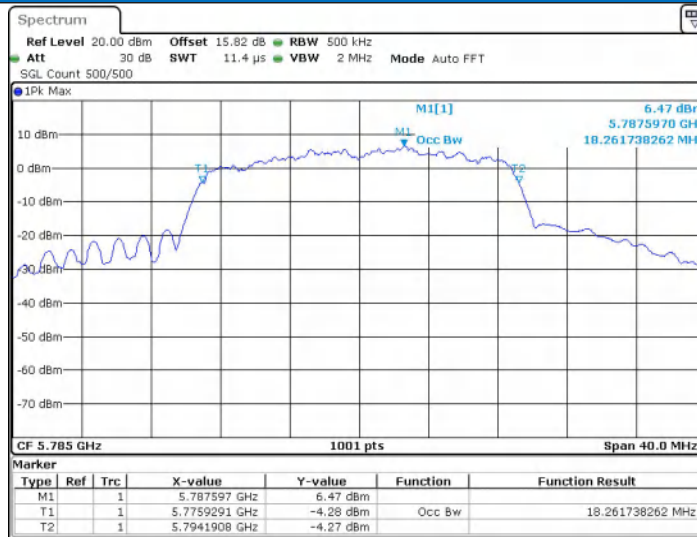
11N20SISO_Ant2_5700



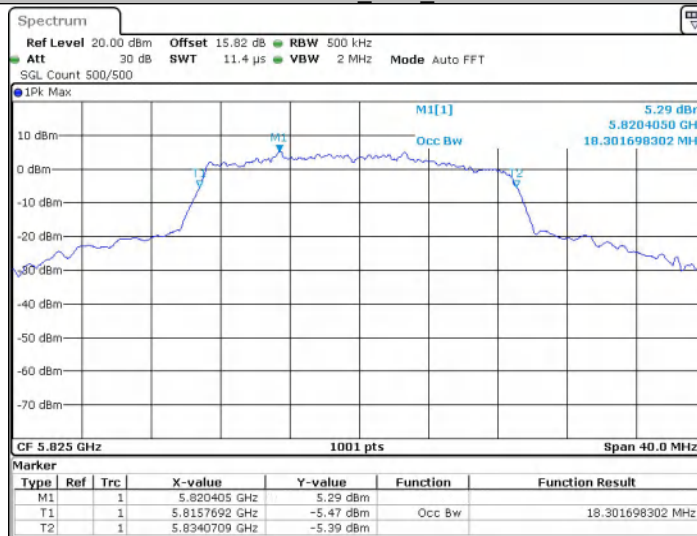
11N20SISO_Ant2_5745



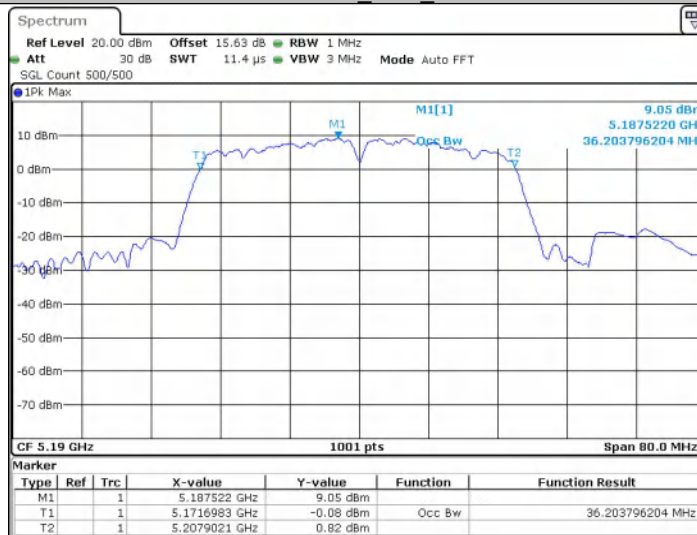
11N20SISO_Ant2_5785



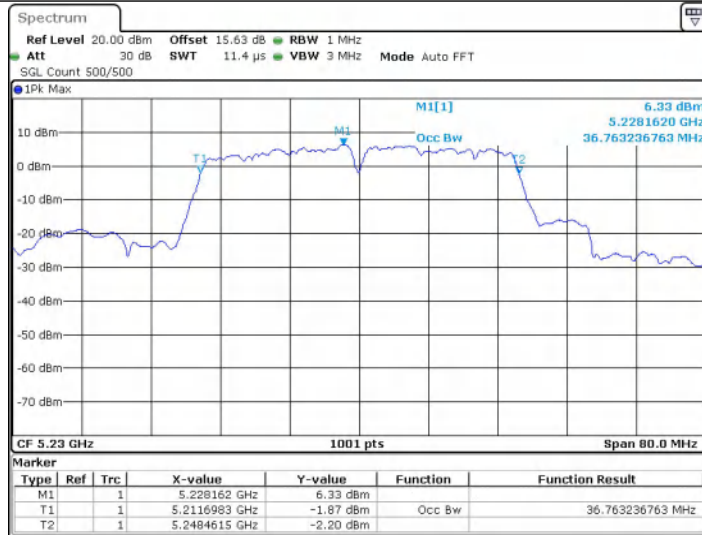
11N20SISO_Ant2_5825



11N40SISO_Ant2_5190

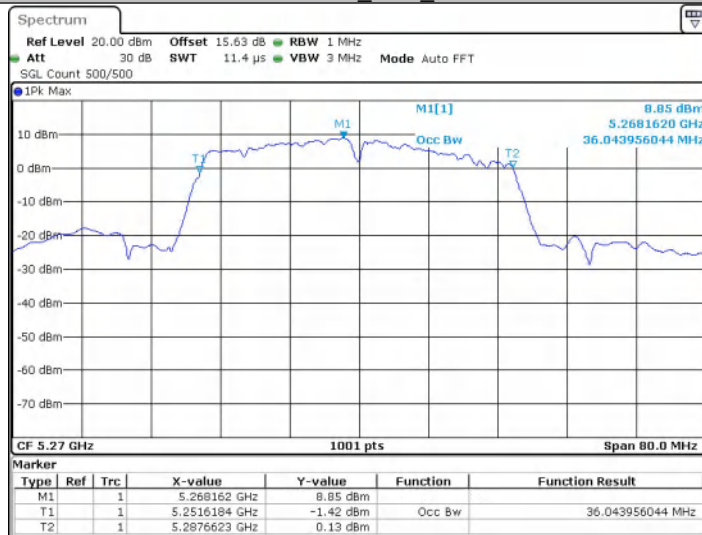


11N40SISO_Ant2_5230



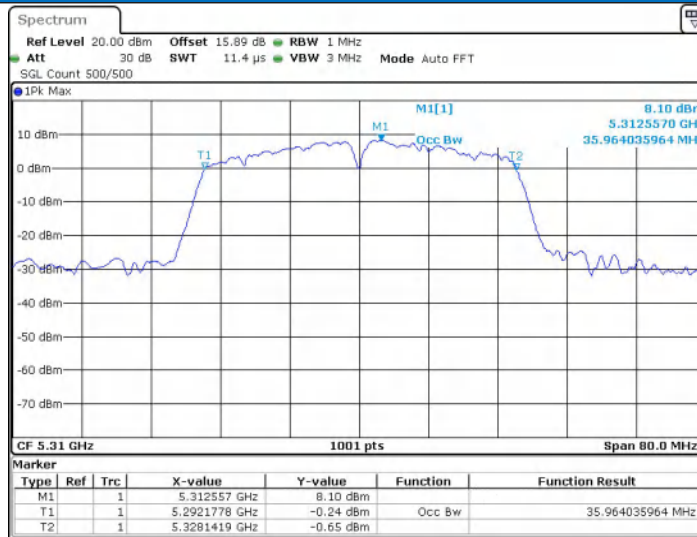
Date: 4 MAR 2026 11:07:28

11N40SISO_Ant2_5270



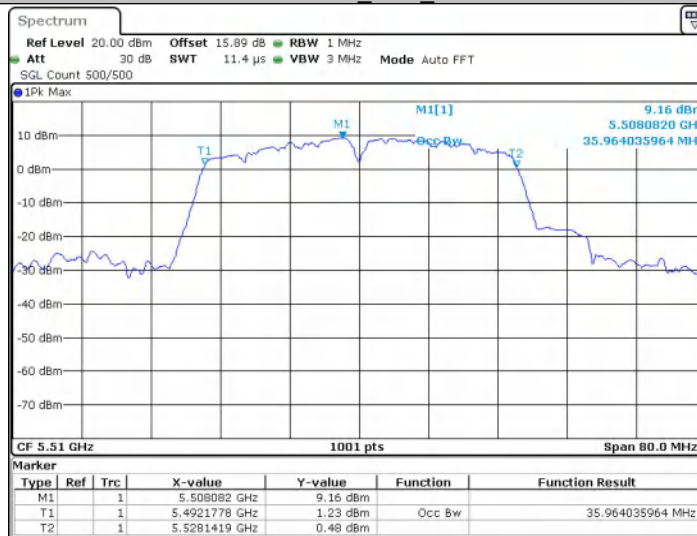
Date: 4 MAR 2026 11:08:36

11N40SISO_Ant2_5310



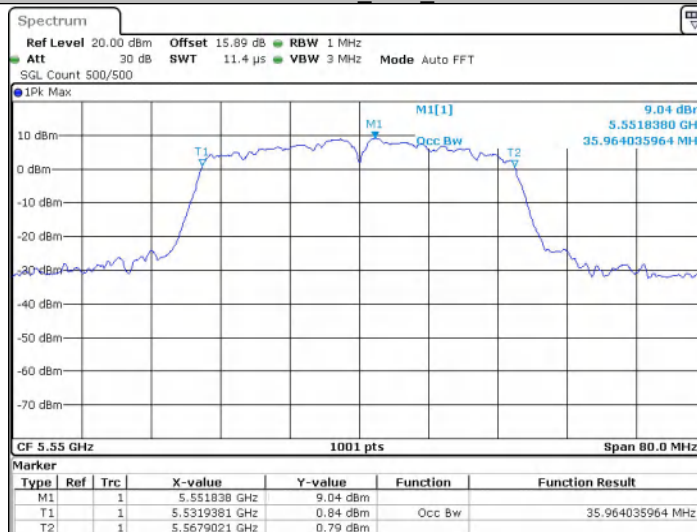
Date: 4 MAR 2026 11:15:59

11N40SISO_Ant2_5510



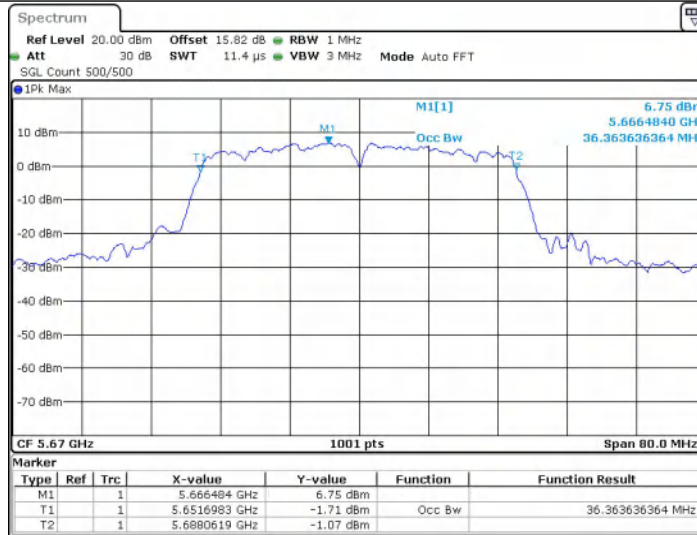
Date: 4 MAR 2026 11:17:36

11N40SISO_Ant2_5550

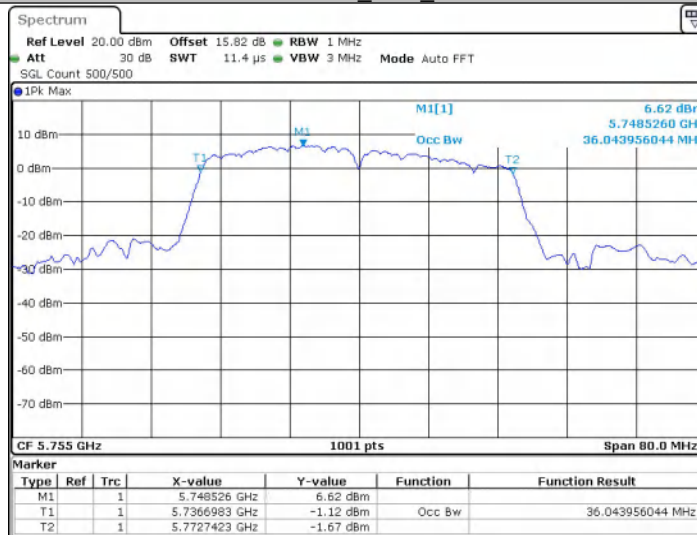


Date: 4 MAR 2026 11:20:22

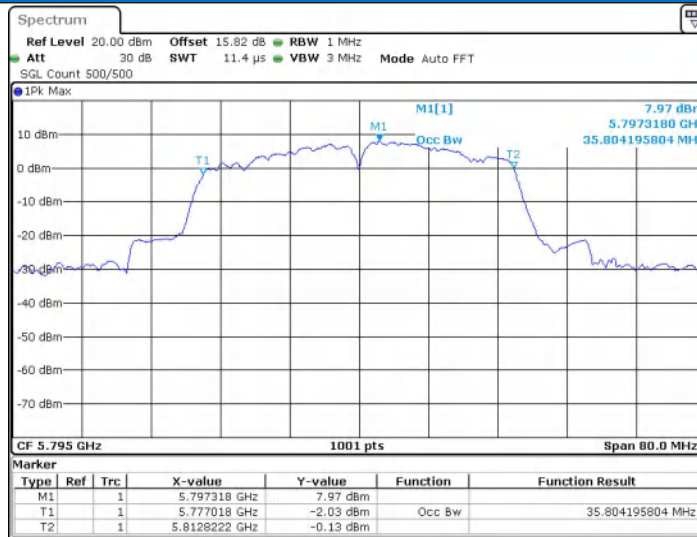
11N40SISO_Ant2_5670



11N40SISO_Ant2_5755

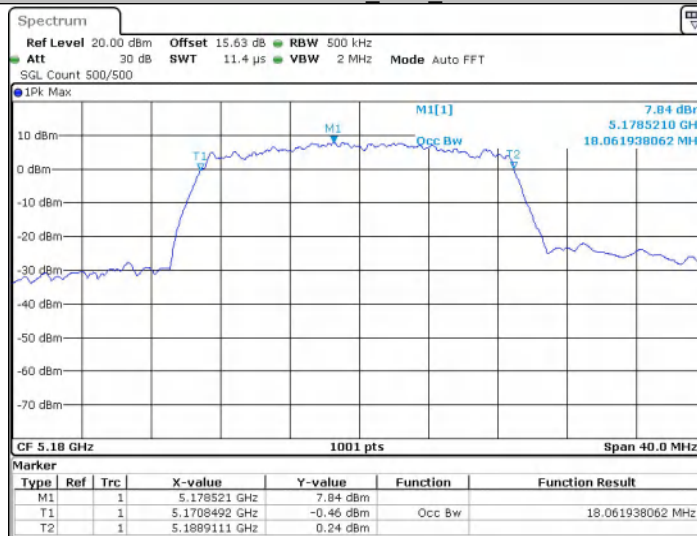


11N40SISO_Ant2_5795



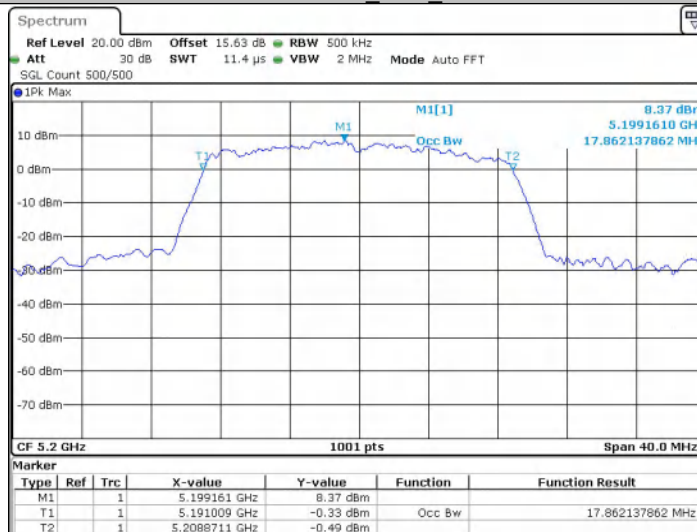
Date: 4 MAR 2026 11:24:44

11AC20SISO_Ant2_5180



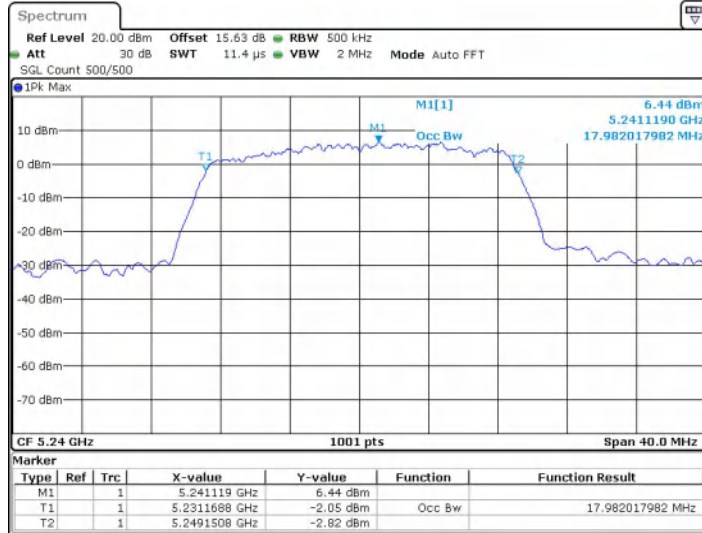
Date: 4 MAR 2026 11:26:13

11AC20SISO_Ant2_5200

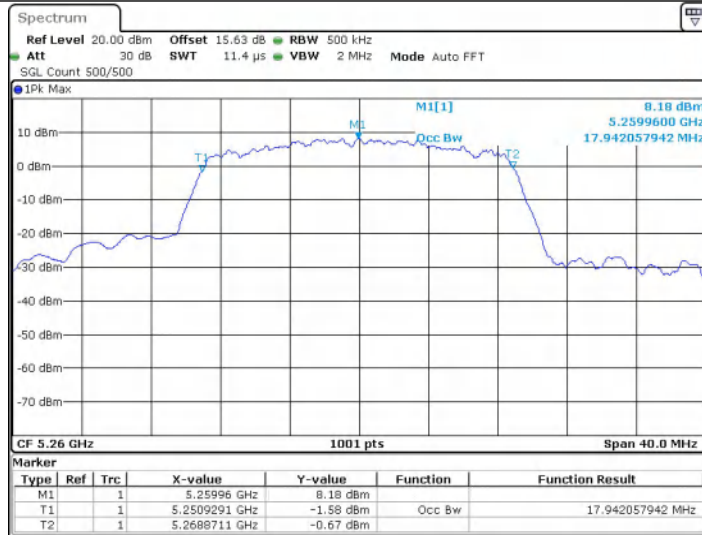


Date: 4 MAR 2026 11:28:25

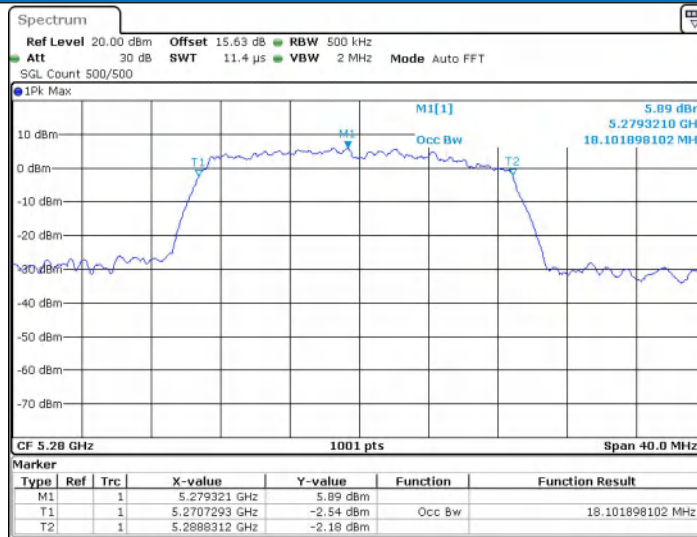
11AC20SISO_Ant2_5240



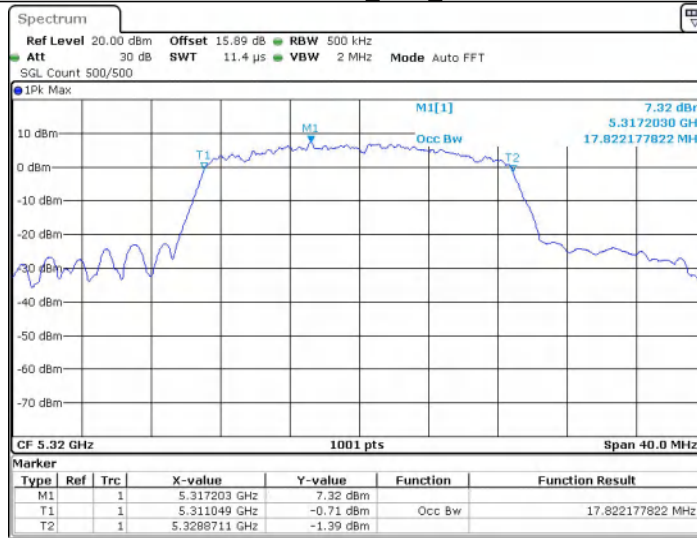
11AC20SISO_Ant2_5260



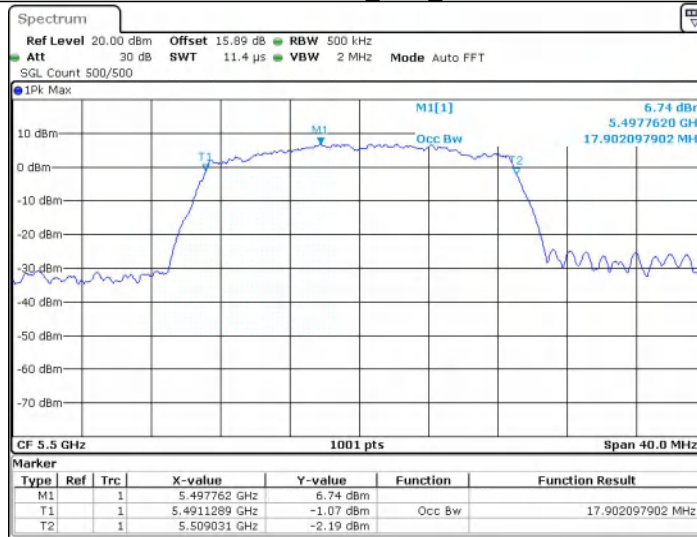
11AC20SISO_Ant2_5280



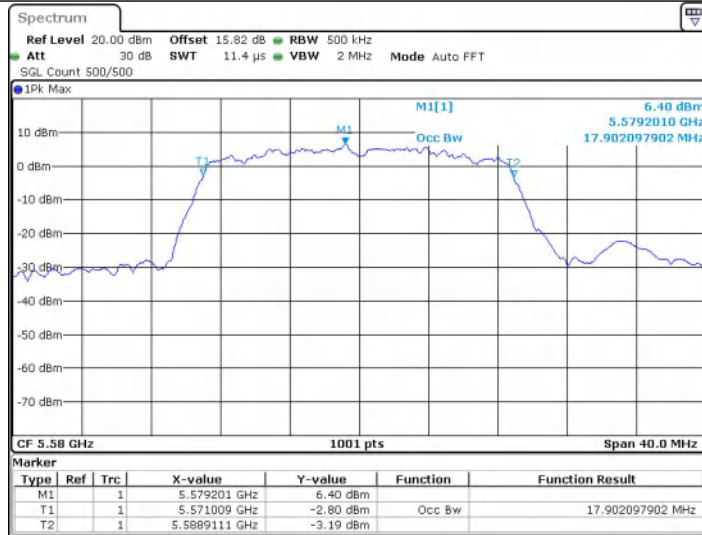
11AC20SISO_Ant2_5320



11AC20SISO_Ant2_5500

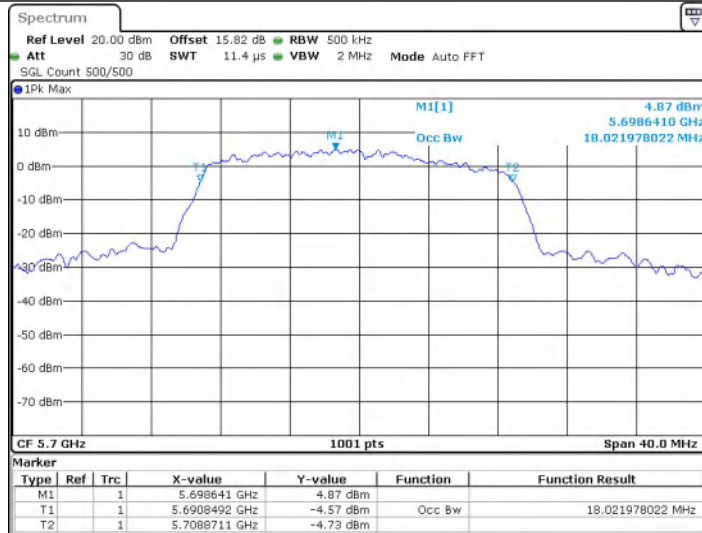


11AC20SISO_Ant2_5580



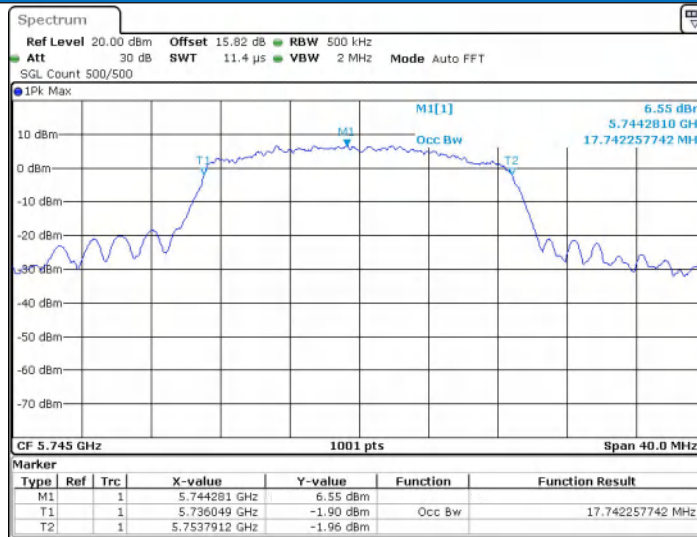
Date: 4 MAR 2026 11:42:45

11AC20SISO_Ant2_5700

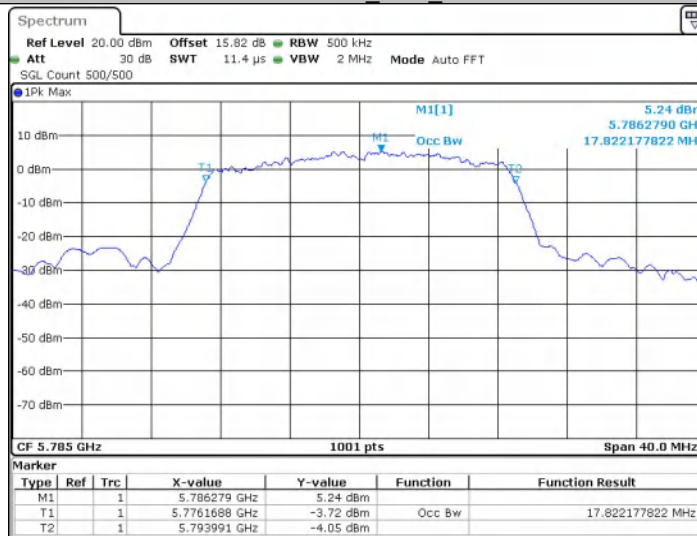


Date: 4 MAR 2026 11:43:47

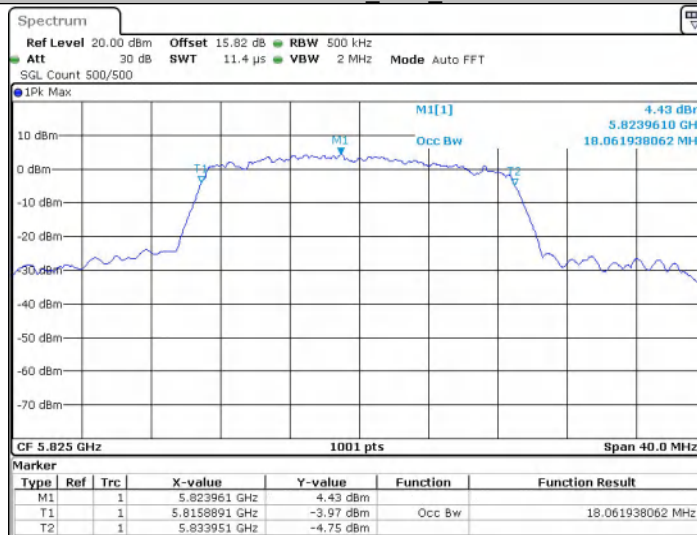
11AC20SISO_Ant2_5745



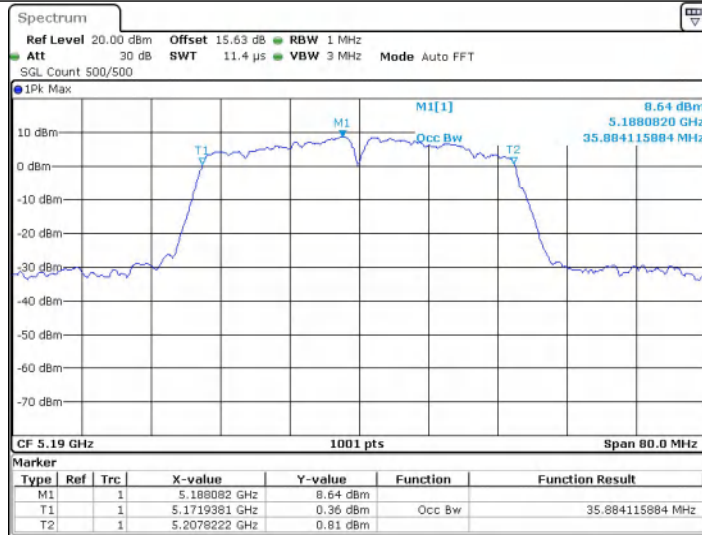
11AC20SISO_Ant2_5785



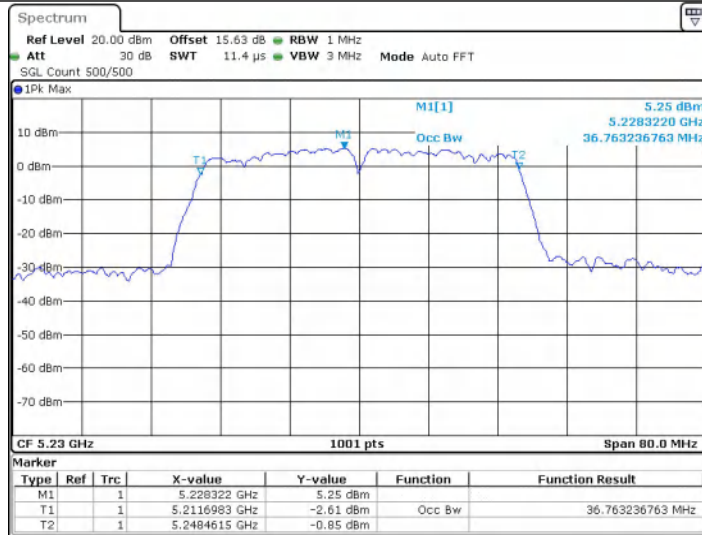
11AC20SISO_Ant2_5825



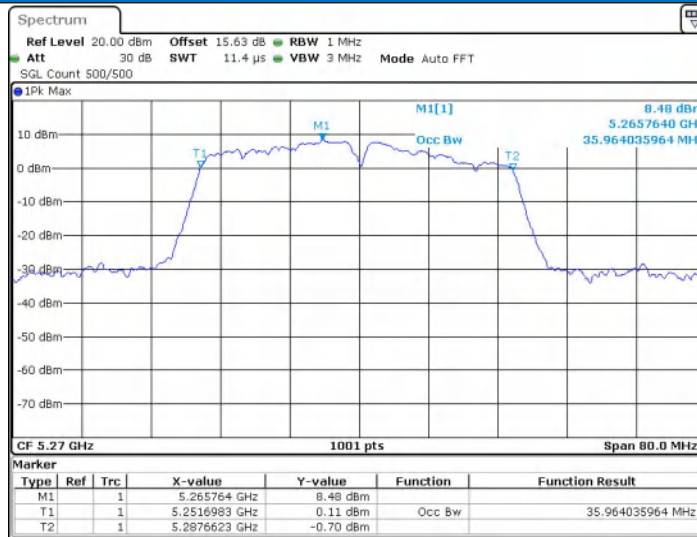
11AC40SISO_Ant2_5190



11AC40SISO_Ant2_5230

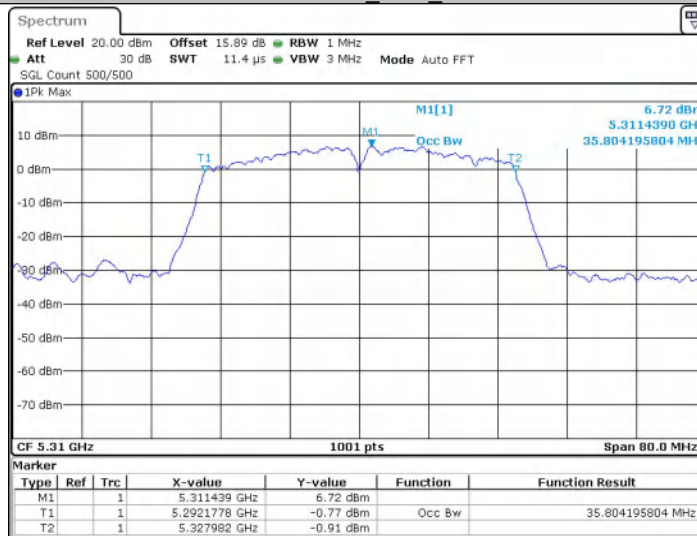


11AC40SISO_Ant2_5270



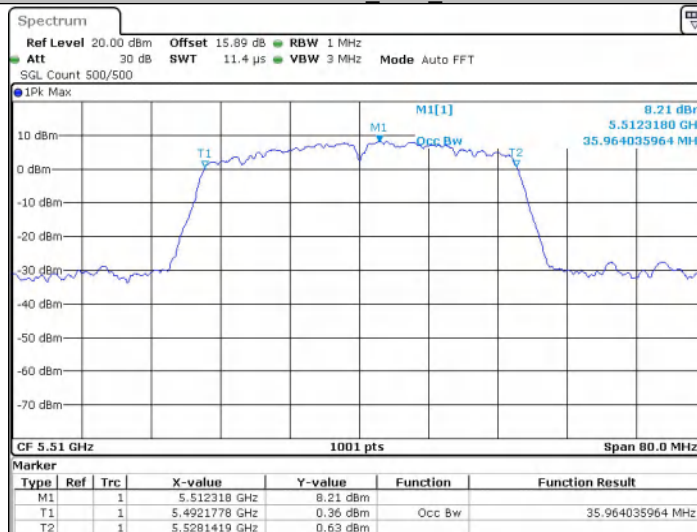
Date: 4 MAR 2026 11:58:01

11AC40SISO_Ant2_5310



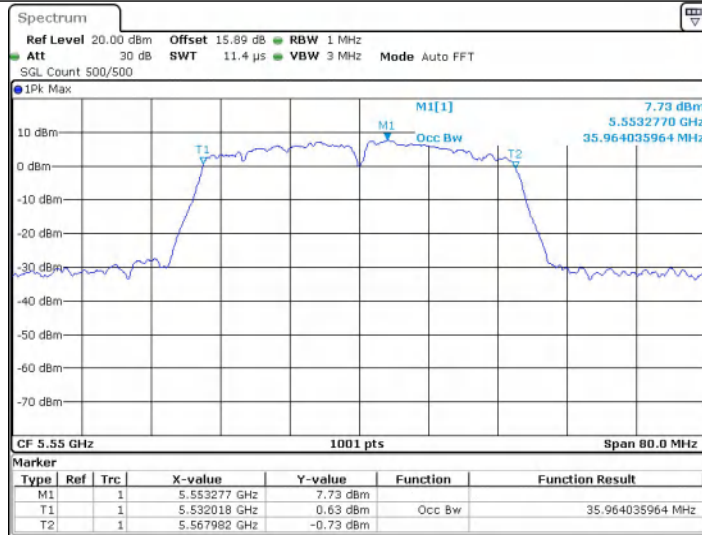
Date: 4 MAR 2026 12:00:06

11AC40SISO_Ant2_5510



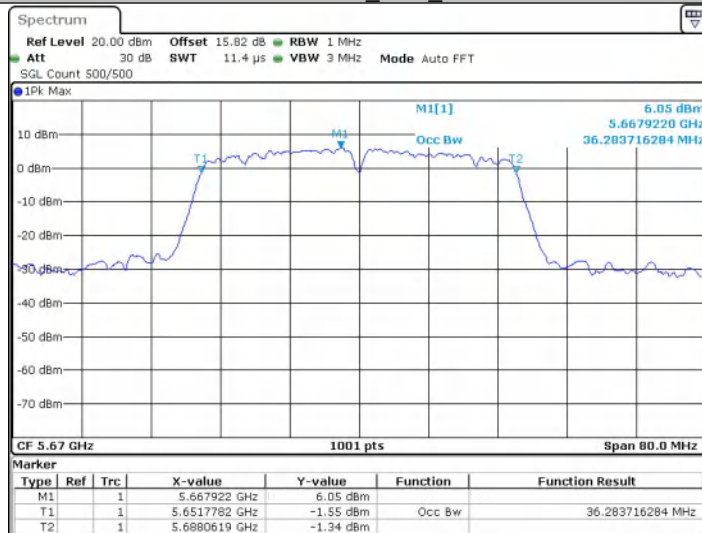
Date: 4 MAR 2026 12:02:03

11AC40SISO_Ant2_5550



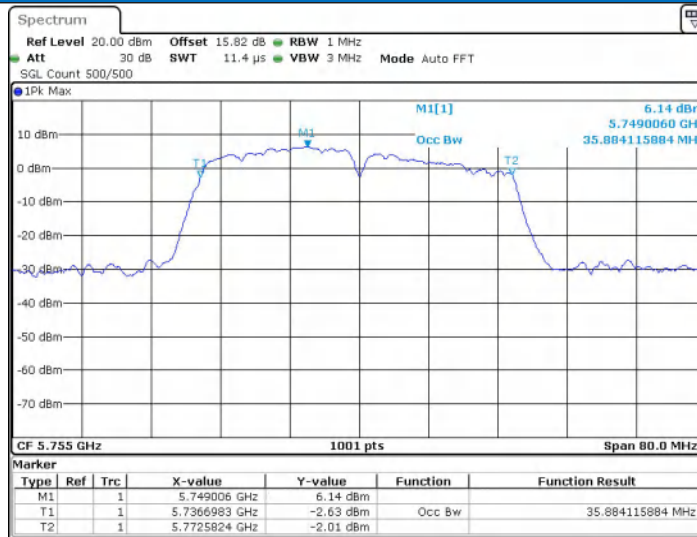
Date: 4 MAR 2026 13:33:39

11AC40SISO_Ant2_5670



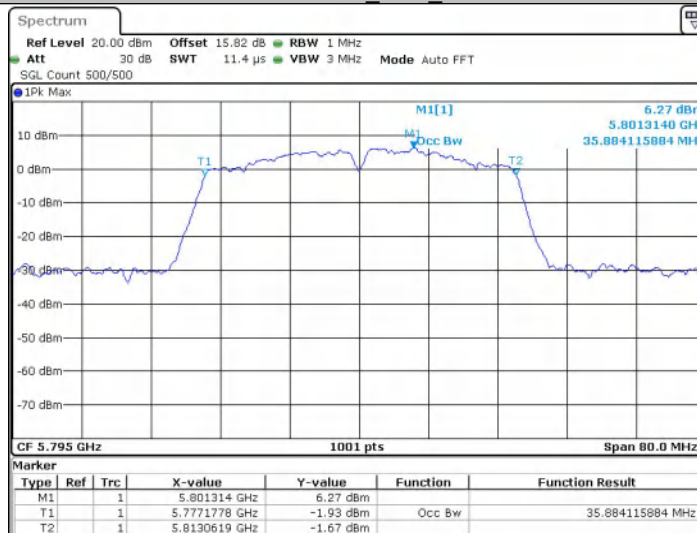
Date: 4 MAR 2026 13:36:44

11AC40SISO_Ant2_5755



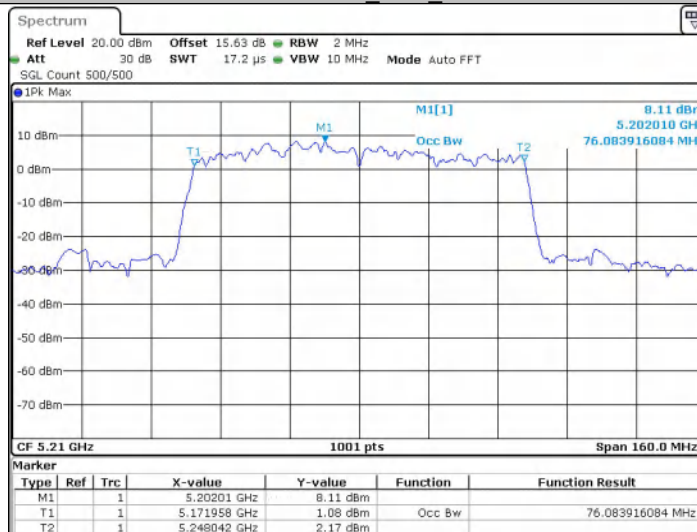
Date: 4 MAR 2026 13:41:01

11AC40SISO_Ant2_5795



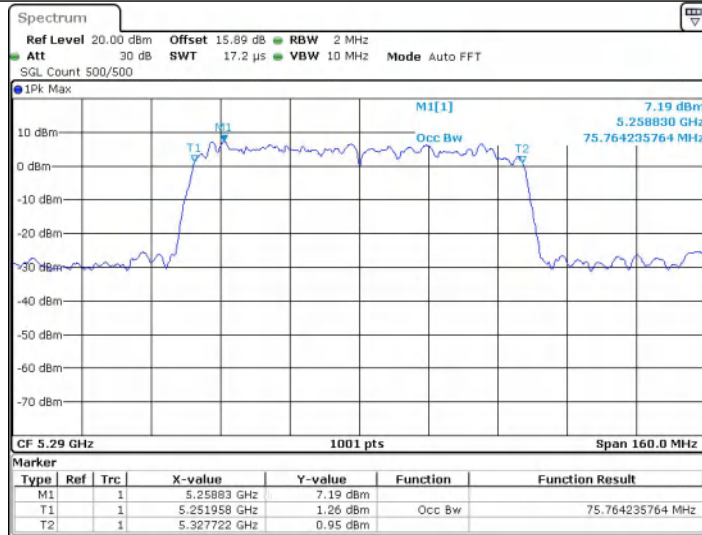
Date: 4 MAR 2026 13:42:47

11AC80SISO_Ant2_5210



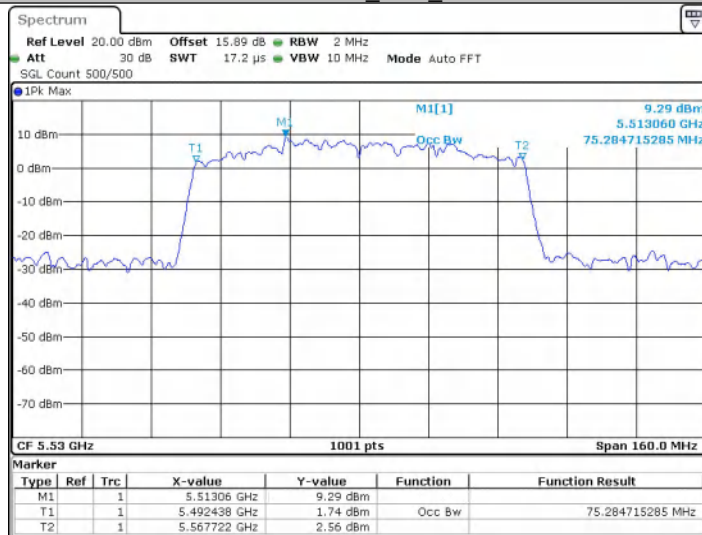
Date: 4 MAR 2026 13:44:39

11AC80SISO_Ant2_5290



Date: 4 MAR 2026 13:47:35

11AC80SISO_Ant2_5530



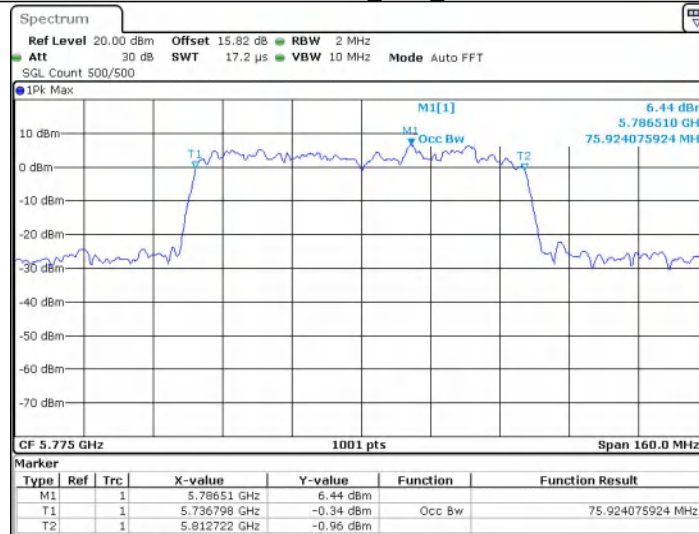
Date: 4 MAR 2026 13:49:41

11AC80SISO_Ant2_5610



Date: 4 MAR 2026 13:56:15

11AC80SISO_Ant2_5775



Date: 4 MAR 2026 14:00:33

Appendix B): Maximum Conduct Output Power

1.Duty Cycle (x)

Test Requirement KDB 789033 D02 II B 1

Test Method: KDB 789033 II B 1

Test Procedure:

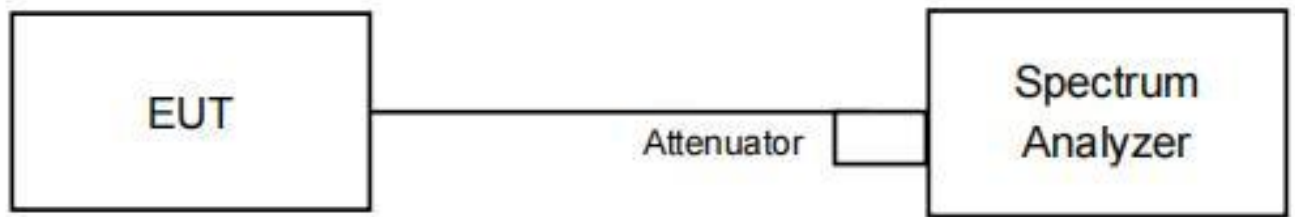
Set RBW = 20MHz

Set VBW = 40MHz

Set detector = peak.

Set span =0Hz

Test Setup Diagram

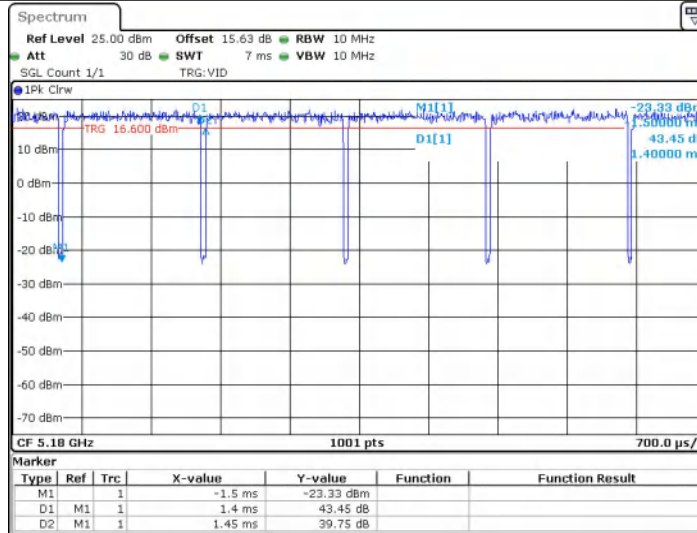


ANT1:

Measurement Data

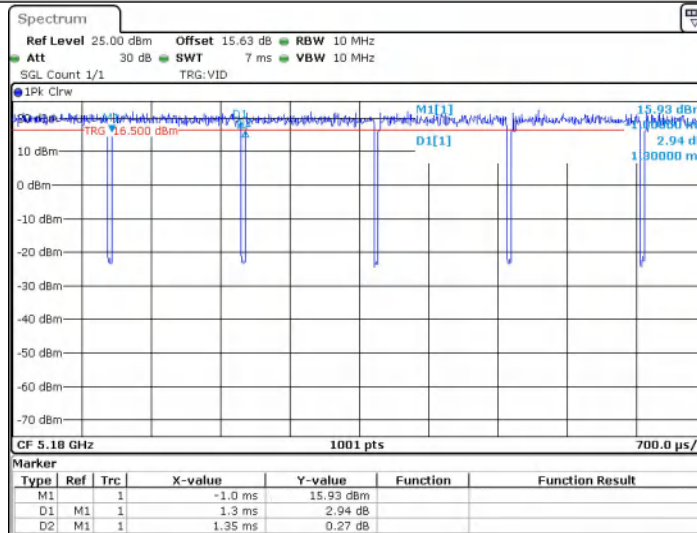
Test Mode	Antenna	Duty Cycle[%]	10log(1/x) Factor[dB]
11A	Ant1	96.55	0.15
11N20	Ant1	96.30	0.16
11N40	Ant1	94.20	0.26
11AC20	Ant1	97.04	0.13
11AC40	Ant1	94.29	0.26
11AC80	Ant1	89.19	0.50

11A



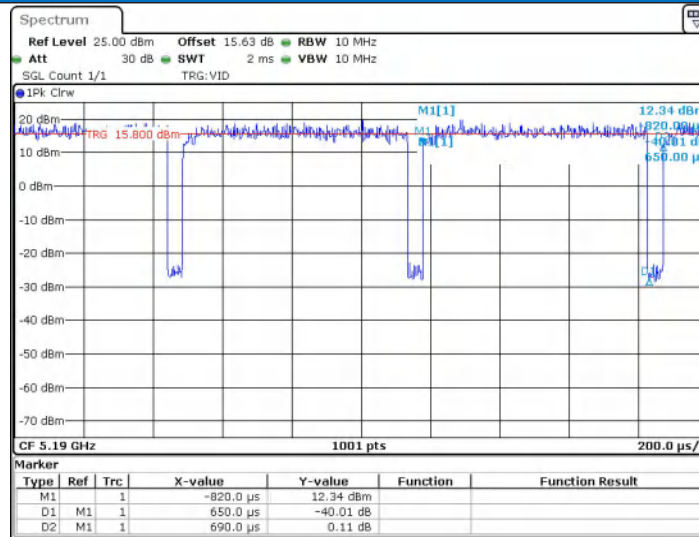
Date: 2 MAR 2026 13:44:51

11N20



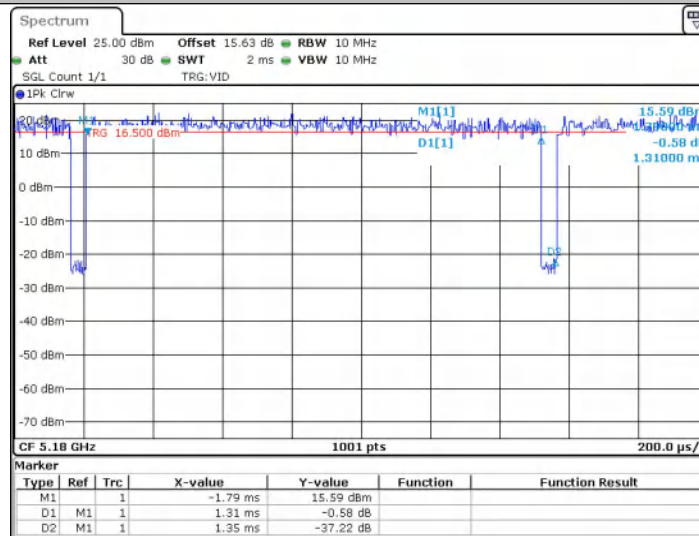
Date: 2 MAR 2026 14:56:45

11N40



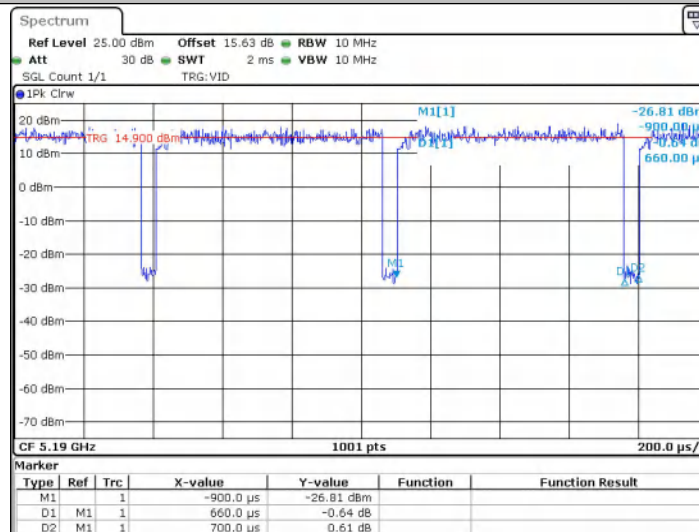
Date: 2 MAR 2026 15:27:44

11AC20



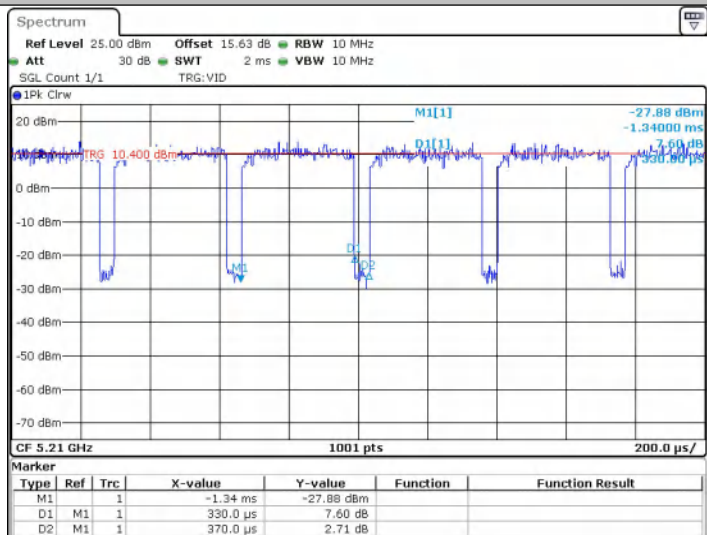
Date: 2 MAR 2026 15:52:07

11AC40



Date: 2 MAR 2026 16:37:37

11AC80



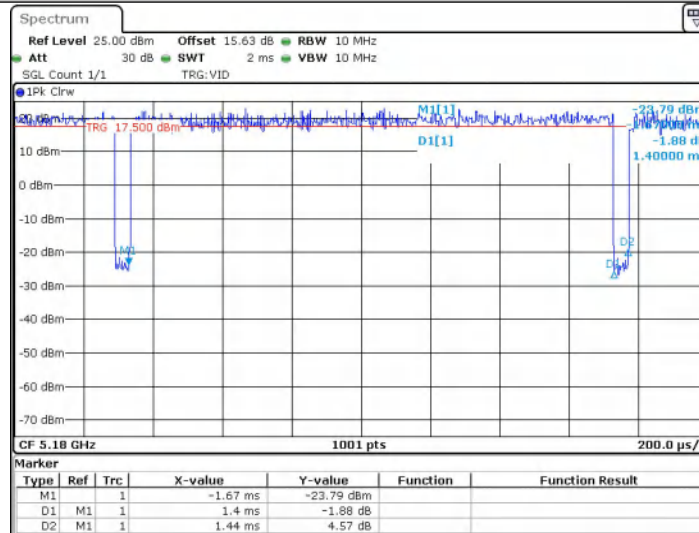
Date: 2 MAR 2026 17:06:12

ANT2:

Measurement Data

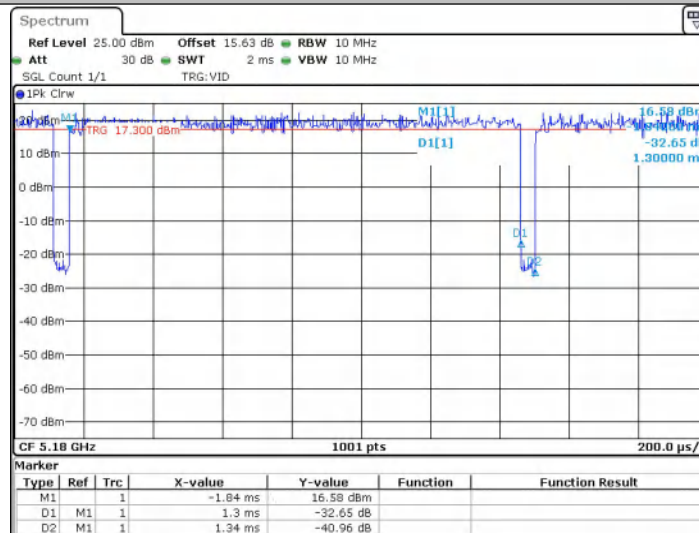
Test Mode	Antenna	Duty Cycle[%]	10log(1/x) Factor[dB]
11A	Ant1	97.22	0.12
11N20	Ant1	97.01	0.13
11N40	Ant1	92.75	0.33
11AC20	Ant1	96.32	0.16
11AC40	Ant1	92.86	0.32
11AC80	Ant1	86.49	0.63

11A



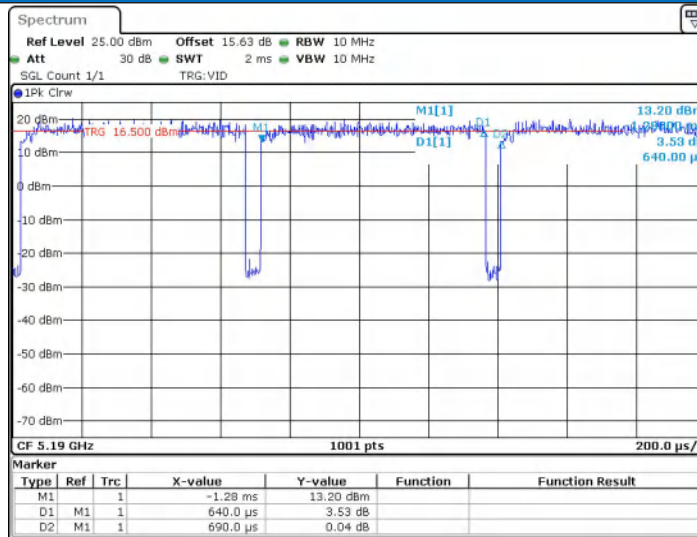
Date: 4 MAR 2026 09:56:38

11N20



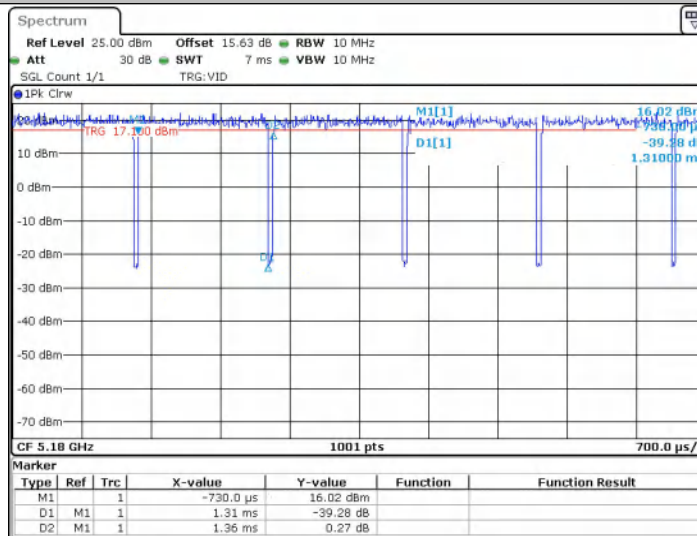
Date: 4 MAR 2026 10:37:45

11N40



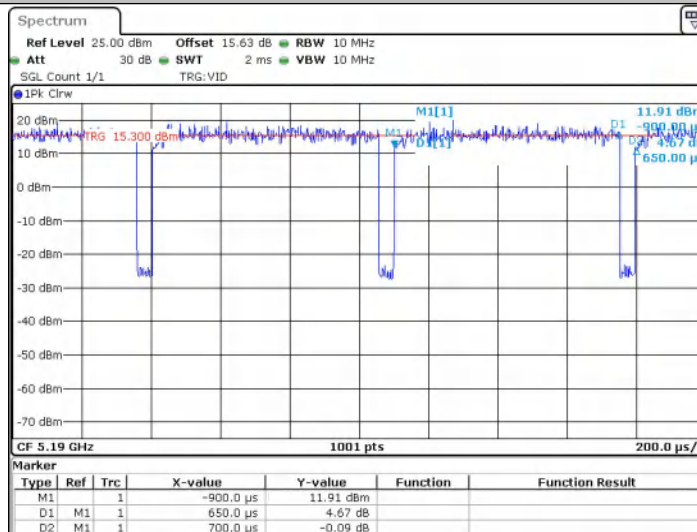
Date: 4 MAR 2026 11:03:00

11AC20



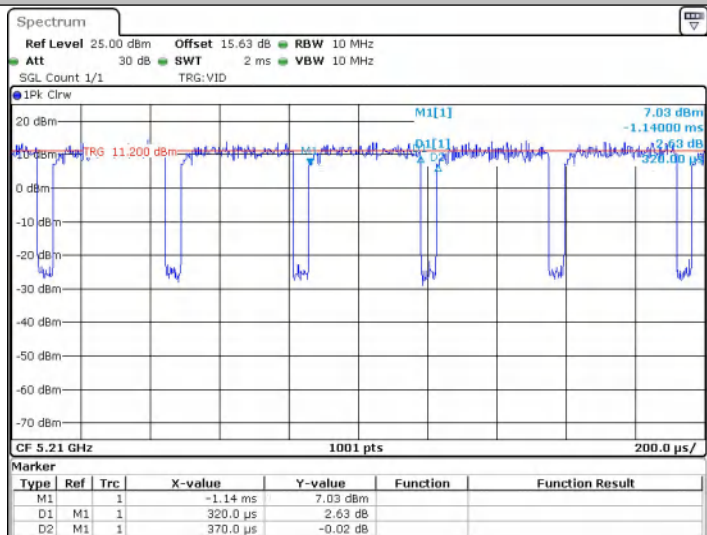
Date: 4 MAR 2026 11:26:28

11AC40



Date: 4 MAR 2026 11:55:13

11AC80



Date: 4 MAR 2026 13:44:51

2. Maximum Conducted Output Power

Test Requirement 47 CFR Part 15, Subpart C 15.407 (a)

Test Method: KDB 789033 D02 II E

Limit:

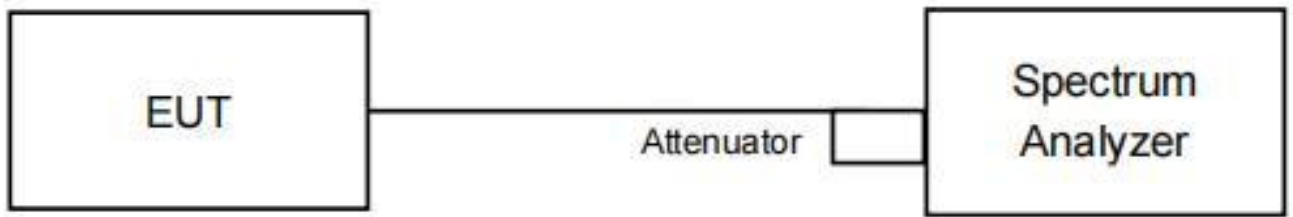
Frequency band(MHz)	Limit
5150-5250	$\leq 1\text{W}(30\text{dBm})$ for master device
	$\leq 250\text{mW}(24\text{dBm})$ for client device
5250-5350	$\leq 250\text{mW}(24\text{dBm})$ for client device or $11\text{dBm}+10\log B^*$
5470-5725	$\leq 250\text{mW}(24\text{dBm})$ for client device or $11\text{dBm}+10\log B^*$
5725-5850	$\leq 1\text{W}(30\text{dBm})$
Remark:	* Where B is the 26dB emission bandwidth in MHz. The maximum conducted output power must be measured over any interval of continuous transmission using instrumentation calibrated in terms of an rms-equivalent voltage.

Test Procedure:

Method SA-2 (trace averaging across on and off times of the EUT transmissions, followed by duty cycle correction).

- (1) Set RBW = 1 MHz.
- (2) Set VBW $\geq$ 3 MHz.
- (3) Detector = power average
- (4) Sweep time = auto.
- (5) Add duty cycle to the measured average power.

Test Setup Diagram



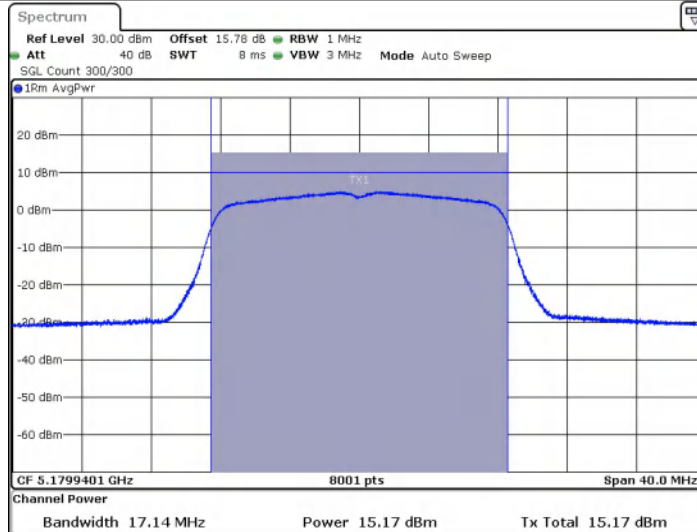
Ant1:

Measurement Data

Test Mode	Antenna	Freq(MHz)	Result [dBm]	Limit [dBm]	Verdict
11A	Ant1	5180	15.17	≤23.98	PASS
		5200	14.77	≤23.98	PASS
		5240	13.33	≤23.98	PASS
		5260	15.07	≤23.91	PASS
		5280	12.72	≤23.91	PASS
		5320	13.92	≤23.90	PASS
		5500	14.29	≤23.92	PASS
		5580	12.99	≤23.98	PASS
		5700	11.60	≤23.98	PASS
		5745	13.57	≤30.00	PASS
		5785	11.93	≤30.00	PASS
		5825	11.02	≤30.00	PASS
11N20SISO	Ant1	5180	14.93	≤23.98	PASS
		5200	14.48	≤23.98	PASS
		5240	13.02	≤23.98	PASS
		5260	14.80	≤23.95	PASS
		5280	12.52	≤23.98	PASS
		5320	13.69	≤23.93	PASS
		5500	14.05	≤23.98	PASS
		5580	12.84	≤23.98	PASS
		5700	11.53	≤23.98	PASS
		5745	13.43	≤30.00	PASS
		5785	11.73	≤30.00	PASS
		5825	10.94	≤30.00	PASS
11N40SISO	Ant1	5190	14.29	≤23.98	PASS
		5230	12.16	≤23.98	PASS
		5270	13.40	≤23.98	PASS
		5310	12.98	≤23.98	PASS
		5510	14.37	≤23.98	PASS
		5550	13.80	≤23.98	PASS
		5670	12.65	≤23.98	PASS
		5755	11.85	≤30.00	PASS
		5795	12.49	≤30.00	PASS

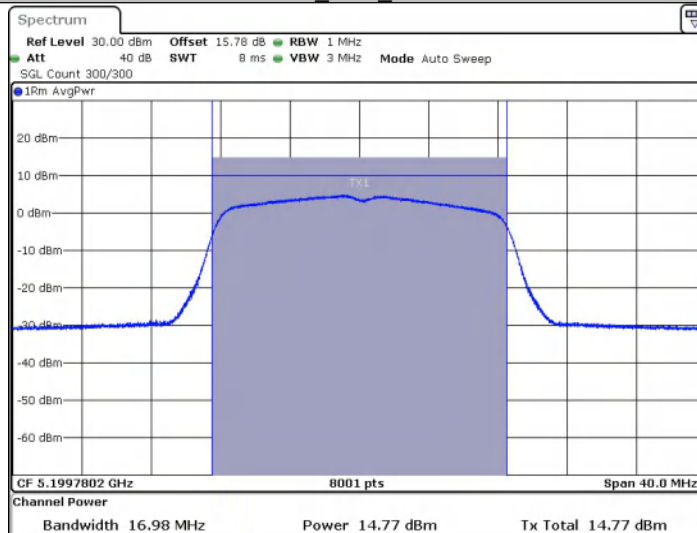
11AC20SIS O	Ant1	5180	14.55	≤ 23.98	PASS
		5200	14.08	≤ 23.98	PASS
		5240	12.66	≤ 23.98	PASS
		5260	14.30	≤ 23.98	PASS
		5280	12.06	≤ 23.98	PASS
		5320	13.50	≤ 23.98	PASS
		5500	13.69	≤ 23.98	PASS
		5580	12.46	≤ 23.98	PASS
		5700	10.88	≤ 23.98	PASS
		5745	13.02	≤ 30.00	PASS
		5785	11.38	≤ 30.00	PASS
		5825	10.57	≤ 30.00	PASS
	Ant1	5190	13.27	≤ 23.98	PASS
		5230	10.99	≤ 23.98	PASS
		5270	10.97	≤ 23.98	PASS
		5310	10.37	≤ 23.98	PASS
		5510	12.14	≤ 23.98	PASS
		5550	11.85	≤ 23.98	PASS
		5670	10.54	≤ 23.98	PASS
		5755	10.04	≤ 30.00	PASS
		5795	11.23	≤ 30.00	PASS
	Ant1	5210	12.17	≤ 23.98	PASS
		5290	12.16	≤ 23.98	PASS
		5530	13.09	≤ 23.98	PASS
		5610	11.78	≤ 23.98	PASS
		5775	10.76	≤ 30.00	PASS

11A_Ant1_5180



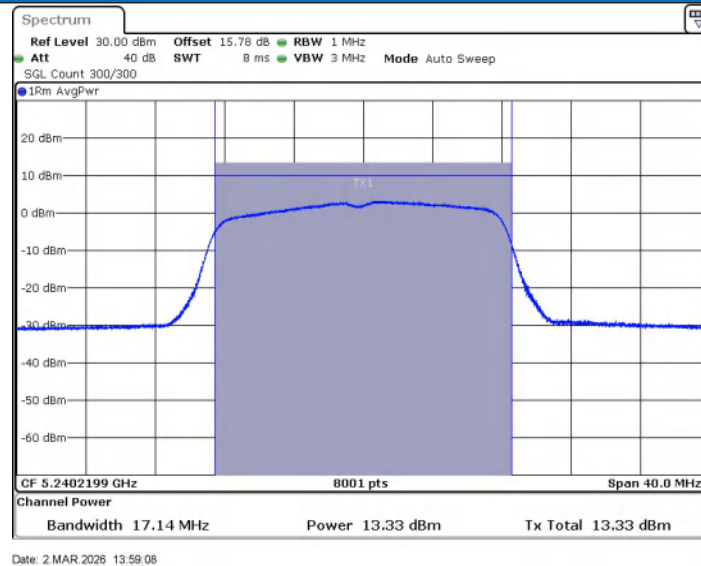
Date: 2.MAR.2026 13:45:01

11A_Ant1_5200

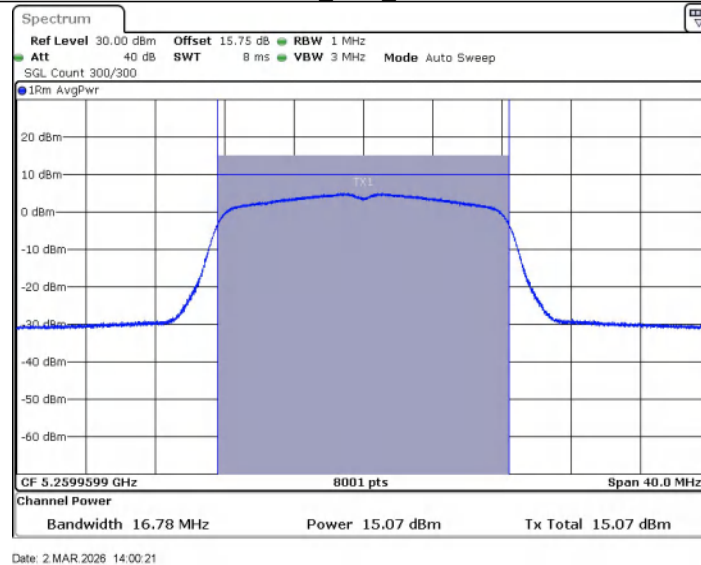


Date: 2.MAR.2026 13:52:10

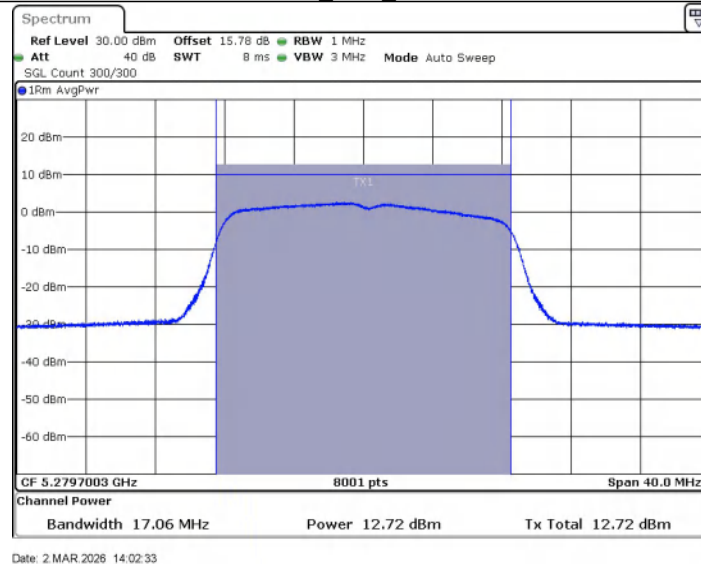
11A_Ant1_5240



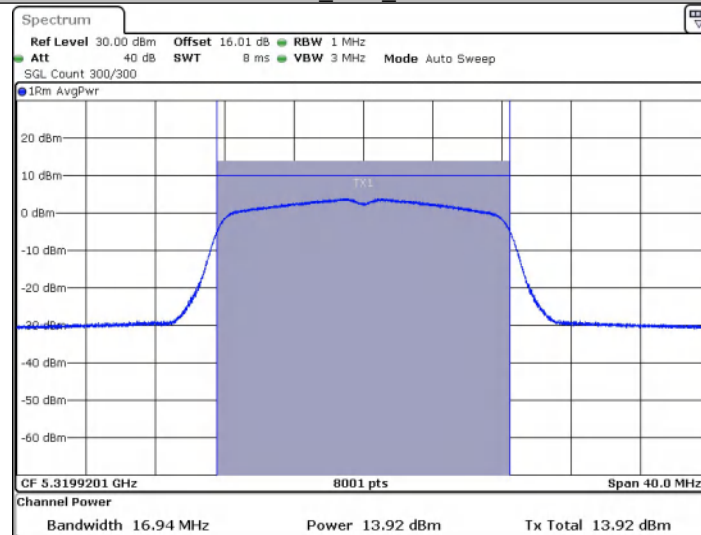
11A_Ant1_5260



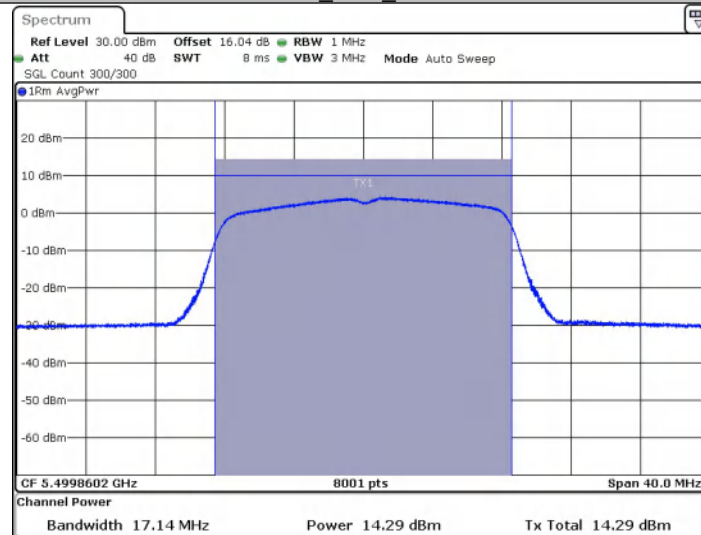
11A_Ant1_5280



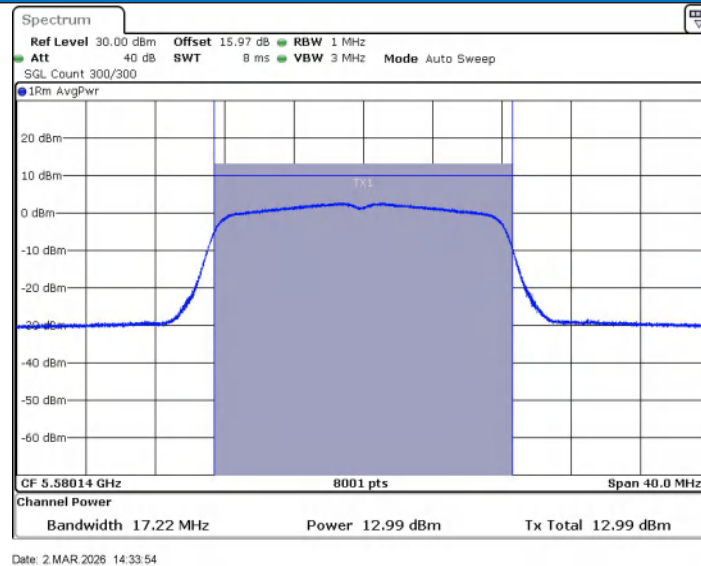
11A_Ant1_5320



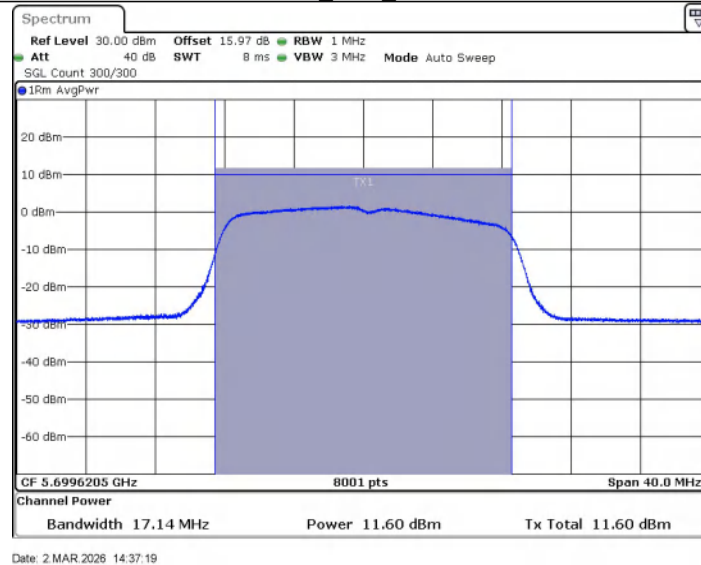
11A_Ant1_5500



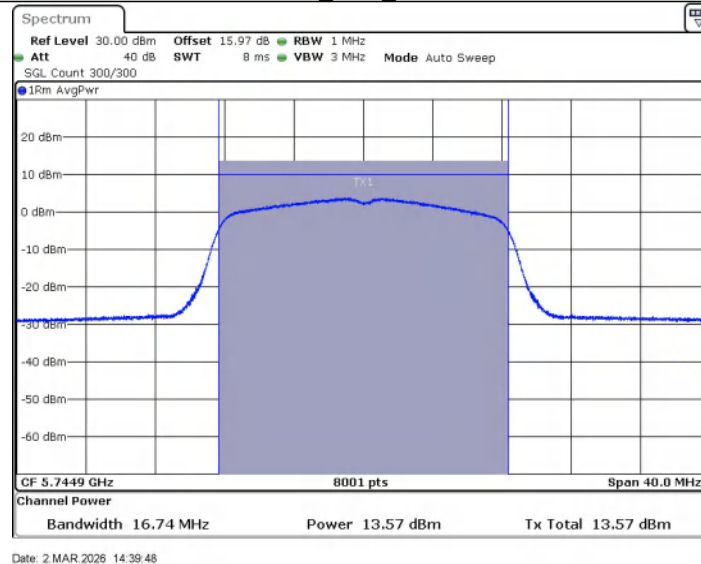
11A_Ant1_5580



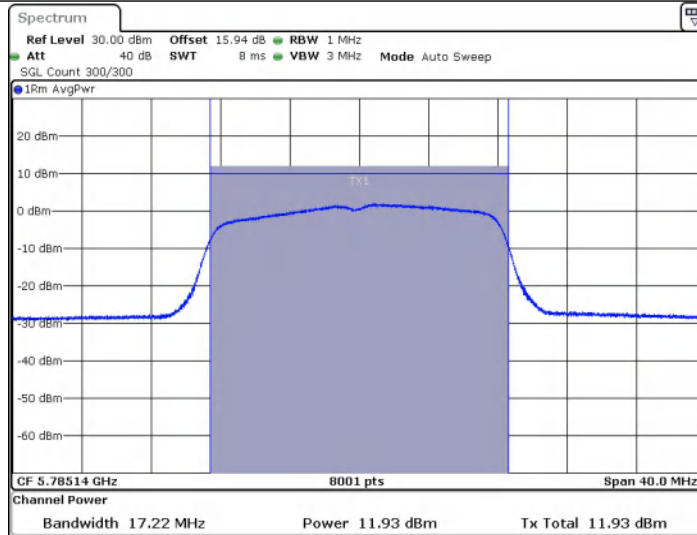
11A_Ant1_5700



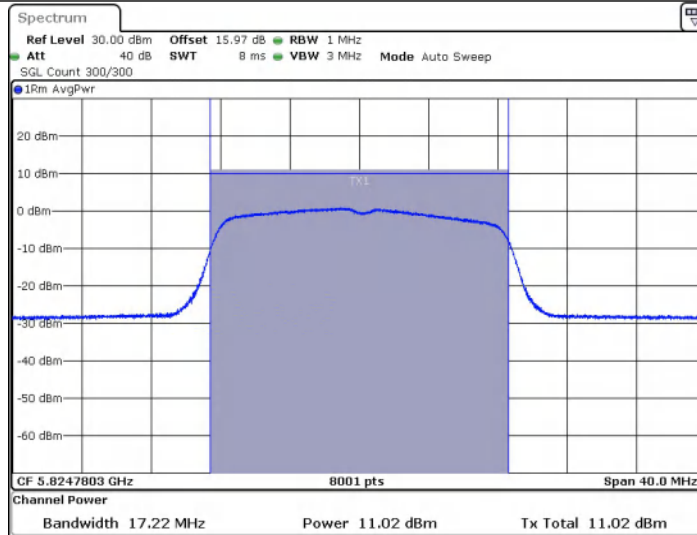
11A_Ant1_5745



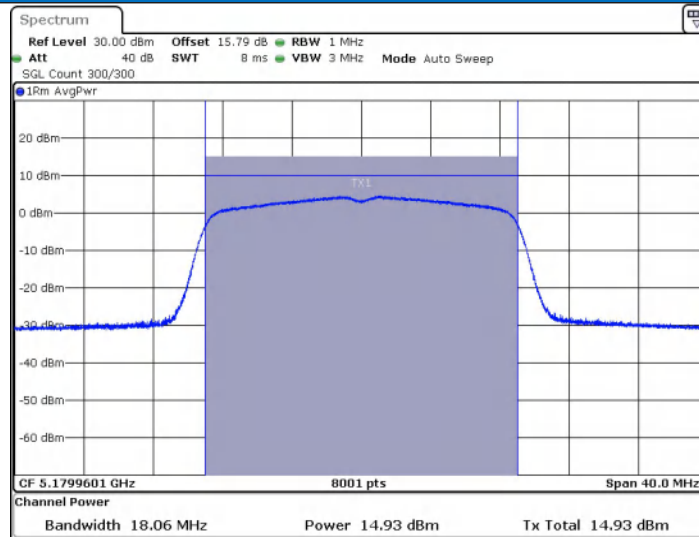
11A_Ant1_5785



11A_Ant1_5825

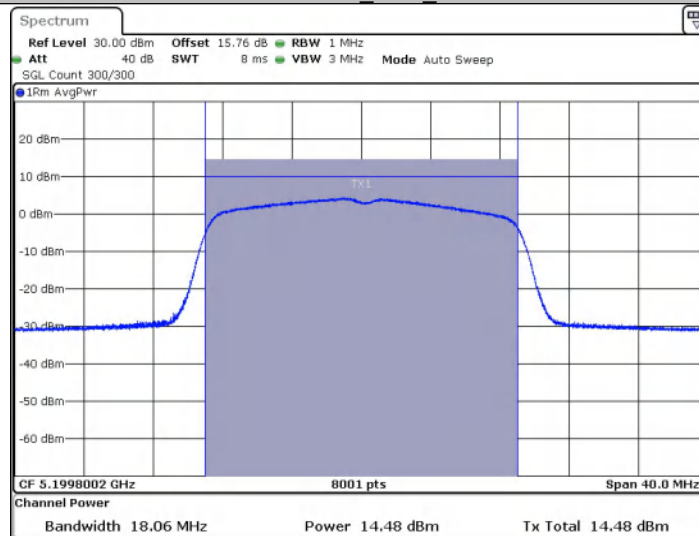


11N20SISO_Ant1_5180



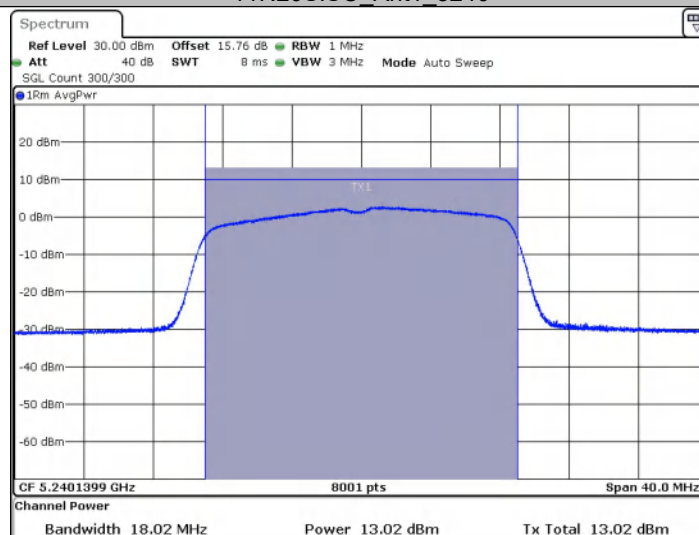
Date: 2.MAR.2026 14:56:54

11N20SISO_Ant1_5200



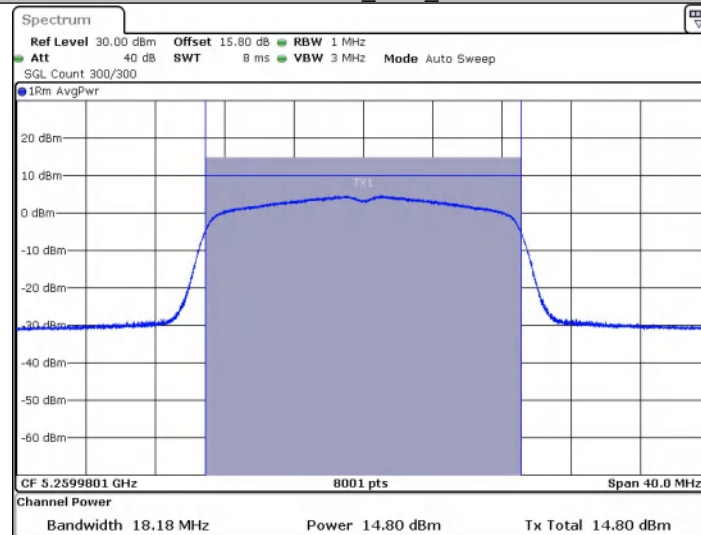
Date: 2.MAR.2026 15:10:26

11N20SISO_Ant1_5240

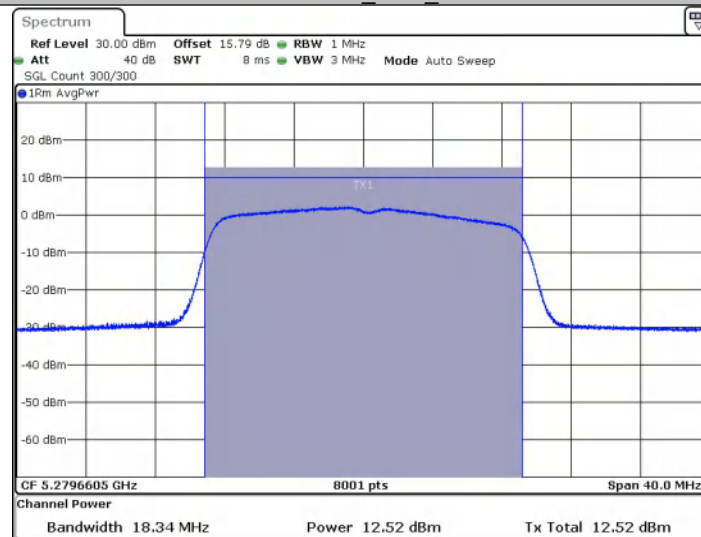


Date: 2.MAR.2026 15:15:02

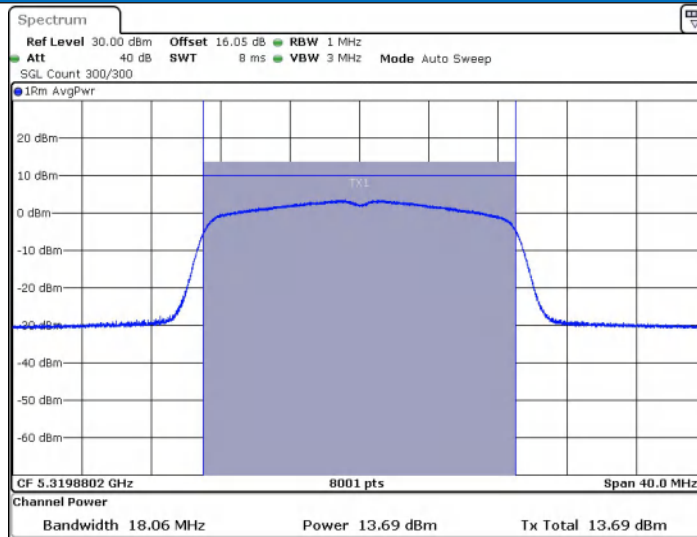
11N20SISO_Ant1_5260



11N20SISO_Ant1_5280

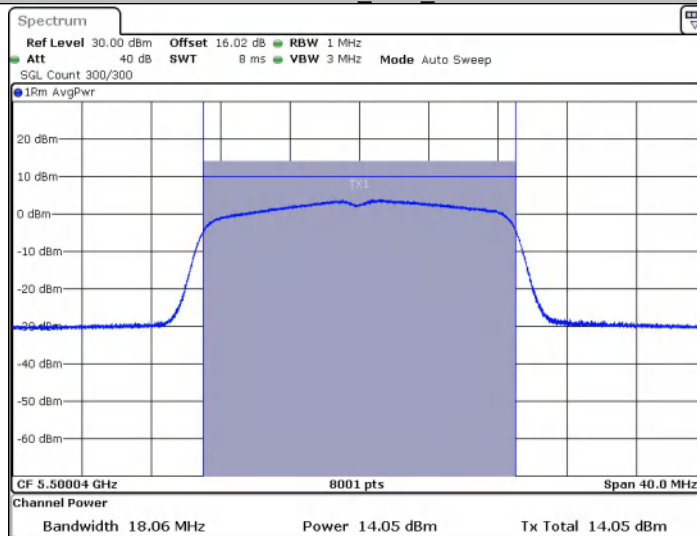


11N20SISO_Ant1_5320



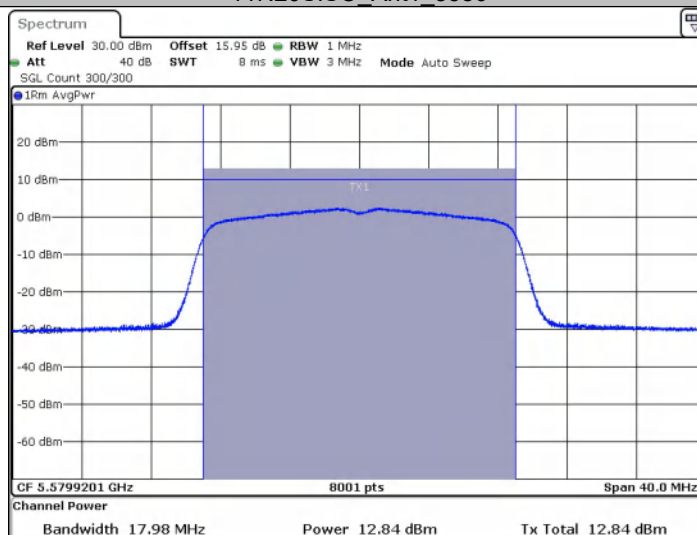
Date: 2.MAR.2026 15:18:04

11N20SISO_Ant1_5500



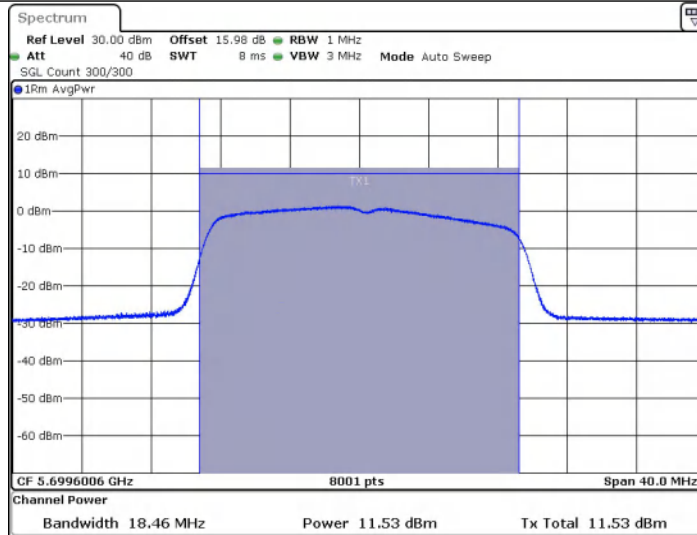
Date: 2.MAR.2026 15:19:32

11N20SISO_Ant1_5580



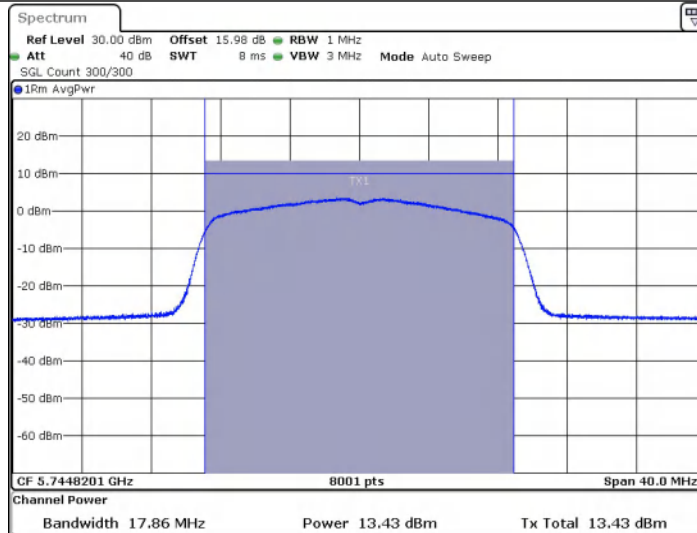
Date: 2.MAR.2026 15:21:06

11N20SISO_Ant1_5700



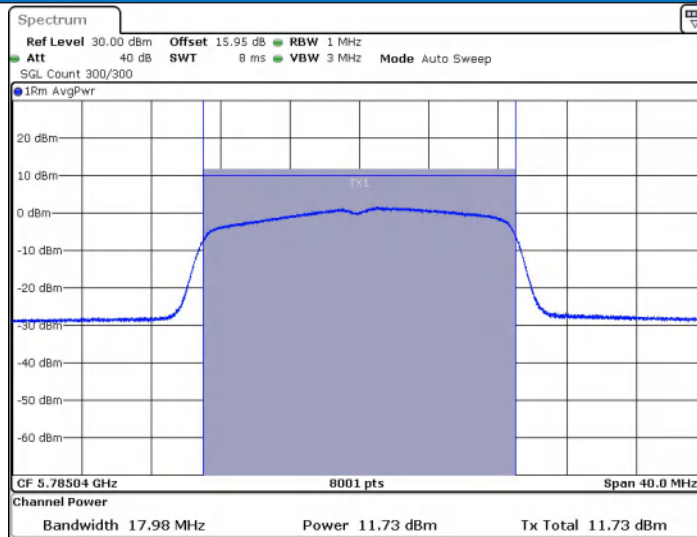
Date: 2.MAR.2026 15:22:12

11N20SISO_Ant1_5745



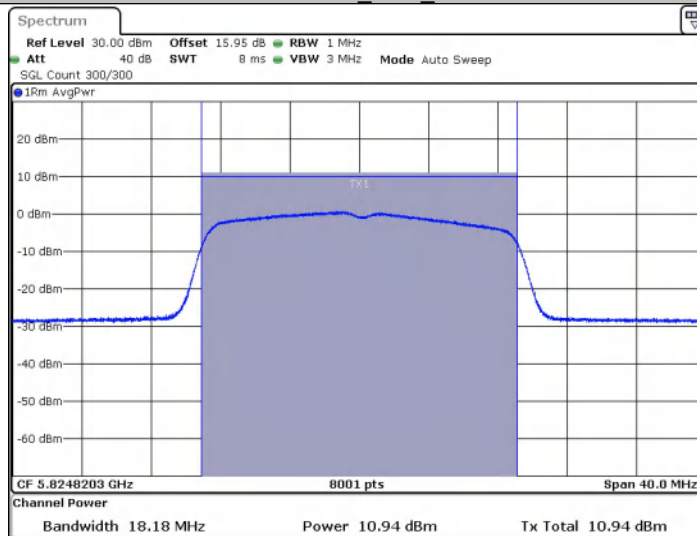
Date: 2.MAR.2026 15:23:46

11N20SISO_Ant1_5785



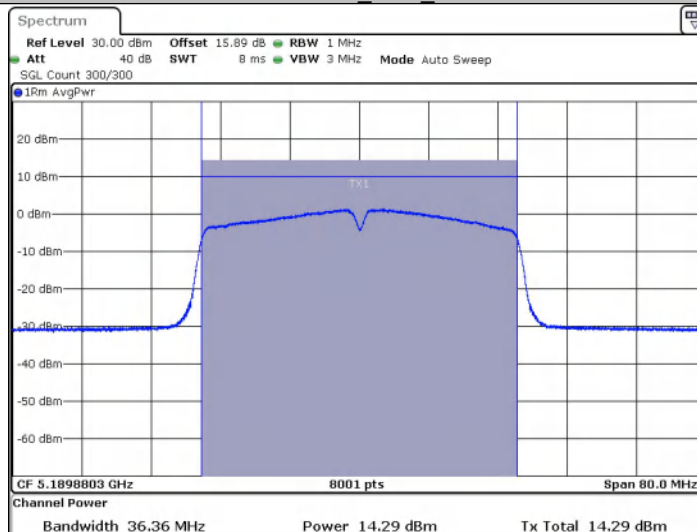
Date: 2.MAR.2026 15:25:16

11N20SISO_Ant1_5825



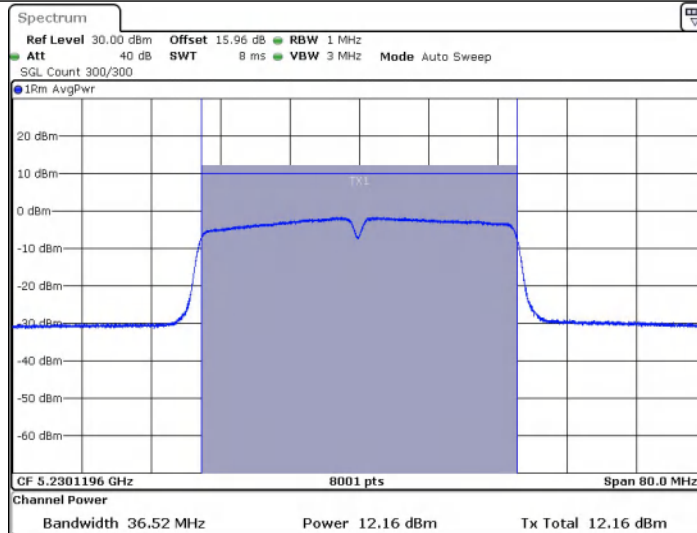
Date: 2.MAR.2026 15:26:22

11N40SISO_Ant1_5190



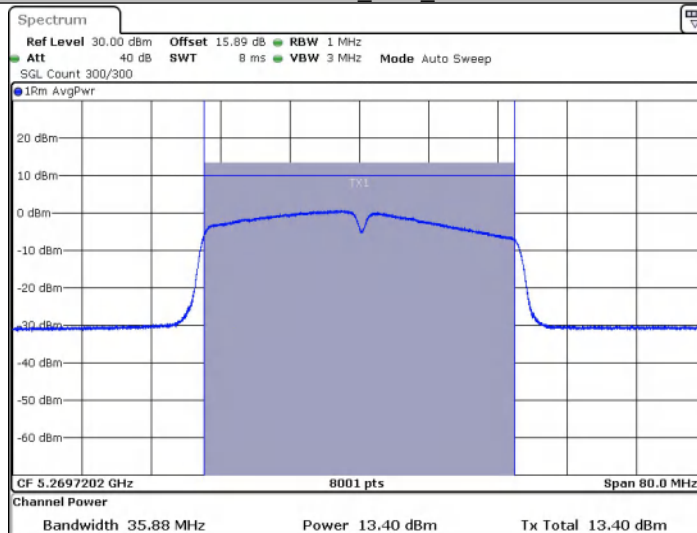
Date: 2.MAR.2026 15:27:53

11N40SISO_Ant1_5230



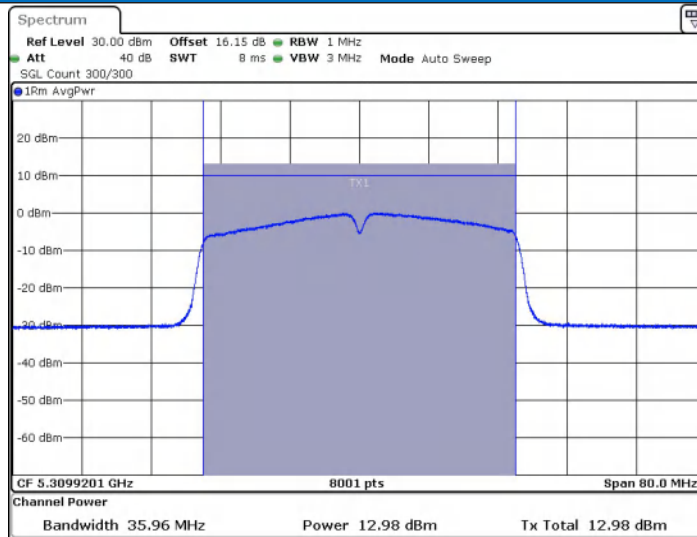
Date: 2 MAR 2026 15:29:48

11N40SISO_Ant1_5270



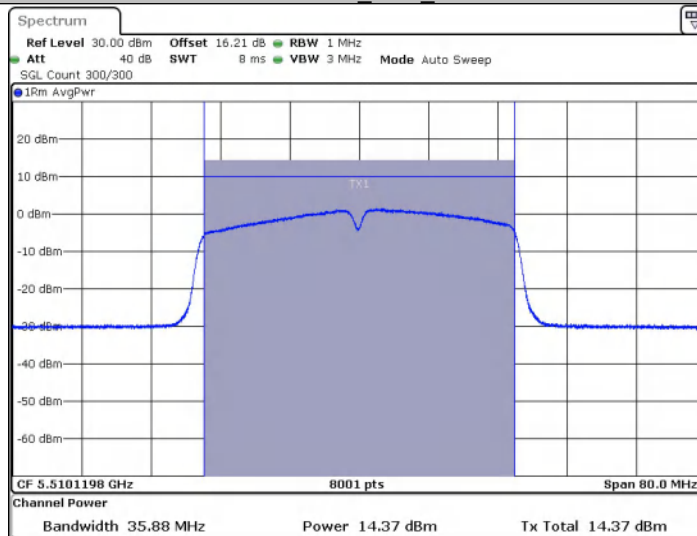
Date: 2 MAR 2026 15:32:23

11N40SISO_Ant1_5310



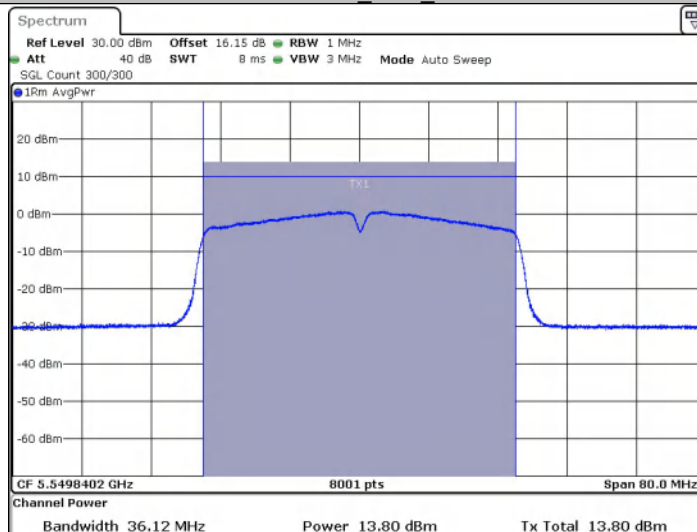
Date: 2.MAR.2026 15:33:54

11N40SISO_Ant1_5510



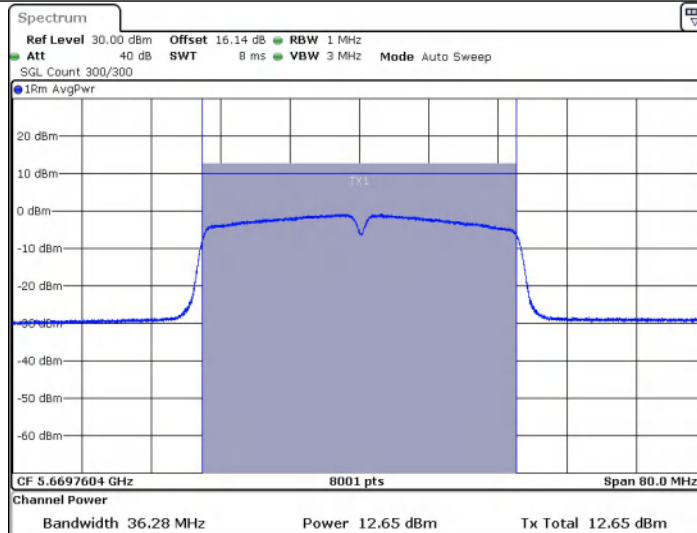
Date: 2.MAR.2026 15:36:01

11N40SISO_Ant1_5550

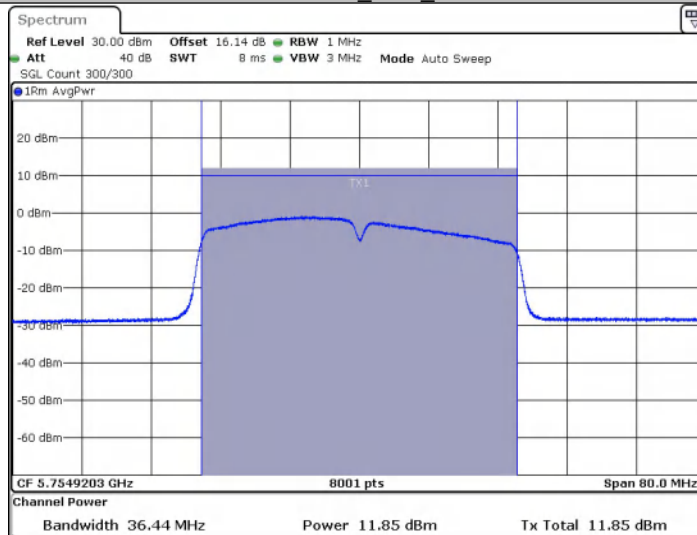


Date: 2.MAR.2026 15:38:17

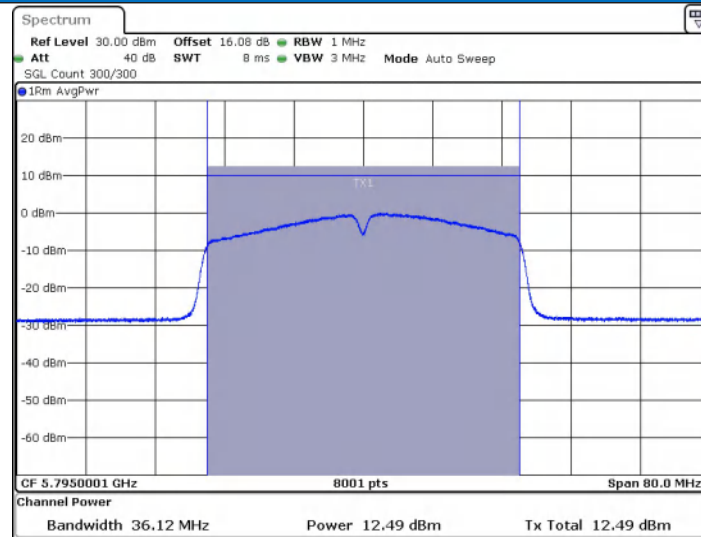
11N40SISO_Ant1_5670



11N40SISO_Ant1_5755

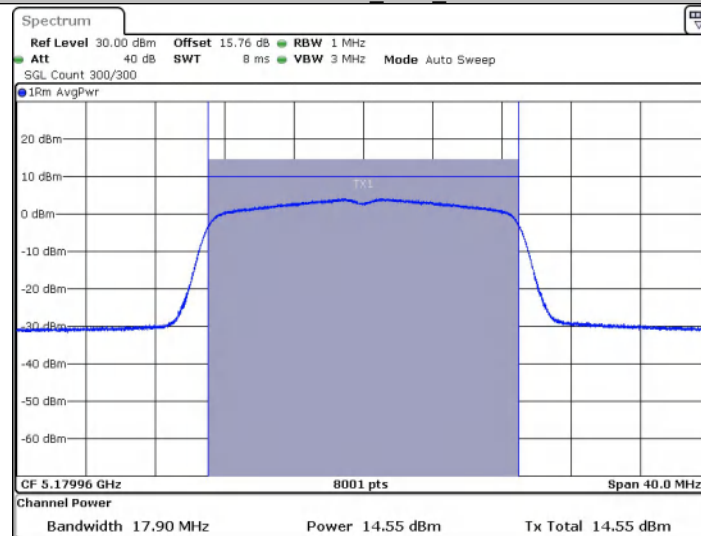


11N40SISO_Ant1_5795



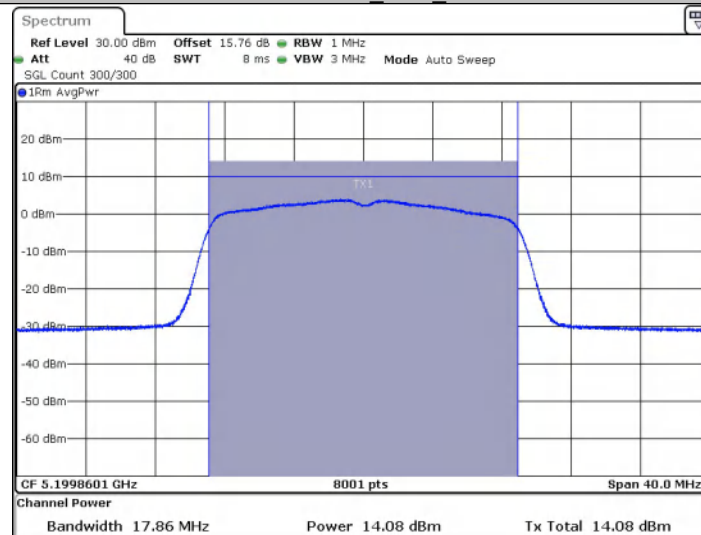
Date: 2.MAR.2026 15:49:04

11AC20SISO_Ant1_5180



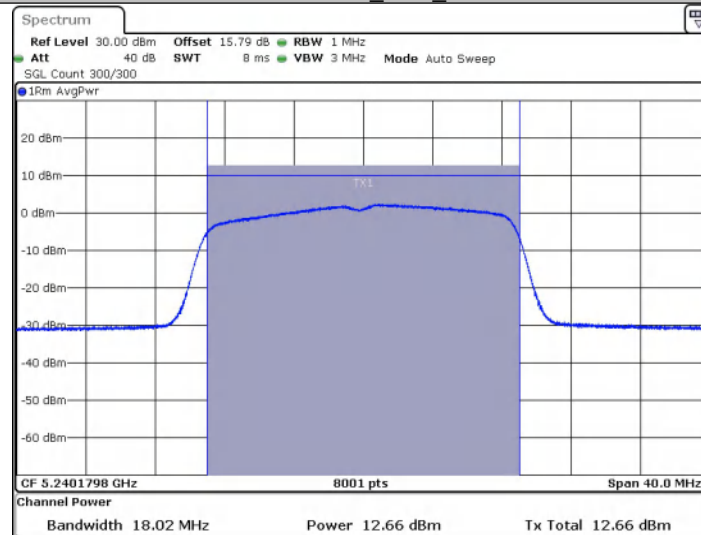
Date: 2.MAR.2026 15:52:16

11AC20SISO_Ant1_5200



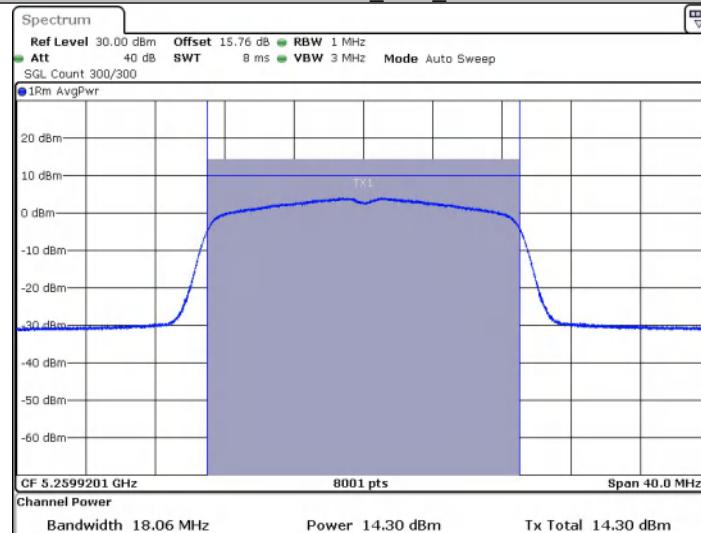
Date: 2.MAR.2026 15:55:08

11AC20SISO_Ant1_5240



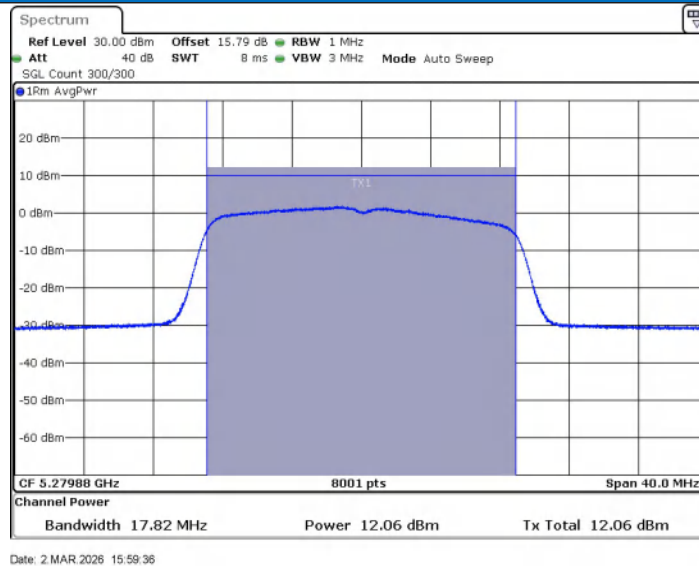
Date: 2 MAR 2026 15:56:09

11AC20SISO_Ant1_5260

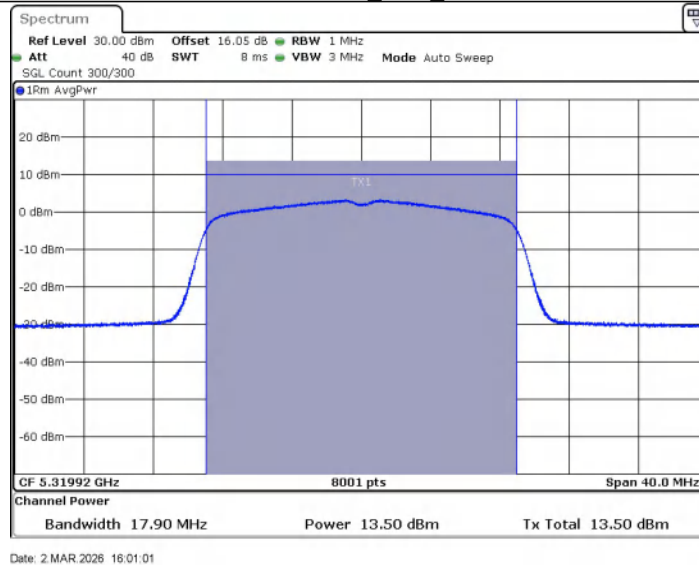


Date: 2 MAR 2026 15:58:13

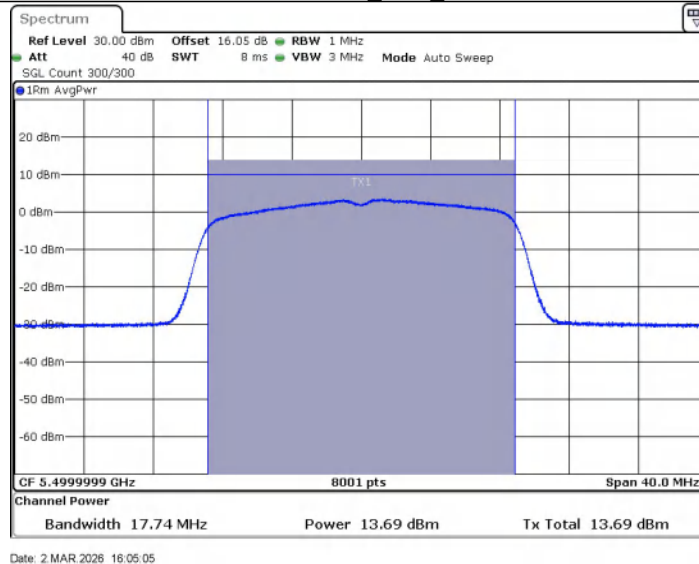
11AC20SISO_Ant1_5280



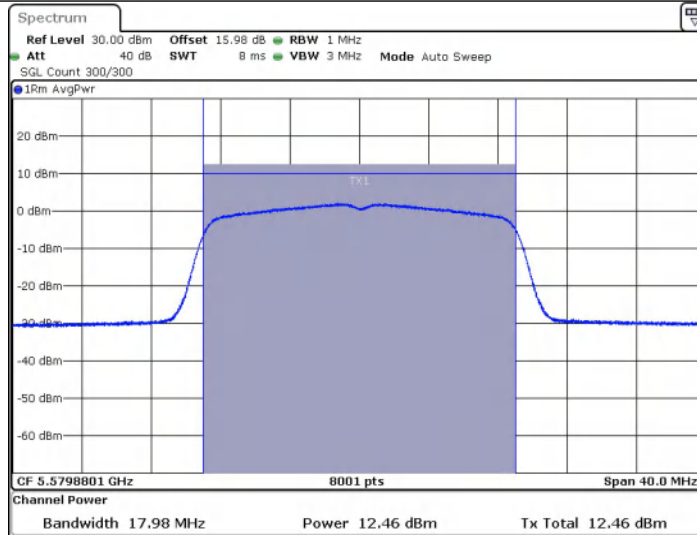
11AC20SISO_Ant1_5320



11AC20SISO_Ant1_5500

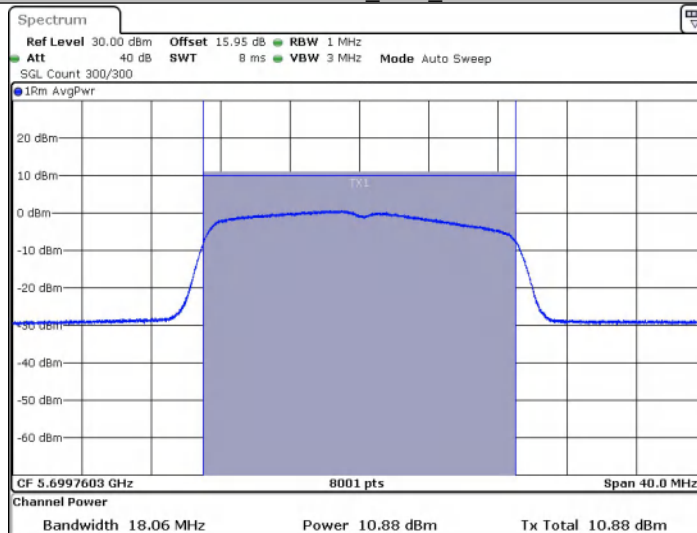


11AC20SISO_Ant1_5580



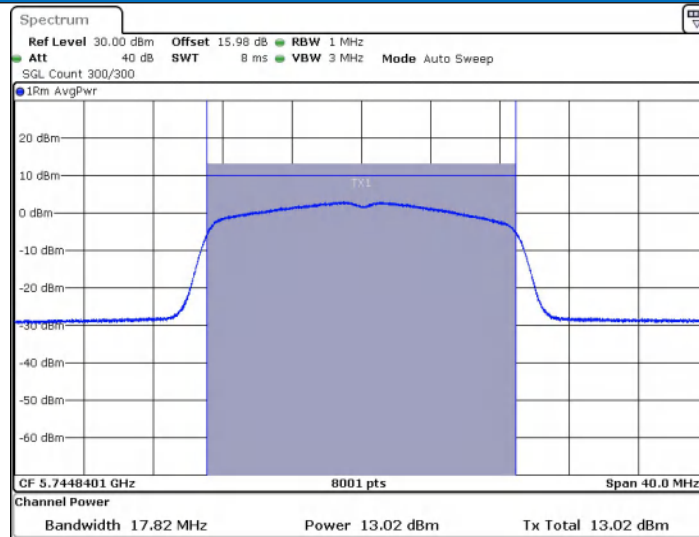
Date: 2.MAR.2026 16:08:18

11AC20SISO_Ant1_5700



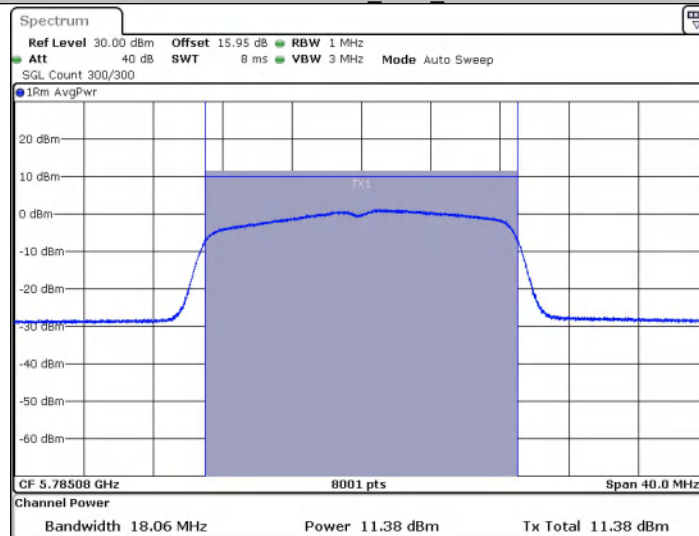
Date: 2.MAR.2026 16:30:08

11AC20SISO_Ant1_5745



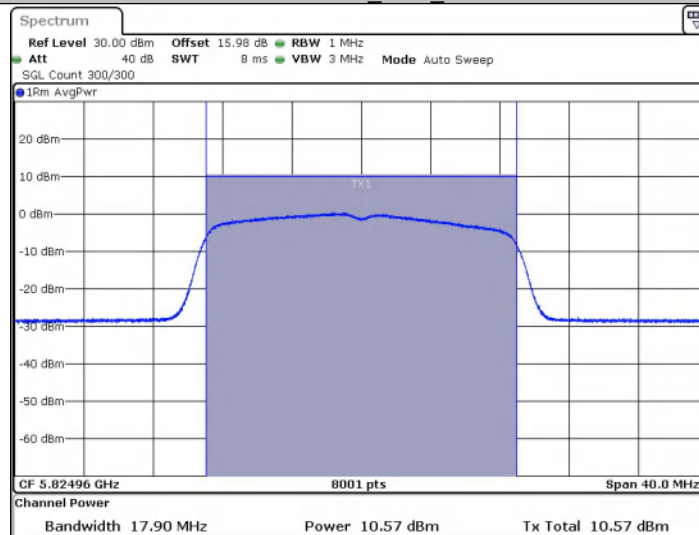
Date: 2.MAR.2026 16:32:23

11AC20SISO_Ant1_5785



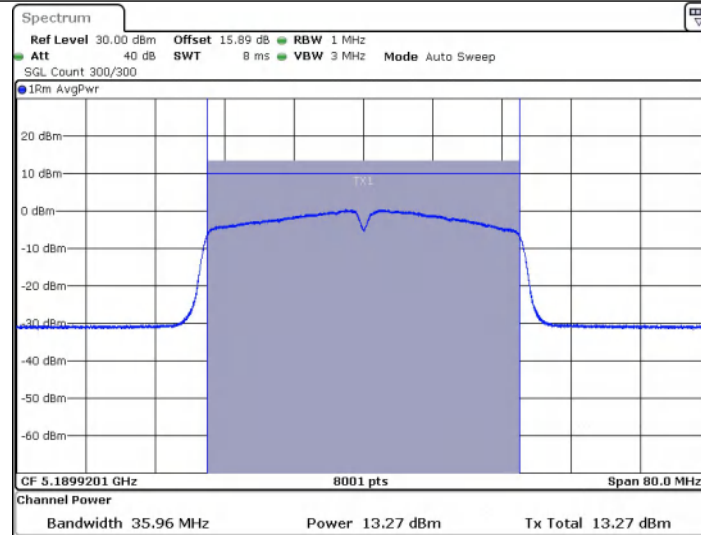
Date: 2.MAR.2026 16:34:51

11AC20SISO_Ant1_5825



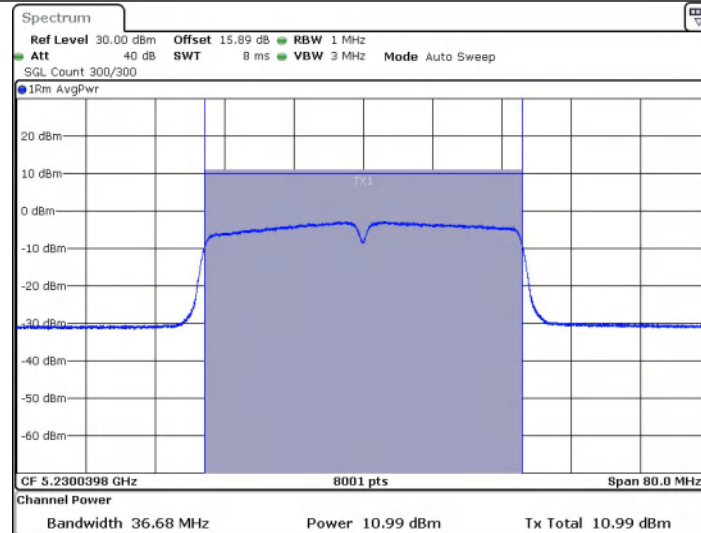
Date: 2.MAR.2026 16:36:10

11AC40SISO_Ant1_5190



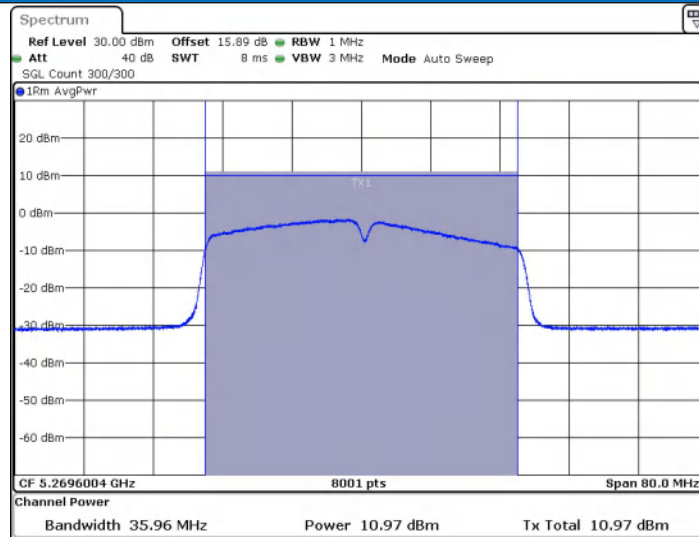
Date: 2 MAR 2026 16:37:46

11AC40SISO_Ant1_5230



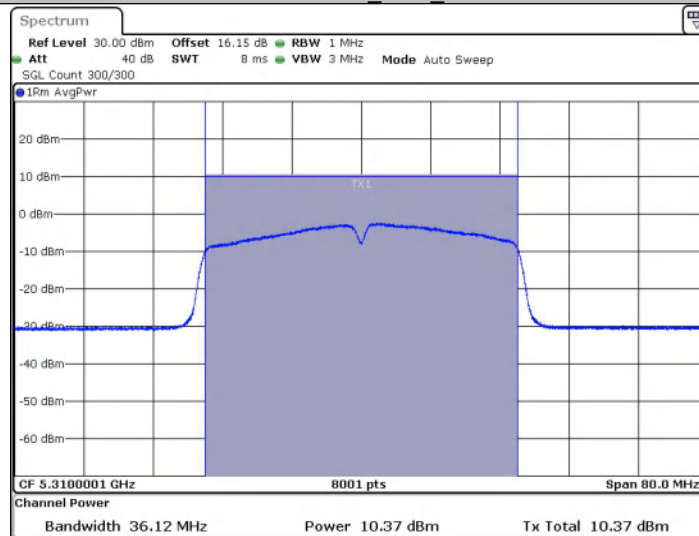
Date: 2 MAR 2026 16:42:31

11AC40SISO_Ant1_5270



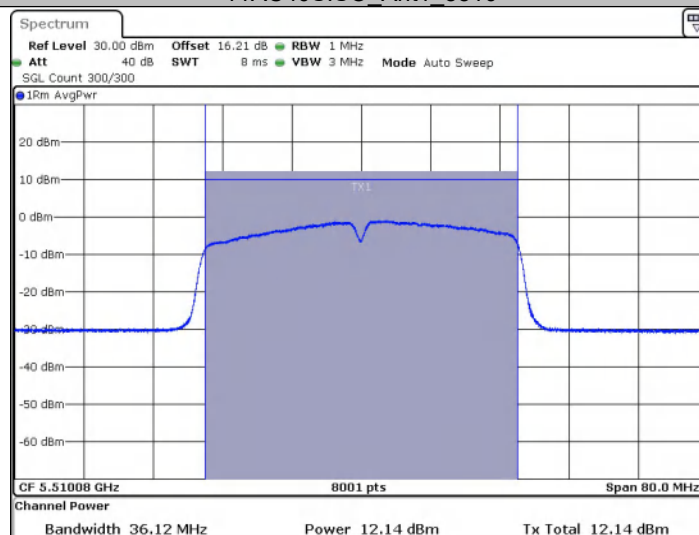
Date: 2.MAR.2026 16:47:01

11AC40SISO_Ant1_5310



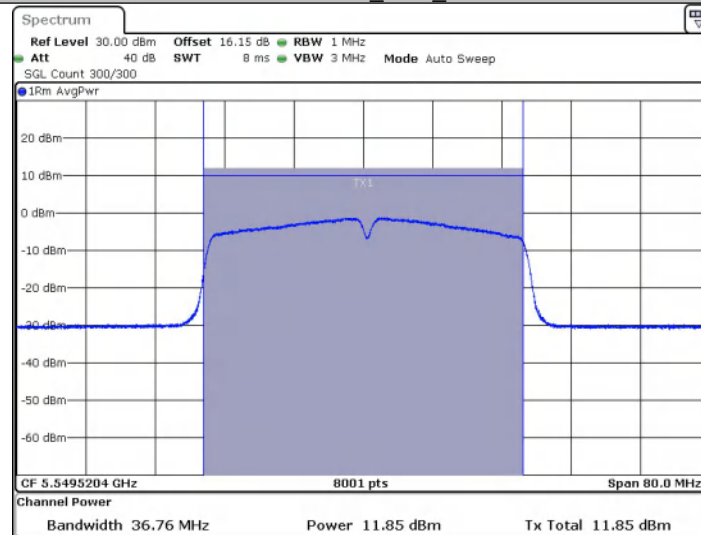
Date: 2.MAR.2026 16:48:10

11AC40SISO_Ant1_5510

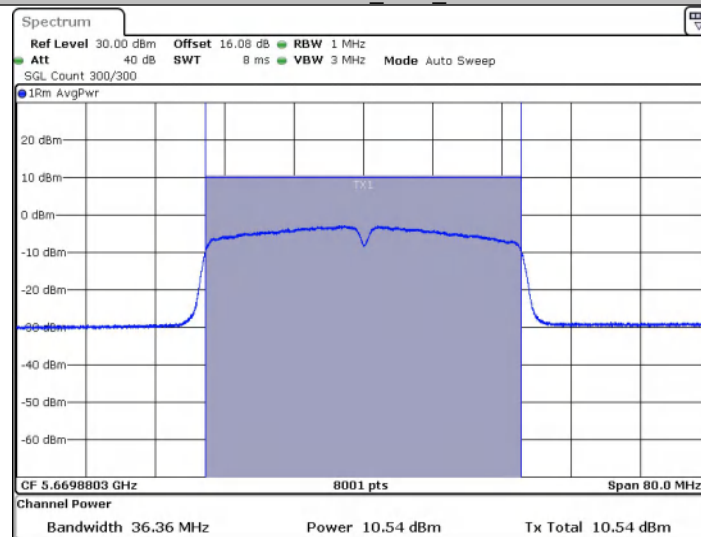


Date: 2.MAR.2026 16:49:40

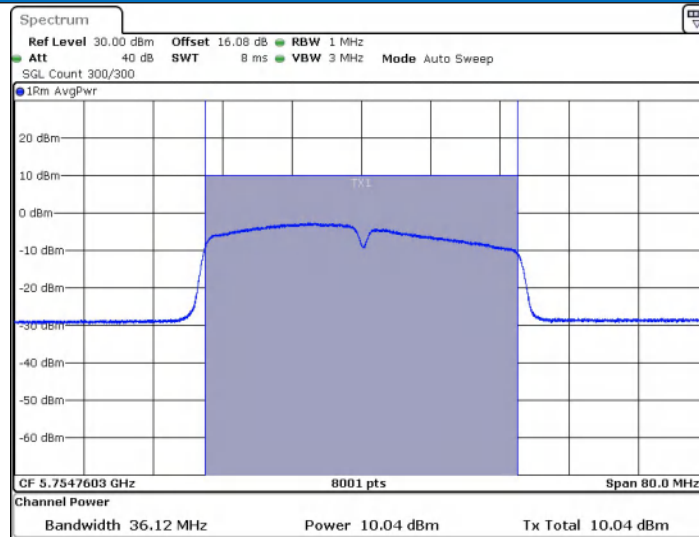
11AC40SISO_Ant1_5550



11AC40SISO_Ant1_5670

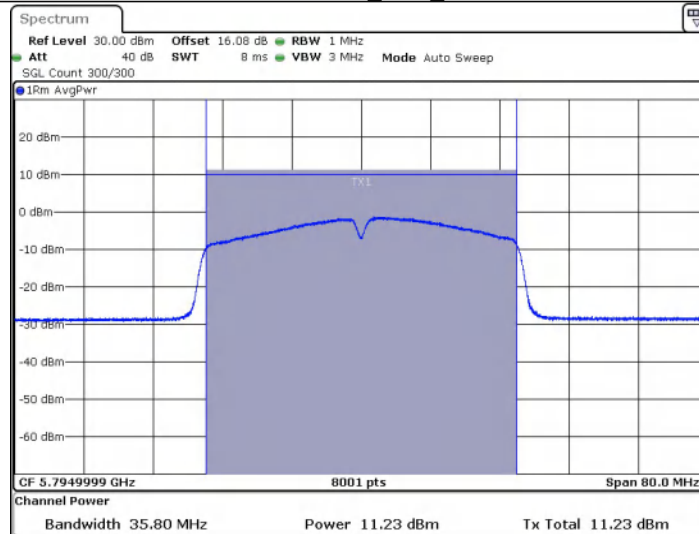


11AC40SISO_Ant1_5755



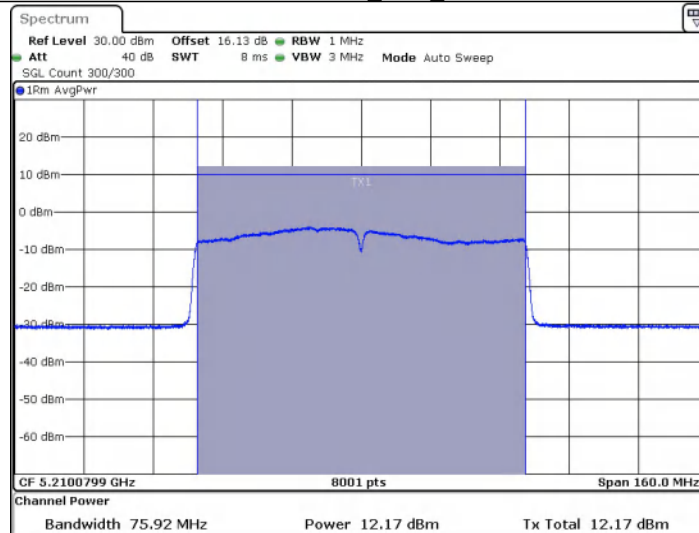
Date: 2 MAR 2026 16:58:37

11AC40SISO_Ant1_5795



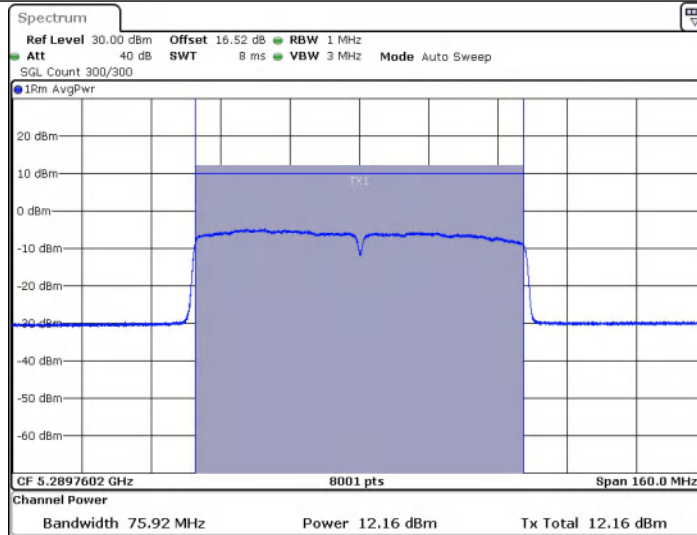
Date: 2 MAR 2026 17:04:25

11AC80SISO_Ant1_5210



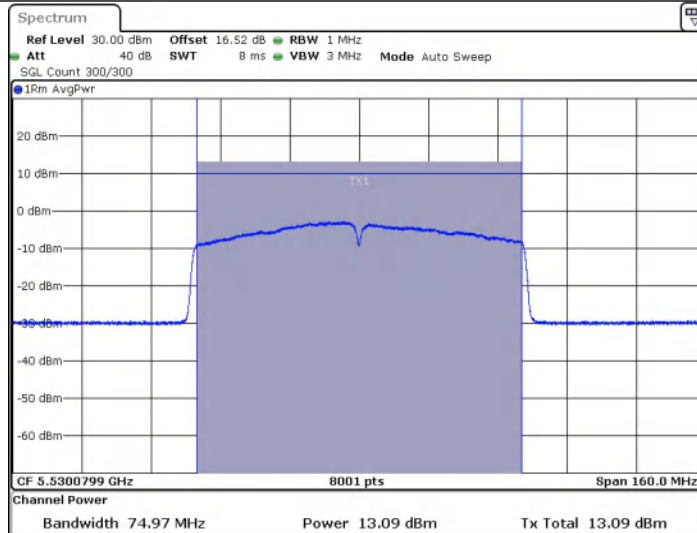
Date: 2 MAR 2026 17:06:22

11AC80SISO_Ant1_5290



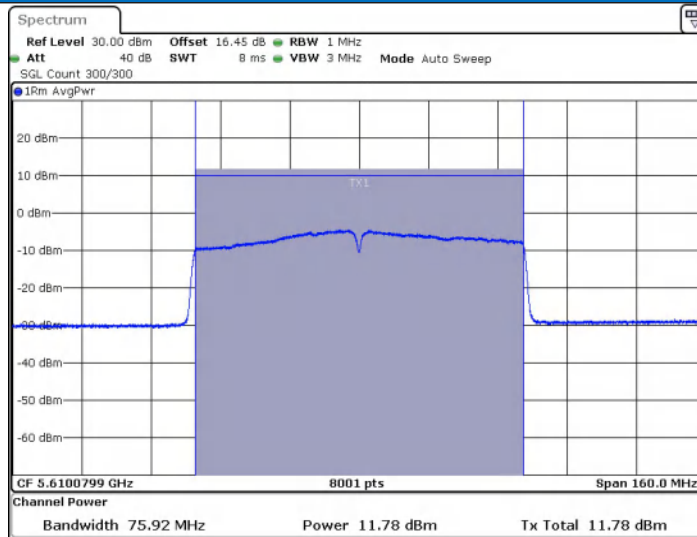
Date: 2.MAR.2026 17:15:08

11AC80SISO_Ant1_5530



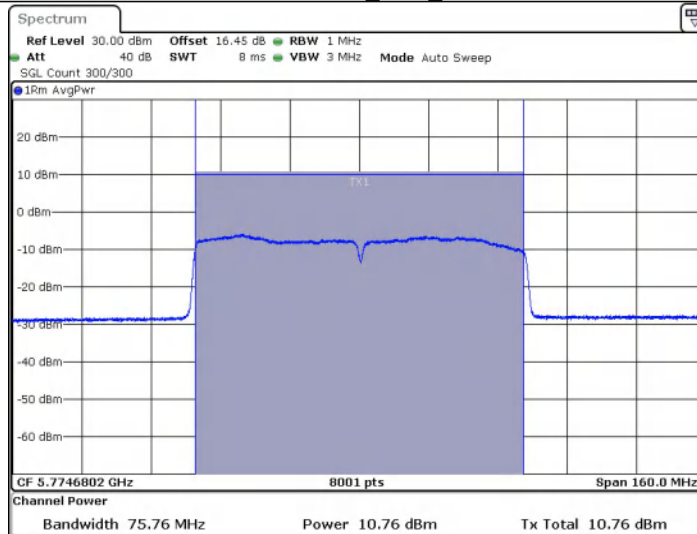
Date: 2.MAR.2026 17:17:41

11AC80SISO_Ant1_5610



Date: 2.MAR.2026 17:20:11

11AC80SISO_Ant1_5775



Date: 2.MAR.2026 17:22:19

Remark:

$Av.Power = Meas.Level + 10 \log(1/duty\ cycle)$

$E.i.r.p = Av.Power + G$,

G = antenna gain in dBi.

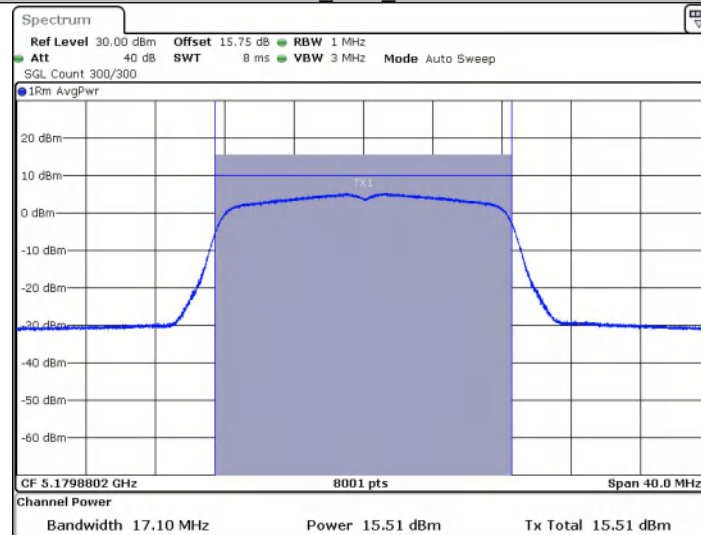
Ant2:

Measurement Data

Test Mode	Antenna	Freq(MHz)	Result [dBm]	Limit [dBm]	Verdict
11A	Ant2	5180	15.51	≤23.98	PASS
		5200	15.46	≤23.98	PASS
		5240	13.88	≤23.98	PASS
		5260	15.67	≤23.90	PASS
		5280	13.20	≤23.98	PASS
		5320	14.47	≤23.83	PASS
		5500	14.76	≤23.81	PASS
		5580	13.46	≤23.88	PASS
		5700	12.22	≤23.98	PASS
		5745	14.19	≤30.00	PASS
		5785	12.55	≤30.00	PASS
		5825	11.60	≤30.00	PASS
11N20SISO	Ant2	5180	15.49	≤23.98	PASS
		5200	15.16	≤23.98	PASS
		5240	13.61	≤23.98	PASS
		5260	15.41	≤23.98	PASS
		5280	13.20	≤23.98	PASS
		5320	14.29	≤23.98	PASS
		5500	14.79	≤23.98	PASS
		5580	13.26	≤23.98	PASS
		5700	12.10	≤23.98	PASS
		5745	14.10	≤30.00	PASS
		5785	12.40	≤30.00	PASS
		5825	11.48	≤30.00	PASS
11N40SISO	Ant2	5190	15.16	≤23.98	PASS
		5230	12.61	≤23.98	PASS
		5270	14.13	≤23.98	PASS
		5310	13.36	≤23.98	PASS
		5510	14.71	≤23.98	PASS
		5550	14.23	≤23.98	PASS
		5670	12.99	≤23.98	PASS
		5755	12.21	≤30.00	PASS
		5795	12.79	≤30.00	PASS

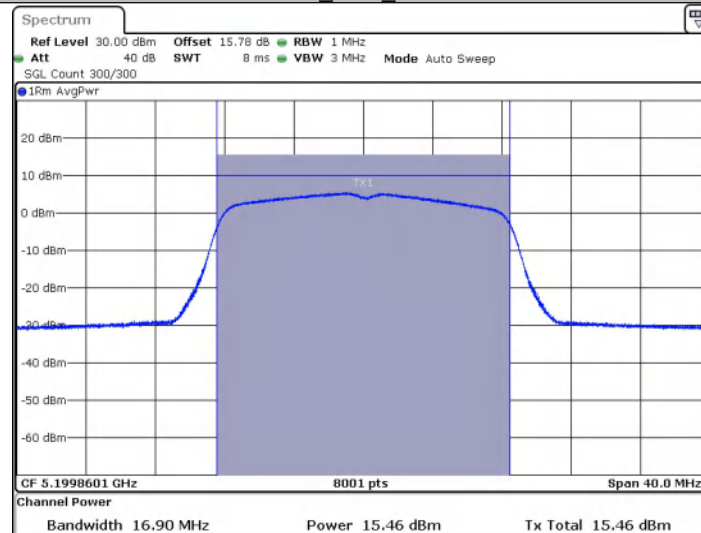
11AC20SIS O	Ant2	5180	14.94	≤ 23.98	PASS
		5200	14.81	≤ 23.98	PASS
		5240	13.22	≤ 23.98	PASS
		5260	14.88	≤ 23.98	PASS
		5280	12.61	≤ 23.98	PASS
		5320	13.74	≤ 23.94	PASS
		5500	14.03	≤ 23.95	PASS
		5580	12.70	≤ 23.98	PASS
		5700	11.58	≤ 23.98	PASS
		5745	13.52	≤ 30.00	PASS
		5785	11.85	≤ 30.00	PASS
		5825	11.03	≤ 30.00	PASS
	Ant2	5190	13.88	≤ 23.98	PASS
		5230	11.62	≤ 23.98	PASS
		5270	12.98	≤ 23.98	PASS
		5310	12.37	≤ 23.98	PASS
		5510	13.72	≤ 23.98	PASS
		5550	13.43	≤ 23.98	PASS
		5670	12.16	≤ 23.98	PASS
		5755	11.27	≤ 30.00	PASS
		5795	11.82	≤ 30.00	PASS
	Ant2	5210	12.99	≤ 23.98	PASS
		5290	12.53	≤ 23.98	PASS
		5530	13.73	≤ 23.98	PASS
		5610	12.52	≤ 23.98	PASS
		5775	11.23	≤ 30.00	PASS

11A_Ant2_5180



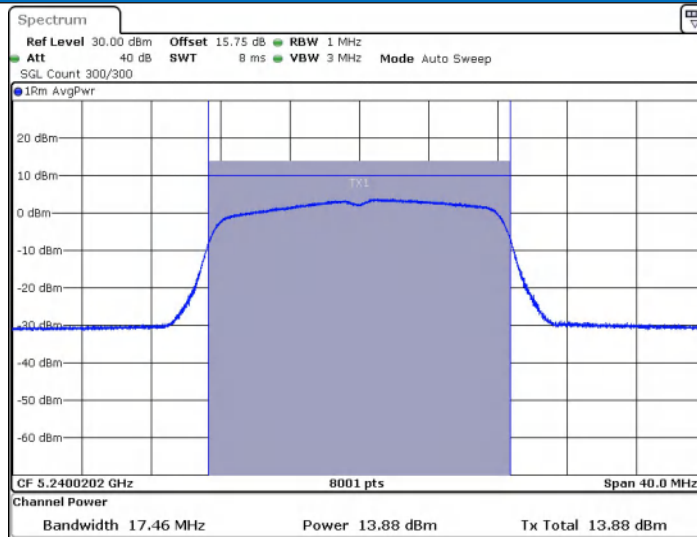
Date: 4 MAR 2026 09:56:48

11A_Ant2_5200



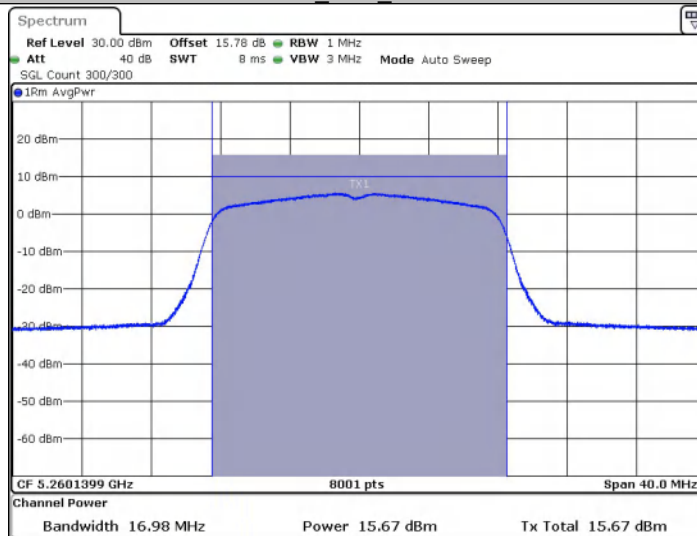
Date: 4 MAR 2026 09:59:29

11A_Ant2_5240



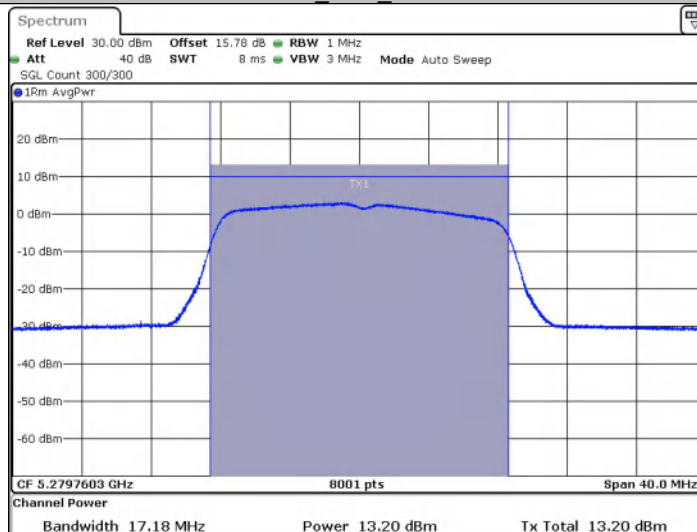
Date: 4 MAR 2026 10:01:28

11A_Ant2_5260



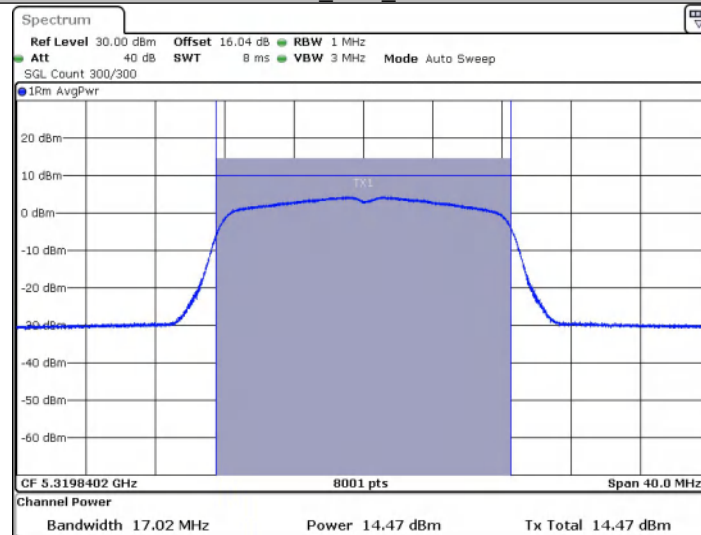
Date: 4 MAR 2026 10:05:05

11A_Ant2_5280



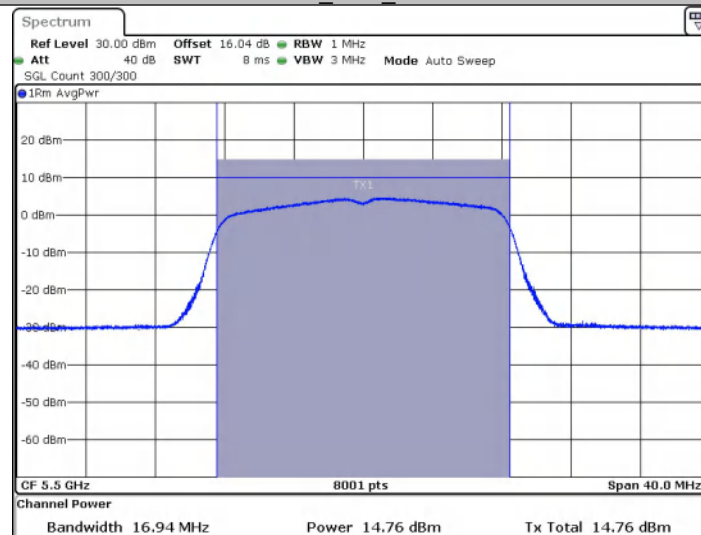
Date: 4 MAR 2026 10:06:11

11A_Ant2_5320



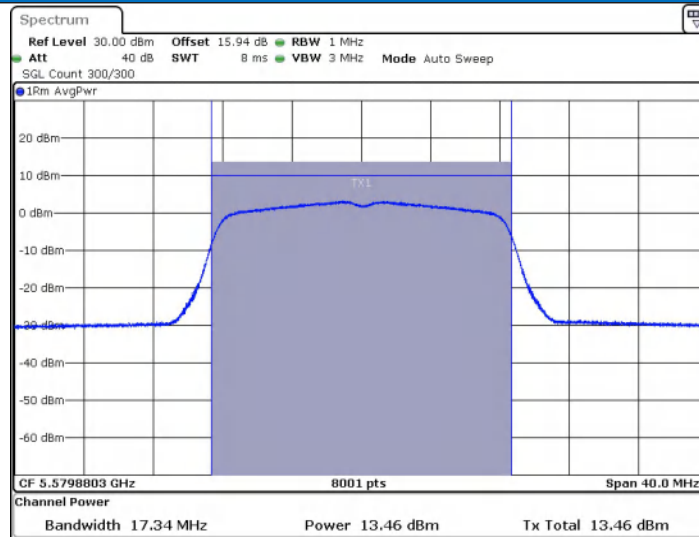
Date: 4.MAR.2026 10:09:17

11A_Ant2_5500



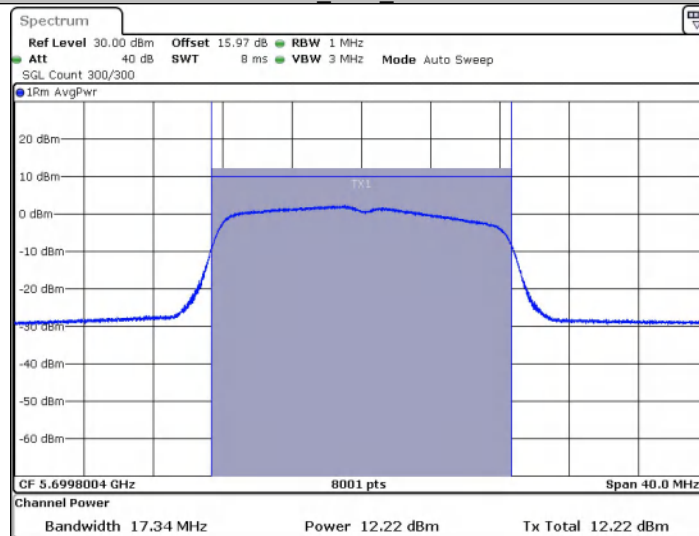
Date: 4.MAR.2026 10:11:19

11A_Ant2_5580



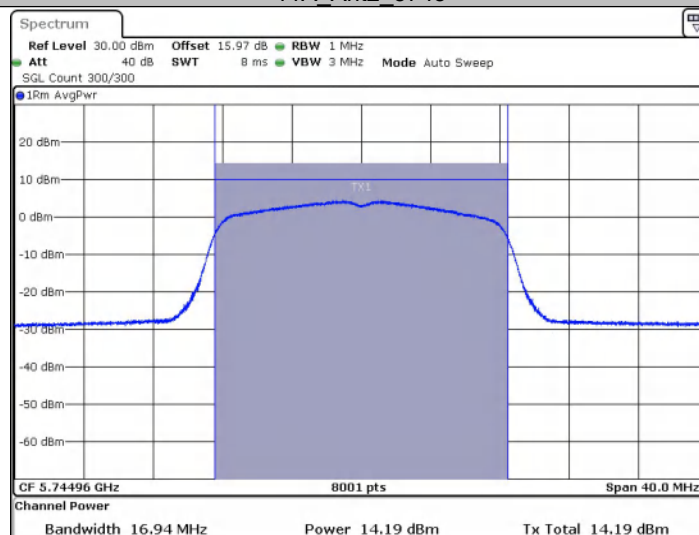
Date: 4 MAR 2026 10:12:47

11A_Ant2_5700



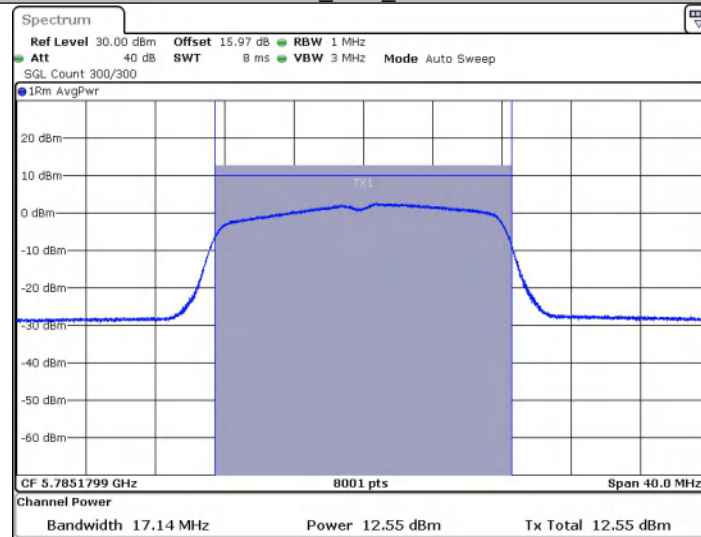
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11A_Ant2_5745

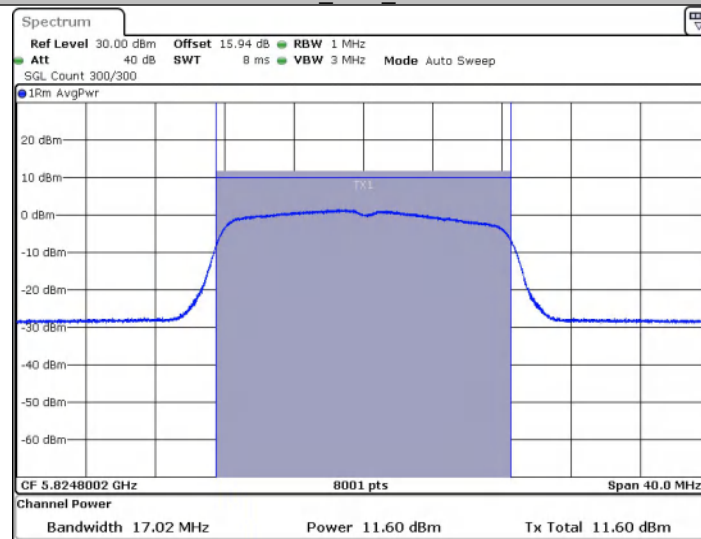


Date: 4 MAR 2026 10:18:44

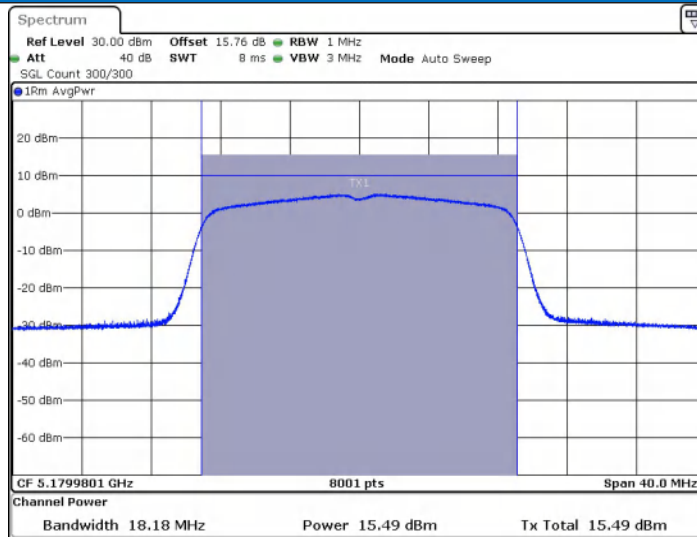
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11A_Ant2_5825

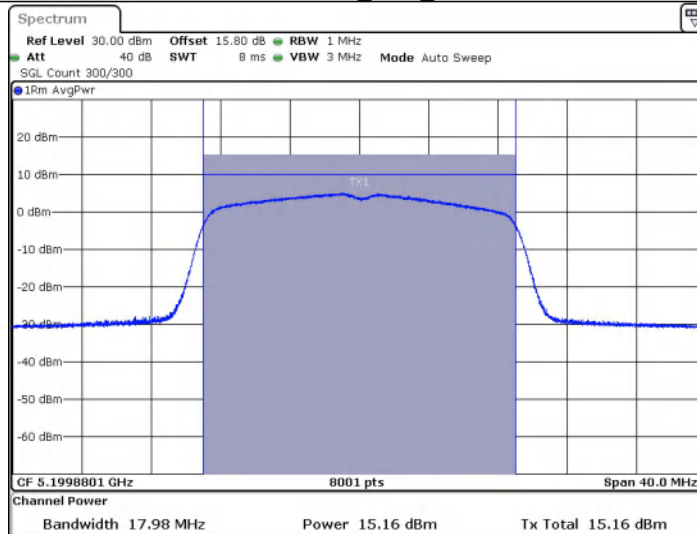


11N20SISO_Ant2_5180



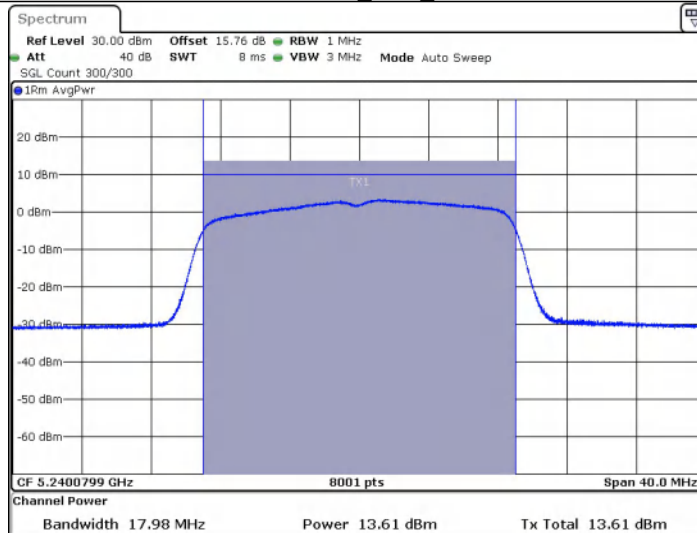
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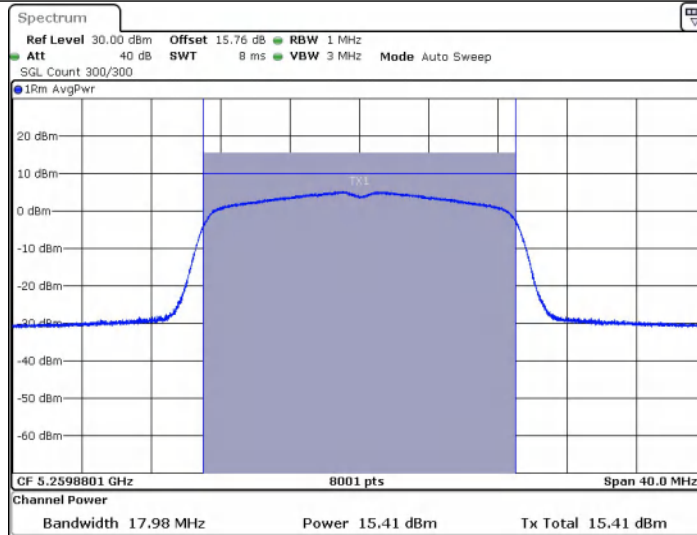
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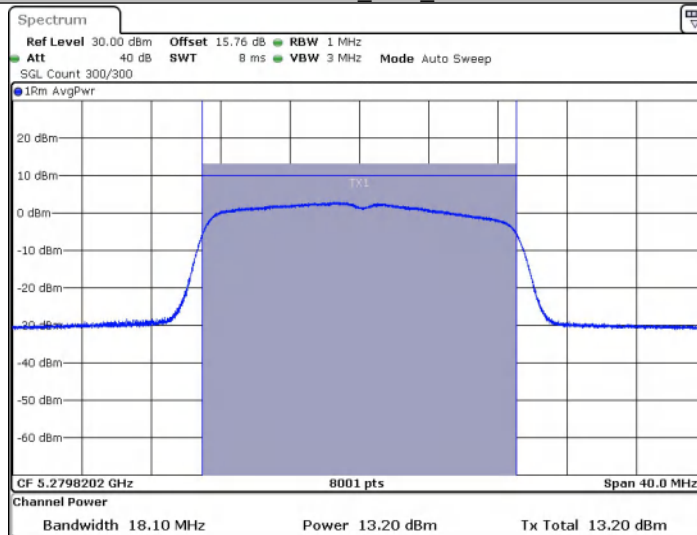
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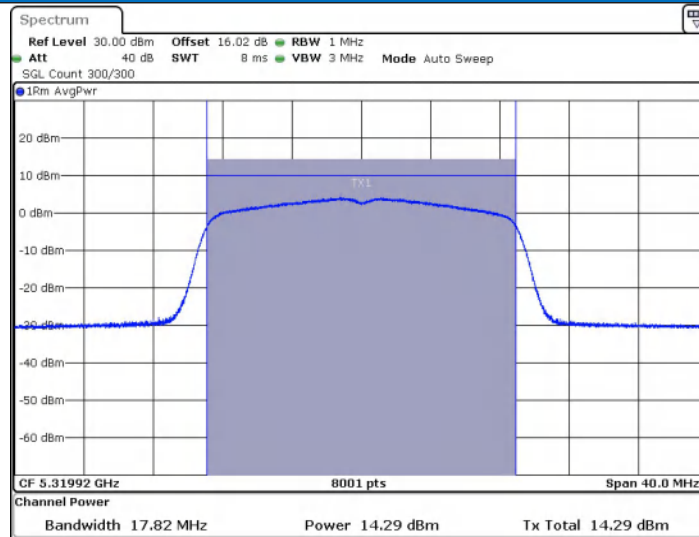
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11N20SISO_Ant2_5280



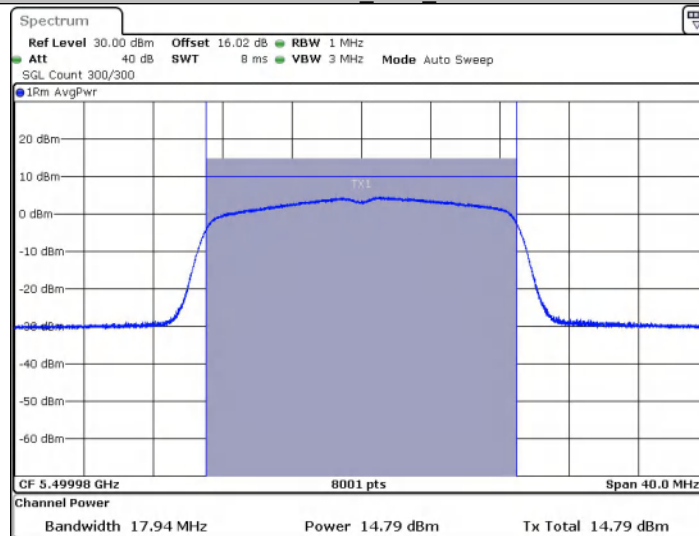
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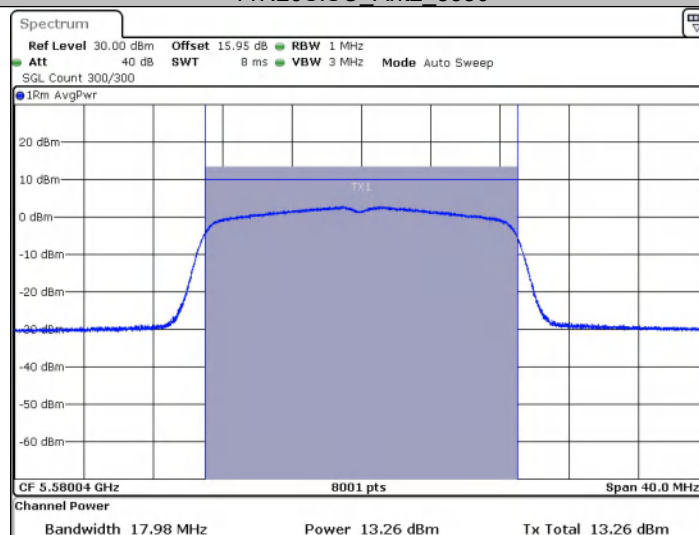
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11N20SISO_Ant2_5500



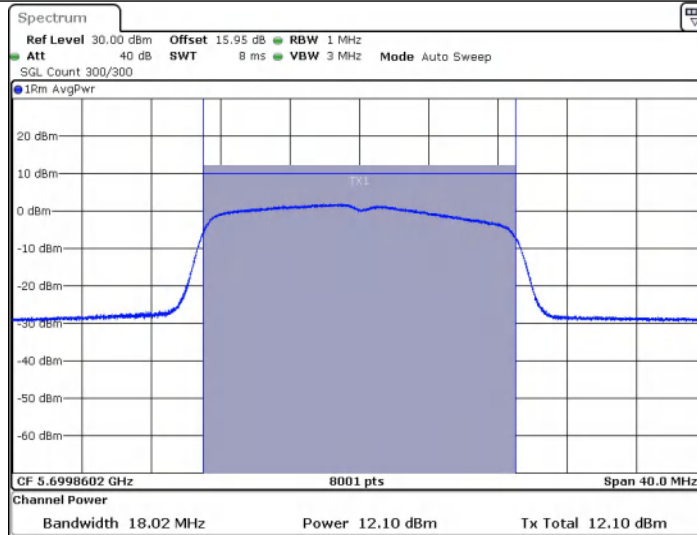
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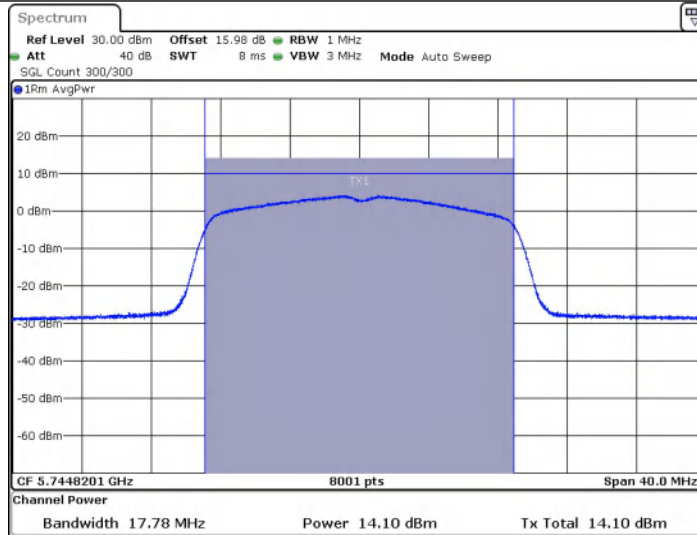


Date: 4 MAR 2026 10:53:16

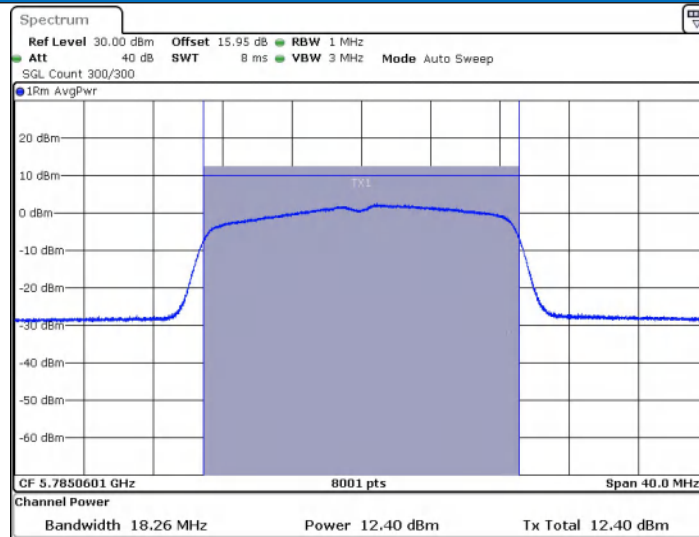
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11N20SISO_Ant2_5745

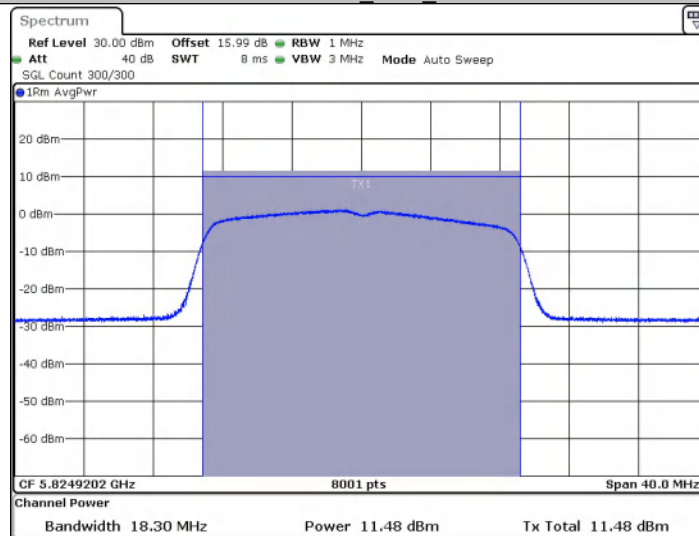


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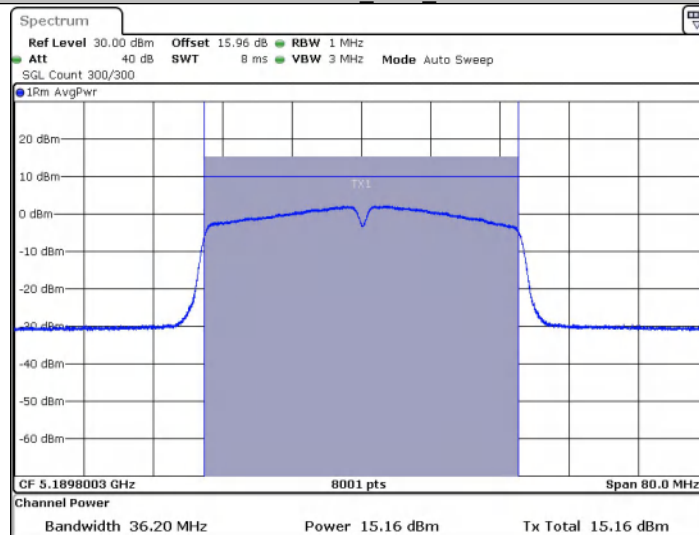
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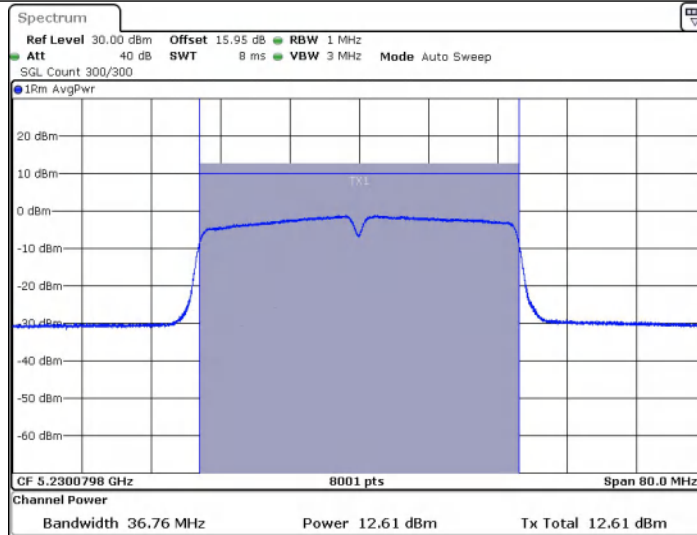
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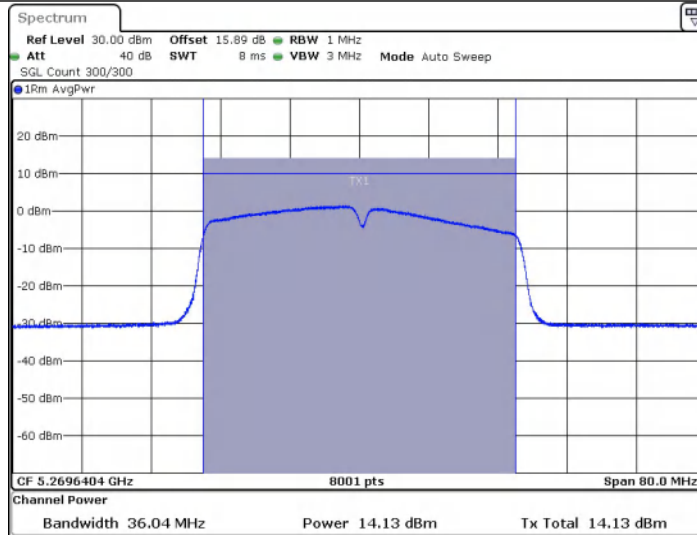


Date: 4 MAR 2026 11:03:10

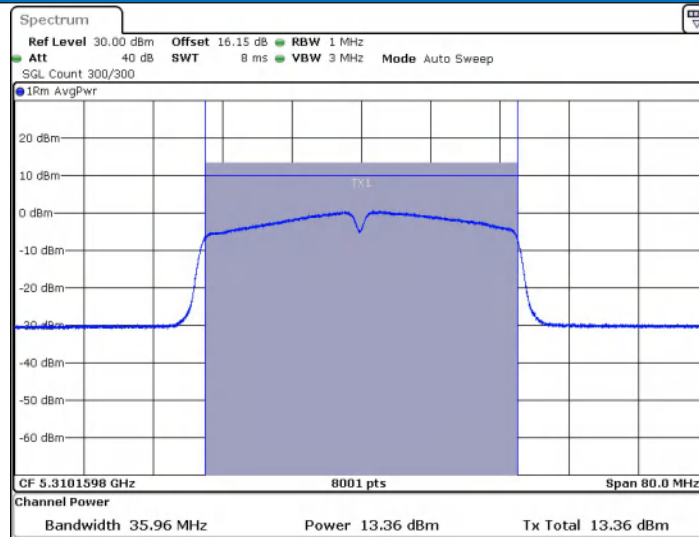
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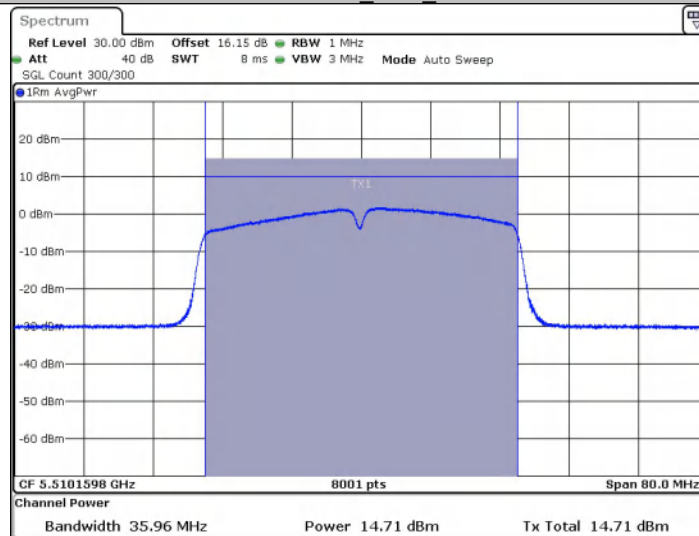


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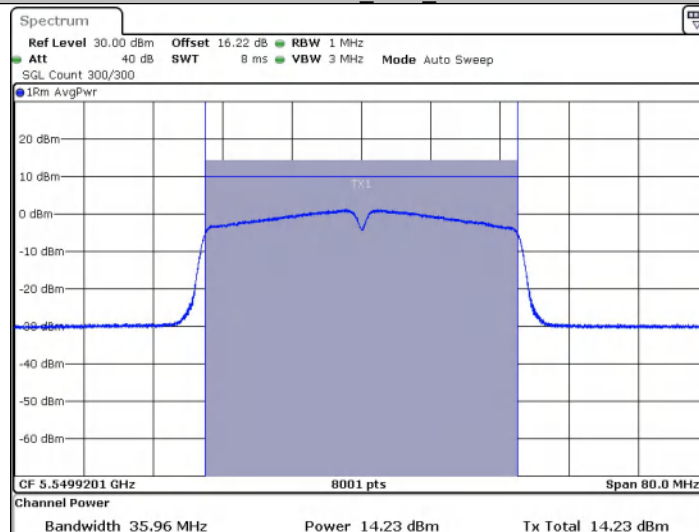
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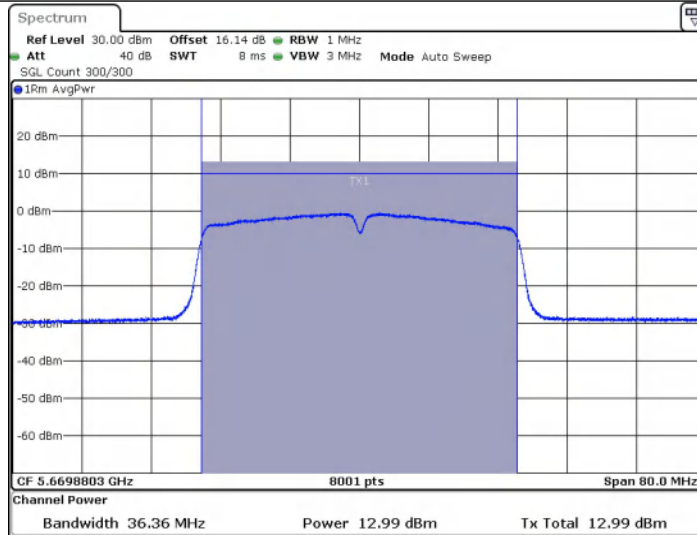
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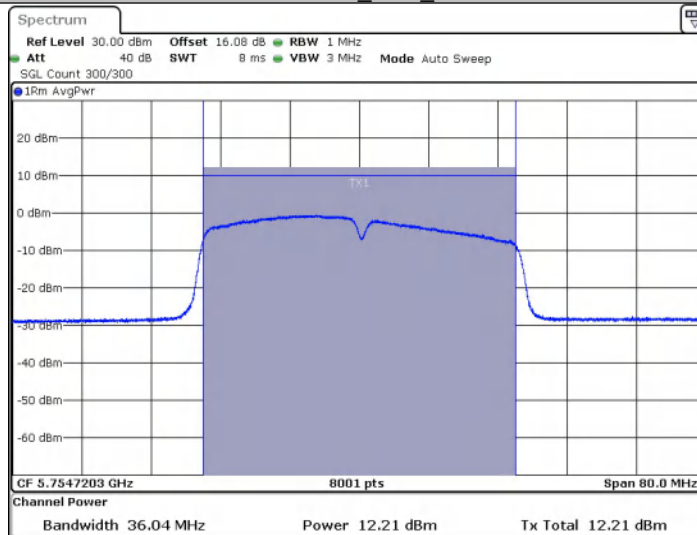


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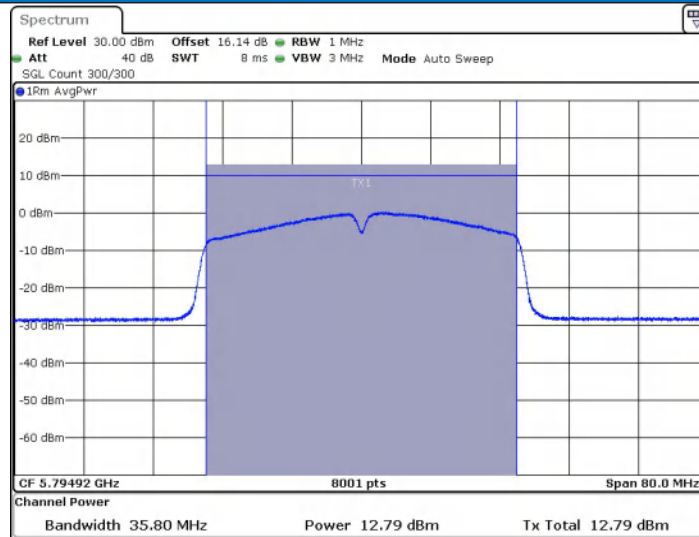
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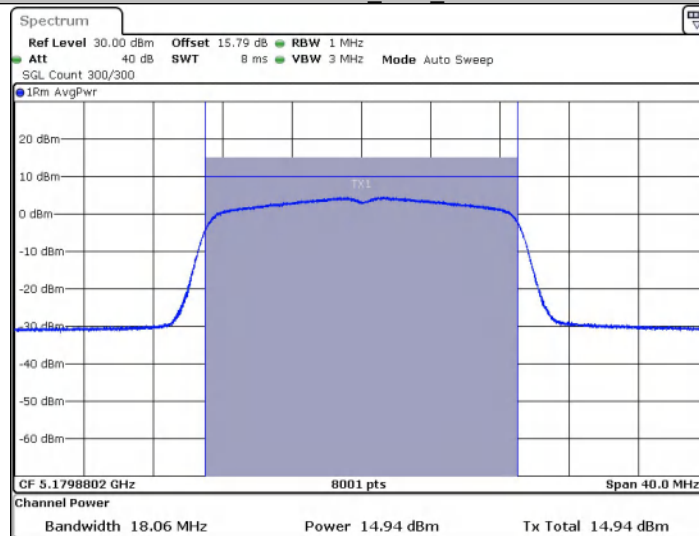


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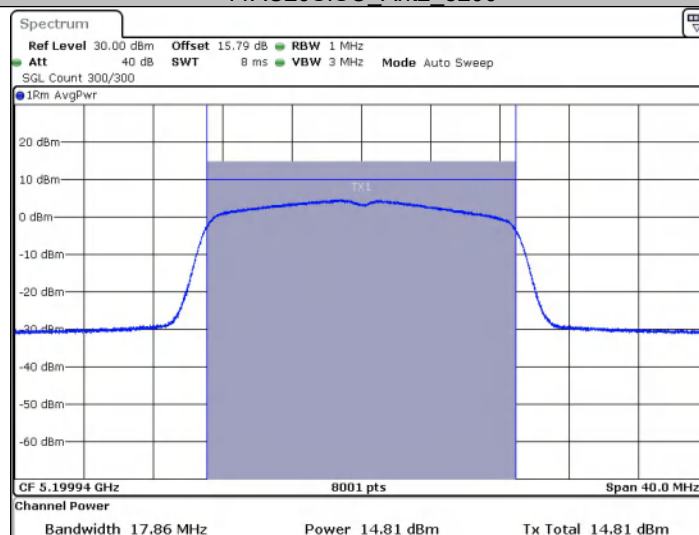
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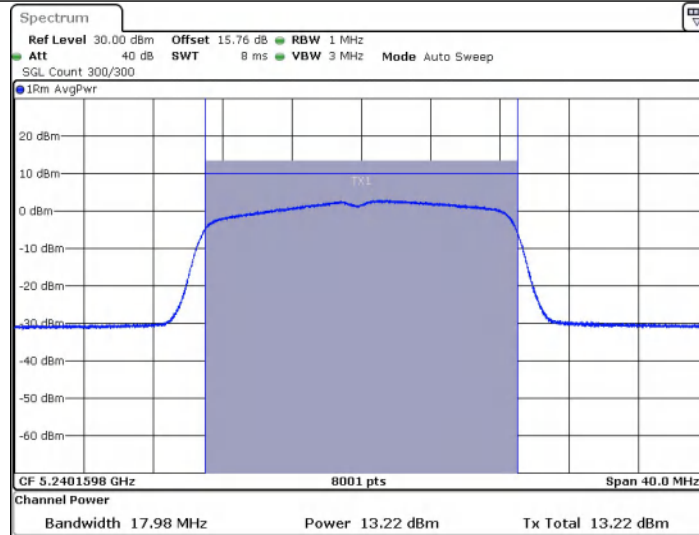
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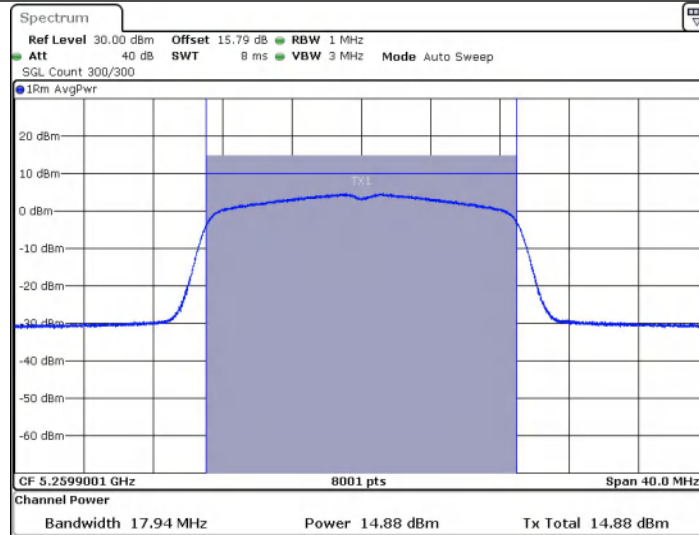


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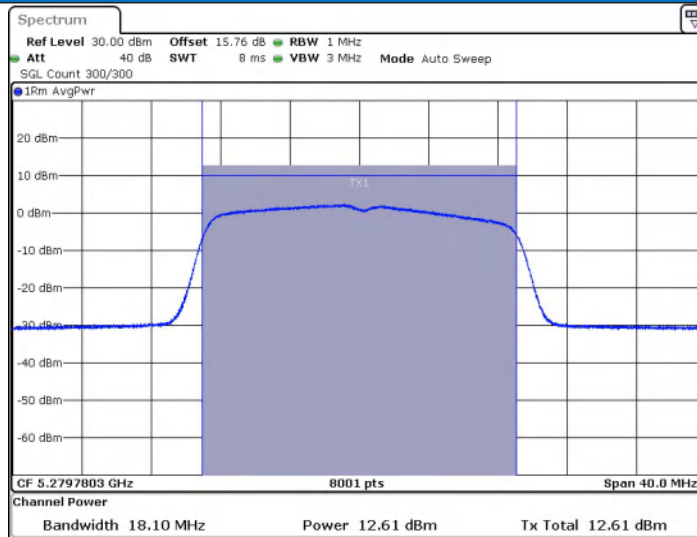
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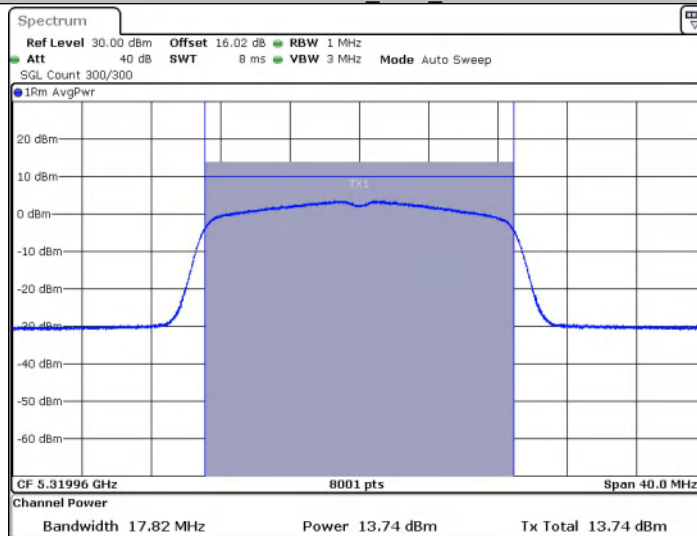


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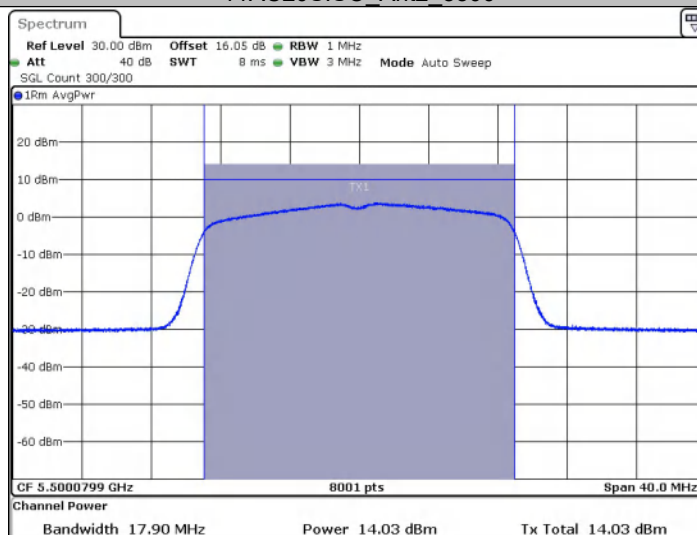
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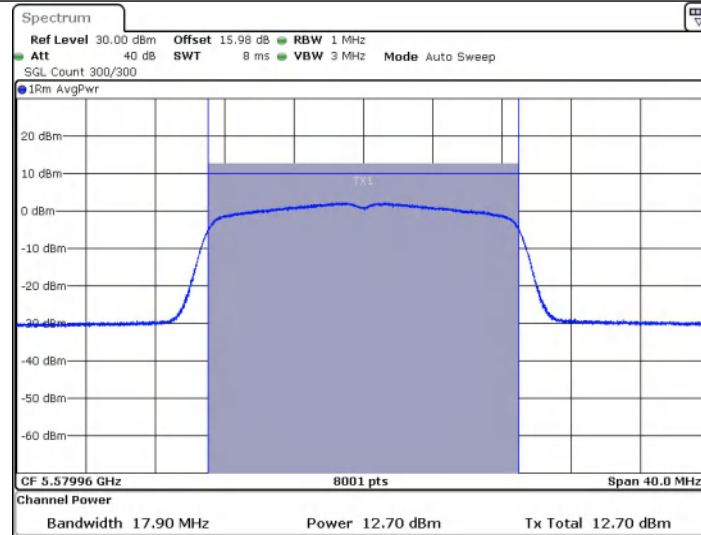
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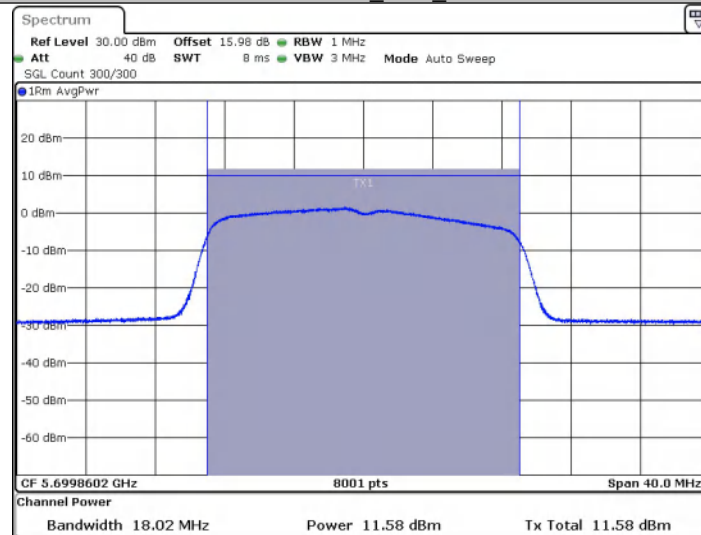
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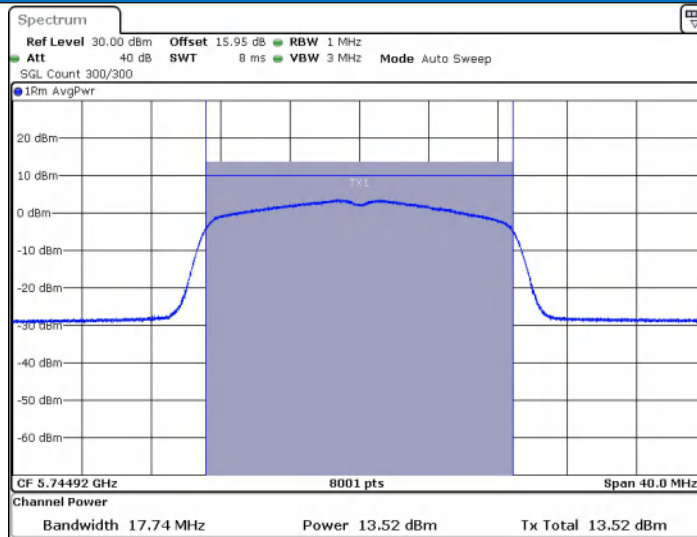
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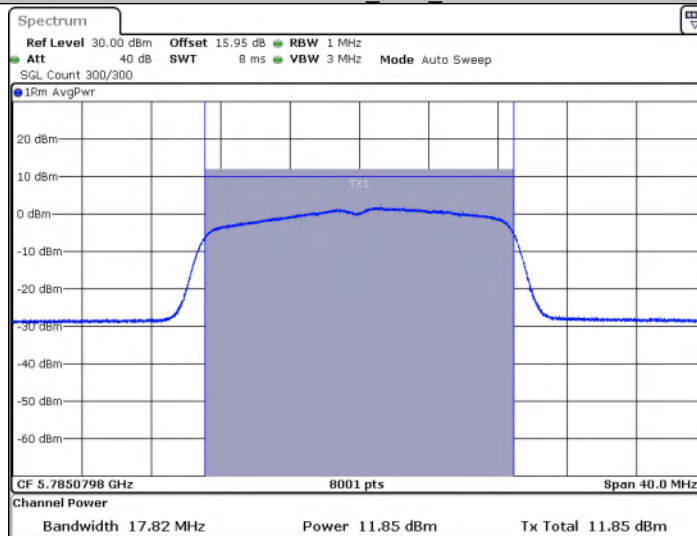
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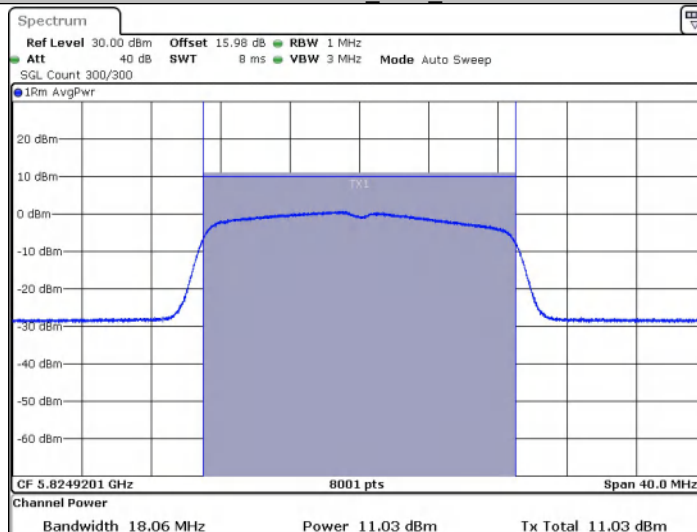
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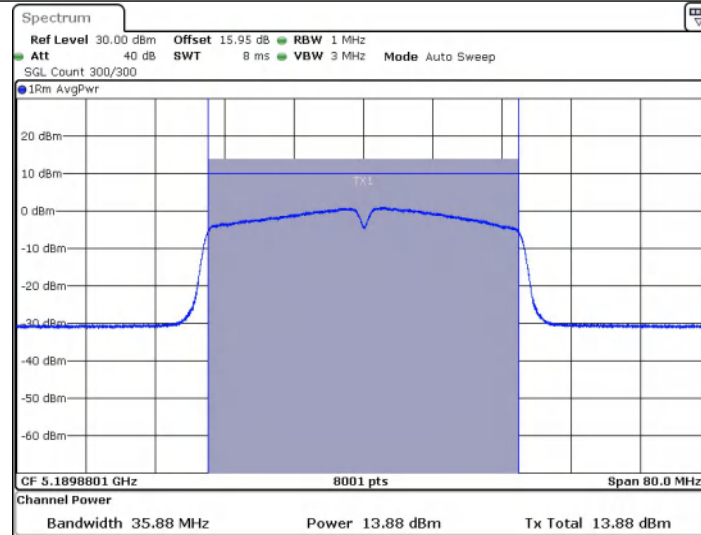
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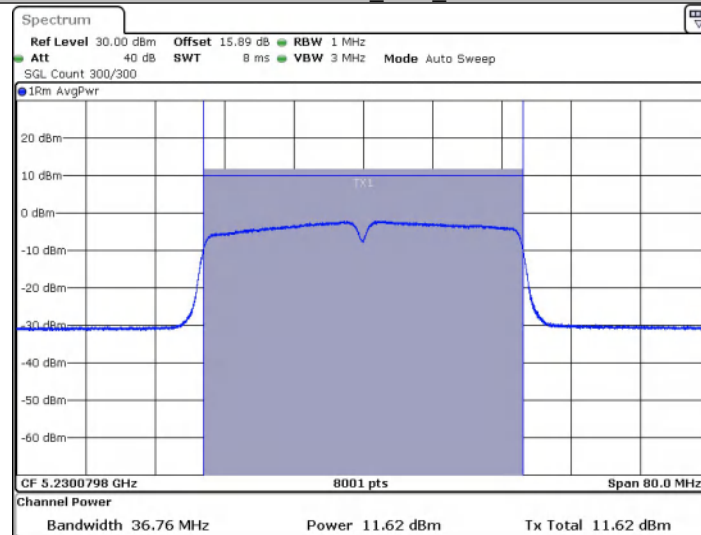
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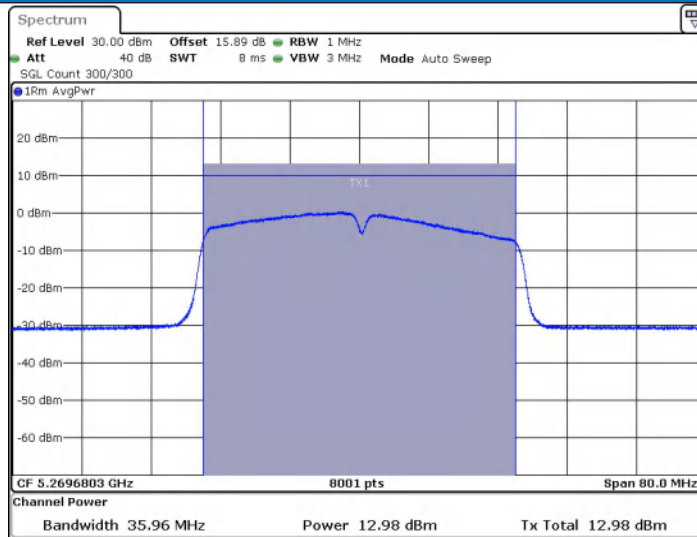
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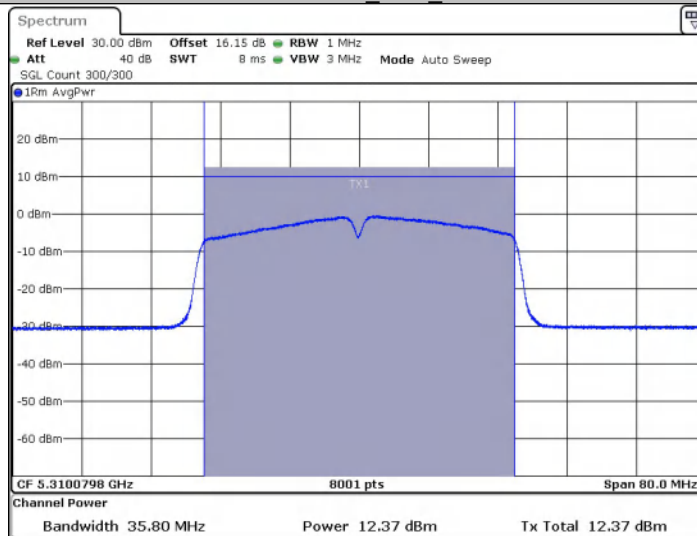
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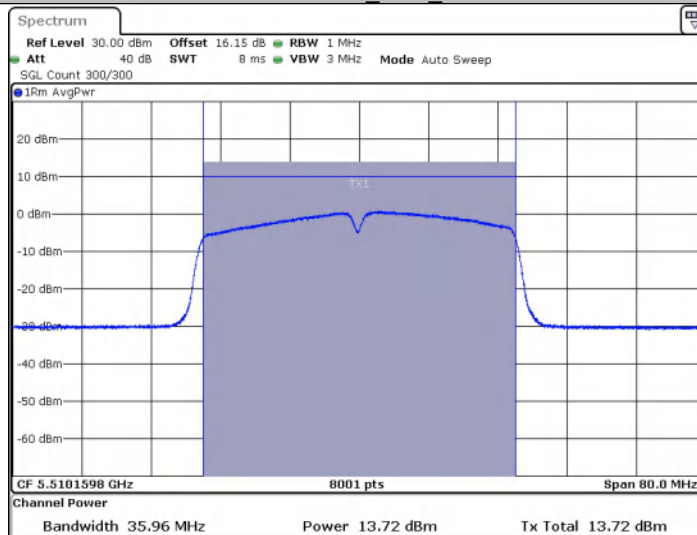
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11AC40SISO_Ant2_5310



Date: 4 MAR 2026 12:00:28

11AC40SISO_Ant2_5510



Date: 4 MAR 2026 12:02:24