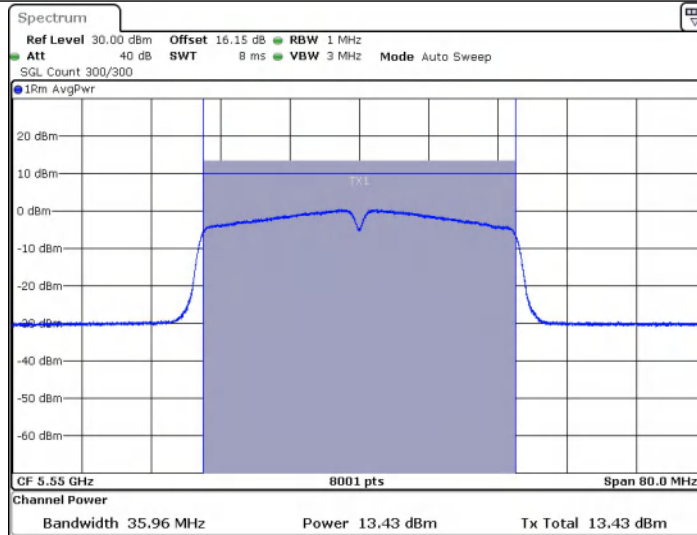
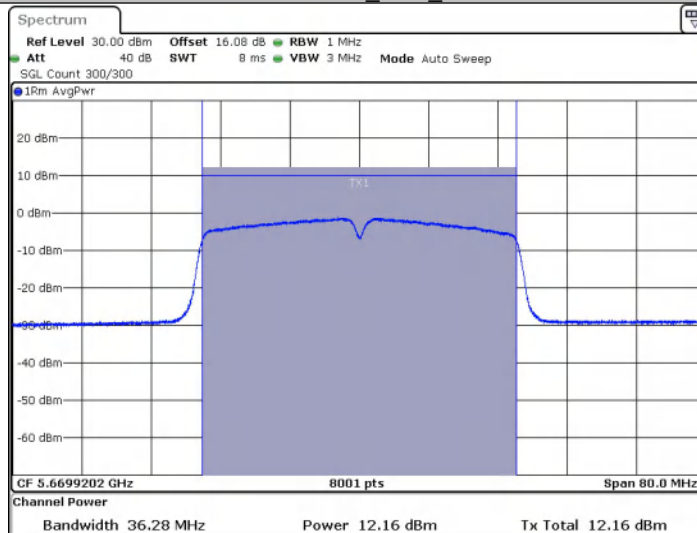


11AC40SISO_Ant2_5550



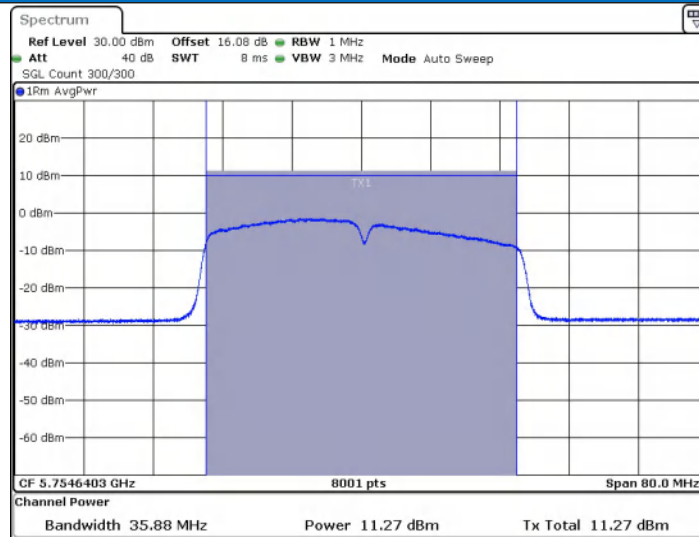
Date: 4 MAR 2026 13:34:01

11AC40SISO_Ant2_5670



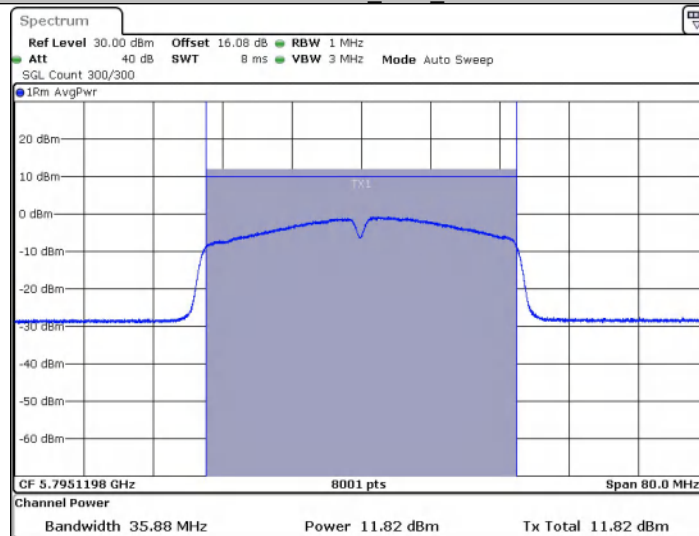
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11AC40SISO_Ant2_5755



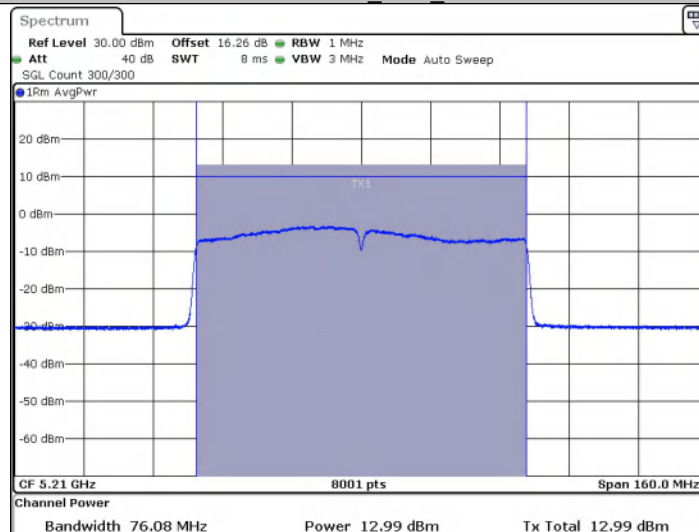
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11AC40SISO_Ant2_5795



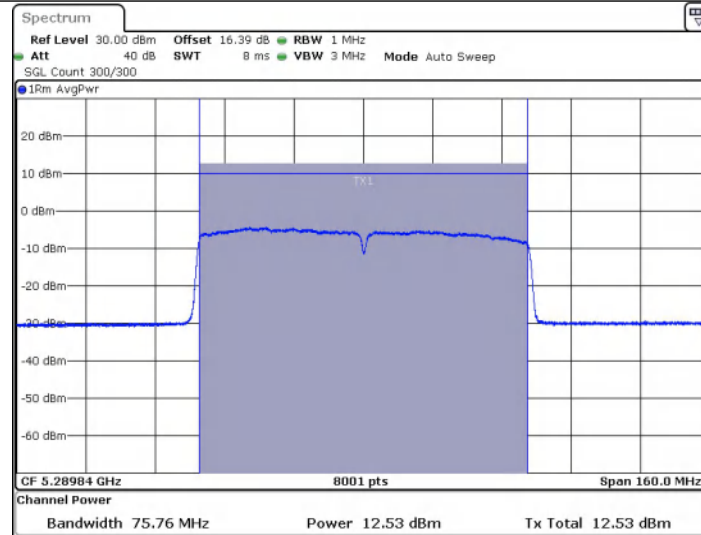
Date: 4 MAR 2026 13:43:09

11AC80SISO_Ant2_5210



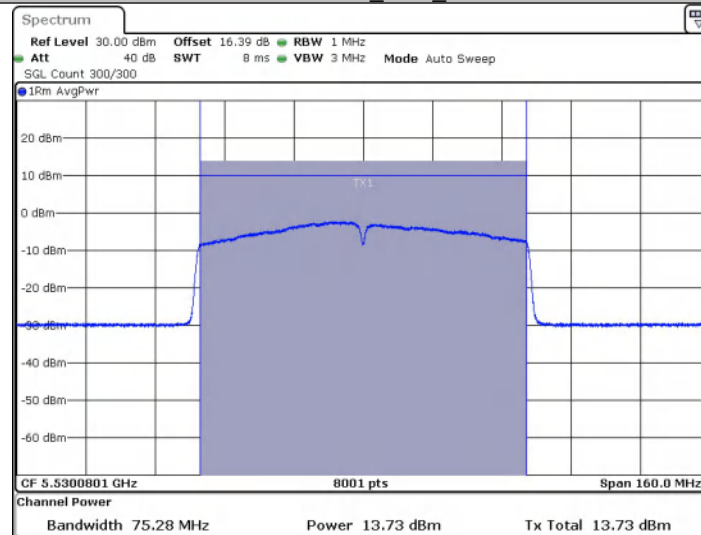
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11AC80SISO_Ant2_5290



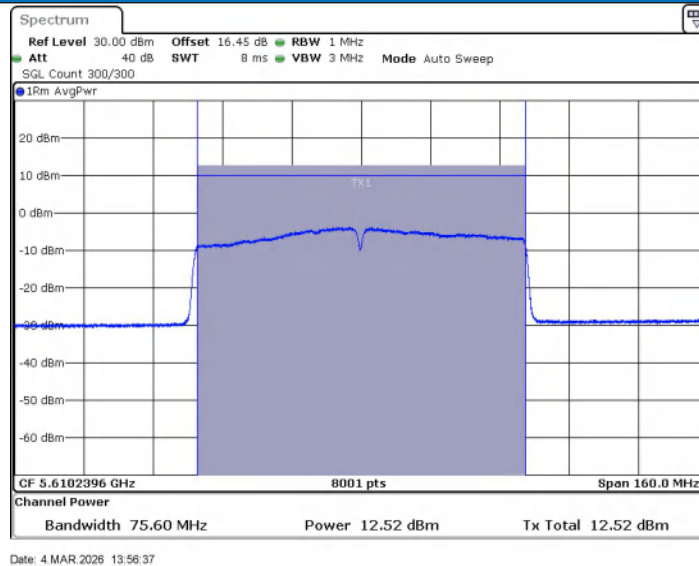
Date: 4 MAR 2026 13:47:57

11AC80SISO_Ant2_5530

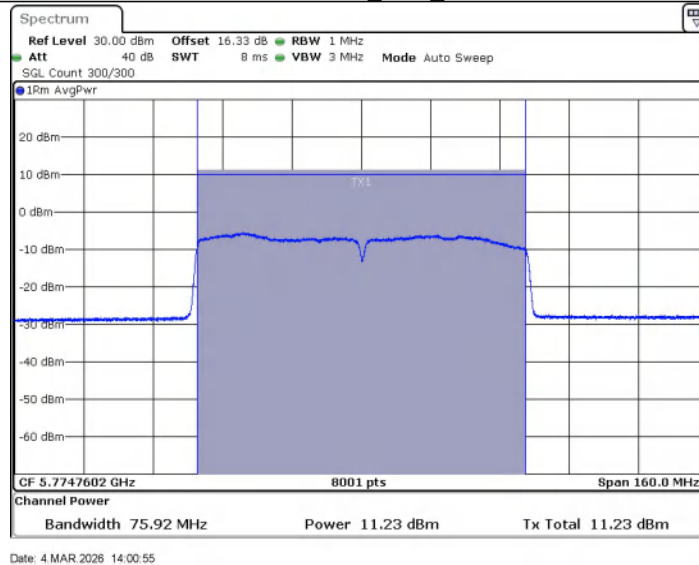


Date: 4 MAR 2026 13:50:02

11AC80SISO_Ant2_5610



11AC80SISO_Ant2_5775



Remark:

$Av.Power = Meas.Level + 10 \log(1/duty\ cycle)$

$E.i.r.p = Av.Power + G$,

G = antenna gain in dBi.

Ant1+Ant2:

Measurement Data

Test Mode	Antenna	Freq(MHz)	Result [dBm]	Limit [dBm]	Verdict
11N20MIM O	Ant1+Ant2	5180	18.23	≤22.04	PASS
		5200	17.84	≤22.04	PASS
		5240	16.34	≤22.04	PASS
		5260	18.13	≤22.04	PASS
		5280	15.88	≤22.04	PASS
		5320	17.01	≤22.04	PASS
		5500	17.45	≤22.04	PASS
		5580	16.07	≤22.04	PASS
		5700	14.83	≤22.04	PASS
		5745	16.79	≤28.04	PASS
		5785	15.09	≤28.04	PASS
		5825	14.23	≤28.04	PASS
11N40MIM O	Ant1+Ant2	5190	17.76	≤22.04	PASS
		5230	15.40	≤22.04	PASS
		5270	16.79	≤22.04	PASS
		5310	16.18	≤22.04	PASS
		5510	17.55	≤22.04	PASS
		5550	17.03	≤22.04	PASS
		5670	15.83	≤22.04	PASS
		5755	15.04	≤28.04	PASS
		5795	15.65	≤28.04	PASS
11AC20MI MO	Ant1+Ant2	5180	17.76	≤22.04	PASS
		5200	17.47	≤22.04	PASS
		5240	15.96	≤22.04	PASS
		5260	17.61	≤22.04	PASS
		5280	15.35	≤22.04	PASS
		5320	16.63	≤22.04	PASS
		5500	16.87	≤22.04	PASS
		5580	15.59	≤22.04	PASS
		5700	14.25	≤22.04	PASS
		5745	16.29	≤28.04	PASS
		5785	14.63	≤28.04	PASS
		5825	13.82	≤28.04	PASS

11AC40MI MO	Ant1+Ant2	5190	16.60	≤ 22.04	PASS
		5230	14.33	≤ 22.04	PASS
		5270	15.10	≤ 22.04	PASS
		5310	14.49	≤ 22.04	PASS
		5510	16.01	≤ 22.04	PASS
		5550	15.72	≤ 22.04	PASS
		5670	14.44	≤ 22.04	PASS
		5755	13.71	≤ 28.04	PASS
		5795	14.55	≤ 28.04	PASS
11AC80MI MO	Ant1+Ant2	5210	15.61	≤ 22.04	PASS
		5290	15.36	≤ 22.04	PASS
		5530	16.43	≤ 22.04	PASS
		5610	15.18	≤ 22.04	PASS
		5775	14.01	≤ 28.04	PASS

Appendix C): Maximum Power Spectral Density

Test Requirement 47 CFR Part 15, Subpart C 15.407 (a)

Test Method: KDB 789033 D02 II F

Test Procedure:

For 5150-5725MHz:

1. Connect EUT RF output port to the Spectrum Analyzer through an RF attenuator
2. Set the EUT Work on operation frequency individually.
3. Set RBW = 1MHz.
4. Set the VBW $\geq 3 \times$ RBW. Detector = Peak. Trace mode = max hold.

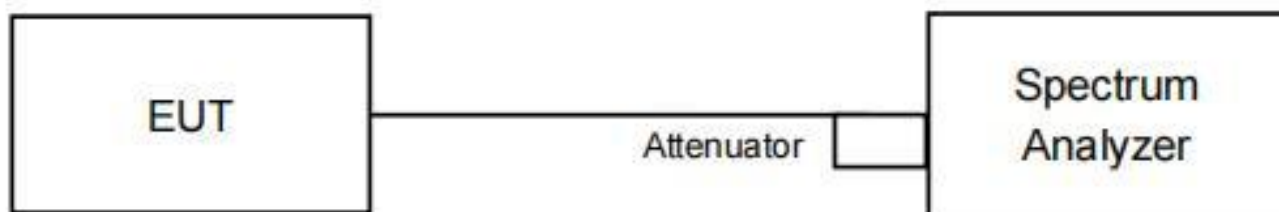
For 5725-5850MHz:

1. Connect EUT RF output port to the Spectrum Analyzer through an RF attenuator
2. Set the EUT Work on operation frequency individually.
3. Set RBW = 500KHz.
4. Set the VBW $\geq 3 \times$ RBW. Detector = Peak. Trace mode = max hold.

Limit:

Frequency band(MHz)	Limit
5150-5250	$\leq 17\text{dBm}$ in 1MHz for master device
	$\leq 11\text{dBm}$ in 1MHz for client device
5250-5350	$\leq 11\text{dBm}$ in 1MHz for client device
5470-5725	$\leq 11\text{dBm}$ in 1MHz for client device
5725-5850	$\leq 30\text{dBm}$ in 500 kHz
Remark:	The maximum power spectral density is measured as a conducted emission by direct connection of a calibrated test instrument to the equipment under test.

Test Setup Diagram



ANT1:

Result Table

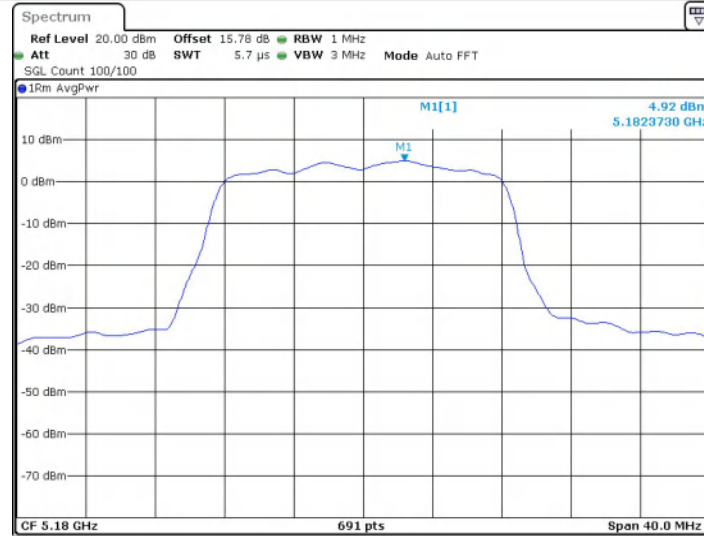
TestMode	Freq(MHz)	Result [dBm/MHz]	Limit[dBm/MHz]	Verdict
11A	5180	4.92	≤11.00	PASS
	5200	4.78	≤11.00	PASS
	5240	2.85	≤11.00	PASS
	5260	4.93	≤11.00	PASS
	5280	1.96	≤11.00	PASS
	5320	3.29	≤11.00	PASS
	5500	4.32	≤11.00	PASS
	5580	2.95	≤11.00	PASS
	5700	1.30	≤11.00	PASS
	5745	0.65	≤30.00	PASS
	5785	-0.92	≤30.00	PASS
	5825	-1.74	≤30.00	PASS
11N20SISO	5180	4.04	≤11.00	PASS
	5200	4.20	≤11.00	PASS
	5240	3.10	≤11.00	PASS
	5260	4.78	≤11.00	PASS
	5280	1.85	≤11.00	PASS
	5320	2.81	≤11.00	PASS
	5500	3.75	≤11.00	PASS
	5580	3.03	≤11.00	PASS
	5700	0.94	≤11.00	PASS
	5745	0.47	≤30.00	PASS
	5785	-1.51	≤30.00	PASS
	5825	-2.41	≤30.00	PASS
11N40SISO	5190	1.92	≤11.00	PASS
	5230	-1.70	≤11.00	PASS
	5270	0.47	≤11.00	PASS
	5310	0.00	≤11.00	PASS
	5510	1.72	≤11.00	PASS
	5550	0.71	≤11.00	PASS
	5670	-1.06	≤11.00	PASS
	5755	-3.92	≤30.00	PASS
	5795	-3.00	≤30.00	PASS
11AC20SISO	5180	4.03	≤11.00	PASS

		5200	4.12	≤ 11.00	PASS
		5240	1.96	≤ 11.00	PASS
		5260	3.95	≤ 11.00	PASS
		5280	2.08	≤ 11.00	PASS
		5320	2.79	≤ 11.00	PASS
		5500	3.22	≤ 11.00	PASS
		5580	1.93	≤ 11.00	PASS
		5700	0.36	≤ 11.00	PASS
		5745	0.35	≤ 30.00	PASS
		5785	-1.54	≤ 30.00	PASS
		5825	-2.49	≤ 30.00	PASS
	11AC40SISO	5190	0.23	≤ 11.00	PASS
		5230	-2.47	≤ 11.00	PASS
		5270	-1.81	≤ 11.00	PASS
		5310	-2.20	≤ 11.00	PASS
		5510	-0.79	≤ 11.00	PASS
		5550	-1.62	≤ 11.00	PASS
		5670	-3.19	≤ 11.00	PASS
		5755	-5.39	≤ 30.00	PASS
		5795	-4.50	≤ 30.00	PASS
	11AC80SISO	5210	-4.10	≤ 11.00	PASS
		5290	-4.49	≤ 11.00	PASS
		5530	-2.47	≤ 11.00	PASS
		5610	-4.29	≤ 11.00	PASS
		5775	-9.09	≤ 30.00	PASS

Remark:

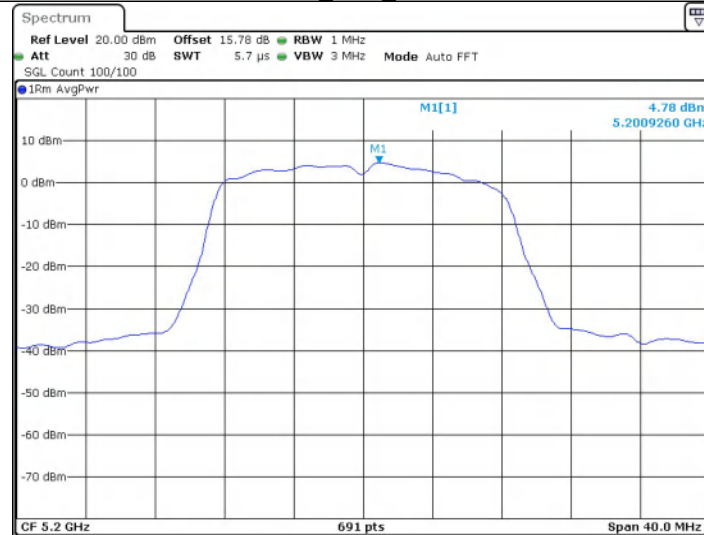
PSD = Meas PSD + Duty Cycle Factor

11A_Ant1_5180



Date: 2 MAR 2026 13:45:07

11A_Ant1_5200

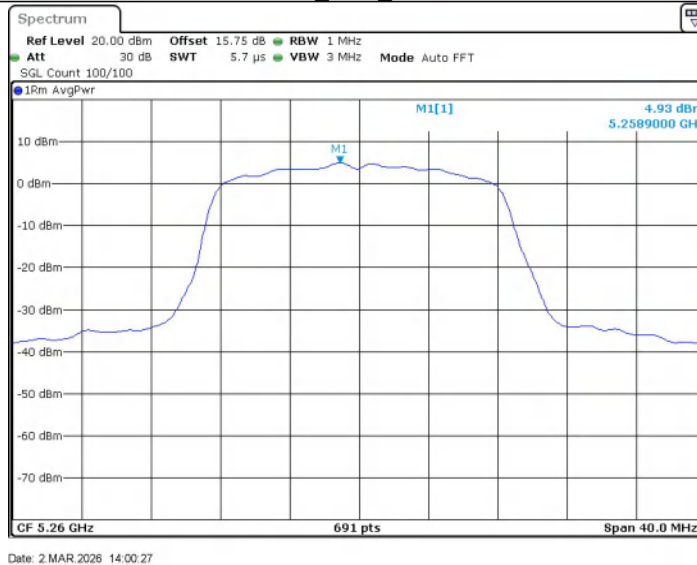


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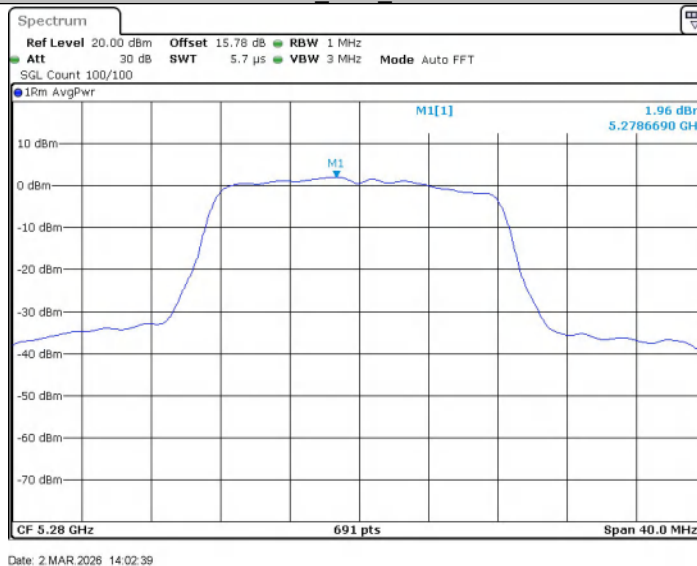
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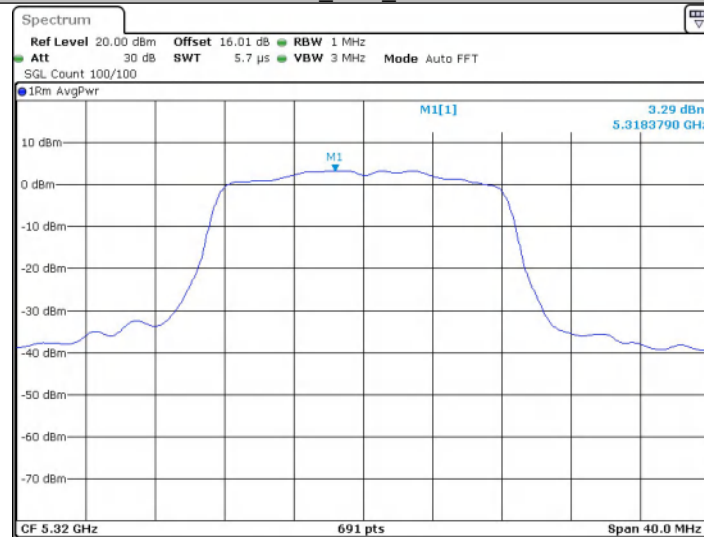
11A_Ant1_5260



11A_Ant1_5280

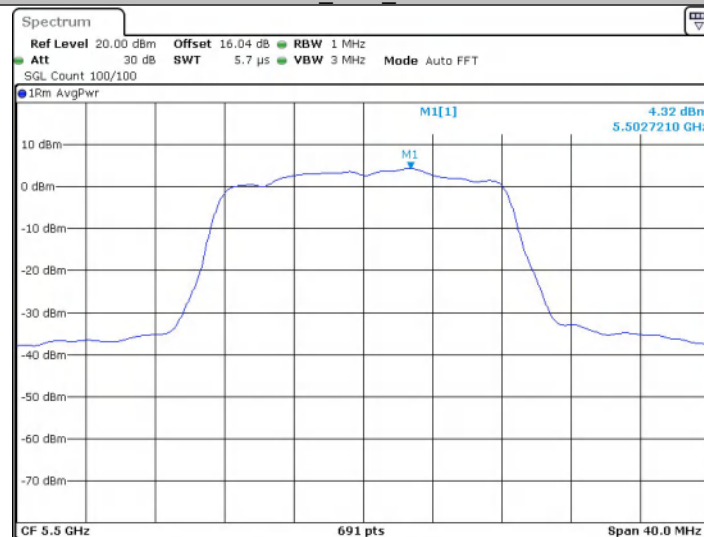


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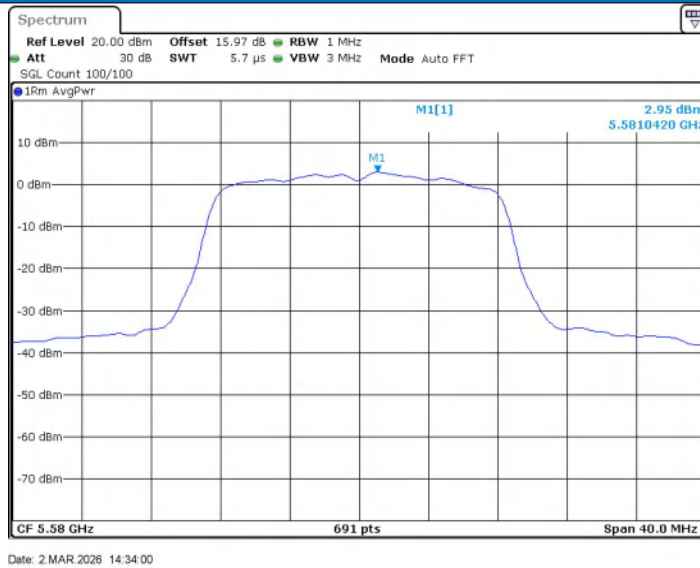
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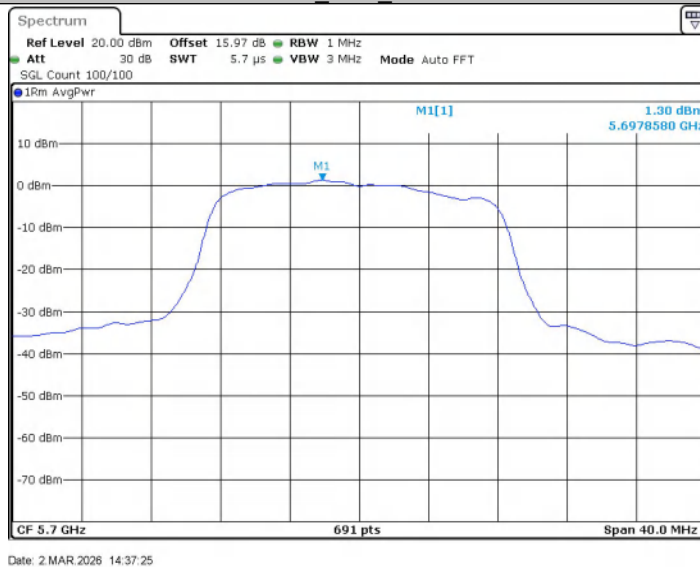


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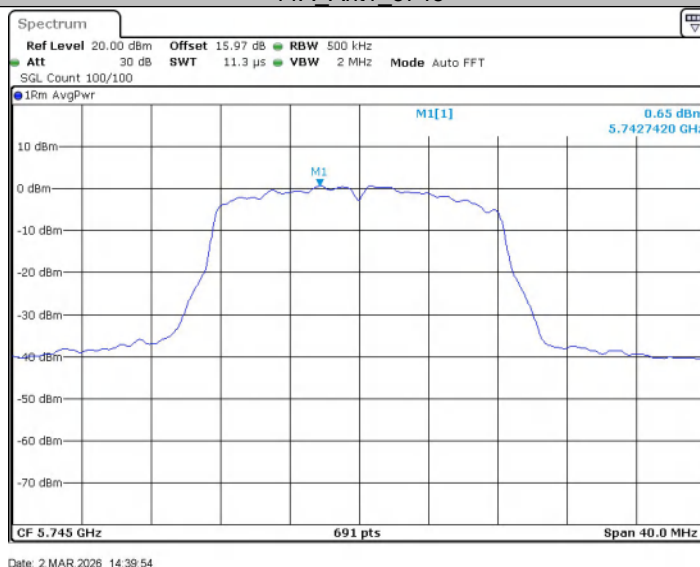
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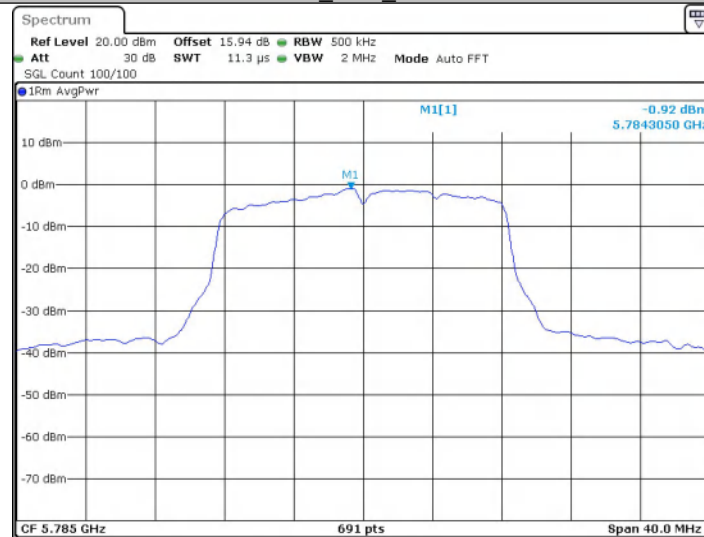
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11A_Ant1_5745

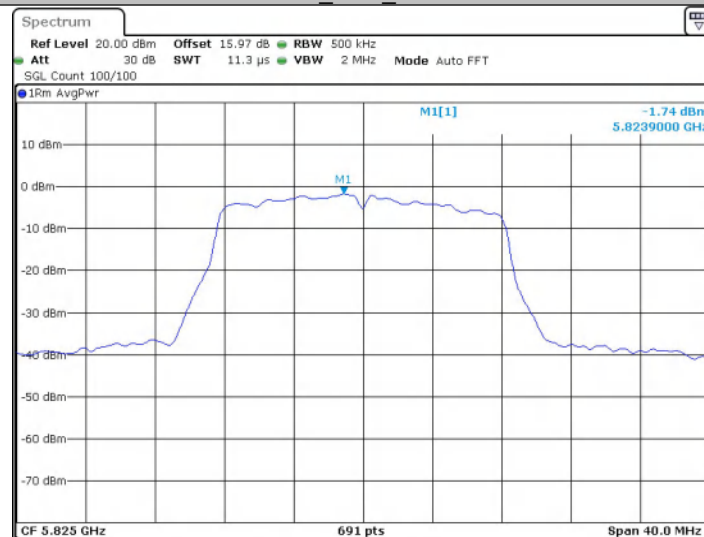


11A_Ant1_5785



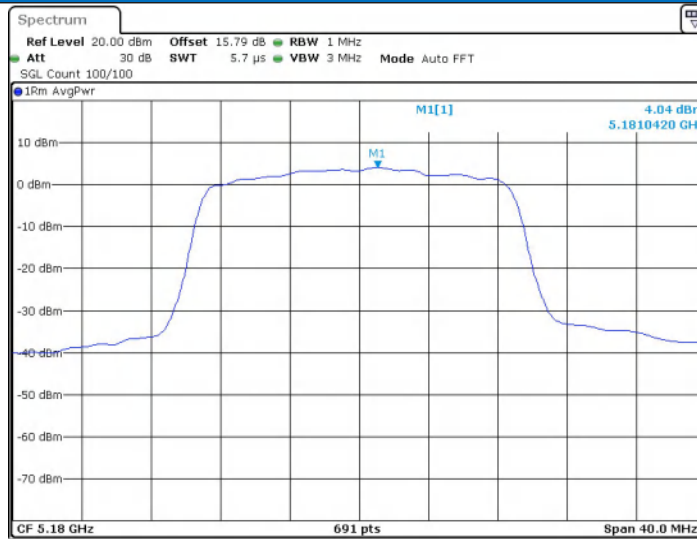
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11A_Ant1_5825



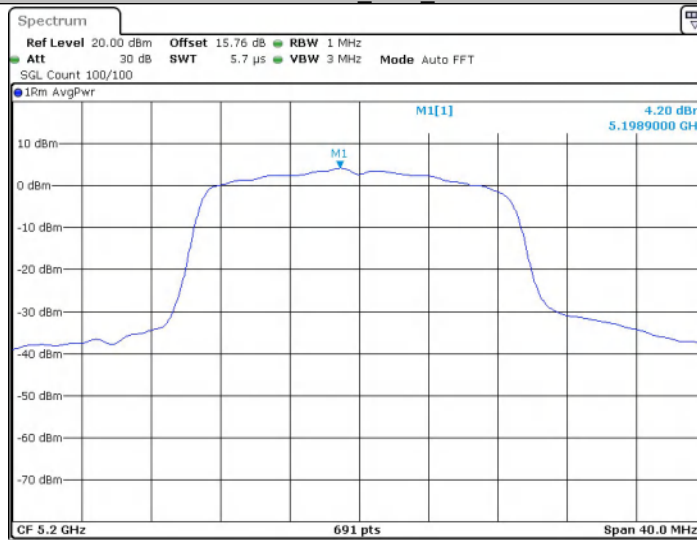
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11N20SISO_Ant1_5180



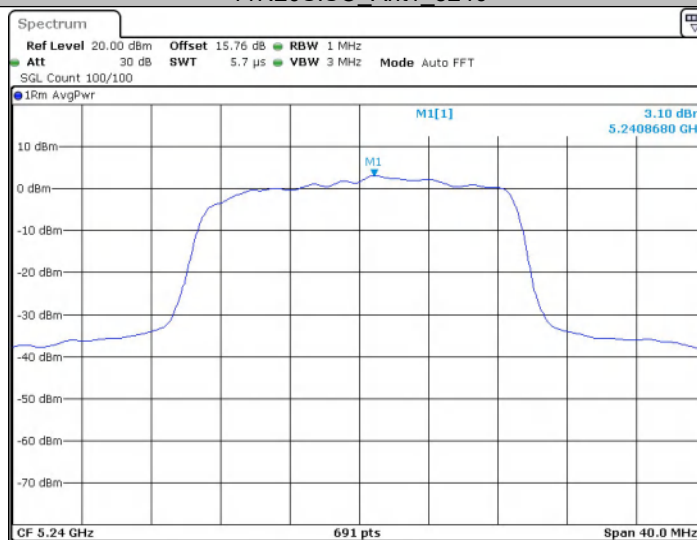
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11N20SISO_Ant1_5200



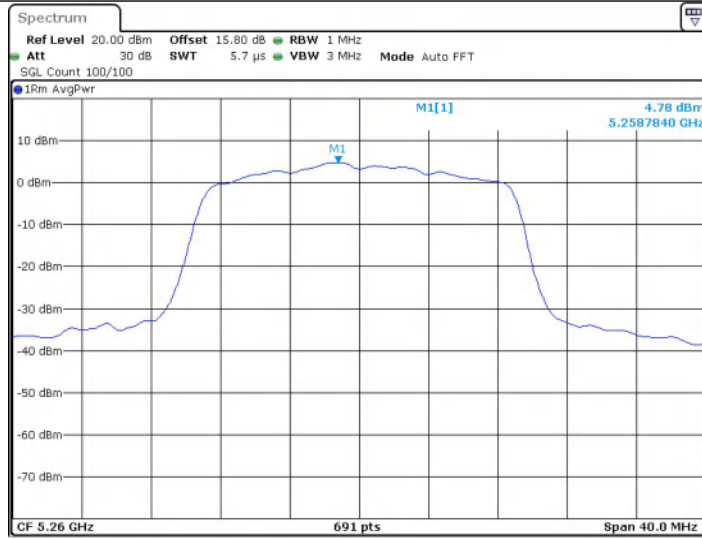
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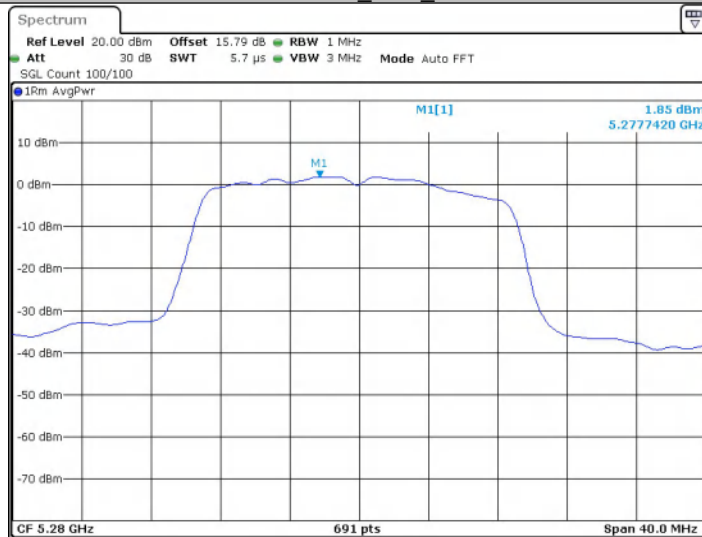
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11N20SISO_Ant1_5260



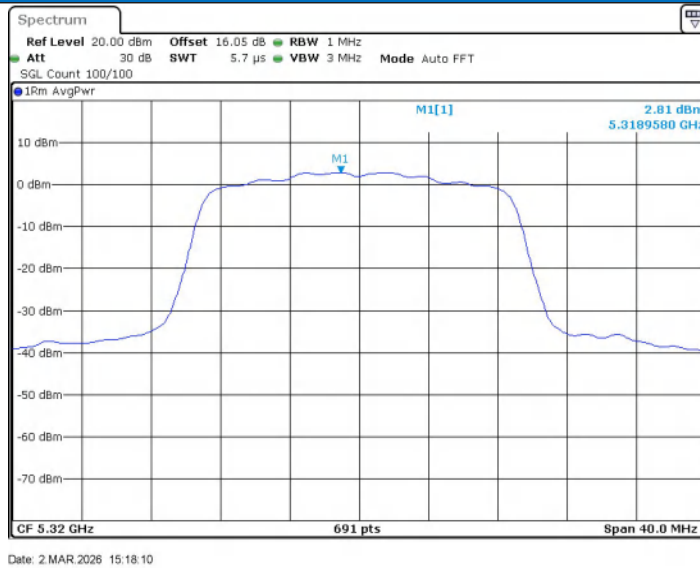
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11N20SISO_Ant1_5280

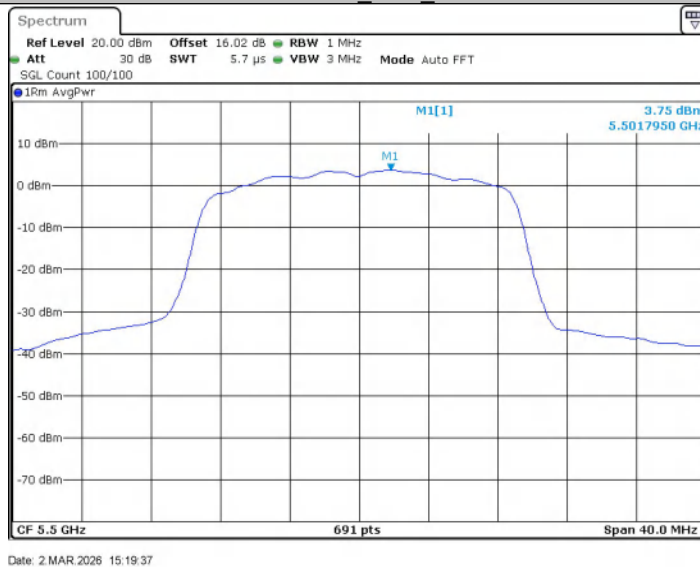


Date: 2 MAR 2026 15:17:12

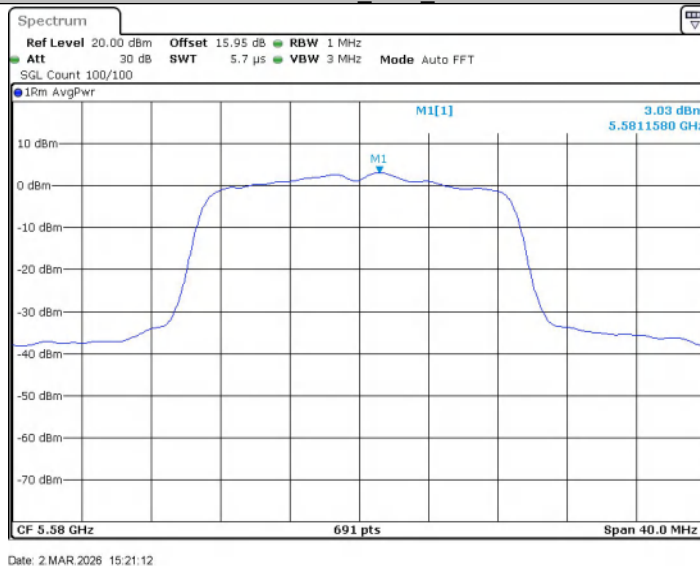
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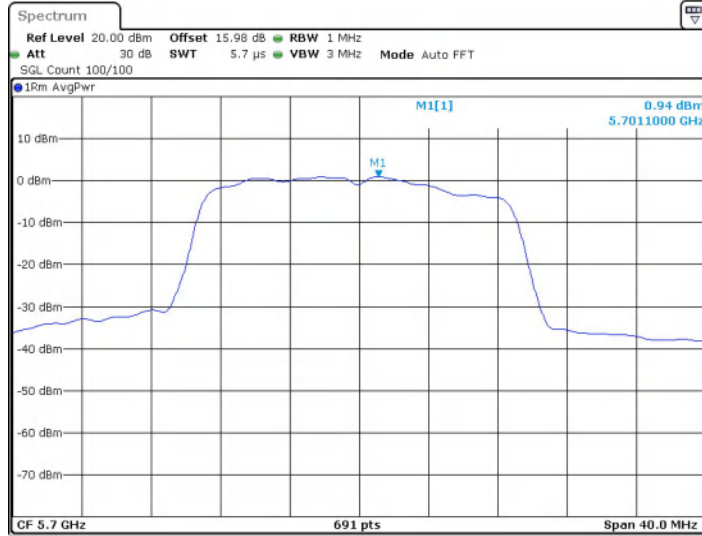
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11N20SISO_Ant1_5580

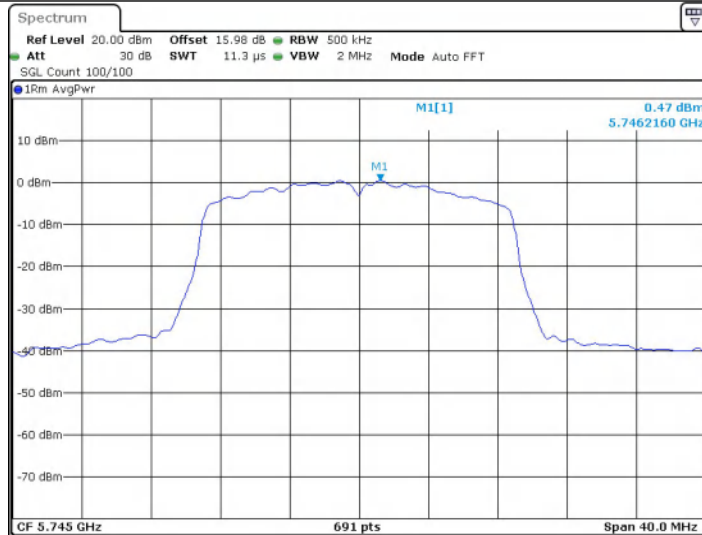


11N20SISO_Ant1_5700



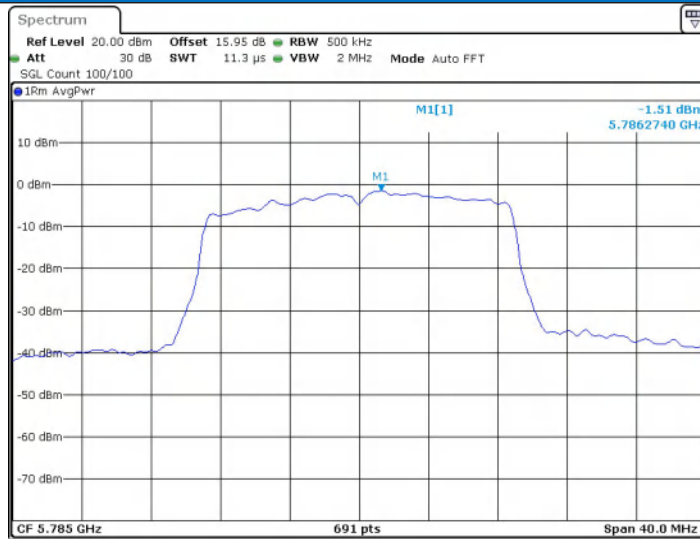
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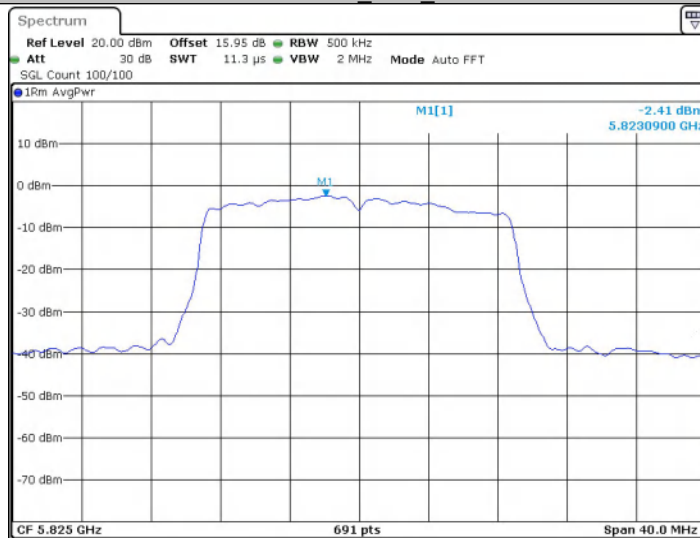
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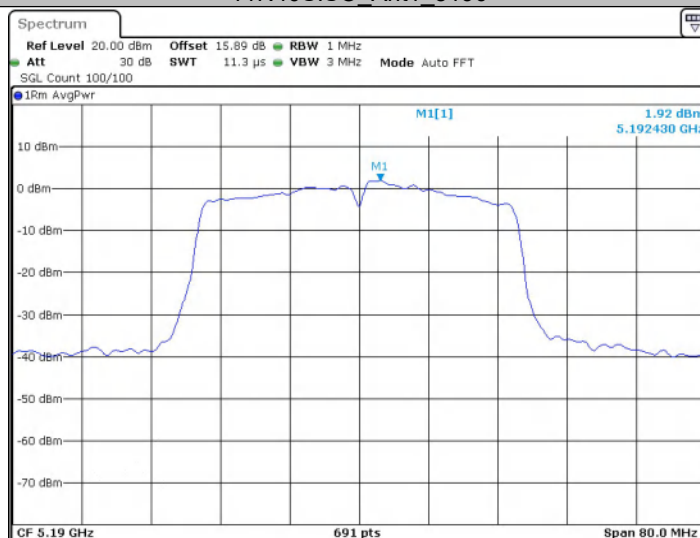
Date: 2 MAR 2026 15:25:22

11N20SISO_Ant1_5825



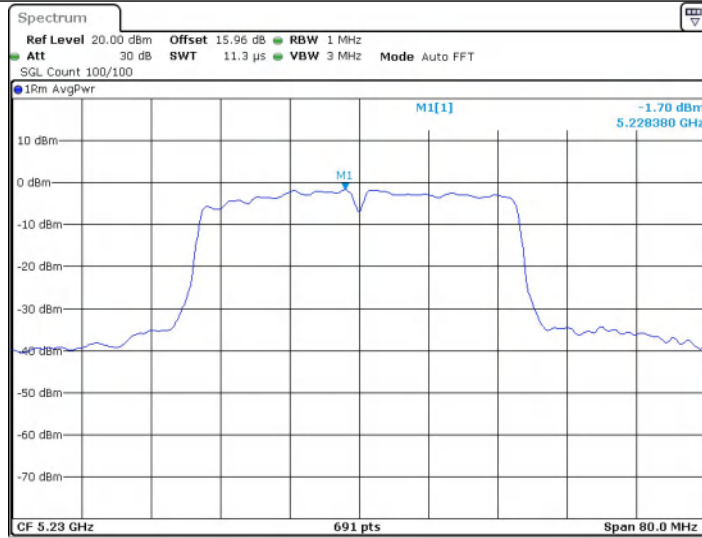
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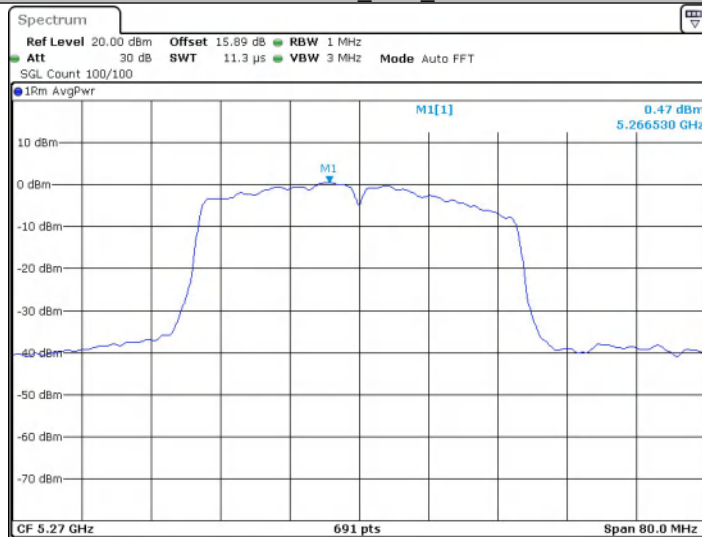
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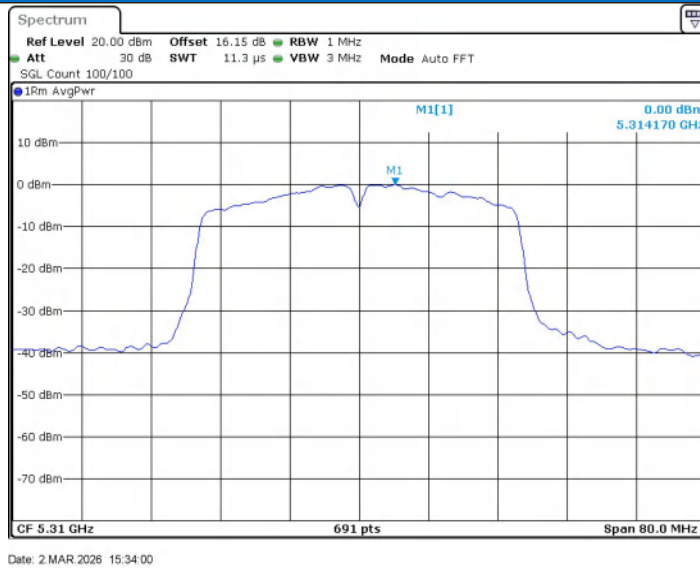
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11N40SISO_Ant1_5270



Date: 2 MAR 2026 15:32:30

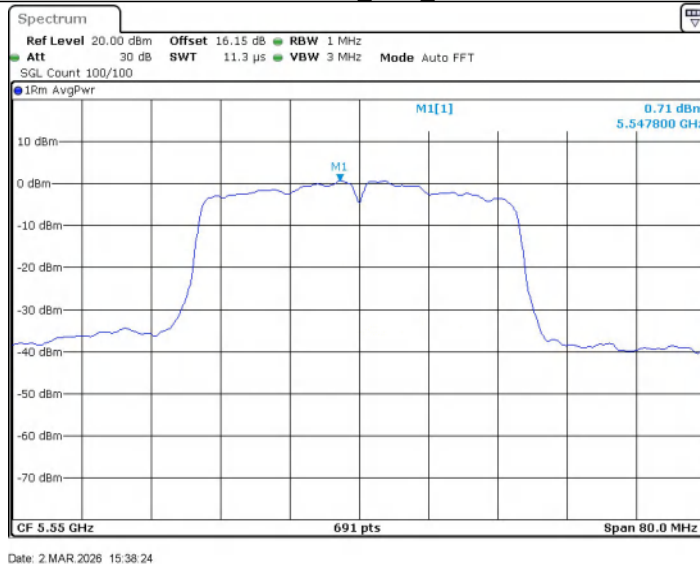
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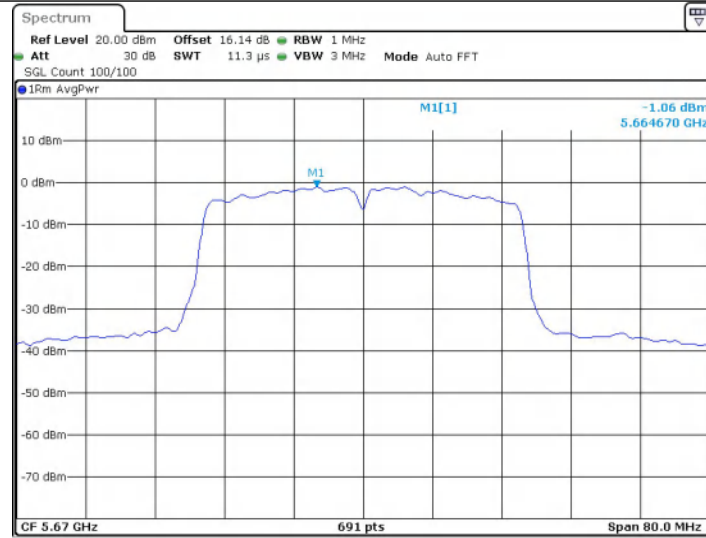
11N40SISO_Ant1_5510



11N40SISO_Ant1_5550

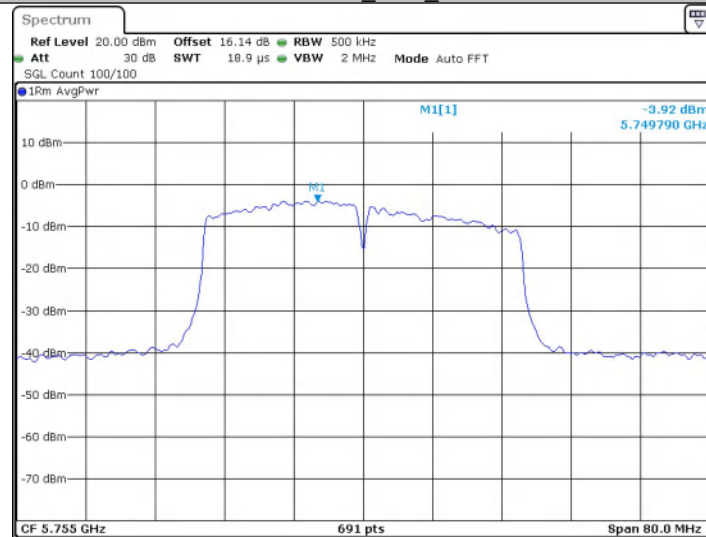


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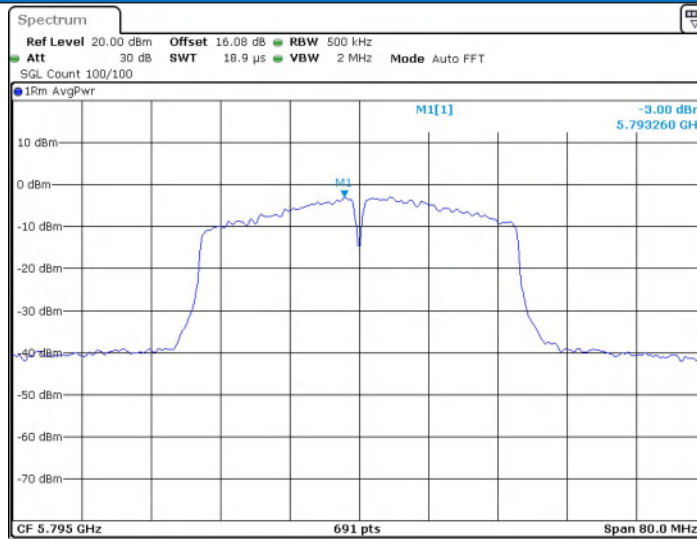
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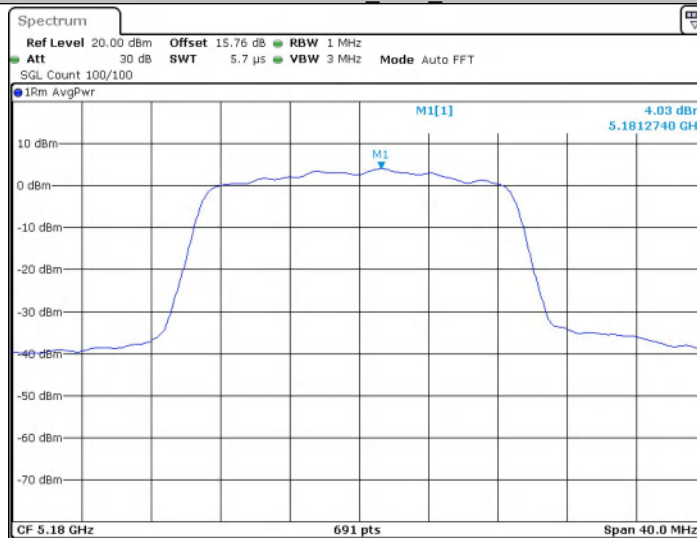
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11N40SISO_Ant1_5795



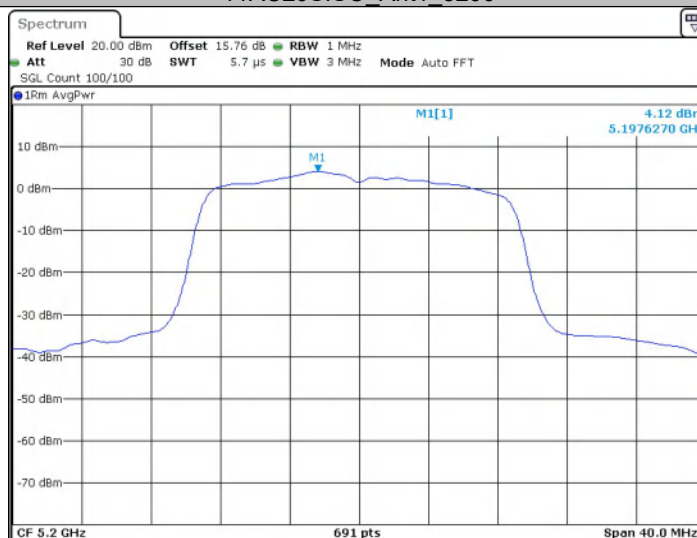
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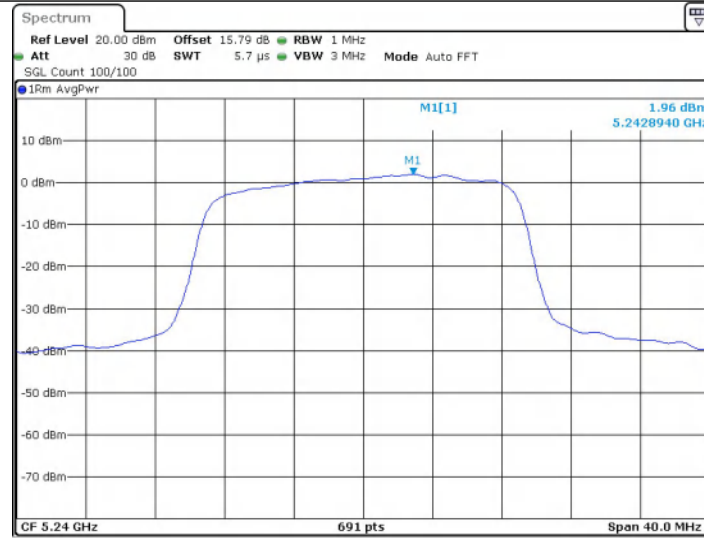
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11AC20SISO_Ant1_5200



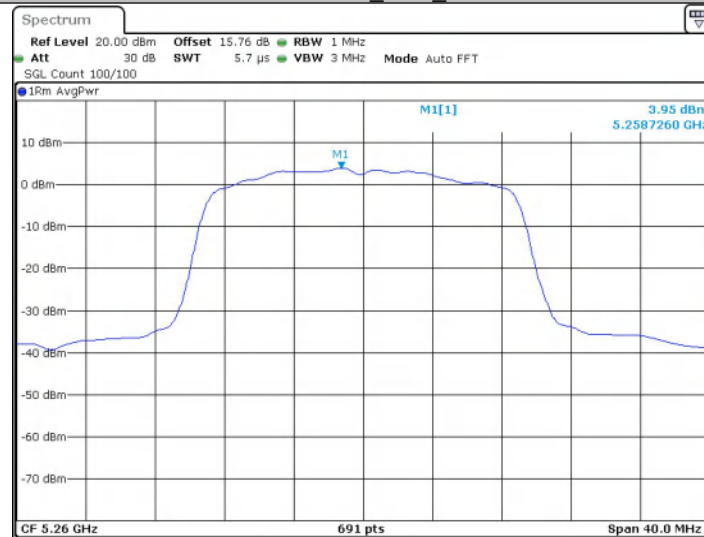
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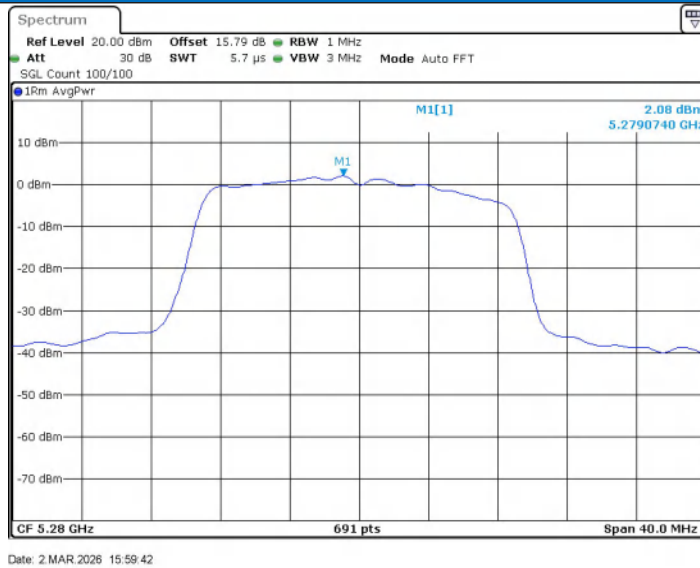
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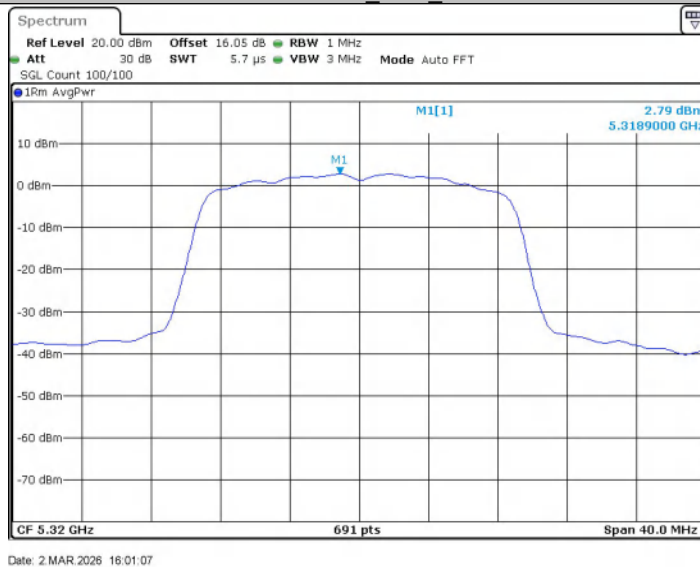


Date: 2 MAR 2026 15:58:19

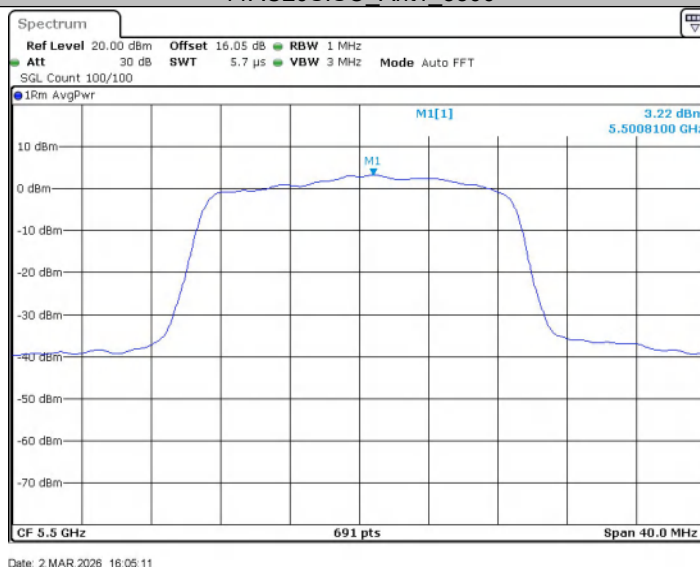
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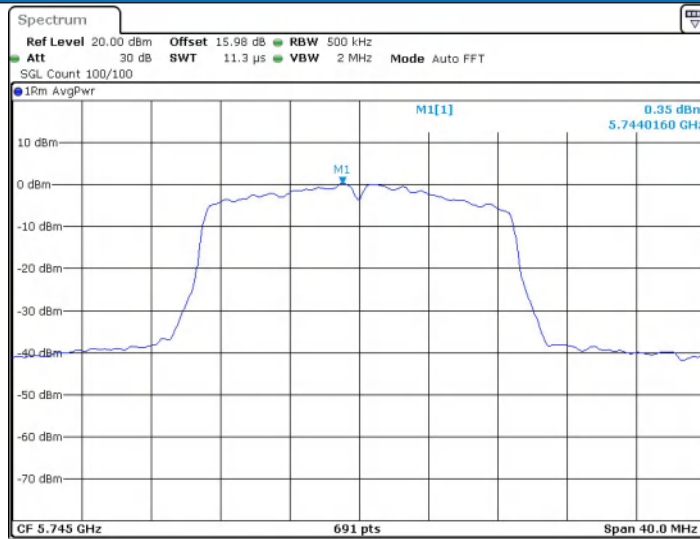


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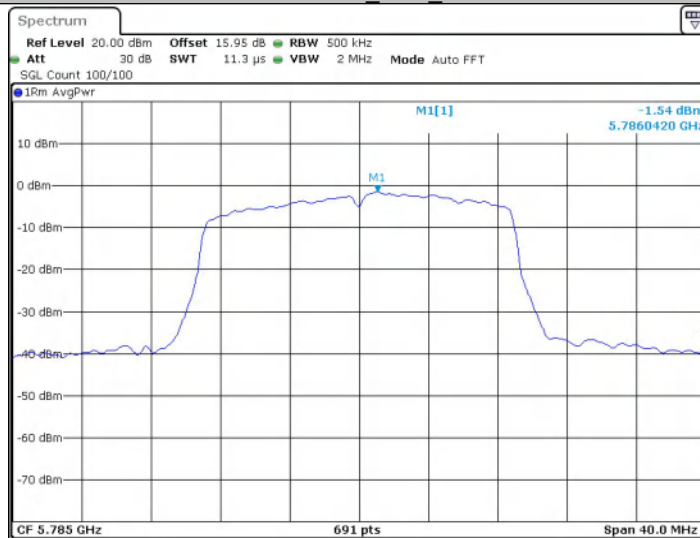
11AC20SISO_Ant1_5500





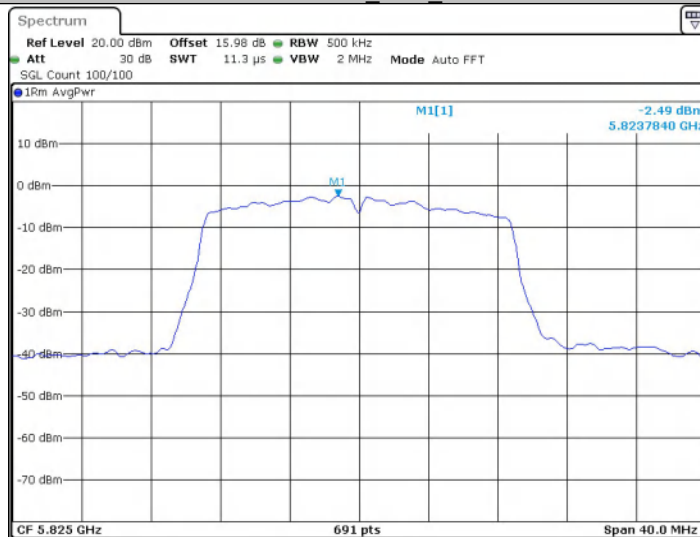
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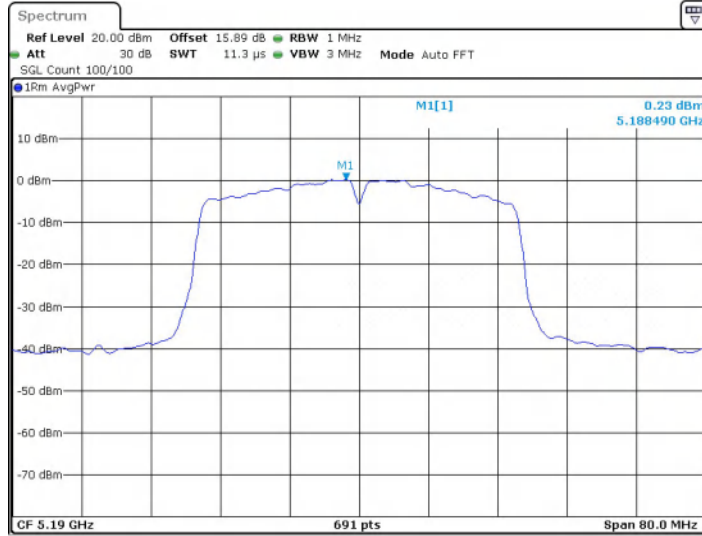
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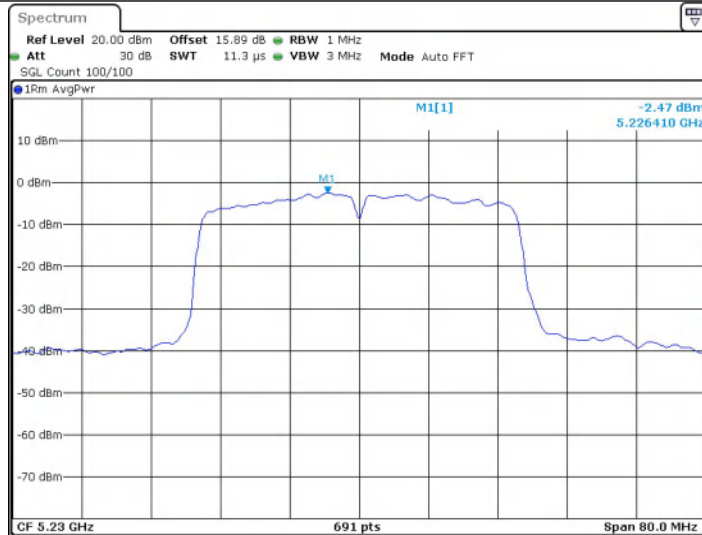


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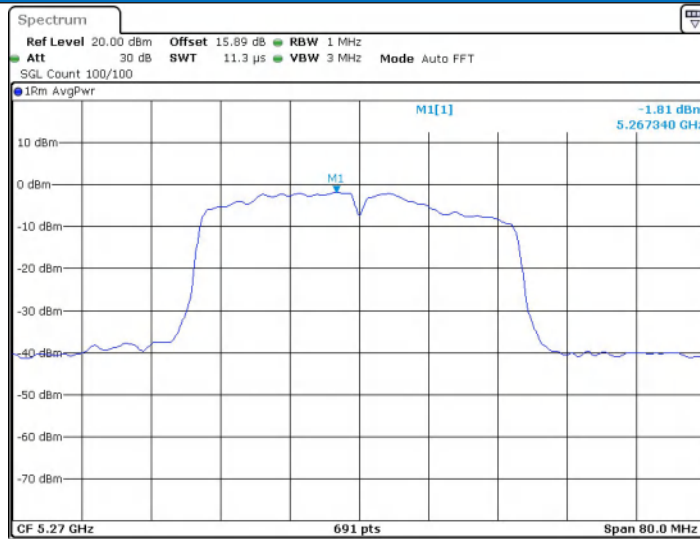
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11AC40SISO_Ant1_5230

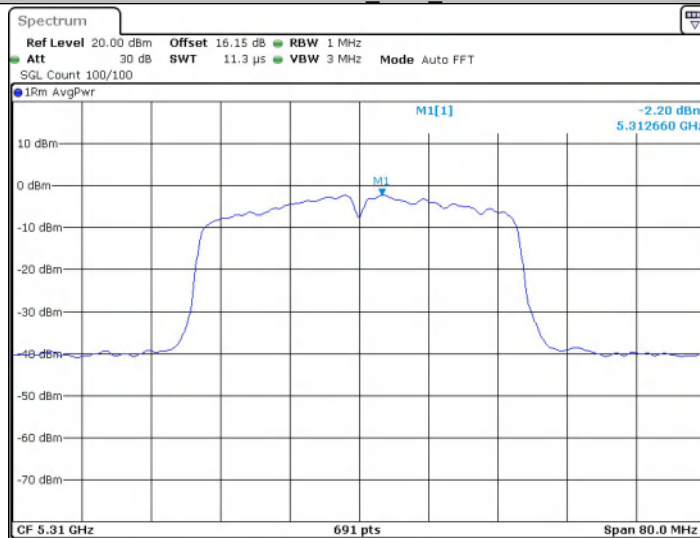


11AC40SISO_Ant1_5270



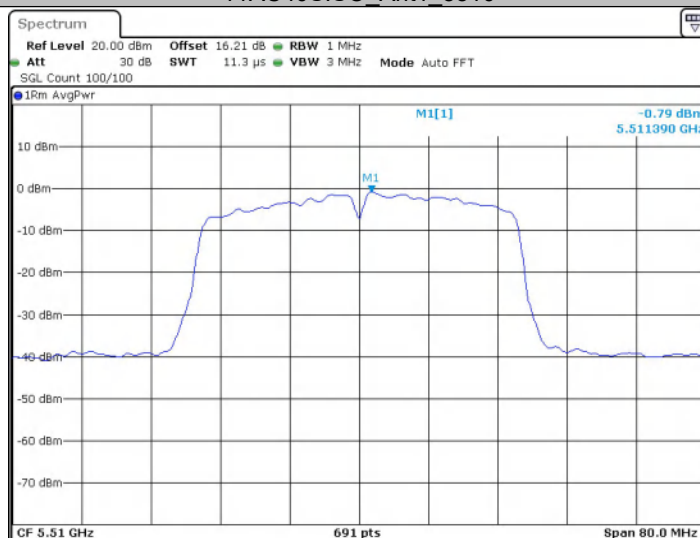
Date: 2.MAR.2026 16:47:08

11AC40SISO_Ant1_5310



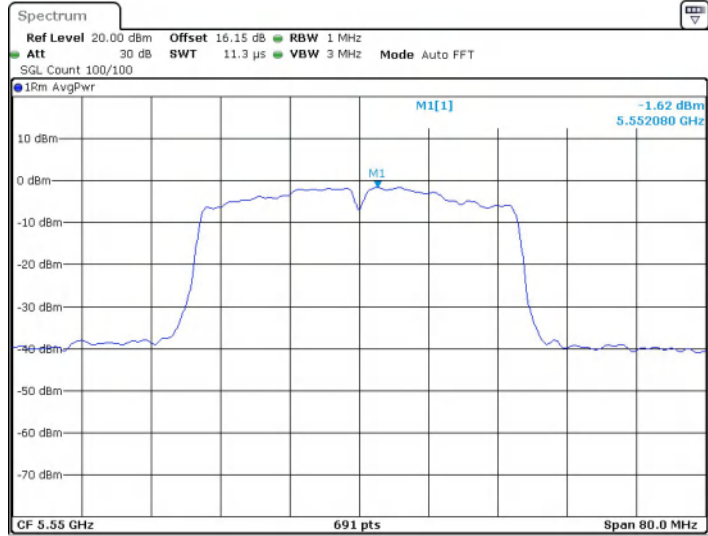
Date: 2.MAR.2026 16:48:16

11AC40SISO_Ant1_5510



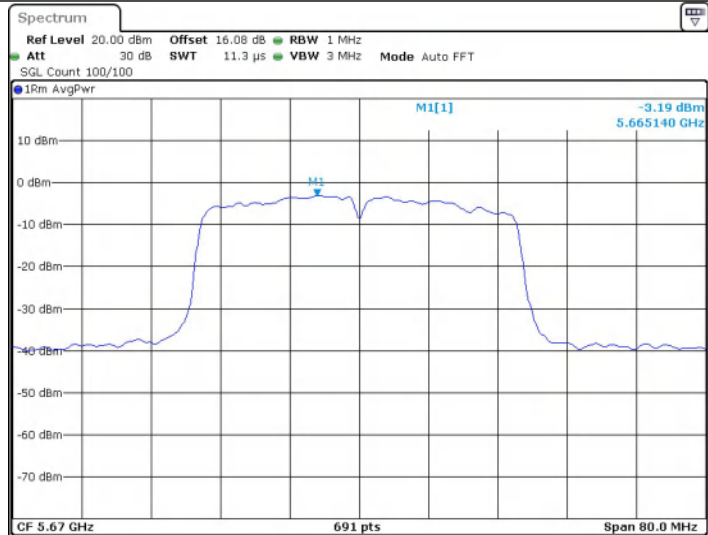
Date: 2.MAR.2026 16:49:47

11AC40SISO_Ant1_5550



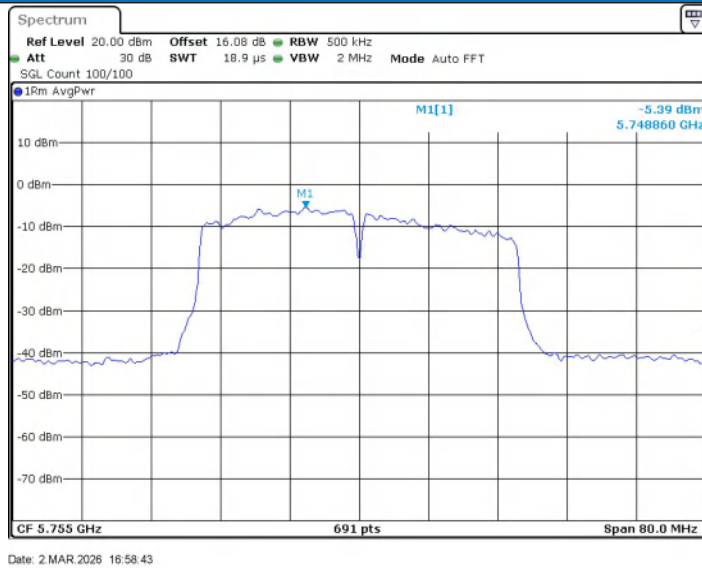
Date: 2 MAR 2026 16:51:33

11AC40SISO_Ant1_5670

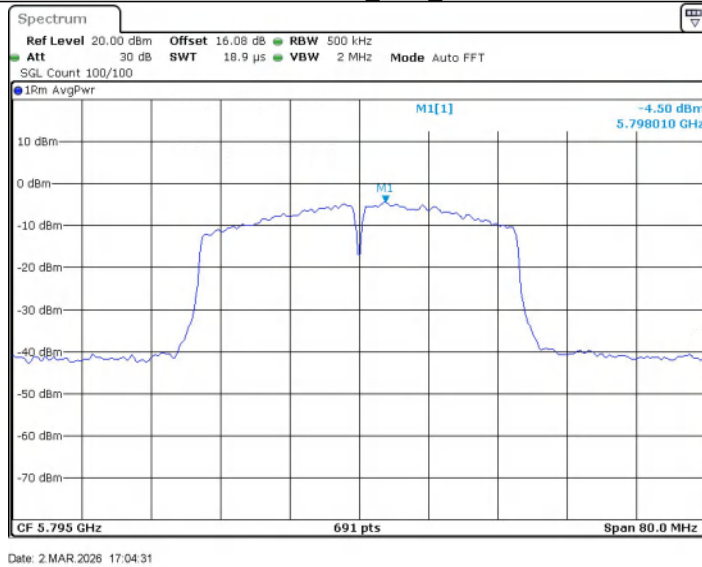


Date: 2 MAR 2026 16:53:34

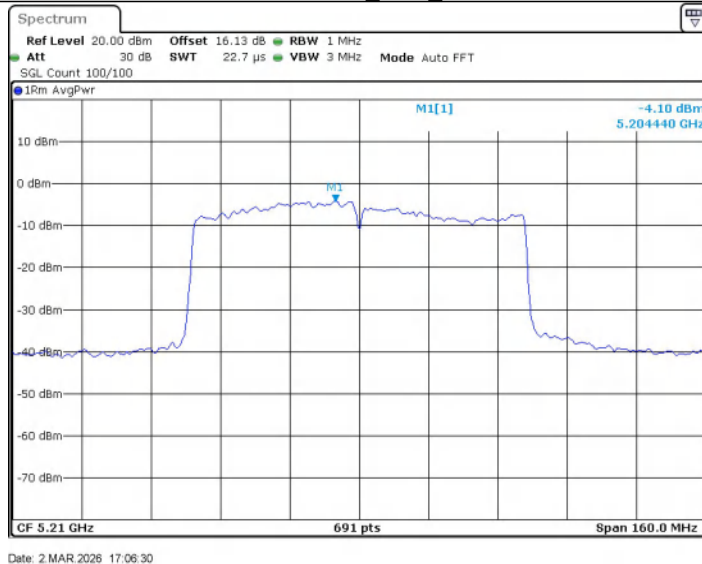
11AC40SISO_Ant1_5755



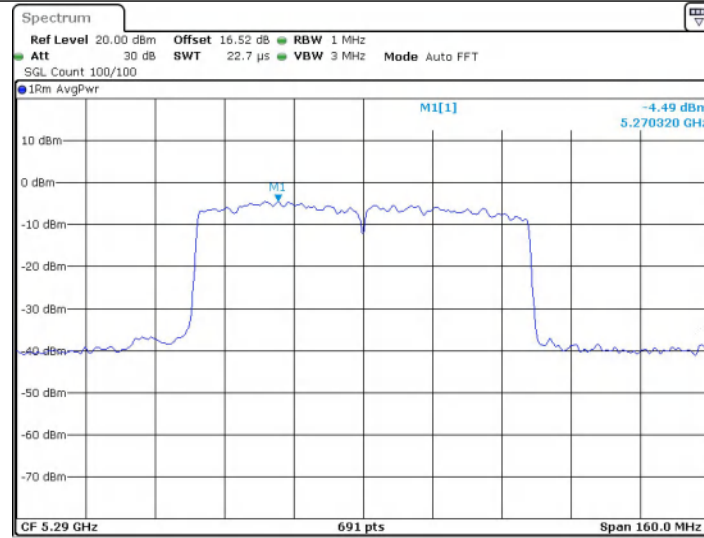
11AC40SISO_Ant1_5795



11AC80SISO_Ant1_5210

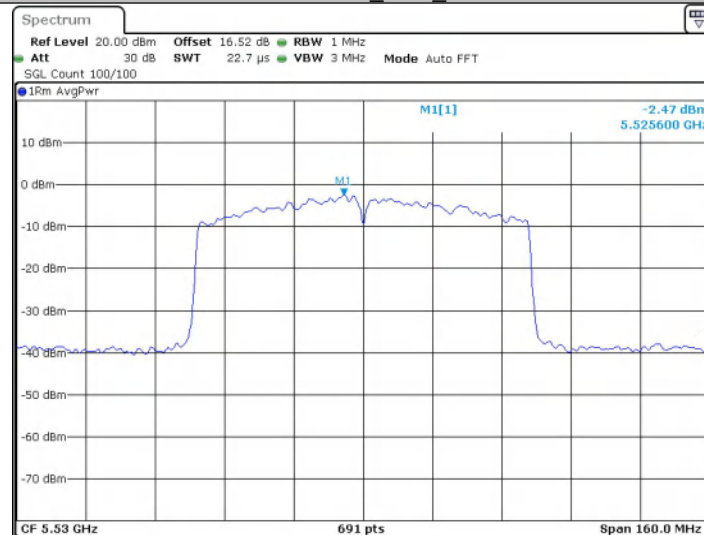


11AC80SISO_Ant1_5290



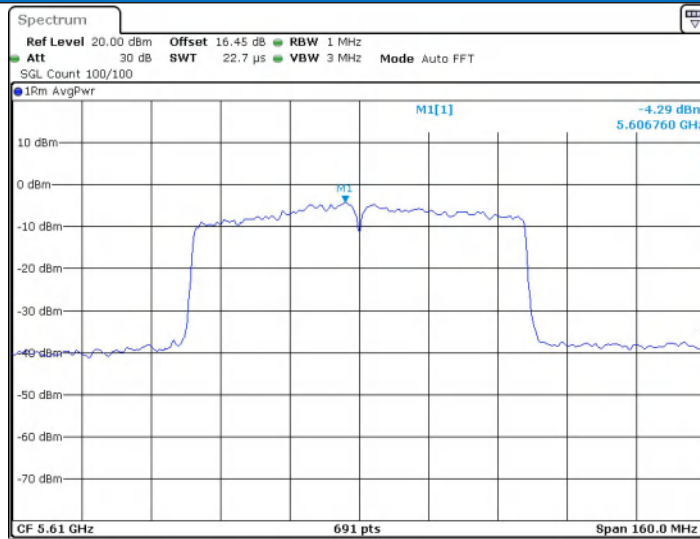
Date: 2 MAR 2026 17:15:15

11AC80SISO_Ant1_5530



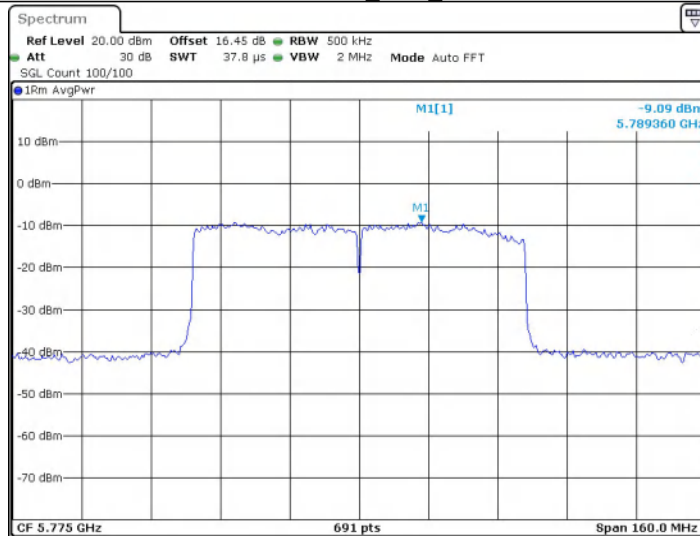
Date: 2 MAR 2026 17:17:48

11AC80SISO_Ant1_5610



Date: 2 MAR 2026 17:20:19

11AC80SISO_Ant1_5775



Date: 2 MAR 2026 17:22:26

ANT2:

Result Table

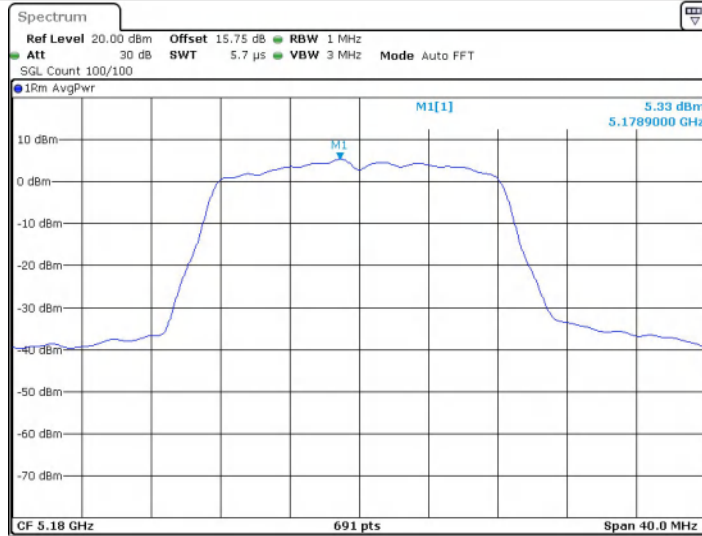
TestMode	Freq(MHz)	Result [dBm/MHz]	Limit[dBm/MHz]	Verdict
11A	5180	5.33	≤11.00	PASS
	5200	5.65	≤11.00	PASS
	5240	3.04	≤11.00	PASS
	5260	5.46	≤11.00	PASS
	5280	2.54	≤11.00	PASS
	5320	4.35	≤11.00	PASS
	5500	5.10	≤11.00	PASS
	5580	2.83	≤11.00	PASS
	5700	2.41	≤11.00	PASS
	5745	1.20	≤30.00	PASS
	5785	-0.42	≤30.00	PASS
	5825	-1.29	≤30.00	PASS
11N20SISO	5180	4.53	≤11.00	PASS
	5200	5.22	≤11.00	PASS
	5240	3.42	≤11.00	PASS
	5260	5.02	≤11.00	PASS
	5280	3.28	≤11.00	PASS
	5320	3.76	≤11.00	PASS
	5500	4.54	≤11.00	PASS
	5580	2.92	≤11.00	PASS
	5700	1.74	≤11.00	PASS
	5745	1.43	≤30.00	PASS
	5785	-0.79	≤30.00	PASS
	5825	-1.80	≤30.00	PASS
11N40SISO	5190	2.62	≤11.00	PASS
	5230	-1.42	≤11.00	PASS
	5270	1.66	≤11.00	PASS
	5310	0.51	≤11.00	PASS
	5510	1.89	≤11.00	PASS
	5550	1.41	≤11.00	PASS
	5670	-0.73	≤11.00	PASS
	5755	-3.58	≤30.00	PASS
	5795	-3.02	≤30.00	PASS
	5825	-1.80	≤30.00	PASS
11AC20SISO	5180	3.90	≤11.00	PASS

		5200	4.45	≤ 11.00	PASS
		5240	2.46	≤ 11.00	PASS
		5260	4.80	≤ 11.00	PASS
		5280	1.88	≤ 11.00	PASS
		5320	3.59	≤ 11.00	PASS
		5500	4.05	≤ 11.00	PASS
		5580	1.72	≤ 11.00	PASS
		5700	0.96	≤ 11.00	PASS
		5745	0.58	≤ 30.00	PASS
		5785	-1.25	≤ 30.00	PASS
		5825	-2.18	≤ 30.00	PASS
	11AC40SISO	5190	0.76	≤ 11.00	PASS
		5230	-2.34	≤ 11.00	PASS
		5270	0.23	≤ 11.00	PASS
		5310	-0.90	≤ 11.00	PASS
		5510	0.81	≤ 11.00	PASS
		5550	0.42	≤ 11.00	PASS
		5670	-1.01	≤ 11.00	PASS
		5755	-4.61	≤ 30.00	PASS
		5795	-3.48	≤ 30.00	PASS
	11AC80SISO	5210	-3.15	≤ 11.00	PASS
		5290	-4.73	≤ 11.00	PASS
		5530	-2.04	≤ 11.00	PASS
		5610	-3.67	≤ 11.00	PASS
		5775	-8.63	≤ 30.00	PASS

Remark:

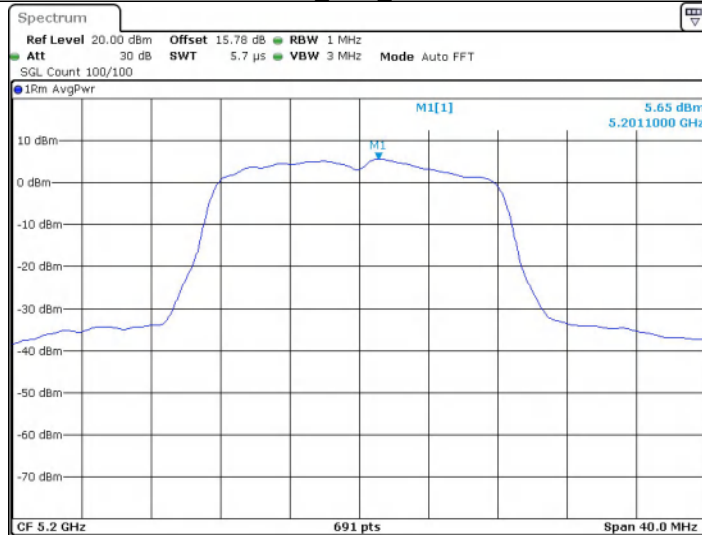
PSD = Meas PSD + Duty Cycle Factor

11A_Ant2_5180



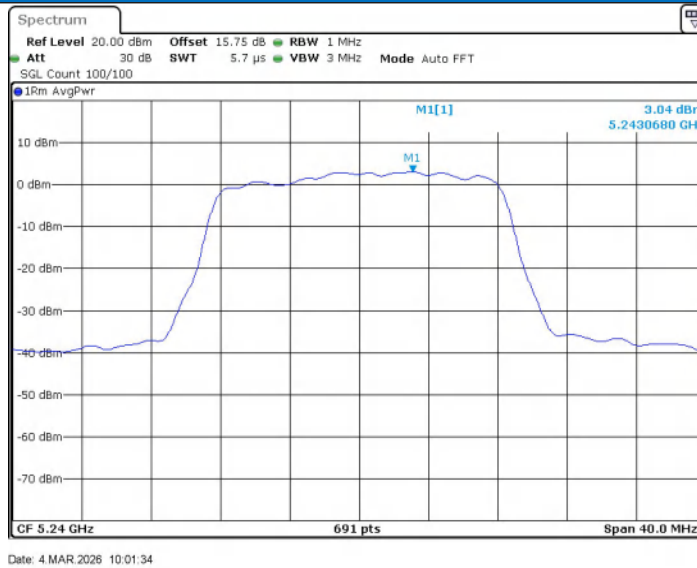
Date: 4 MAR 2026 09:56:54

11A_Ant2_5200

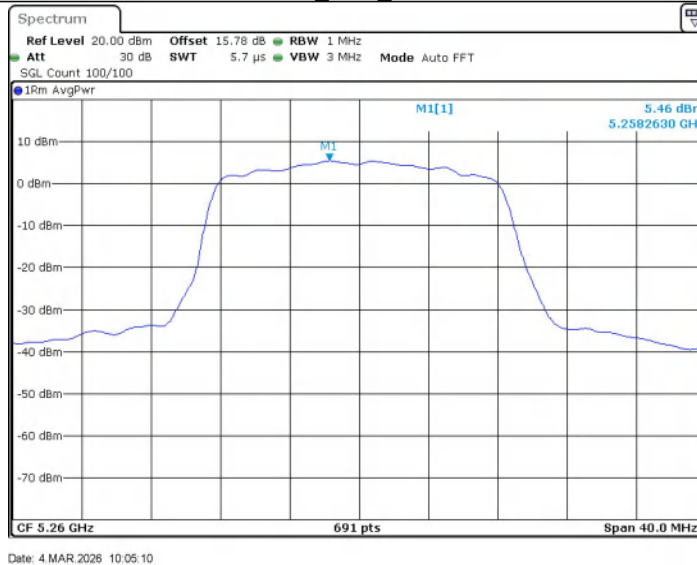


Date: 4 MAR 2026 09:59:35

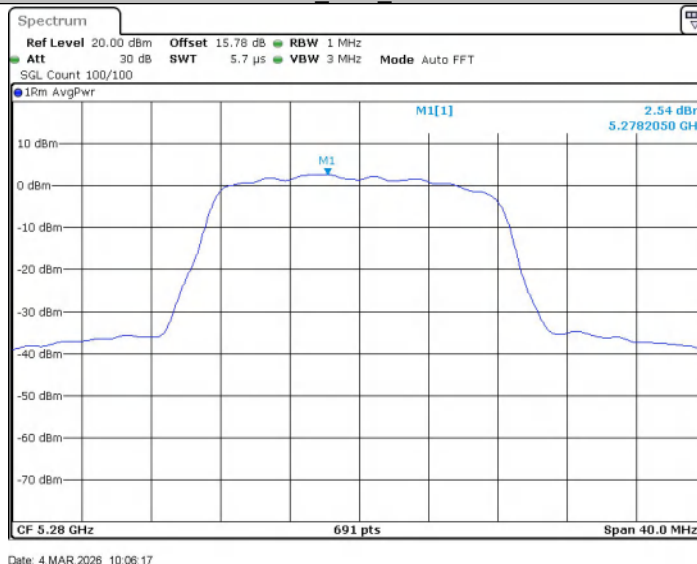
11A_Ant2_5240



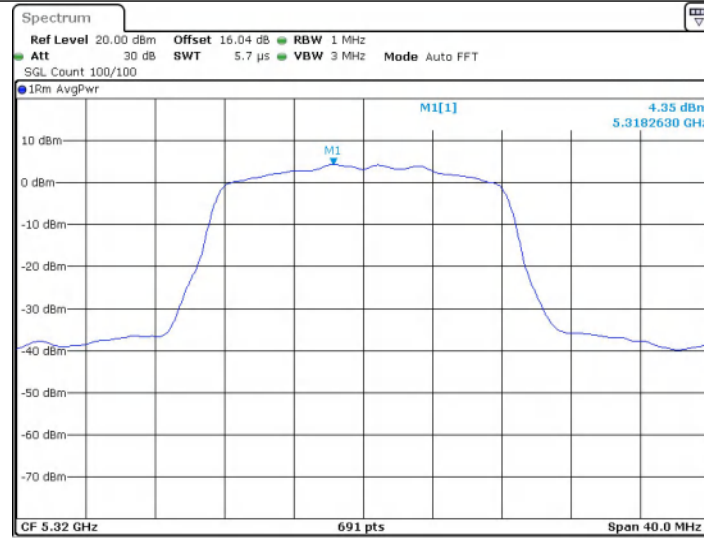
11A_Ant2_5260



11A_Ant2_5280

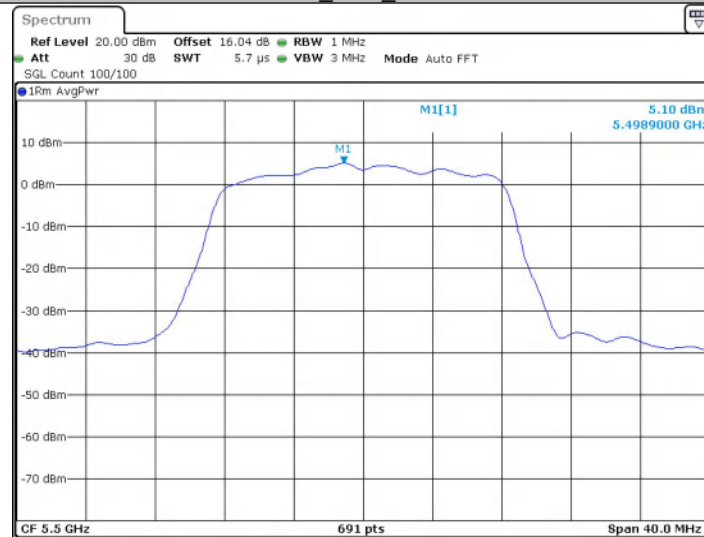


11A_Ant2_5320



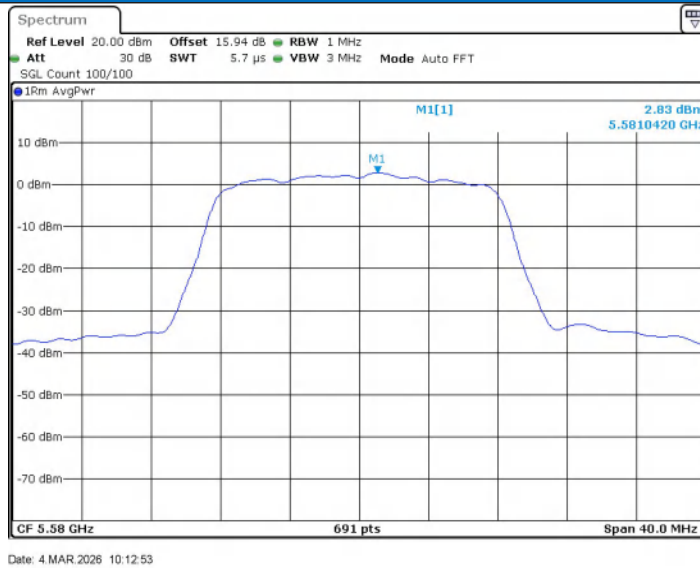
Date: 4 MAR 2026 10:09:22

11A_Ant2_5500

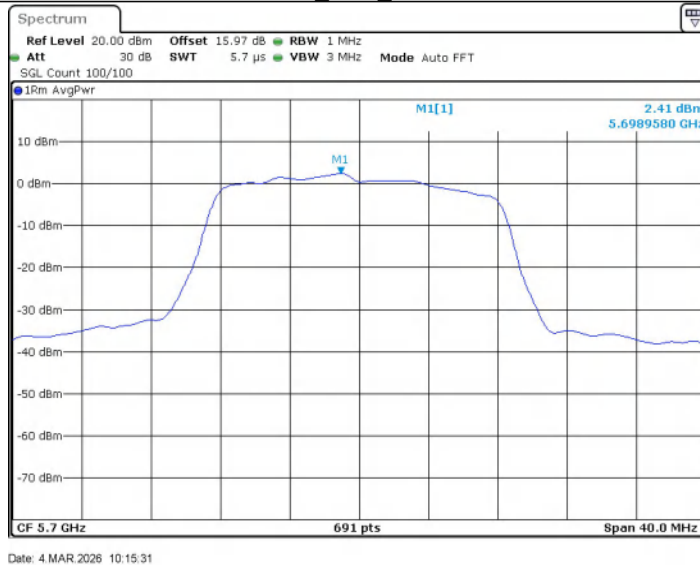


Date: 4 MAR 2026 10:11:25

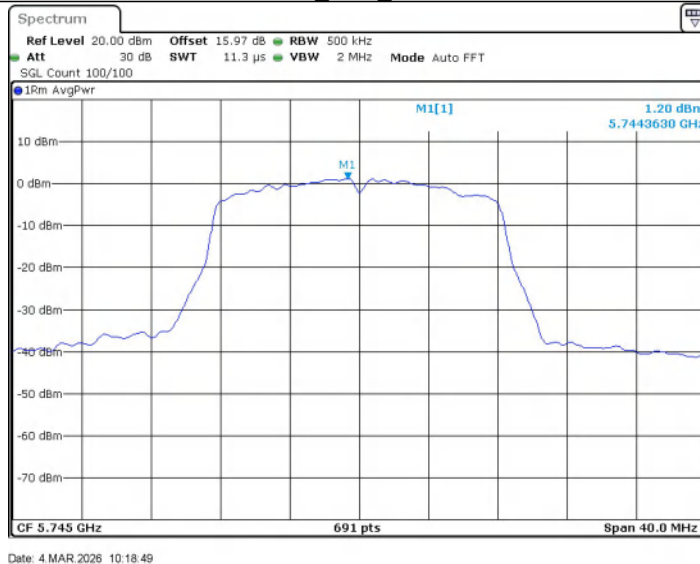
11A_Ant2_5580

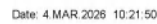


11A_Ant2_5700

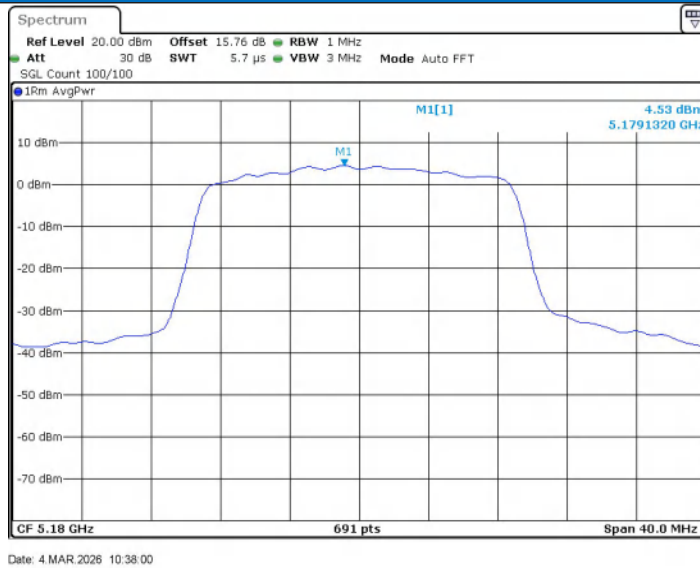


11A_Ant2_5745

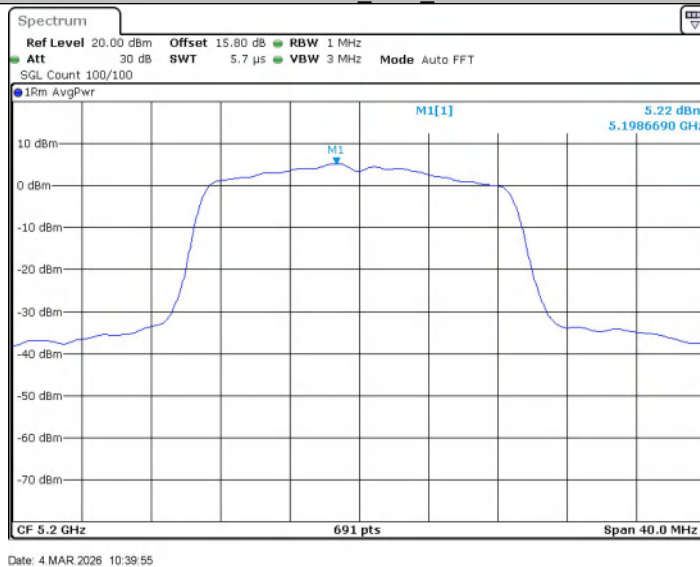




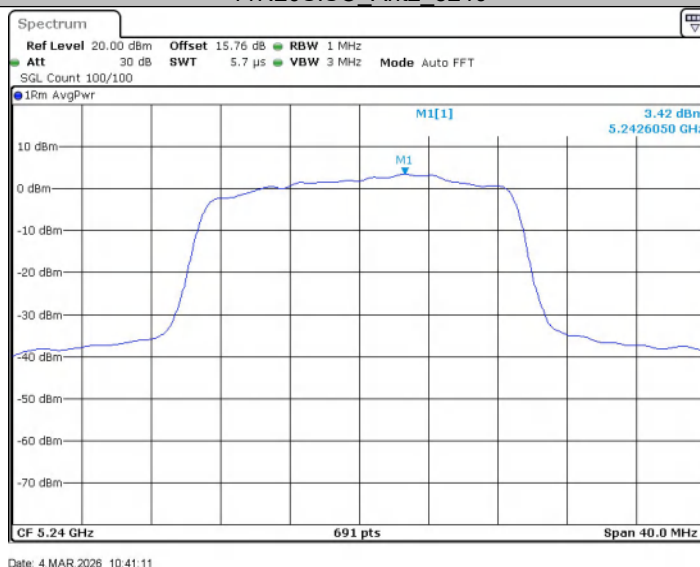
Page:200 of 289



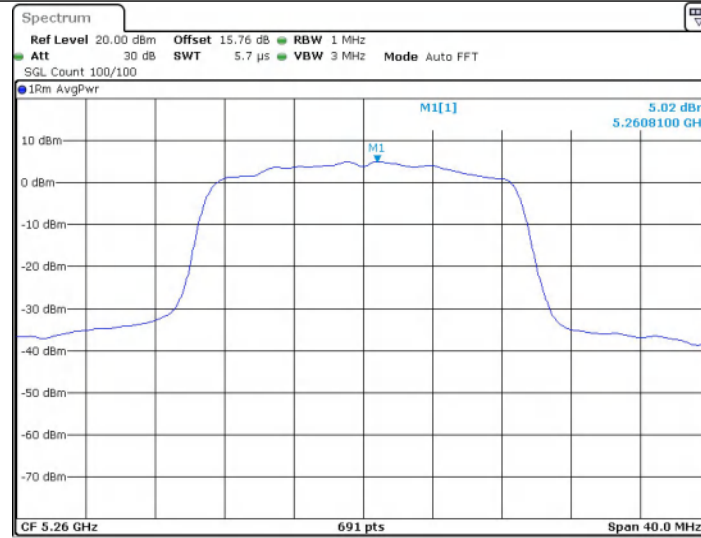
11N20SISO_Ant2_5200



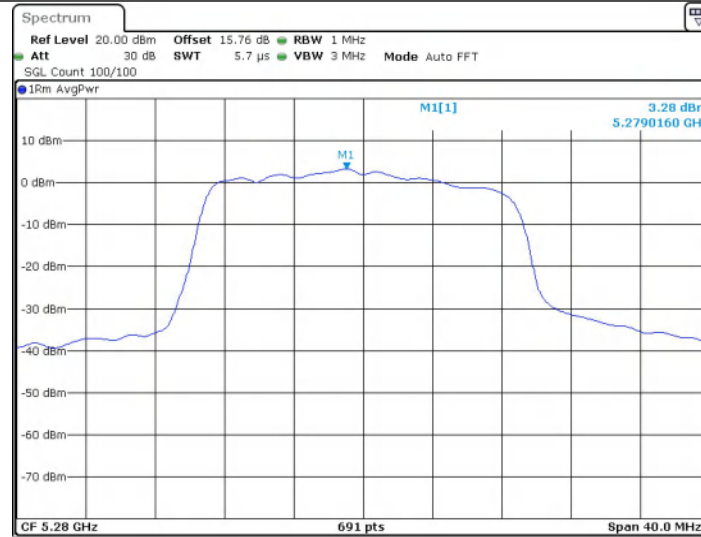
11N20SISO_Ant2_5240



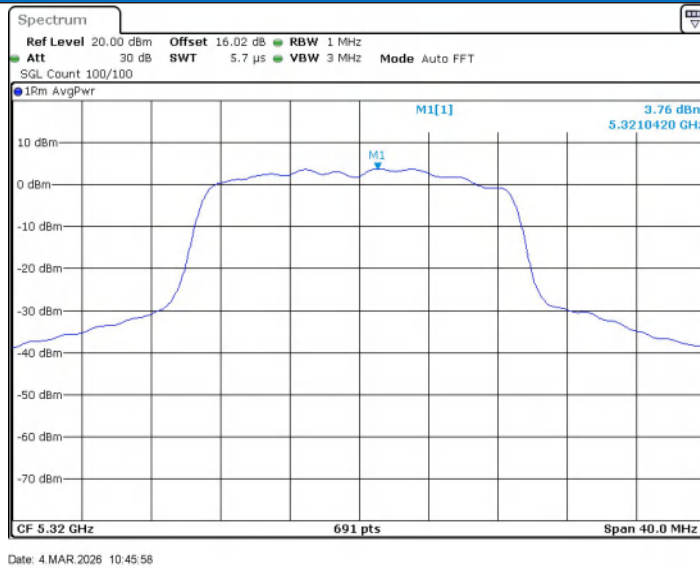
11N20SISO_Ant2_5260



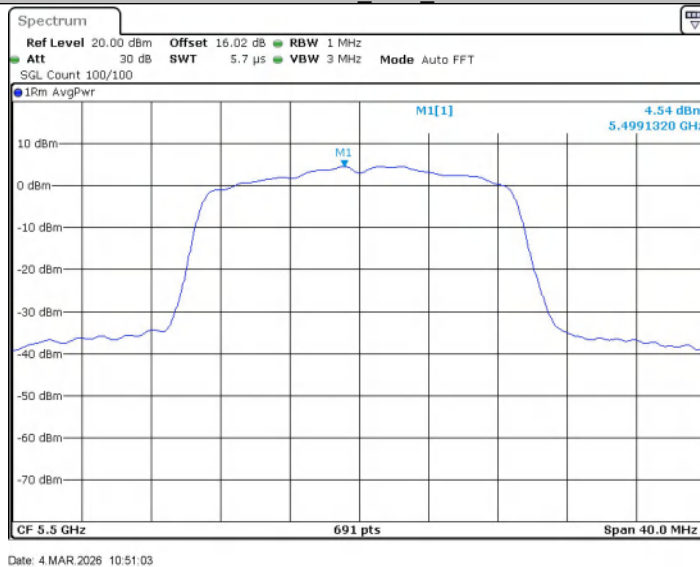
11N20SISO_Ant2_5280



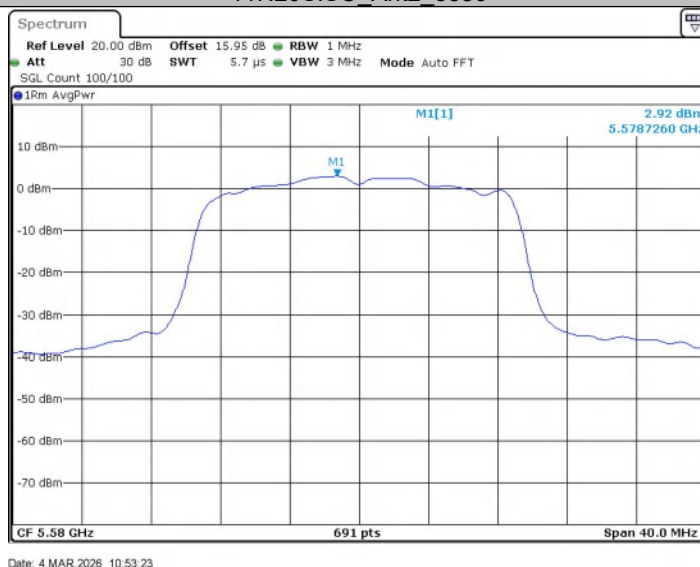
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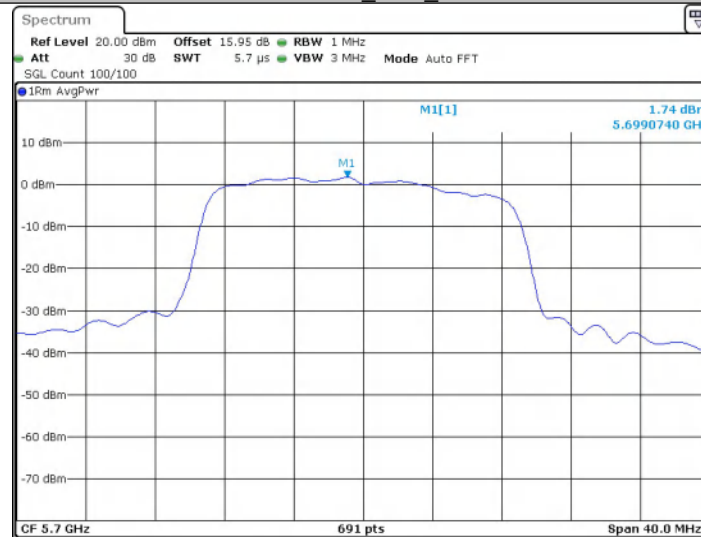
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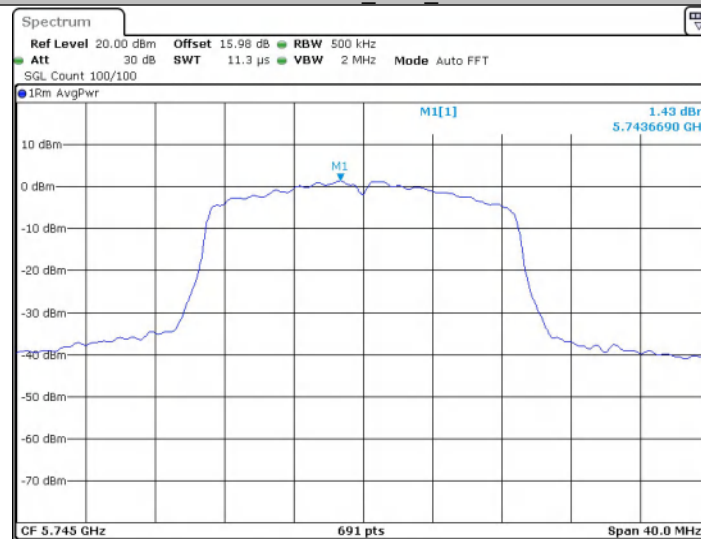
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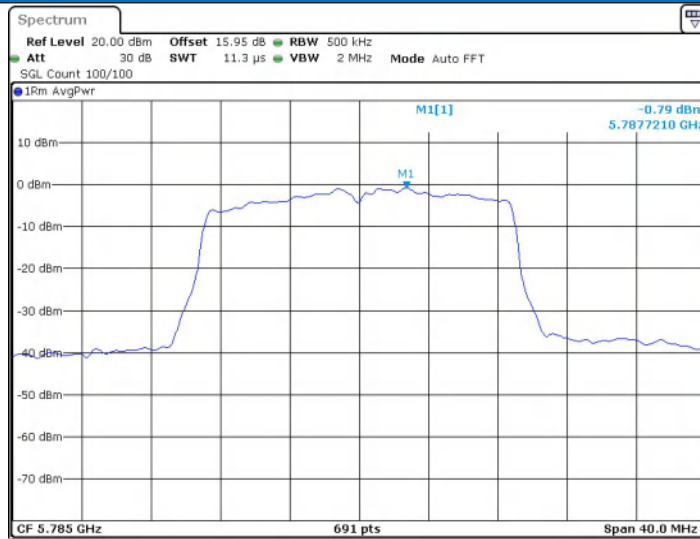
11N20SISO_Ant2_5700



11N20SISO_Ant2_5745

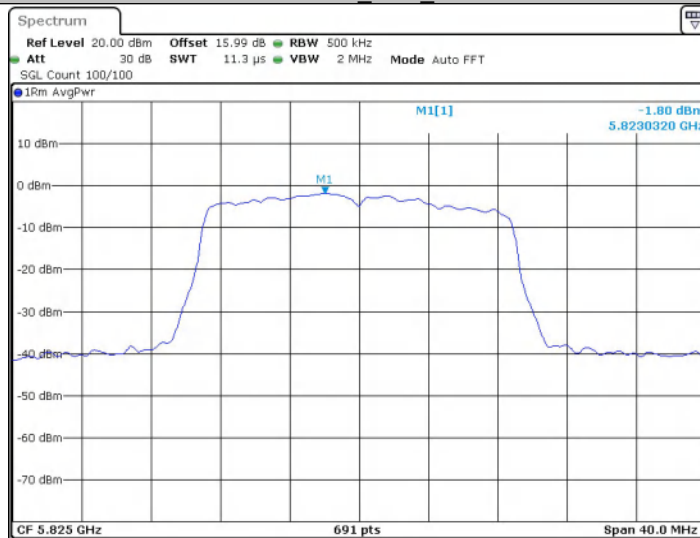


11N20SISO_Ant2_5785



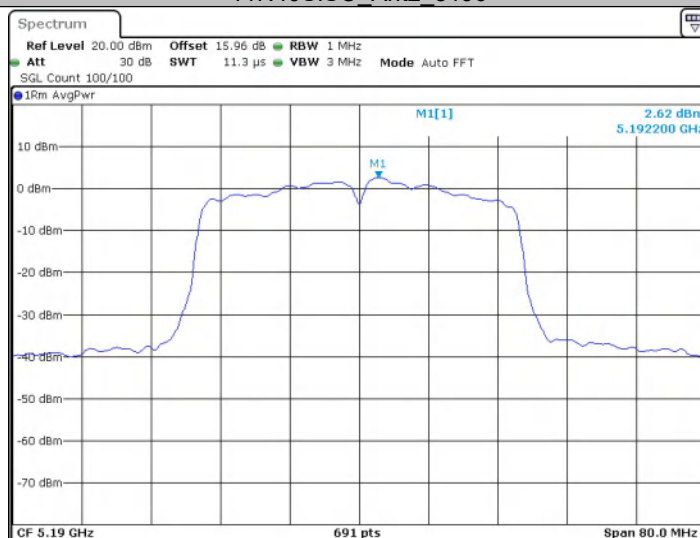
Date: 4 MAR 2026 10:58:33

11N20SISO_Ant2_5825



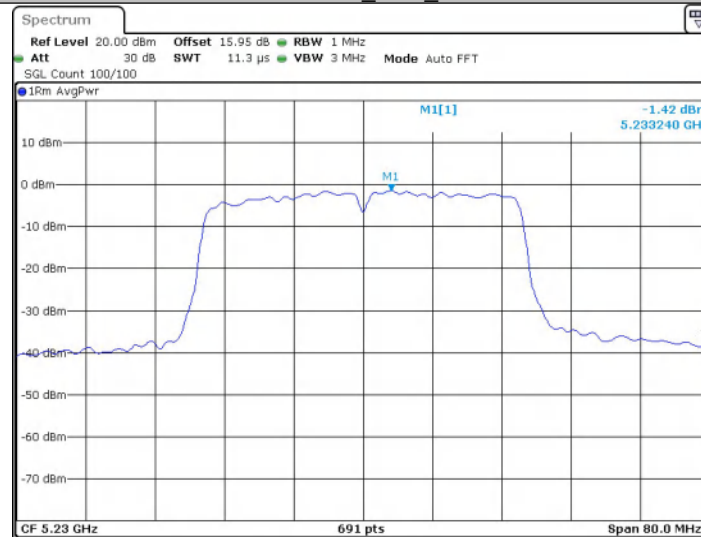
Date: 4 MAR 2026 11:00:22

11N40SISO_Ant2_5190



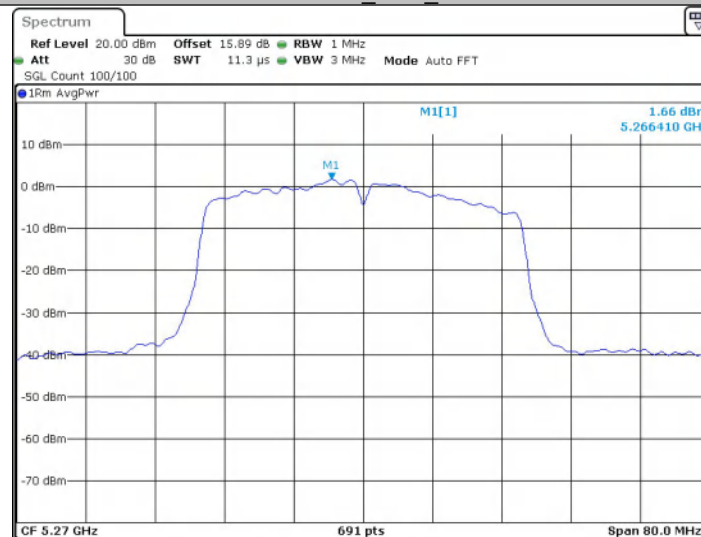
Date: 4 MAR 2026 11:03:16

11N40SISO_Ant2_5230



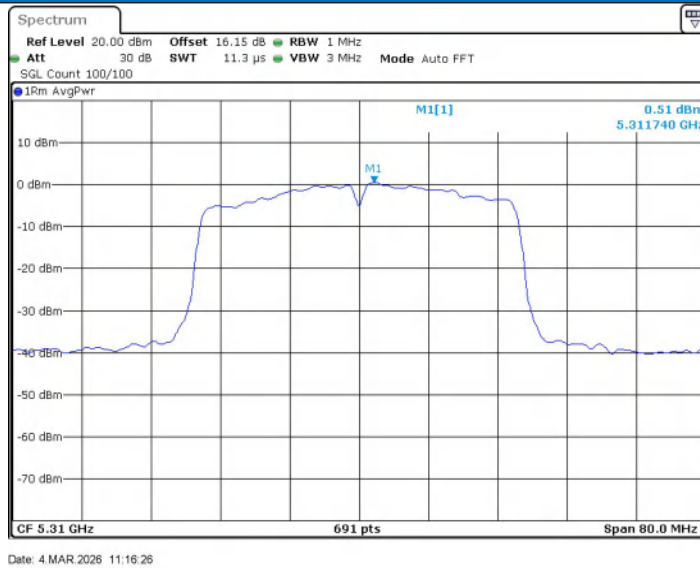
Date: 4 MAR 2026 11:07:56

11N40SISO_Ant2_5270

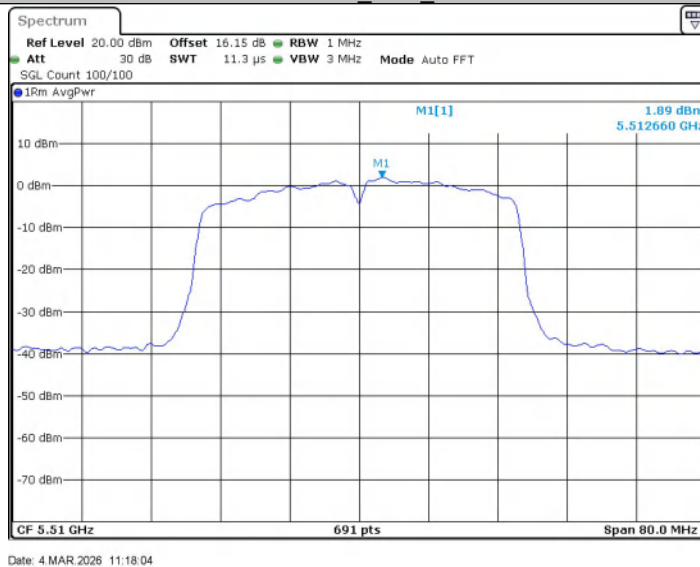


Date: 4 MAR 2026 11:09:04

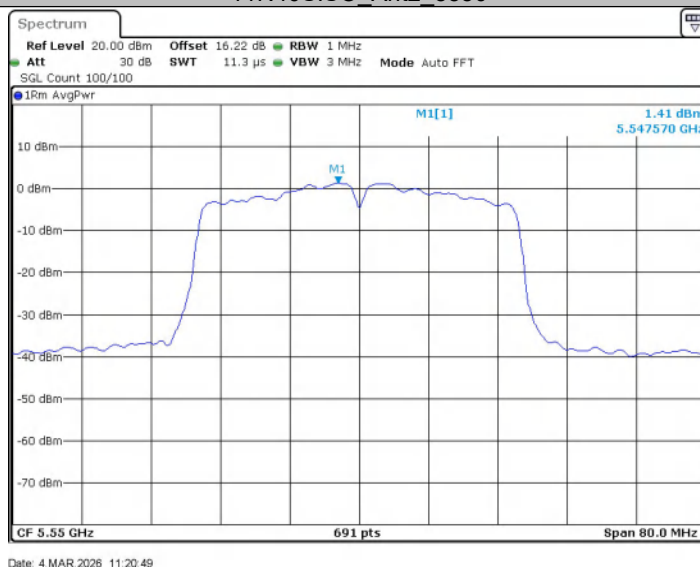
11N40SISO_Ant2_5310



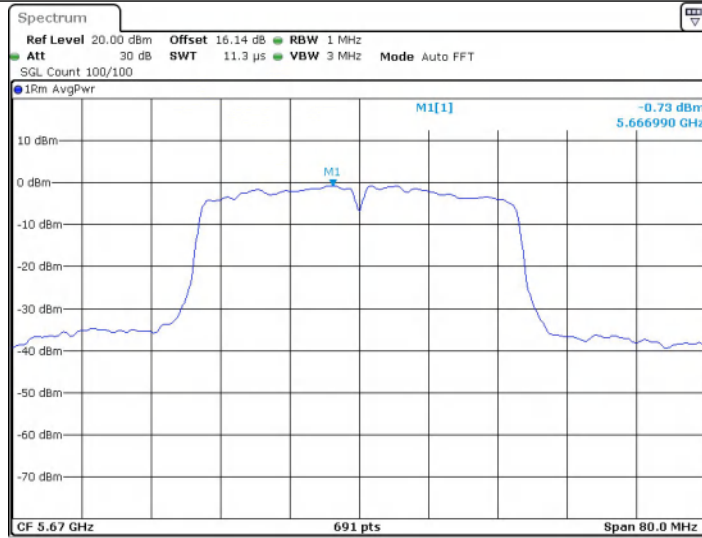
11N40SISO_Ant2_5510



11N40SISO_Ant2_5550

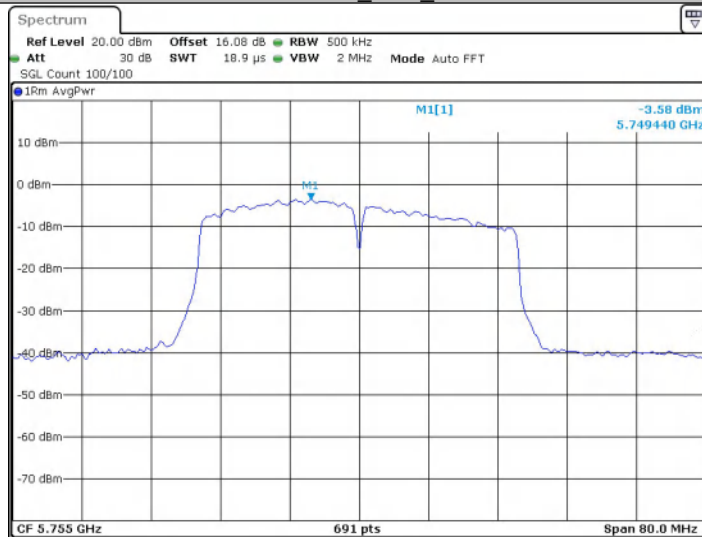


11N40SISO_Ant2_5670



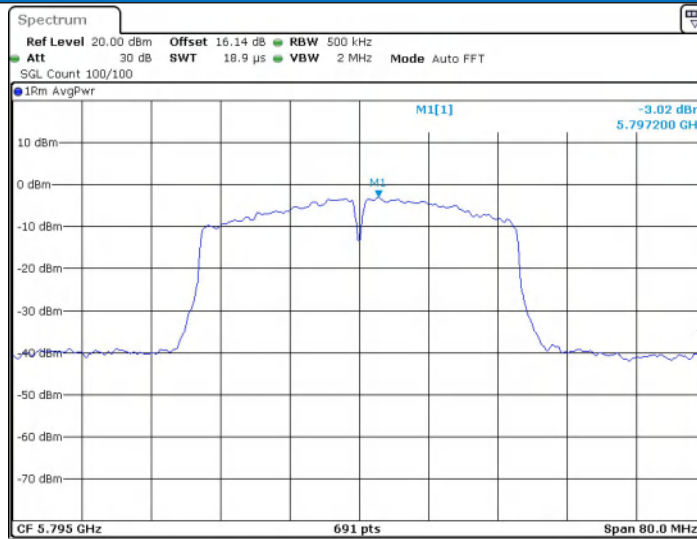
Date: 4 MAR 2026 11:21:54

11N40SISO_Ant2_5755



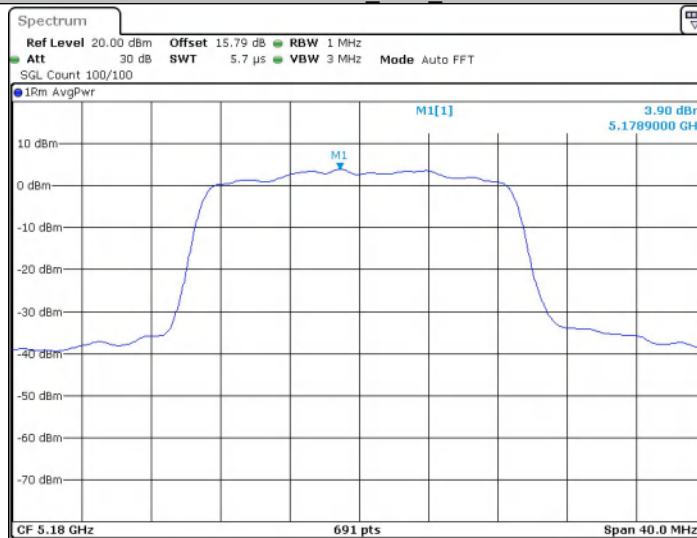
Date: 4 MAR 2026 11:23:27

11N40SISO_Ant2_5795



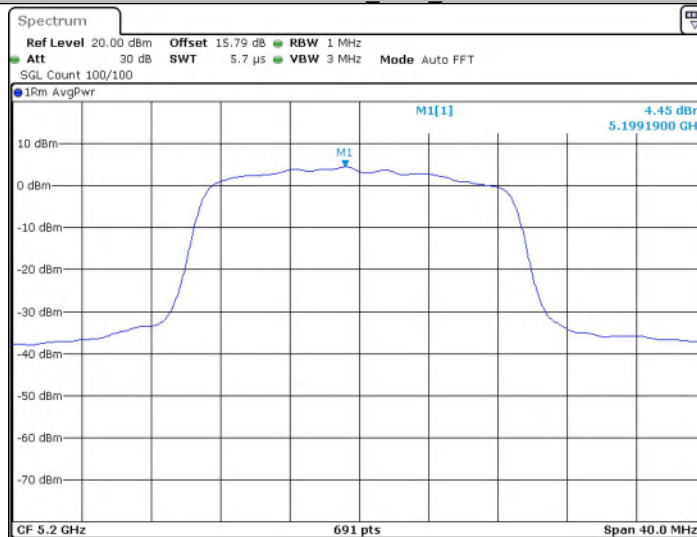
Date: 4 MAR 2026 11:25:11

11AC20SISO_Ant2_5180



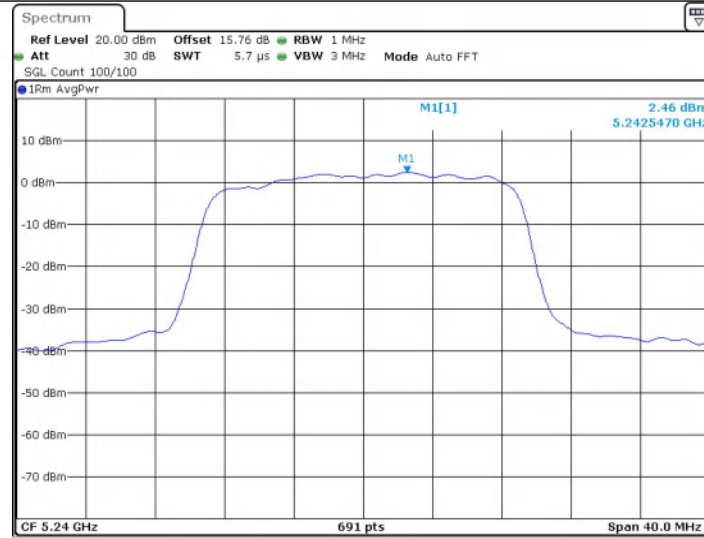
Date: 4 MAR 2026 11:26:43

11AC20SISO_Ant2_5200



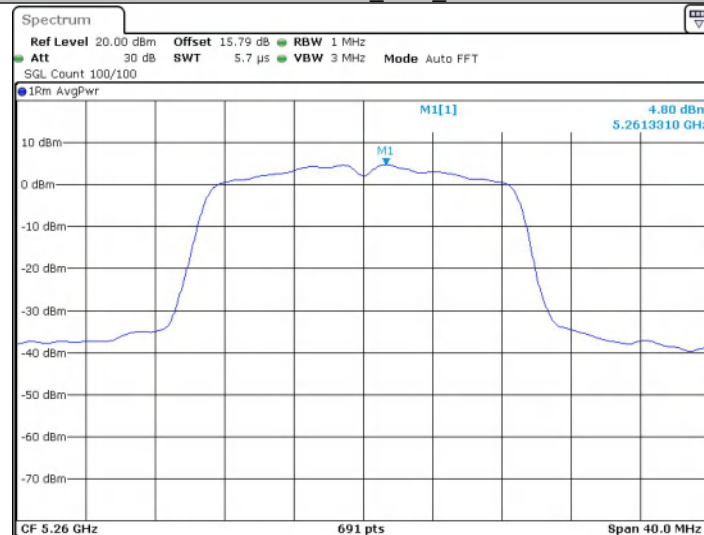
Date: 4 MAR 2026 11:28:54

11AC20SISO_Ant2_5240



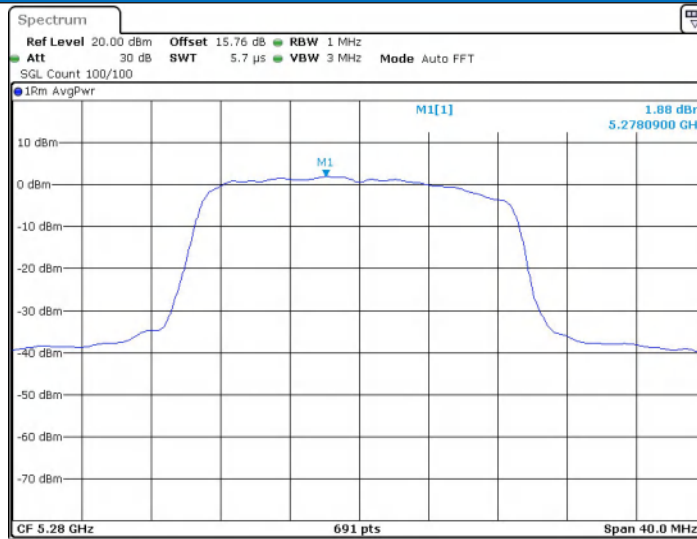
Date: 4 MAR 2026 11:30:06

11AC20SISO_Ant2_5260



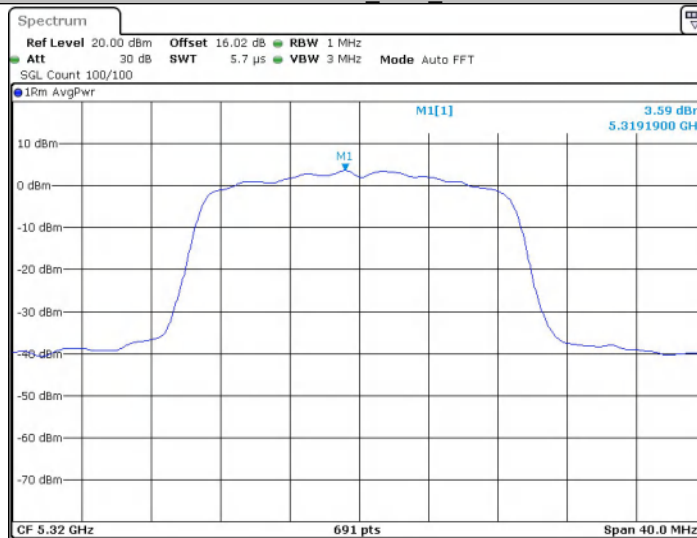
Date: 4 MAR 2026 11:34:50

11AC20SISO_Ant2_5280



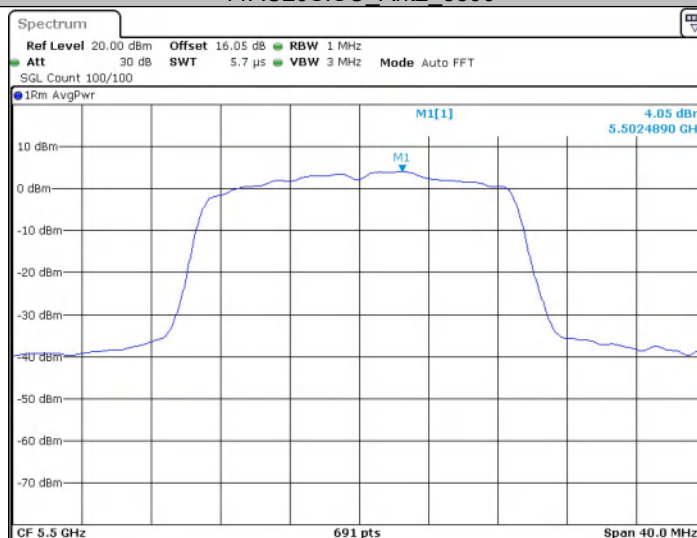
Date: 4 MAR 2026 11:35:54

11AC20SISO_Ant2_5320



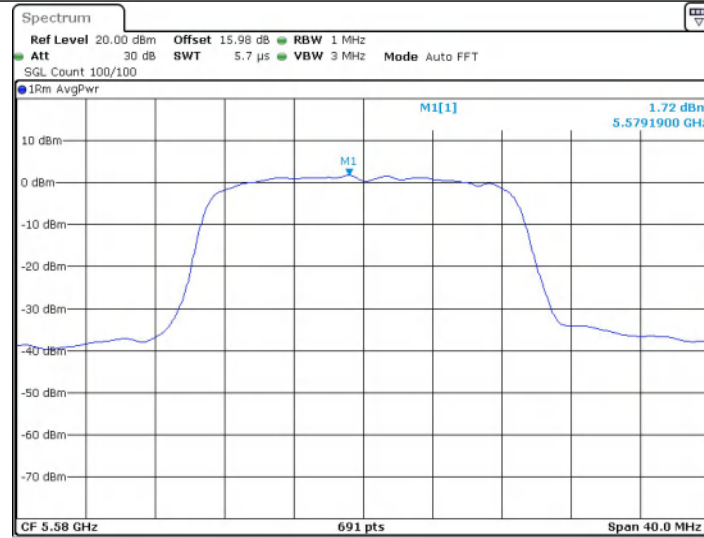
Date: 4 MAR 2026 11:39:27

11AC20SISO_Ant2_5500



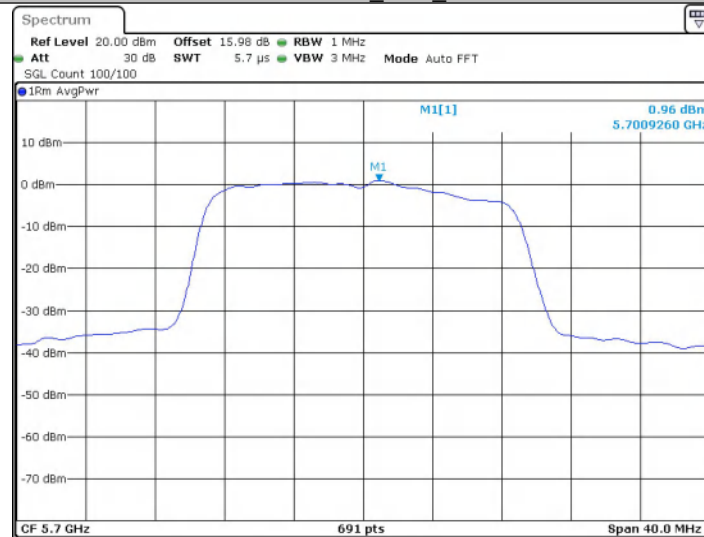
Date: 4 MAR 2026 11:41:29

11AC20SISO_Ant2_5580



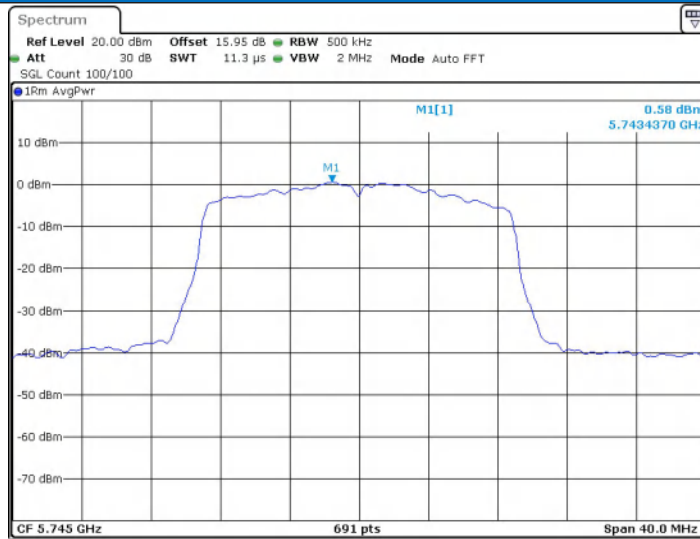
Date: 4 MAR 2026 11:43:14

11AC20SISO_Ant2_5700



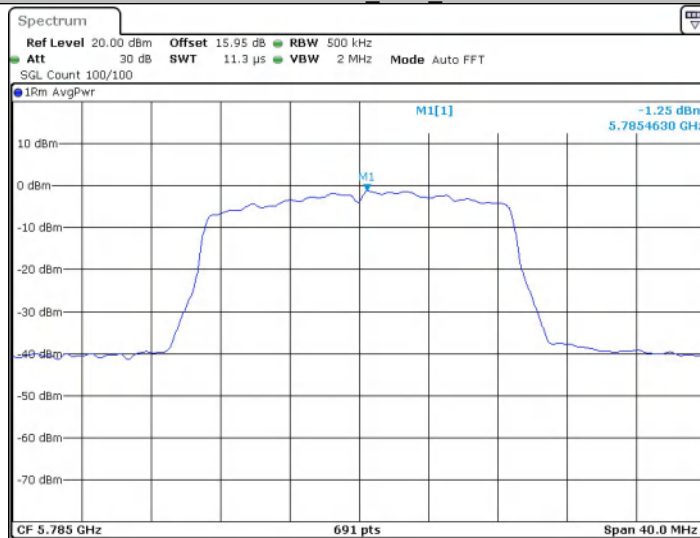
Date: 4 MAR 2026 11:44:16

11AC20SISO_Ant2_5745



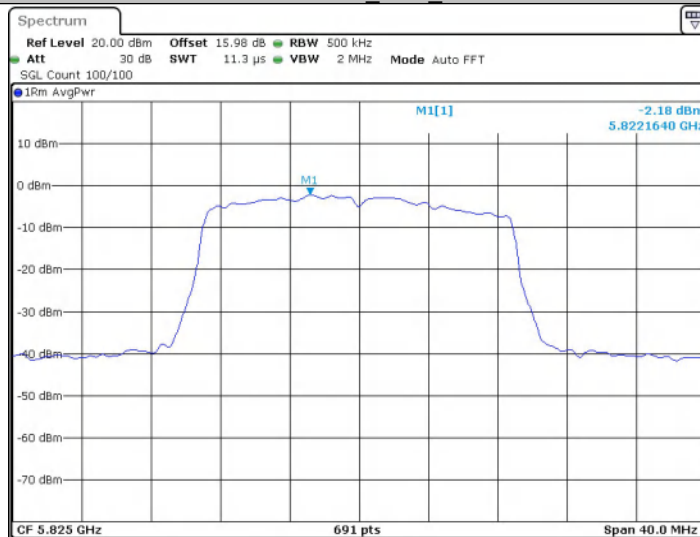
Date: 4 MAR 2026 11:46:25

11AC20SISO_Ant2_5785



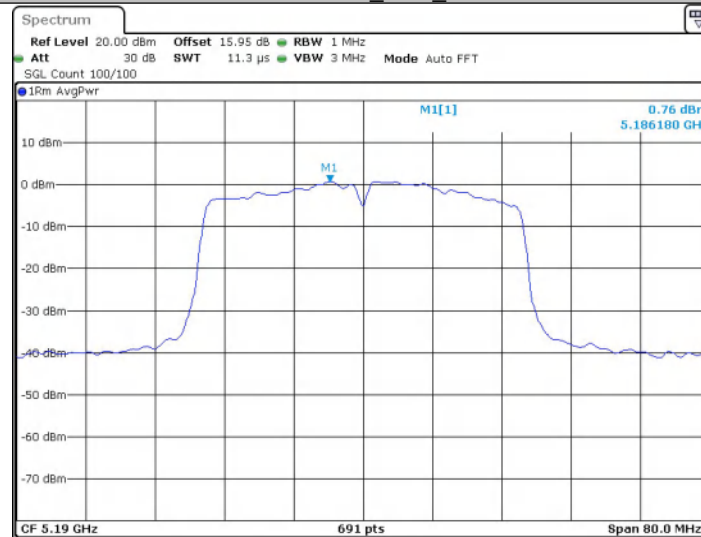
Date: 4 MAR 2026 11:50:13

11AC20SISO_Ant2_5825

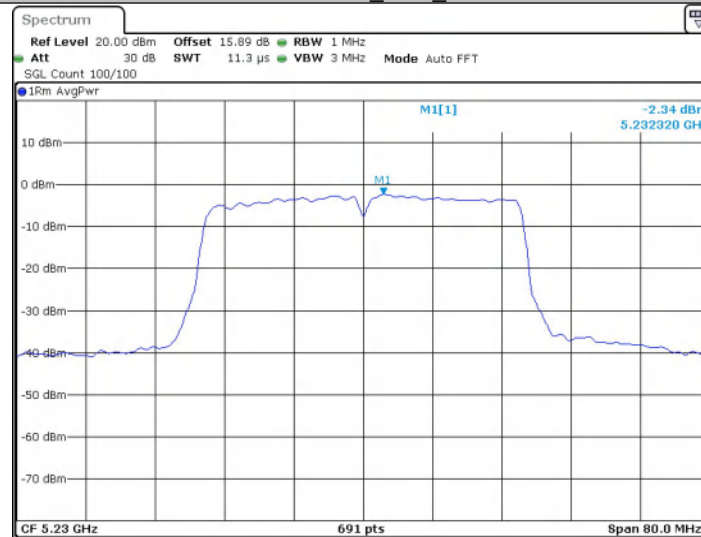


Date: 4 MAR 2026 11:51:45

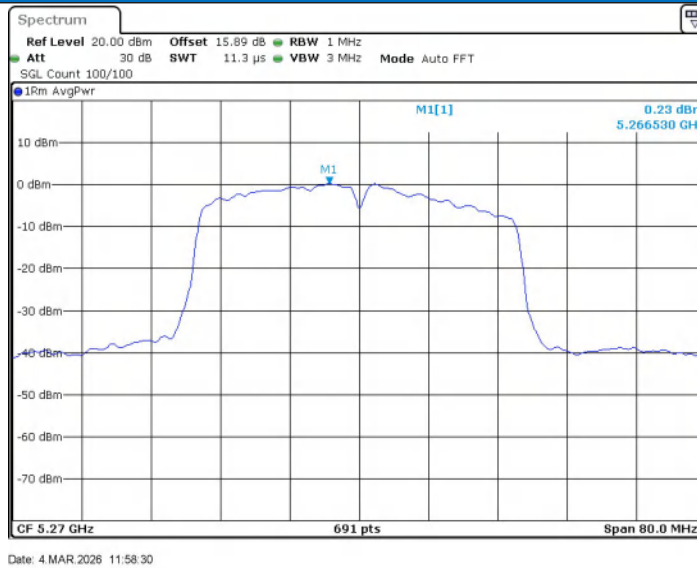
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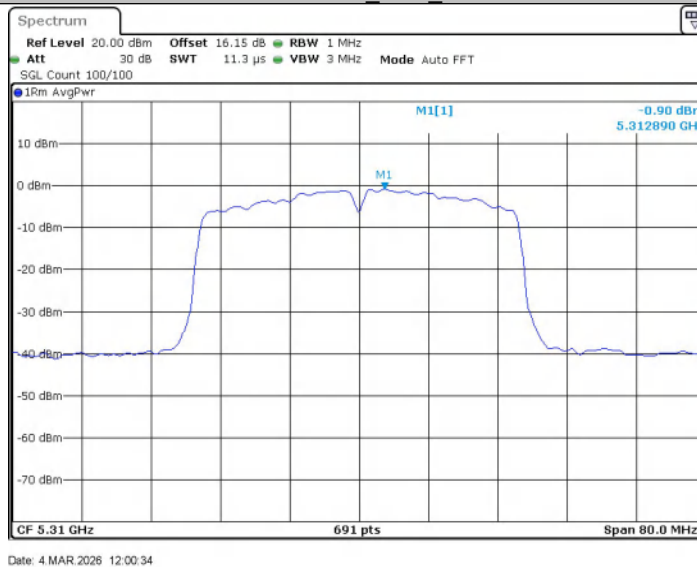
11AC40SISO_Ant2_5230



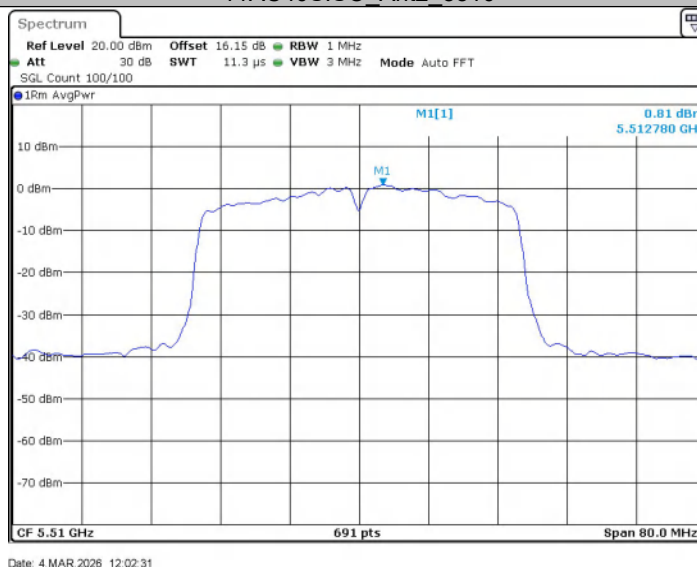
11AC40SISO_Ant2_5270



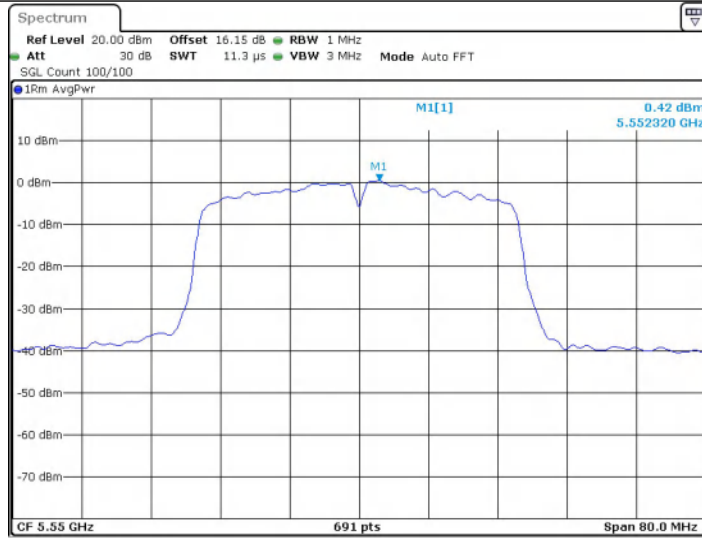
11AC40SISO_Ant2_5310



11AC40SISO_Ant2_5510

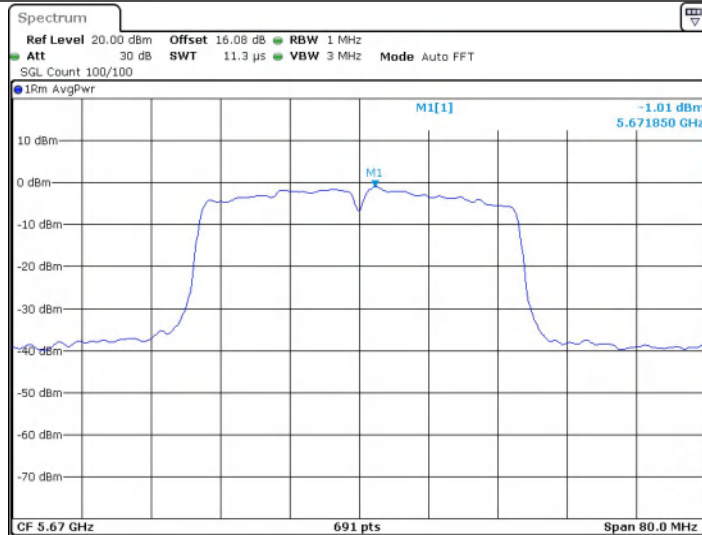


11AC40SISO_Ant2_5550



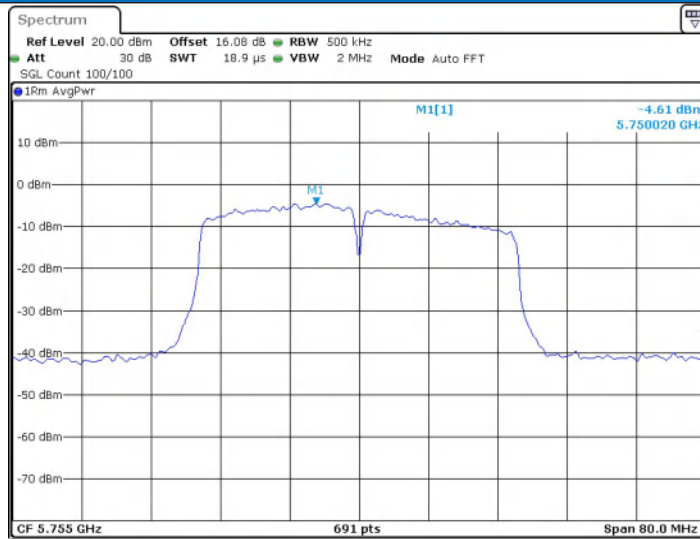
Date: 4 MAR 2026 13:34:07

11AC40SISO_Ant2_5670



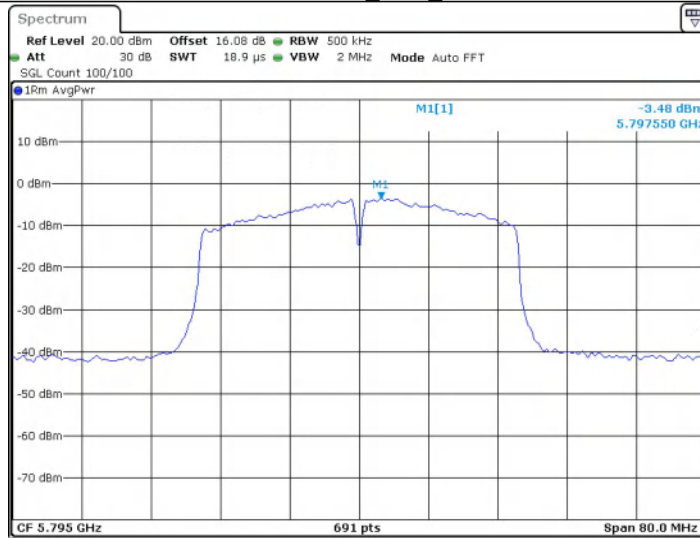
Date: 4 MAR 2026 13:37:12

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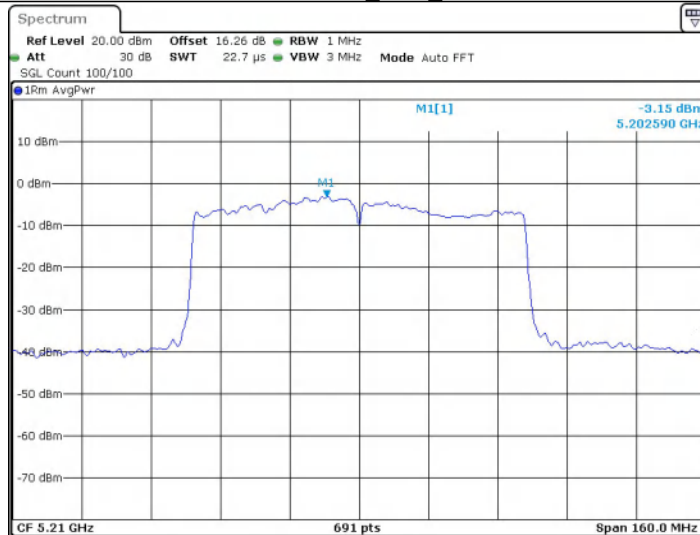
Date: 4 MAR 2026 13:41:29

11AC40SISO_Ant2_5795



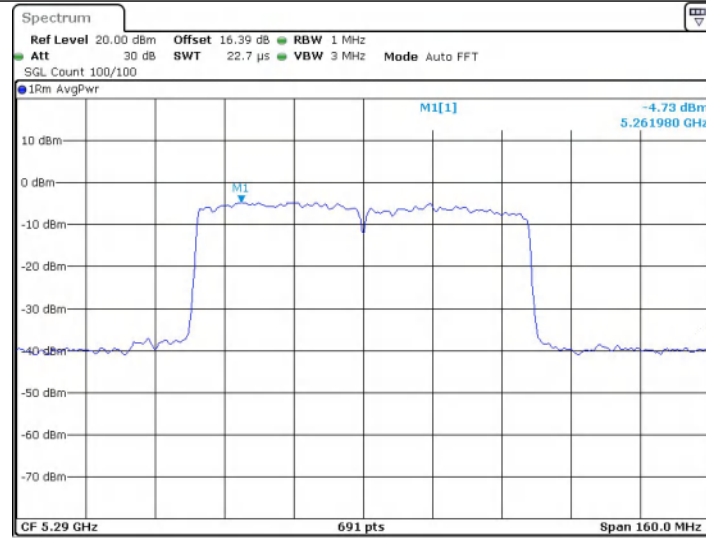
Date: 4 MAR 2026 13:43:15

11AC80SISO_Ant2_5210



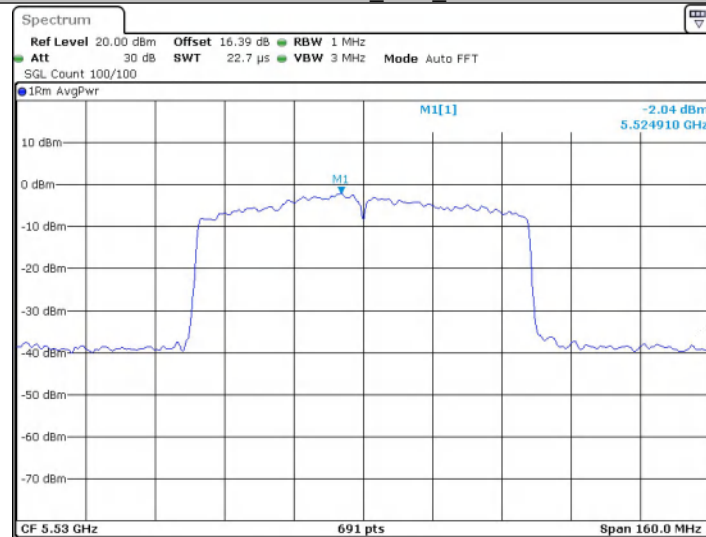
Date: 4 MAR 2026 13:45:09

11AC80SISO_Ant2_5290



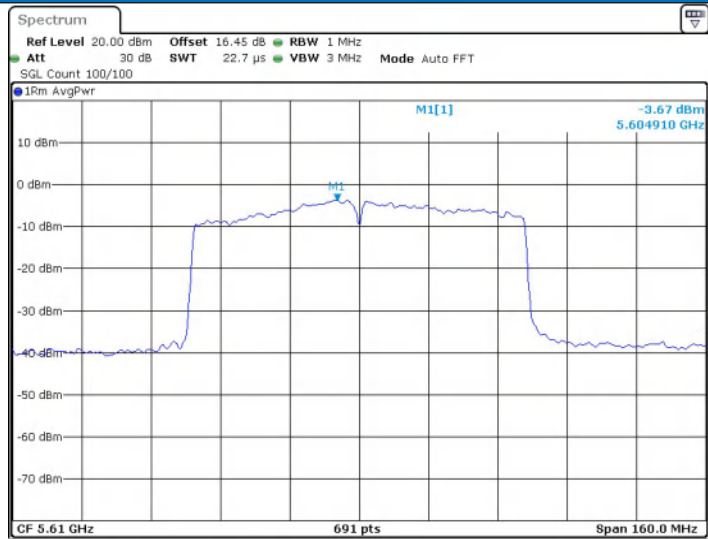
Date: 4 MAR 2026 13:48:05

11AC80SISO_Ant2_5530



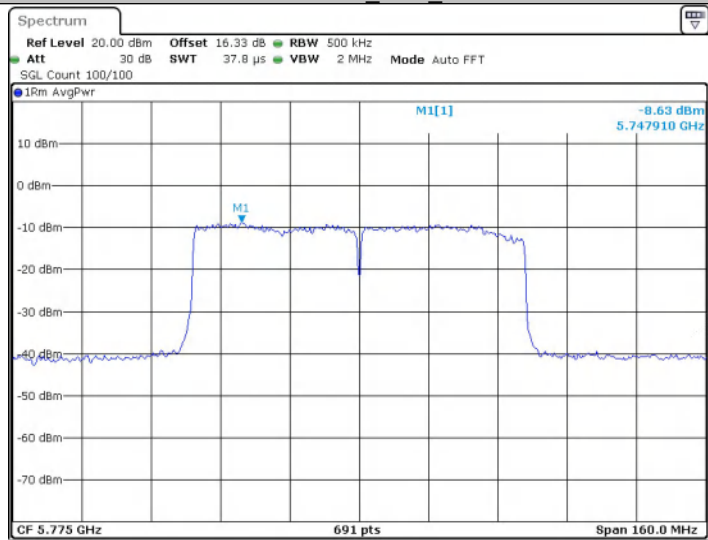
Date: 4 MAR 2026 13:50:10

11AC80SISO_Ant2_5610



Date: 4 MAR 2026 13:56:44

11AC80SISO_Ant2_5775



Date: 4 MAR 2026 14:01:03

ANT1+ANT2:

TestMode	Freq(MHz)	Result [dBm/MHz]	Limit[dBm/MHz]	Verdict
11N20MIMO	5180	7.30	≤9.04	PASS
	5200	7.75	≤9.04	PASS
	5240	6.27	≤9.04	PASS
	5260	7.91	≤9.04	PASS
	5280	5.63	≤9.04	PASS
	5320	6.32	≤9.04	PASS
	5500	7.17	≤9.04	PASS
	5580	5.99	≤9.04	PASS
	5700	4.37	≤9.04	PASS
	5745	3.99	≤28.04	PASS
	5785	1.88	≤28.04	PASS
	5825	0.92	≤28.04	PASS
11N40MIMO	5190	5.29	≤9.04	PASS
	5230	1.45	≤9.04	PASS
	5270	4.12	≤9.04	PASS
	5310	3.27	≤9.04	PASS
	5510	4.82	≤9.04	PASS
	5550	4.08	≤9.04	PASS
	5670	2.12	≤9.04	PASS
	5755	-0.74	≤28.04	PASS
	5795	0.00	≤28.04	PASS
11AC20MIMO	5180	6.98	≤9.04	PASS
	5200	7.30	≤9.04	PASS
	5240	5.23	≤9.04	PASS
	5260	7.41	≤9.04	PASS
	5280	4.99	≤9.04	PASS
	5320	6.22	≤9.04	PASS
	5500	6.67	≤9.04	PASS
	5580	4.84	≤9.04	PASS
	5700	3.68	≤9.04	PASS
	5745	3.48	≤28.04	PASS
	5785	1.62	≤28.04	PASS
	5825	0.68	≤28.04	PASS
11AC40MIMO	5190	3.51	≤9.04	PASS
	5230	0.61	≤9.04	PASS

	5270	2.34	≤9.04	PASS
	5310	1.51	≤9.04	PASS
	5510	3.09	≤9.04	PASS
	5550	2.53	≤9.04	PASS
	5670	1.05	≤9.04	PASS
	5755	-1.97	≤28.04	PASS
	5795	-0.95	≤28.04	PASS
11AC80MIMO	5210	-0.59	≤9.04	PASS
	5290	-1.60	≤9.04	PASS
	5530	0.76	≤9.04	PASS
	5610	-0.96	≤9.04	PASS
	5775	-5.84	≤28.04	PASS

Appendix D): Band Edge Measurements

Test Requirement 47 CFR Part 15, Subpart C 15.209 & 15.407(b)

Test Method: KDB 789033 D02 II G

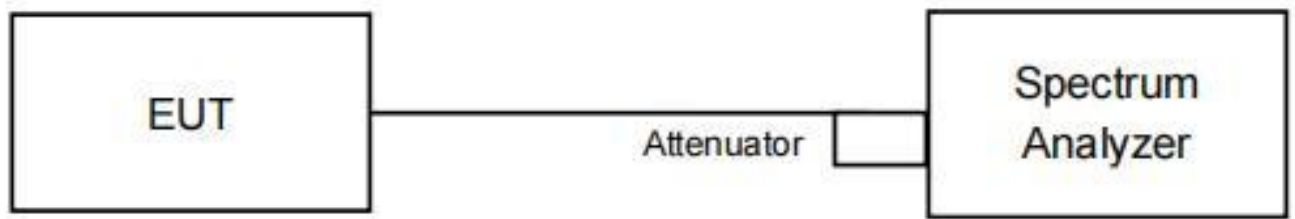
Test Procedure:

1. The EUT operates at transmitting mode. The operate channel is tested to verify the largest transmission and spurious emissions power at the continuous transmission mode.
2. Set the spectrum analyzer in the following setting in order to capture the lower and upper band-edges of the emission:
 - (a) PEAK: RBW=VBW=1MHz / Sweep=AUTO
 - (b) AVERAGE: RBW=1MHz ; VBW=1/on time(1KHz) / Sweep=AUTO

Limit:

For transmitters operating in the 5.15-5.25 GHz band:	All emissions outside of the 5.15-5.35 GHz band shall not exceed an e.i.r.p. of -27 dBm/MHz (68.2dBuV/m).
For transmitters operating in the 5.25-5.35 GHz band:	All emissions outside of the 5.15-5.35 GHz band shall not exceed an e.i.r.p. of -27 dBm/MHz (68.2dBuV/m).
For transmitters operating in the 5.47-5.725 GHz band:	All emissions outside of the 5.47-5.725 GHz band shall not exceed an e.i.r.p. of -27 dBm/MHz (68.2dBuV/m).
For transmitters operating in the 5.725-5.85 GHz band:	(i) All emissions shall be limited to a level of -27 dBm/MHz (68.2dBuV/m) at 75 MHz or more above or below the band edge increasing linearly to 10 dBm/MHz (105.2dBuV/m) at 25 MHz above or below the band edge, and from 25 MHz above or below the band edge increasing linearly to a level of 15.6 dBm/MHz (110.8dBuV/m) at 5 MHz above or below the band edge, and from 5 MHz above or below the band edge increasing linearly to a level of 27 dBm/MHz (122.2dBuV/m) at the band edge.

Test Setup Diagram



ANT1:

7.1.1 Test Result B1/2/3

TestMode	ChName	Freq(MHz)	Result[dBm]	Limit[dBm]	Verdict
11A	Low	5180	-38.24	≤ -27	PASS
	High	5320	-38.35	≤ -27	PASS
	Low	5500	-38	≤ -27	PASS
	High	5700	-37.79	≤ -27	PASS
11N20SISO	Low	5180	-37.86	≤ -27	PASS
	High	5320	-38.45	≤ -27	PASS
	Low	5500	-37.43	≤ -27	PASS
	High	5700	-38.42	≤ -27	PASS
11N40SISO	Low	5190	-37.74	≤ -27	PASS
	High	5310	-37.02	≤ -27	PASS
	Low	5510	-37.55	≤ -27	PASS
	High	5670	-38.38	≤ -27	PASS
11AC20SISO	Low	5180	-37.83	≤ -27	PASS
	High	5320	-37.64	≤ -27	PASS
	Low	5500	-37.73	≤ -27	PASS
	High	5700	-38.26	≤ -27	PASS
11AC40SISO	Low	5190	-38.14	≤ -27	PASS
	High	5310	-38.65	≤ -27	PASS
	Low	5510	-37.2	≤ -27	PASS
	High	5670	-38.09	≤ -27	PASS
11AC80SISO	Low	5210	-39.05	≤ -27	PASS
	High	5290	-38.33	≤ -27	PASS
	Low	5530	-38.43	≤ -27	PASS
	High	5610	-37.75	≤ -27	PASS

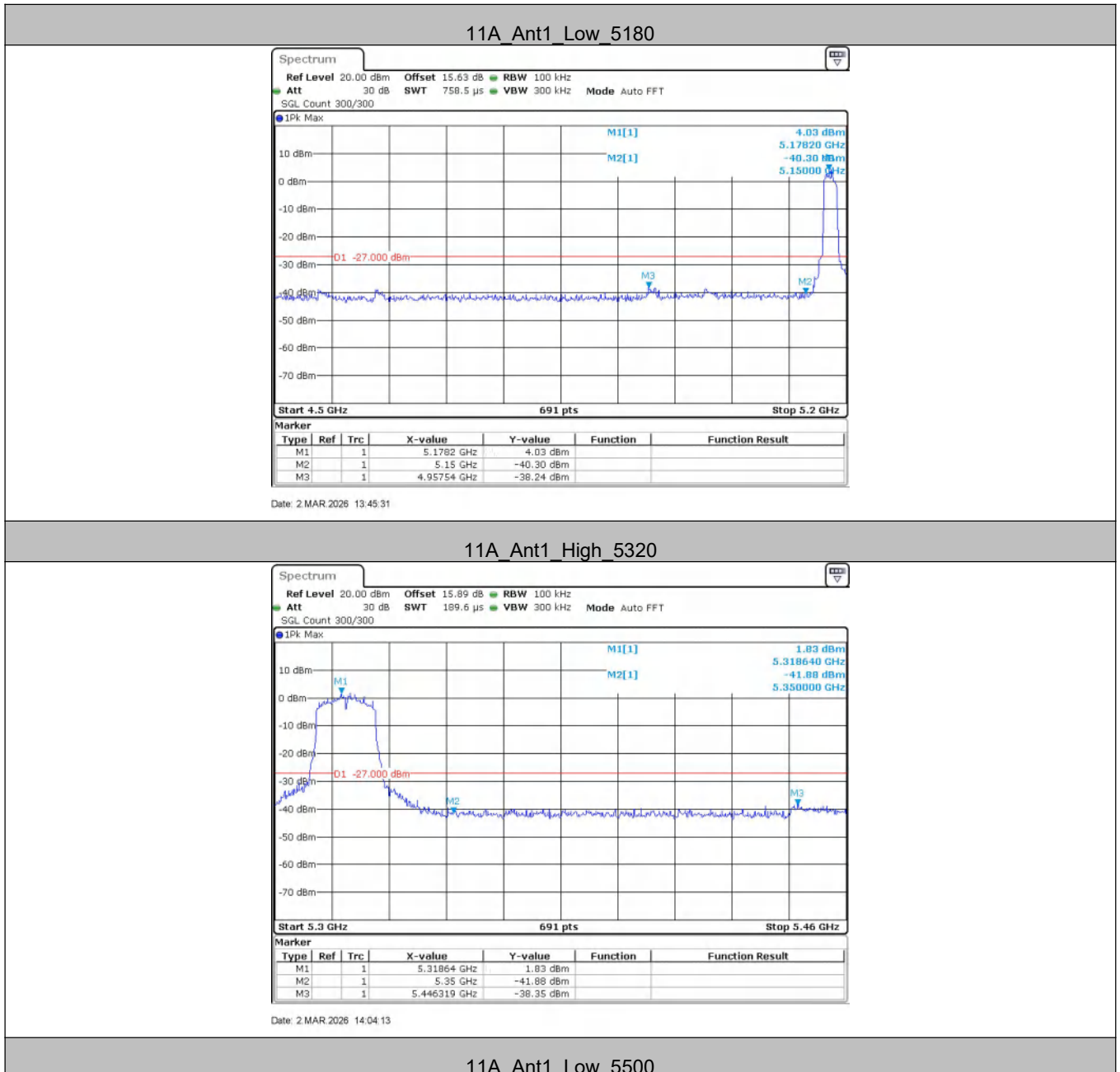
7.1.2 Test Result B4

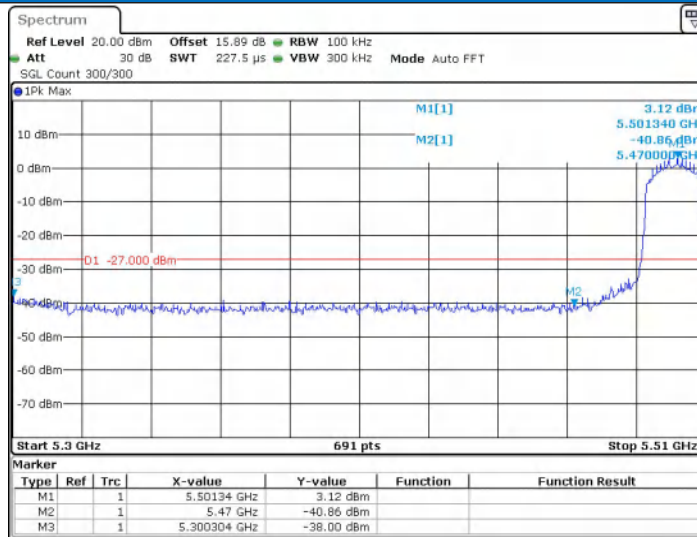
TestMode	ChName	Freq(MHz)	FreqRange [MHz]	Result [dBm]	Limit [dBm]	Verdict
11A	Low	5745	5650~5700	-40.4	≤ -26.26	PASS
			5700~5720	-39.22	≤ 10.42	PASS
			5720~5725	-39.96	≤ 15.98	PASS
			5760~5650	-40.46	≤ -27	PASS
	High	5825	5850~5855	-39.13	≤ 16.10	PASS

			5855~5875	-38.65	≤ 10.24	PASS
			5875~5925	-39.35	≤ -26.73	PASS
			5925~5935	-37.78	≤ -27	PASS
11N20SISO	Low	5745	5650~5700	-40.26	≤ -26.75	PASS
			5700~5720	-38.63	≤ 11.49	PASS
			5720~5725	-37.12	≤ 16.36	PASS
			5760~5650	-39.79	≤ -27	PASS
	High	5825	5850~5855	-40.05	≤ 16.10	PASS
			5855~5875	-38.87	≤ 10.02	PASS
			5875~5925	-39.13	≤ -26.73	PASS
			5925~5935	-38.1	≤ -27	PASS
11N40SISO	Low	5755	5650~5700	-40.6	≤ -26.50	PASS
			5700~5720	-38.19	≤ 10.76	PASS
			5720~5725	-34.34	≤ 17.68	PASS
			5780~5650	-40.25	≤ -27	PASS
	High	5795	5850~5855	-38.24	≤ 16.39	PASS
			5855~5875	-38.81	≤ 10.27	PASS
			5875~5925	-38.14	≤ -25.20	PASS
			5925~5935	-38.69	≤ -27	PASS
11AC20SISO	Low	5745	5650~5700	-40.39	≤ -26.75	PASS
			5700~5720	-39.72	≤ 10.09	PASS
			5720~5725	-38.95	≤ 17.50	PASS
			5760~5650	-39.38	≤ -27	PASS
	High	5825	5850~5855	-39.33	≤ 16.10	PASS
			5855~5875	-38.34	≤ 10.89	PASS
			5875~5925	-39.12	≤ -26.58	PASS
			5925~5935	-38.26	≤ -27	PASS
11AC40SISO	Low	5755	5650~5700	-41.14	≤ -26.94	PASS
			5700~5720	-39.7	≤ 10.93	PASS
			5720~5725	-38.27	≤ 18.13	PASS
			5780~5650	-39.72	≤ -27	PASS
	High	5795	5850~5855	-38.68	≤ 16.94	PASS
			5855~5875	-38.69	≤ 10.34	PASS
			5875~5925	-38.9	≤ -26.08	PASS
			5925~5935	-39.21	≤ -27	PASS
11AC80SISO	Low	5775	5650~5700	-38.88	≤ -25.71	PASS
			5700~5720	-39.26	≤ 10.01	PASS

			5720~5725	-37.85	≤ 18.23	PASS
			5800~5650	-38.95	≤ -27	PASS
	High	5775	5850~5855	-38.6	≤ 15.98	PASS
			5855~5875	-39.25	≤ 10.02	PASS
			5875~5925	-38.94	≤ -26.86	PASS
			5925~5935	-38.62	≤ -27	PASS

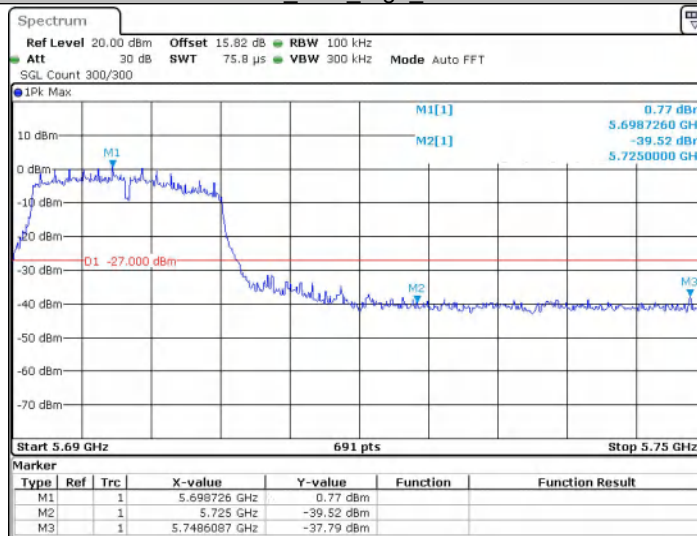
7.1.3 Test Graphs B1/2/3





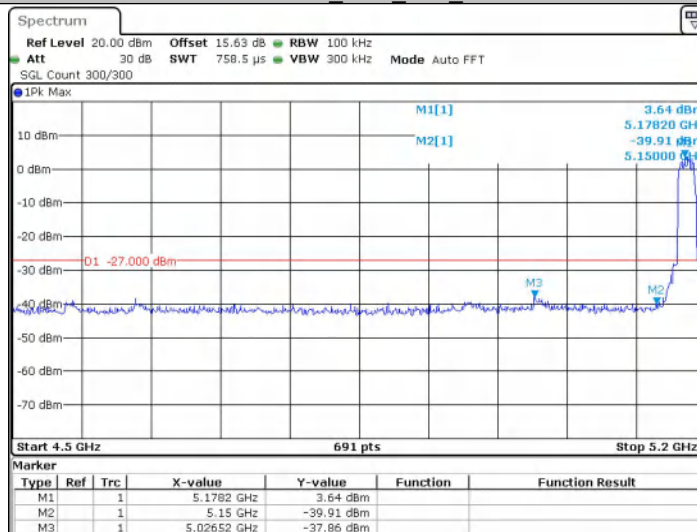
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11A Ant1_High_5700



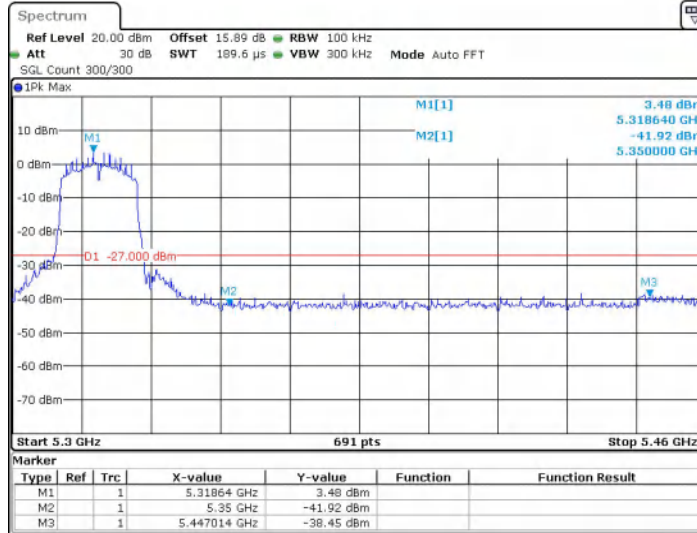
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11N20SISO_Ant1_Low_5180



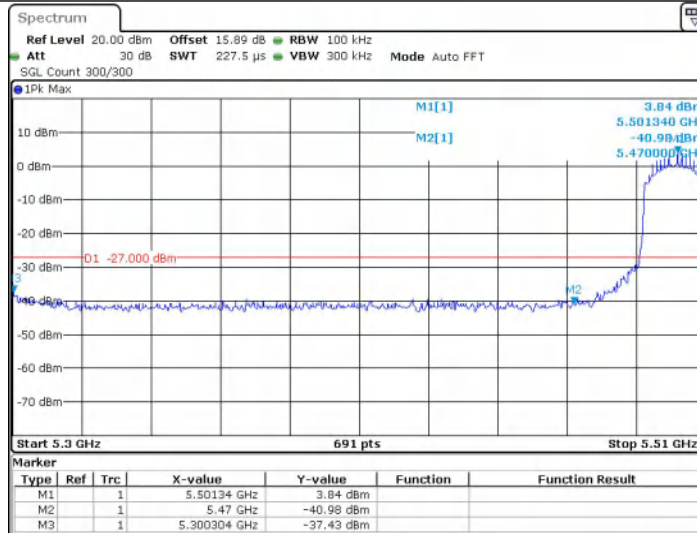
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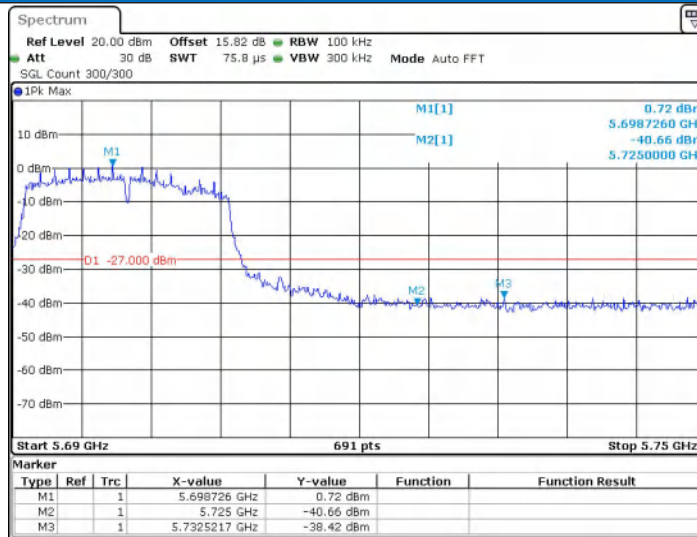
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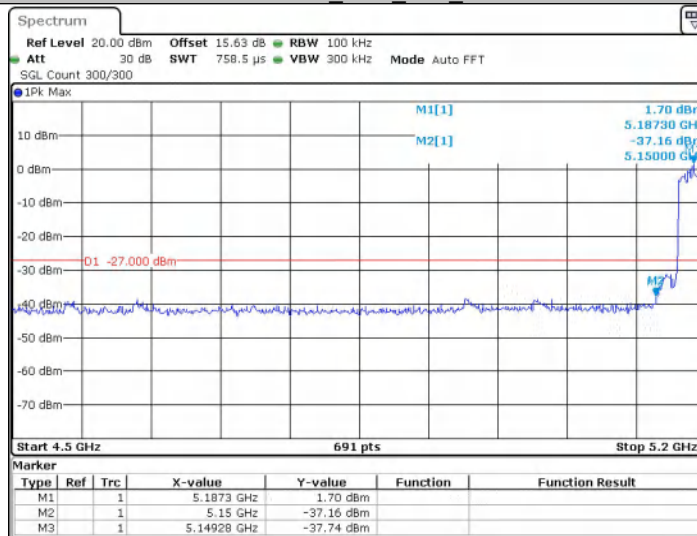
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11N20SISO_Ant1_High_5700



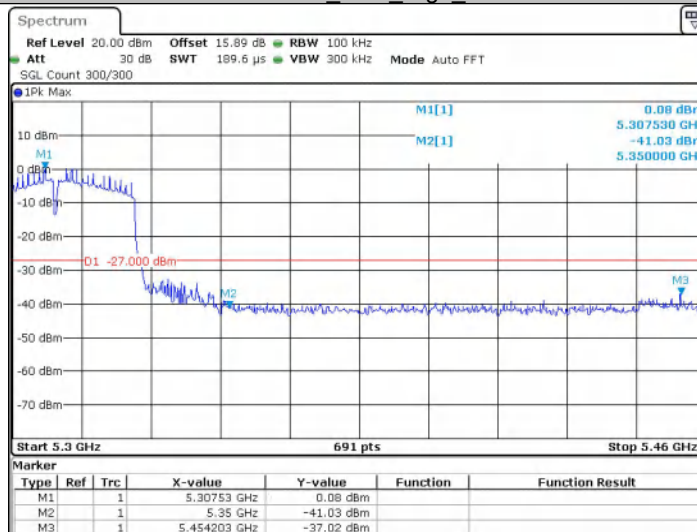
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11N40SISO_Ant1_Low_5190



Date: 2 MAR 2026 15:28:24

11N40SISO_Ant1_High_5310



Date: 2 MAR 2026 15:34:09