

The diagram illustrates the Z100A system architecture, divided into a MECH UNIT and a main CPU/IO section.

MECH UNIT: This section includes a TRAVEL MOTOR, a BRUSHLESS SPINDLE MOTOR, a TEMPERATURE SENSOR, and a TILT sensor. It also features a PICK-UP mechanism and a LOADING MOTOR. The MECH UNIT is connected to the main CPU via a series of control lines (A, B, C, D, E, F, G, H, MP-1, MP-2).

Central CPU/IO Section: The core of the system is the IC101 (UPC3330) ANALOG SIGNAL PROCESSOR, which interfaces with the MECH UNIT. It is connected to a CONTROL LOGIC block, which in turn manages the RF AGC/EO, S/H, and FOCUS TRACK blocks. The CPU also includes a MOBILE LFP PROCESSOR and a MOBILE AGC. The system is powered by a VCC+5V supply, which is derived from a VCC+12V1 supply through a VCC+5V regulator (IC306, MM1563DF).

Peripheral Components: The system includes several other ICs and components:

- IC102 (UPD63630):** A central processing unit that interfaces with the CPU and manages the READ CHANNEL, WRITE STRATEGY, EFM ENCODER, EFM DECODER, CIRC ENDEC, and ATIP PLL/DECODER.
- IC103 (PSS6165023FTV):** A SYSTEM CONTROL MICROCOMPUTER that manages the CPU, SDRAM, and various control lines (RST, SCK, SO, ST, CP, FE, TE, MOBILE).
- IC104 (BD794FS):** A DRIVER block that controls the TRAVEL MOTOR, LOADING MOTOR, and TILT sensor.
- IC105 (MM1563DF):** A Flash ROM that provides non-volatile storage.
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- IC217**

