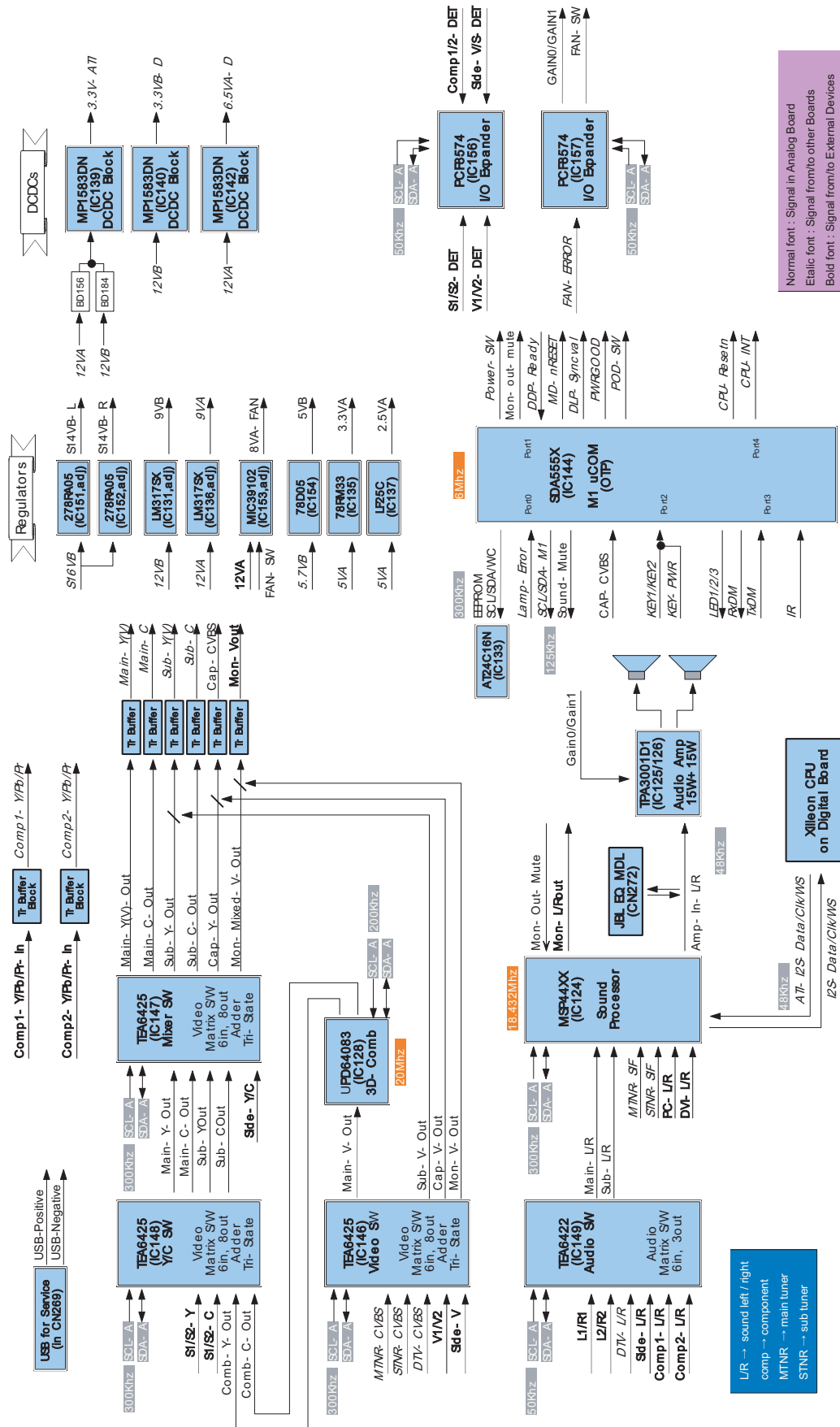


7-2-3 Analog Board Block Diagram



DLP-CLOCK Flow (Using ATI)

