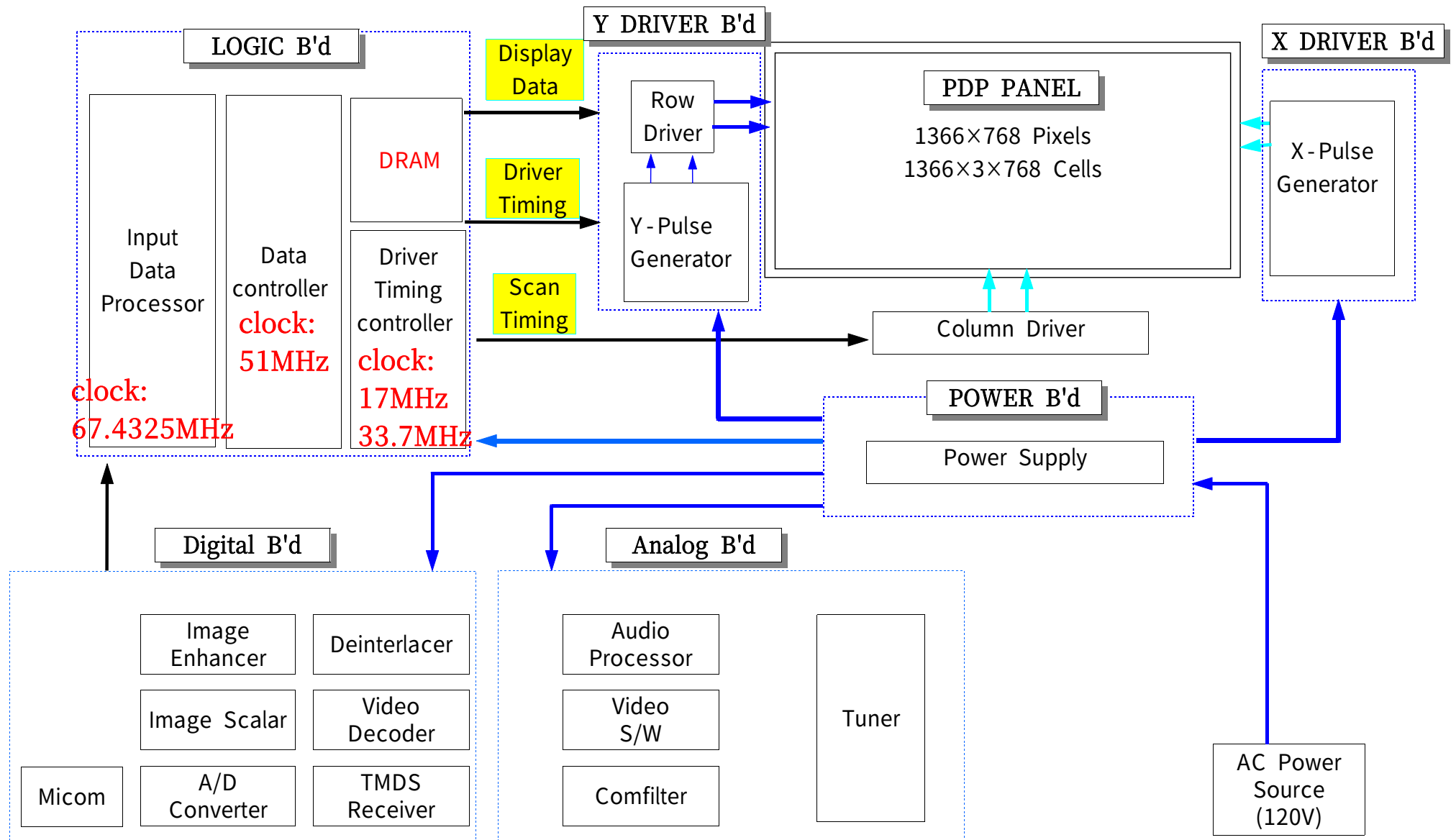
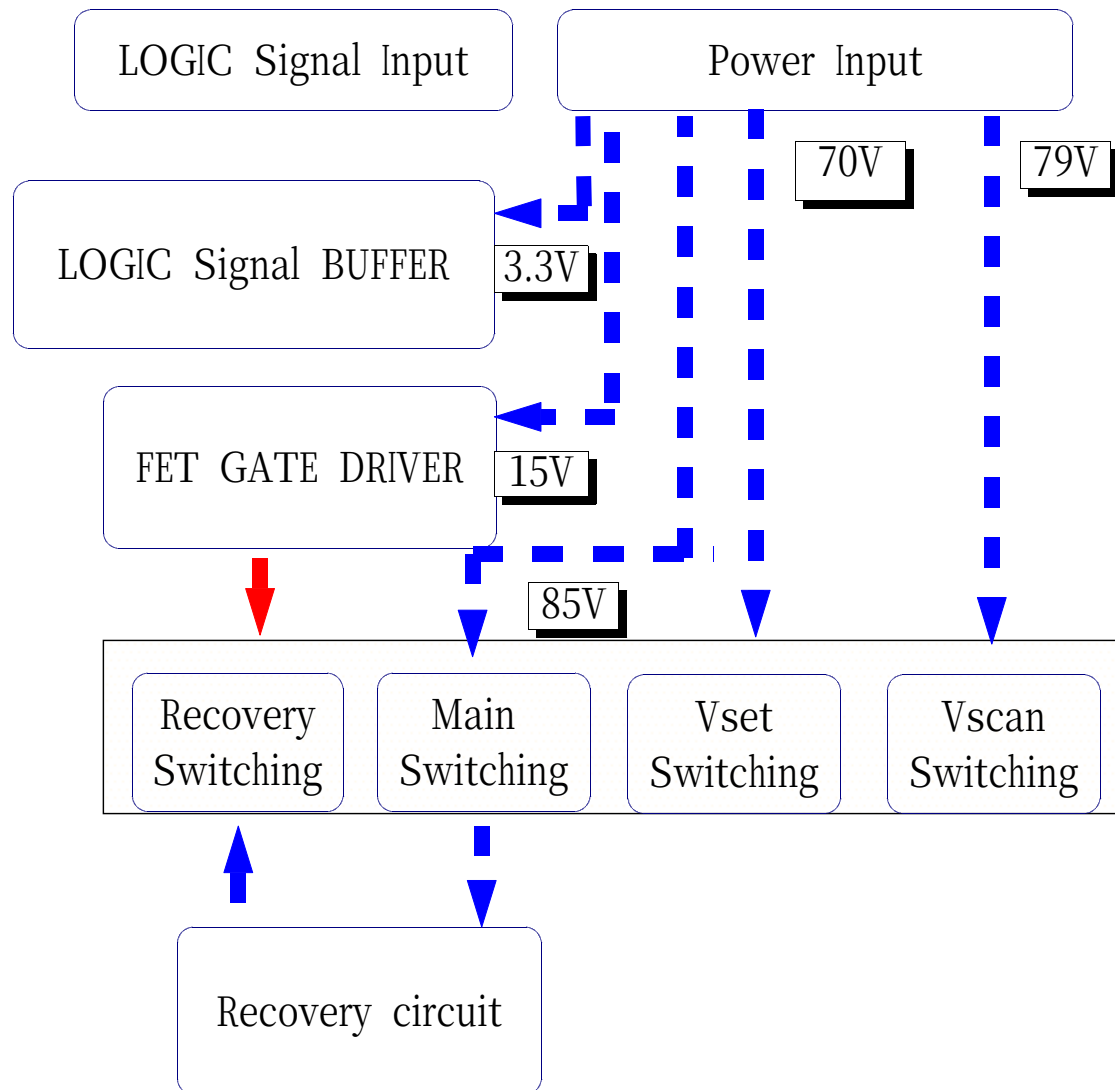


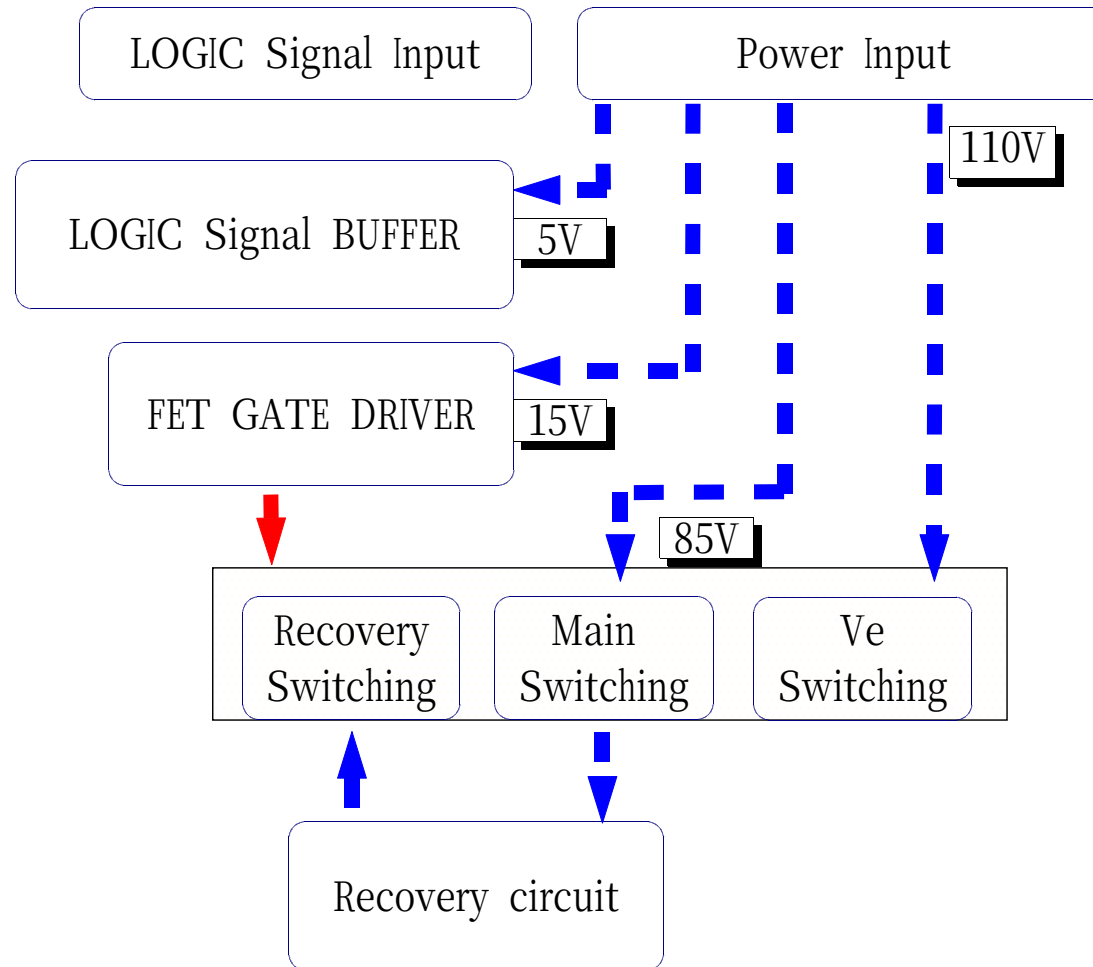
PDP MONITOR (HPN6339) BLOCK DIAGRAM



HPN6339 Y Driver B'd Block Diagram



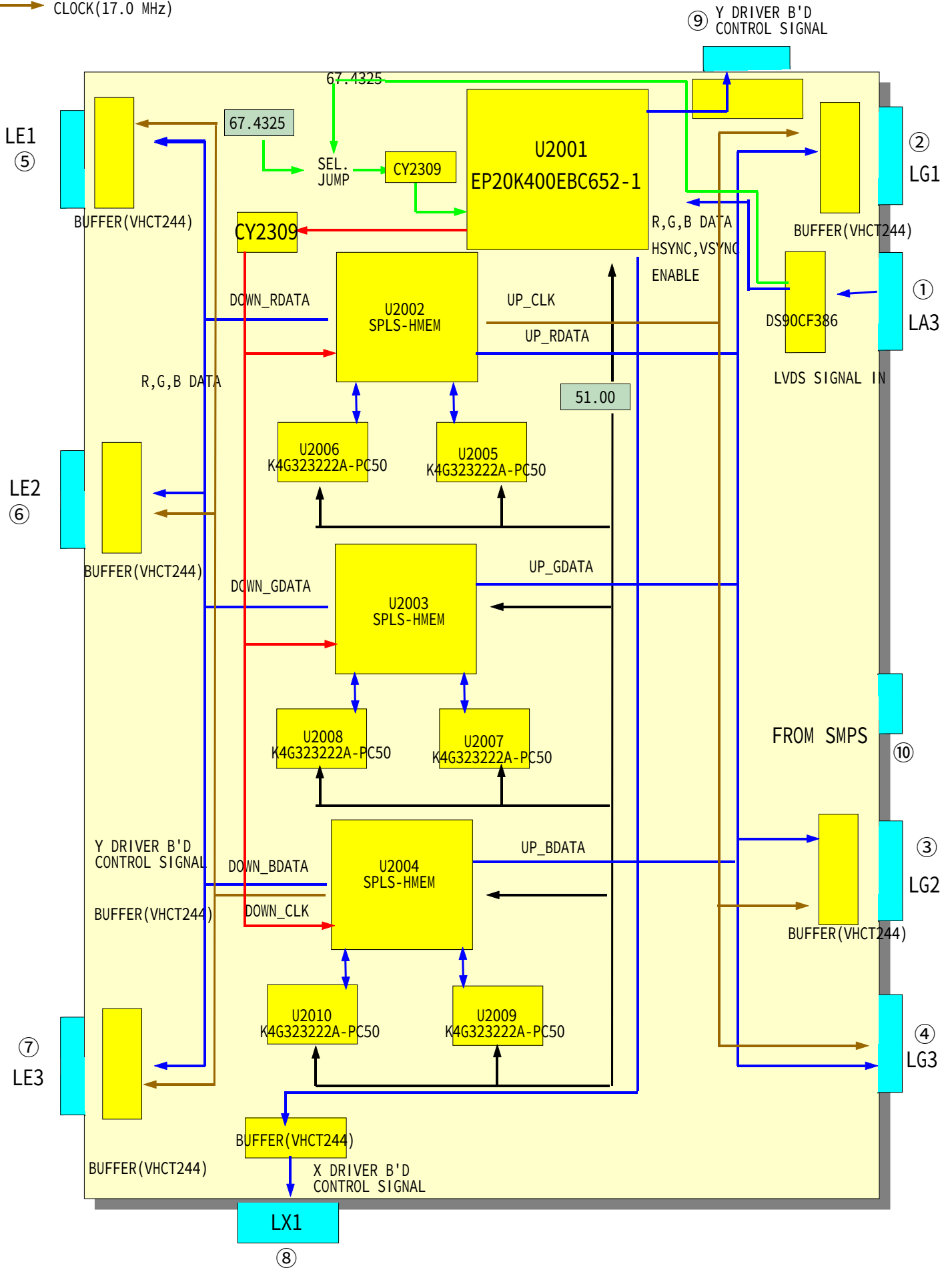
HPN6339 X Driver B'd Block Diagram



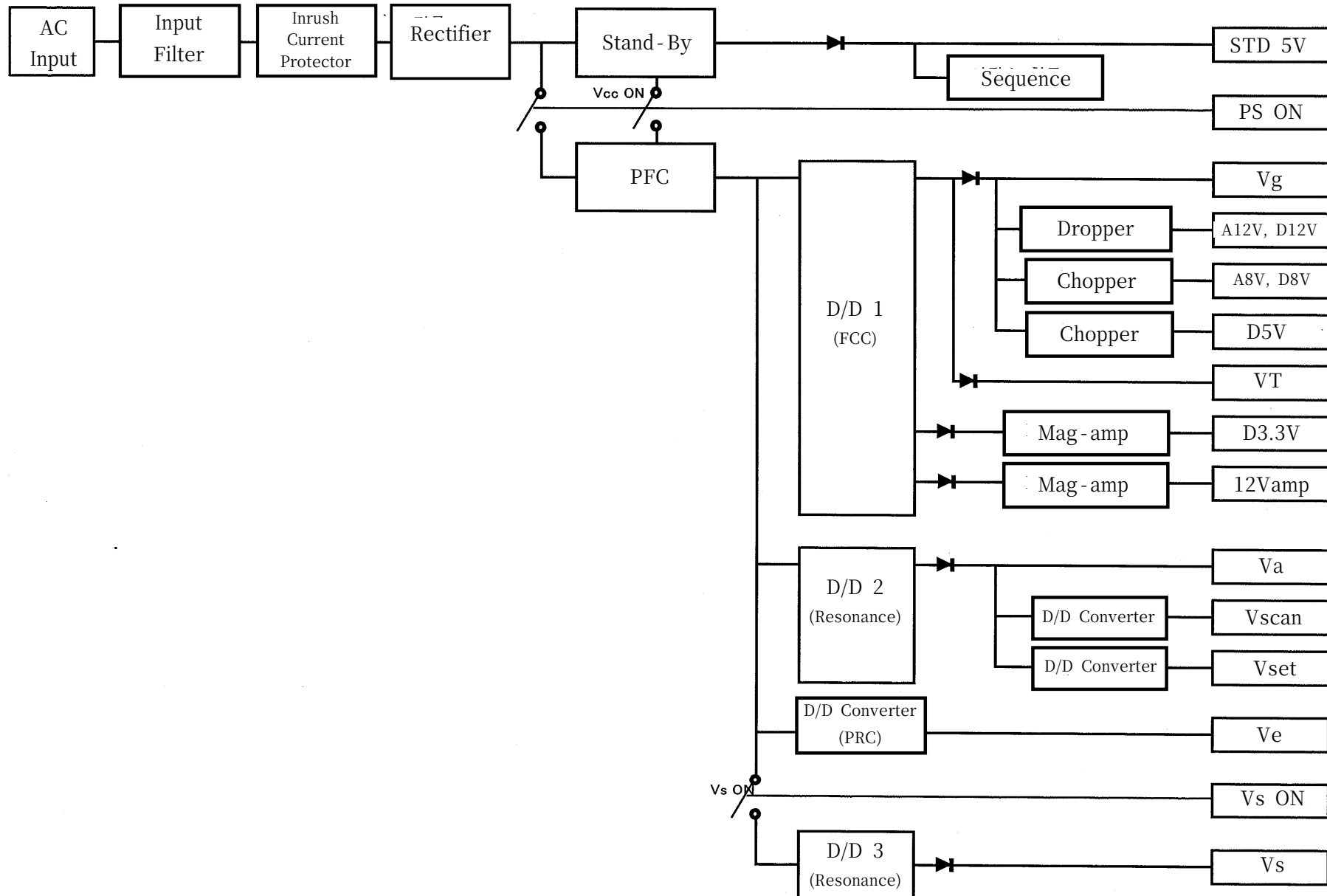
- CRYSTAL
- DIGITAL SIGNAL
- CLOCK(67.4325 MHz)
- CLOCK(51.0 MHz)
- CLOCK(33.7 MHz)
- CLOCK(17.0 MHz)

1F3A 로직보드 IC 배치 및 신호 흐름도.

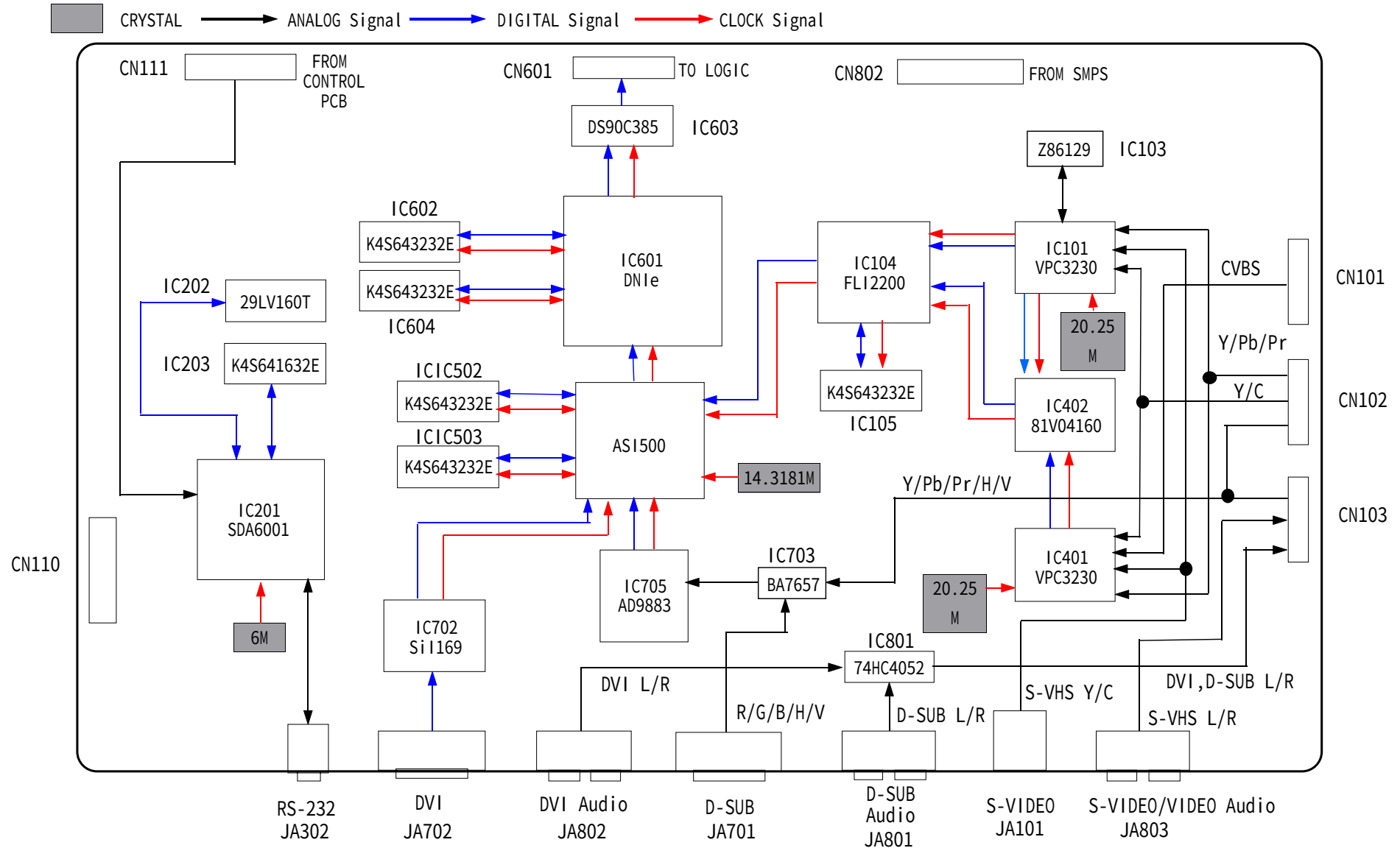
2002.02.05



HPN6339 SMPS Block Diagram



1. DIGITAL BOARD IC & SIGNAL BLOCK DIAGRAM



2. ANALOG BOARD IC & SIGNAL BLOCK DIAGRAM

