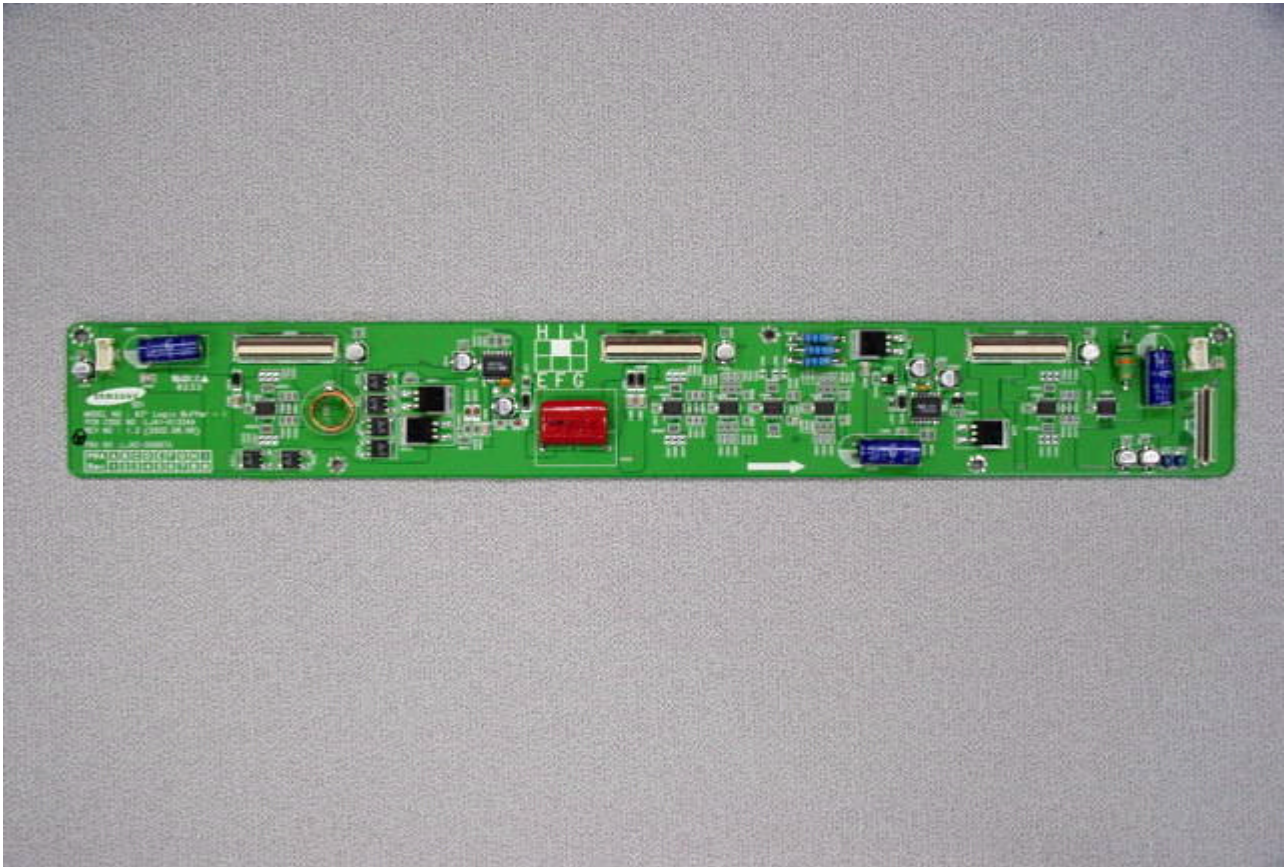
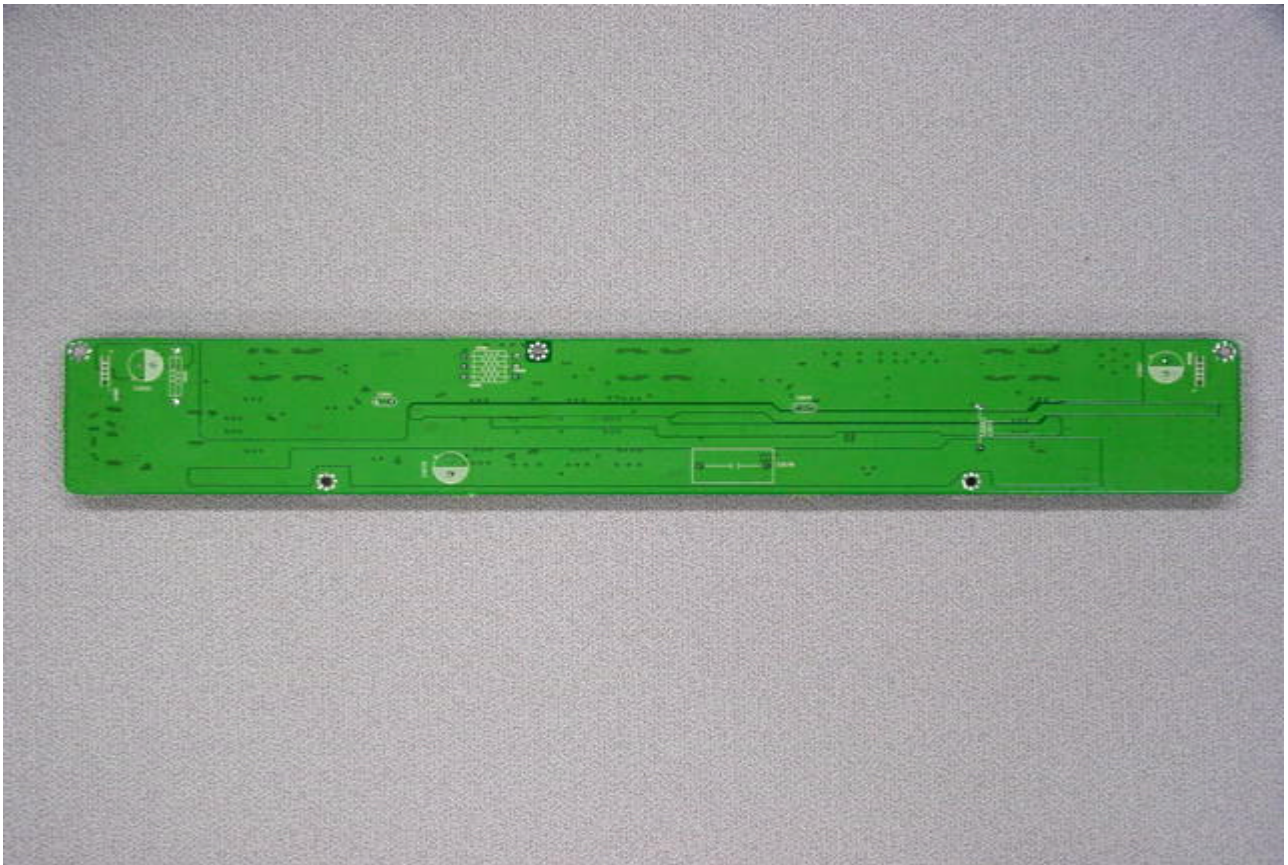
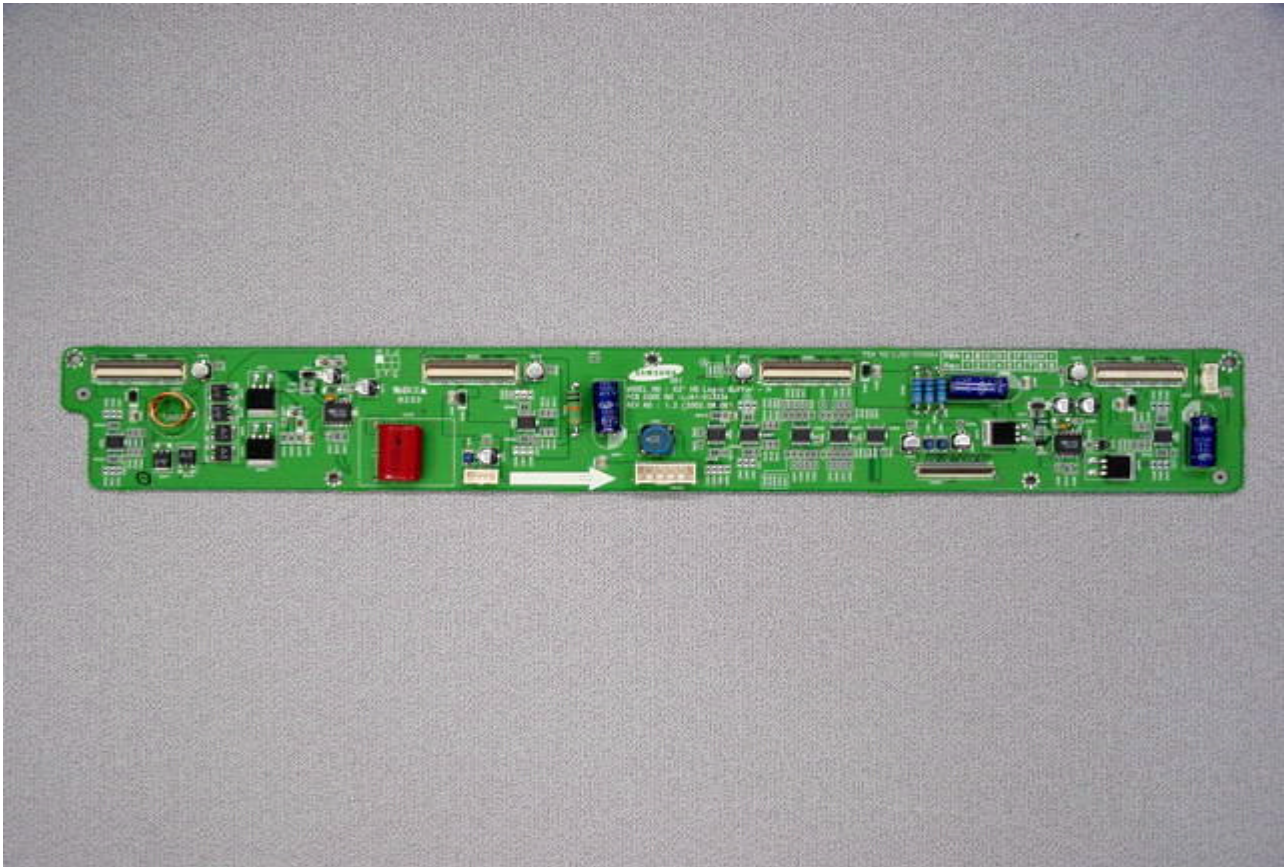




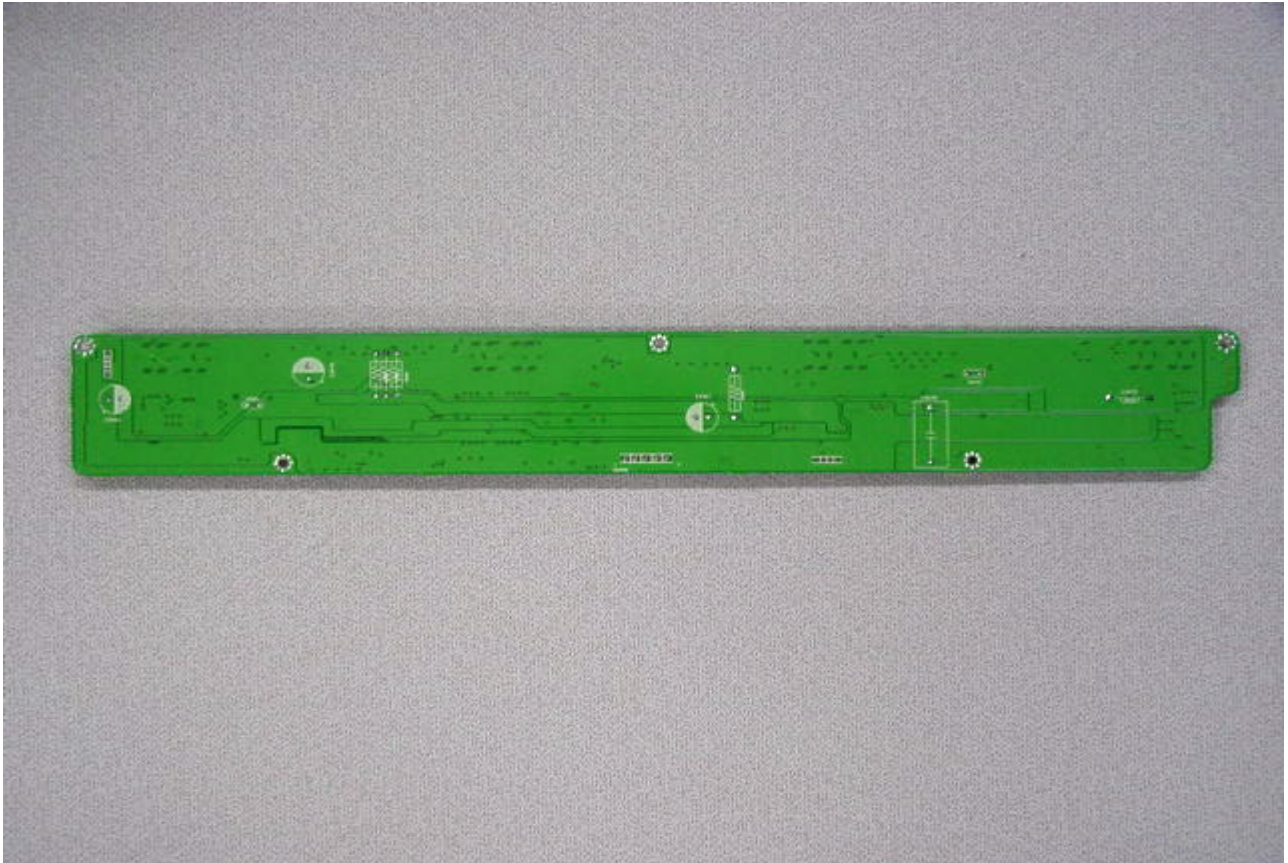
Logic buffer I board top view



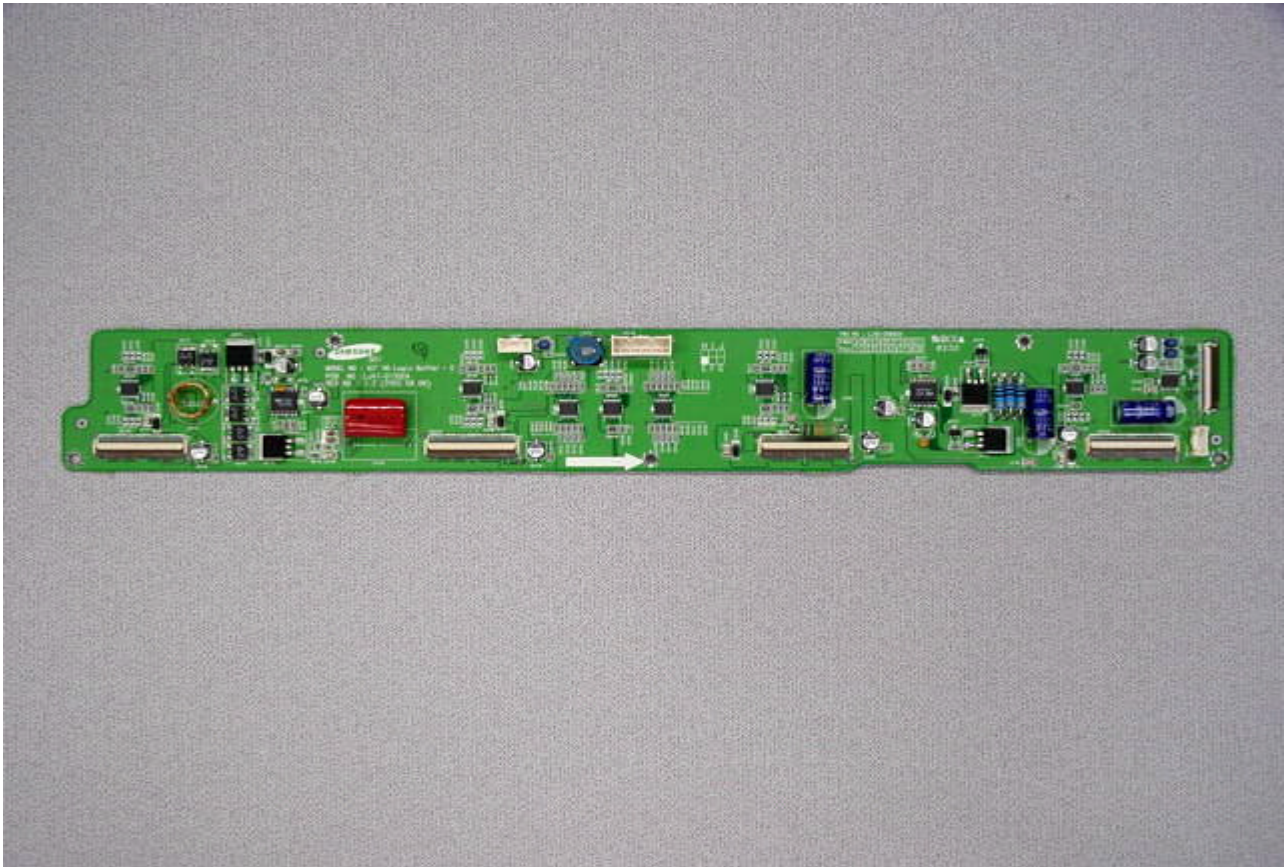
Logic buffer I board bottom view



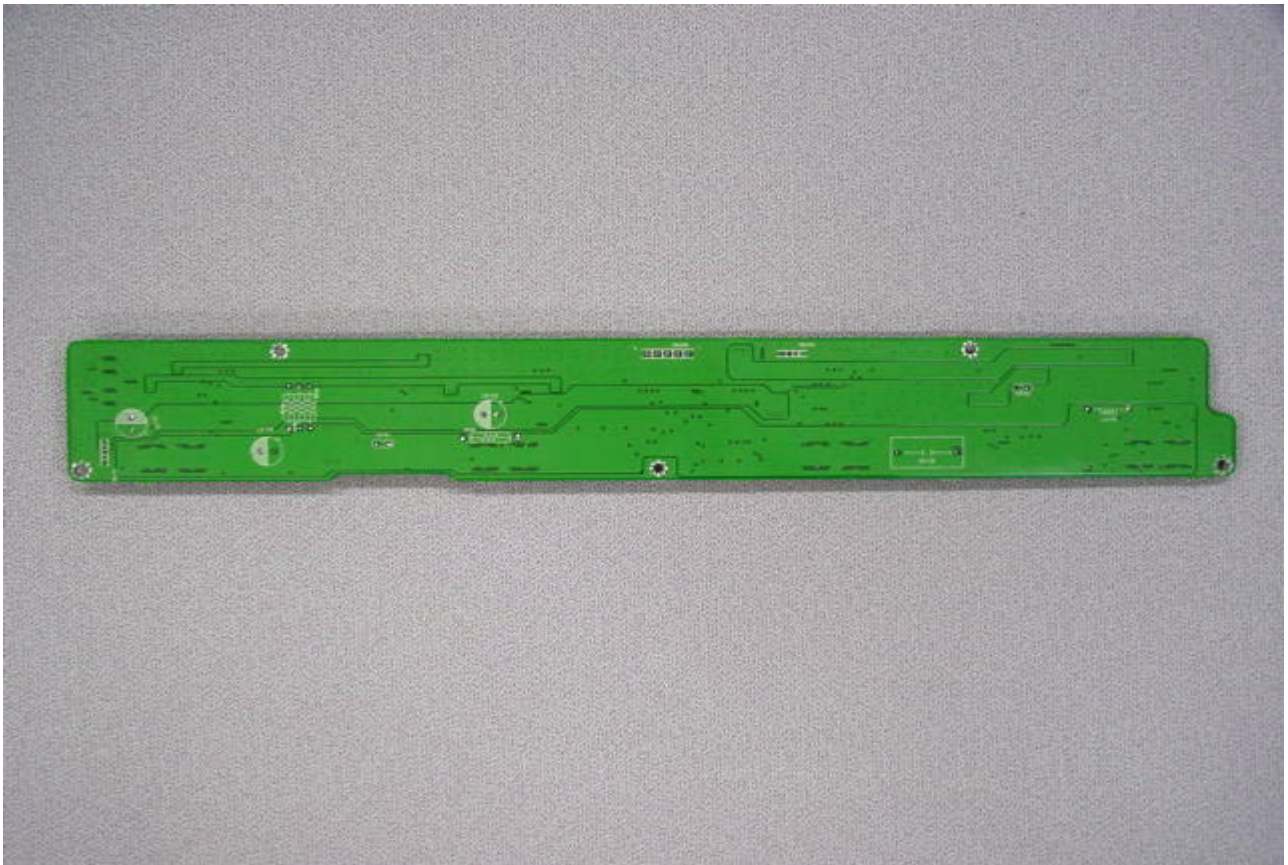
Logic buffer H board top view



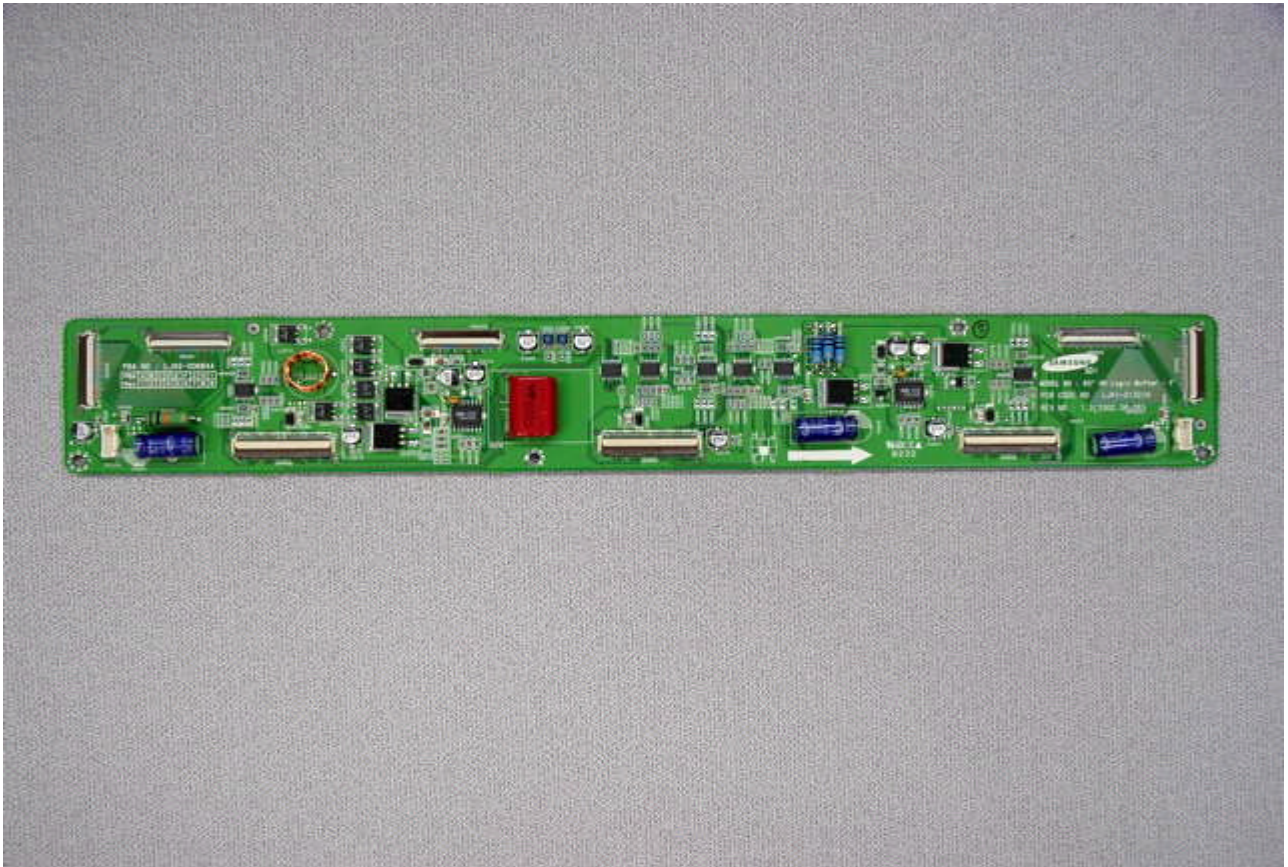
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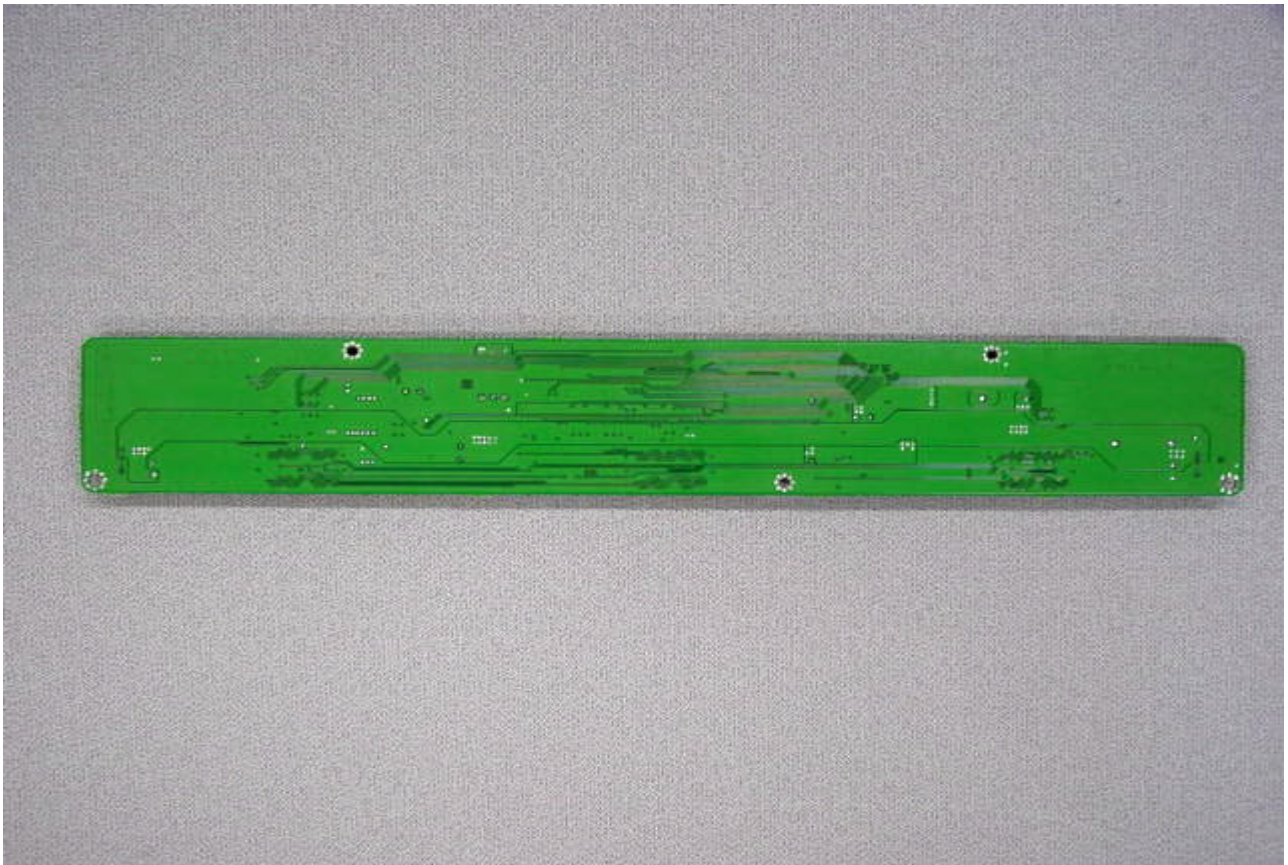
Logic buffer E board top view



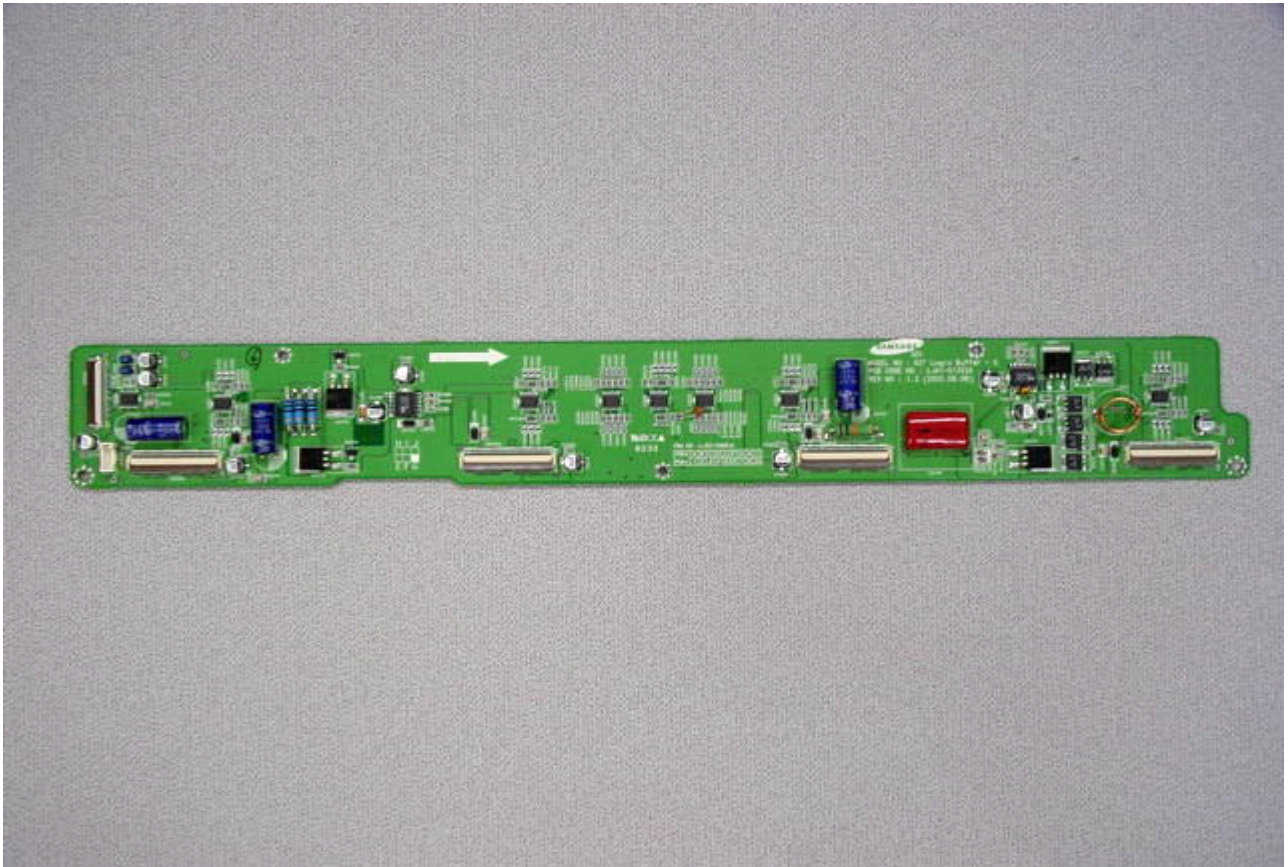
Logic buffer E board bottom view



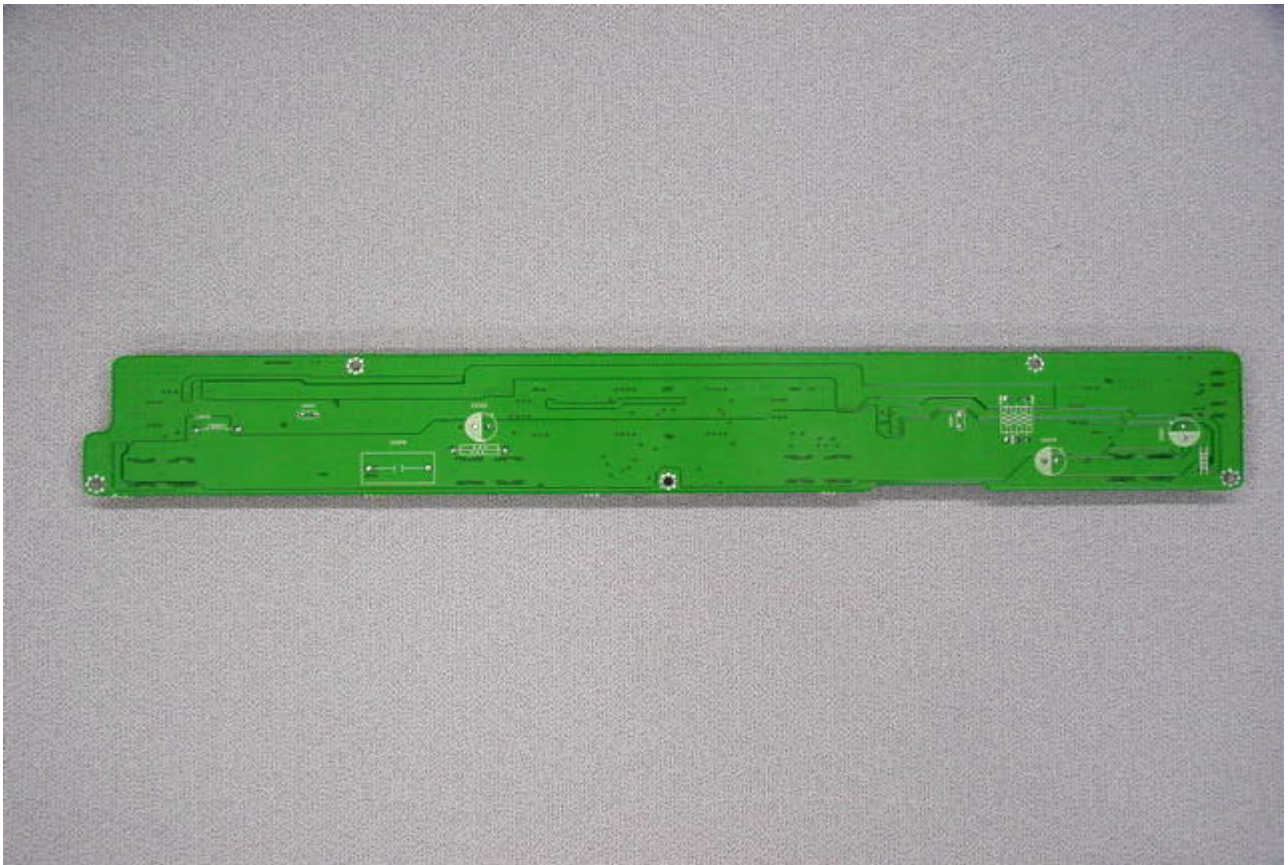
Logic buffer F board top view



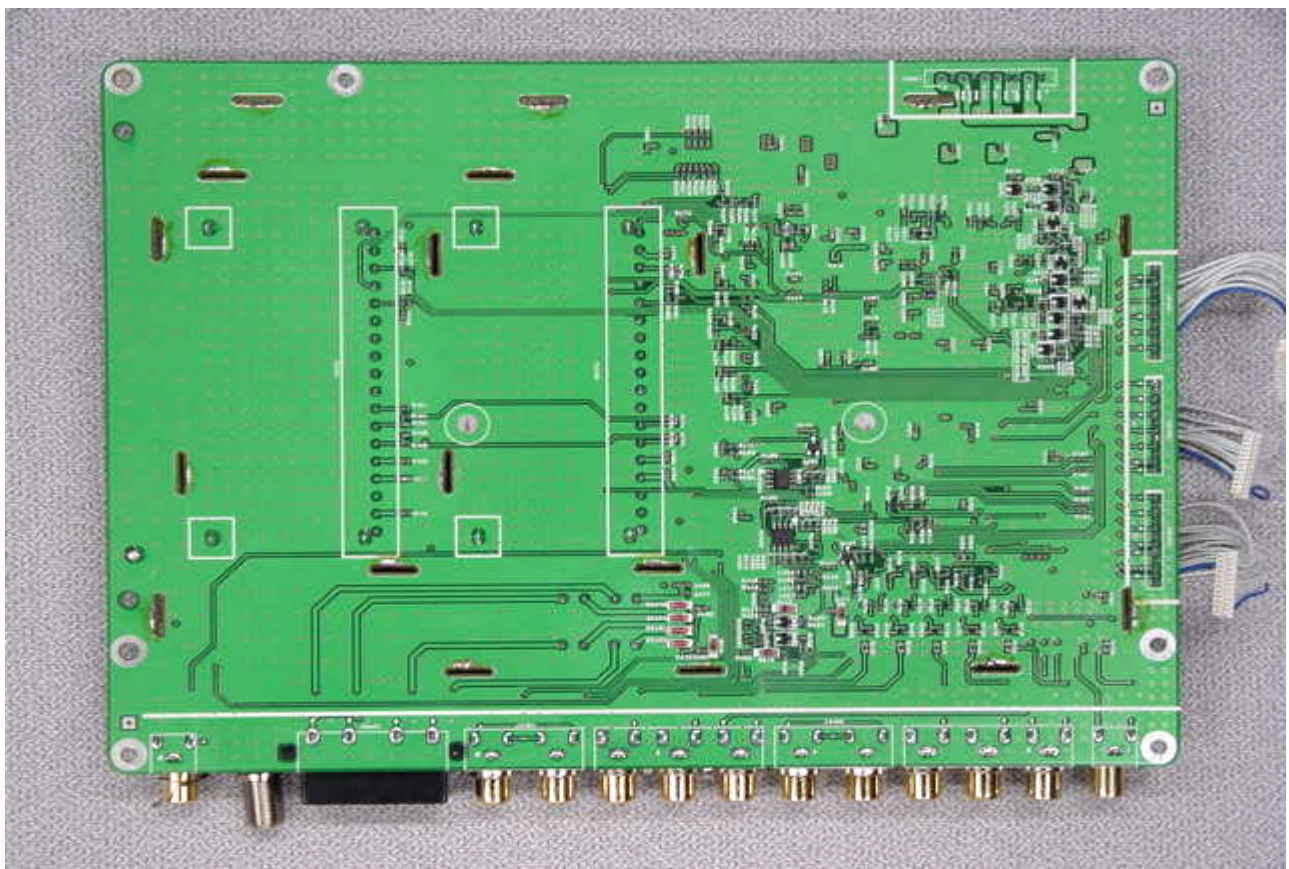
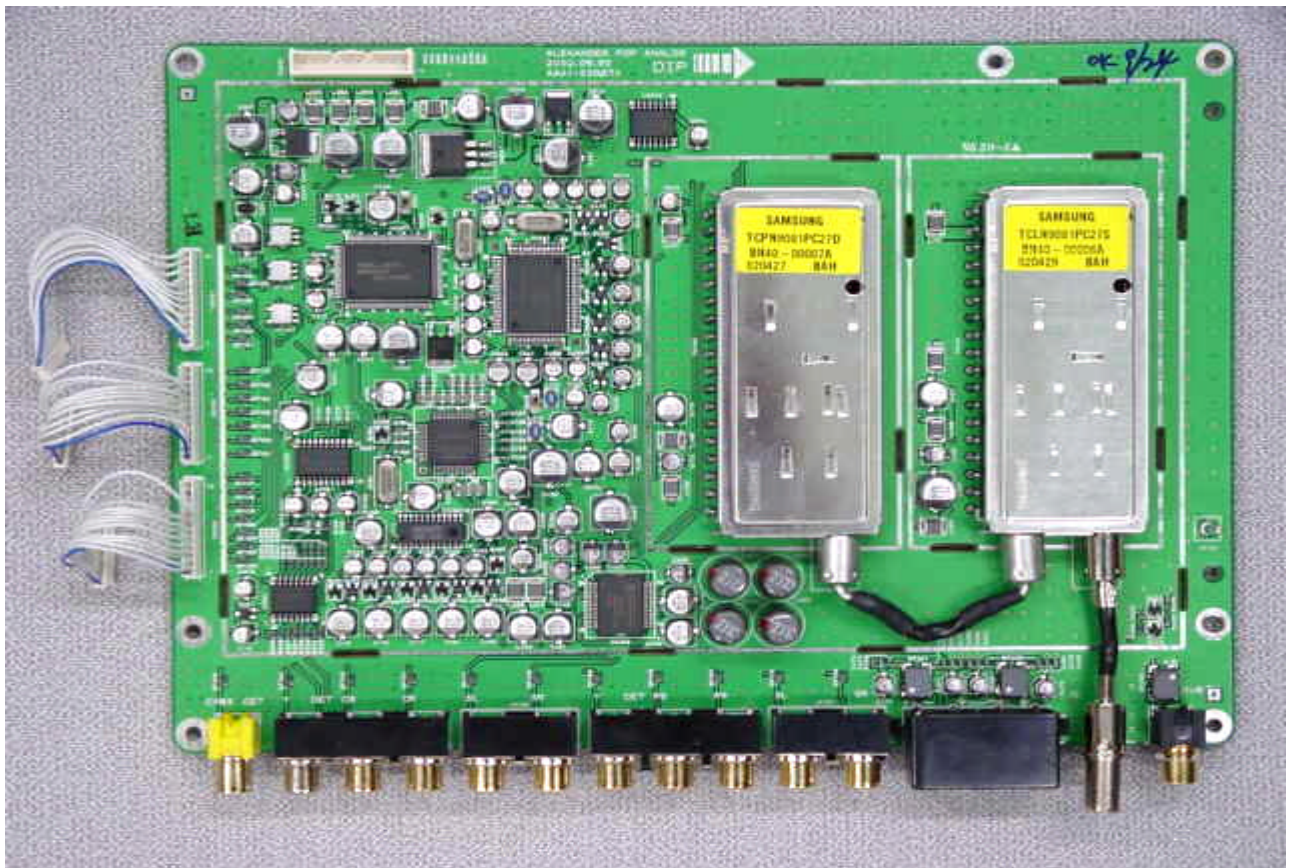
Logic buffer F board bottom view

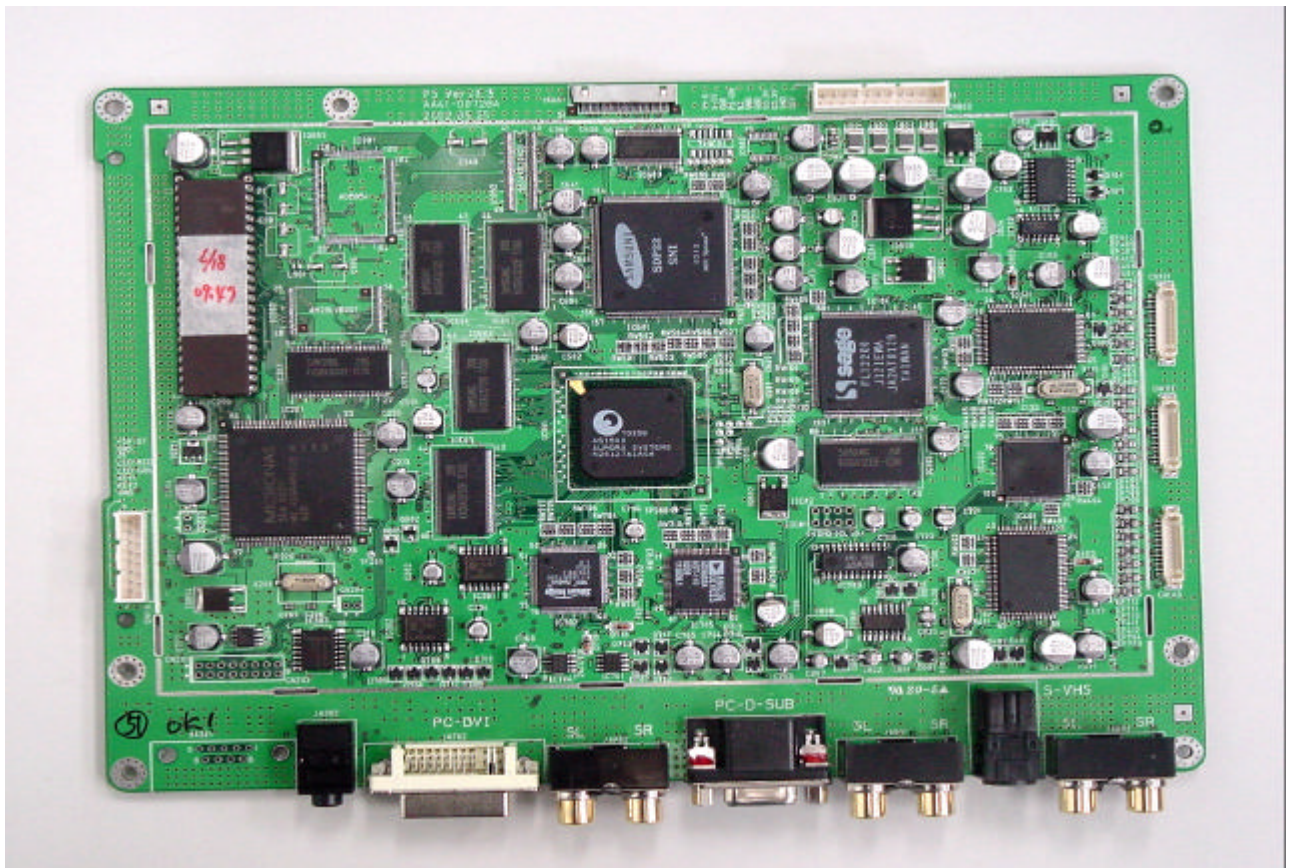


Logic buffer G board top view

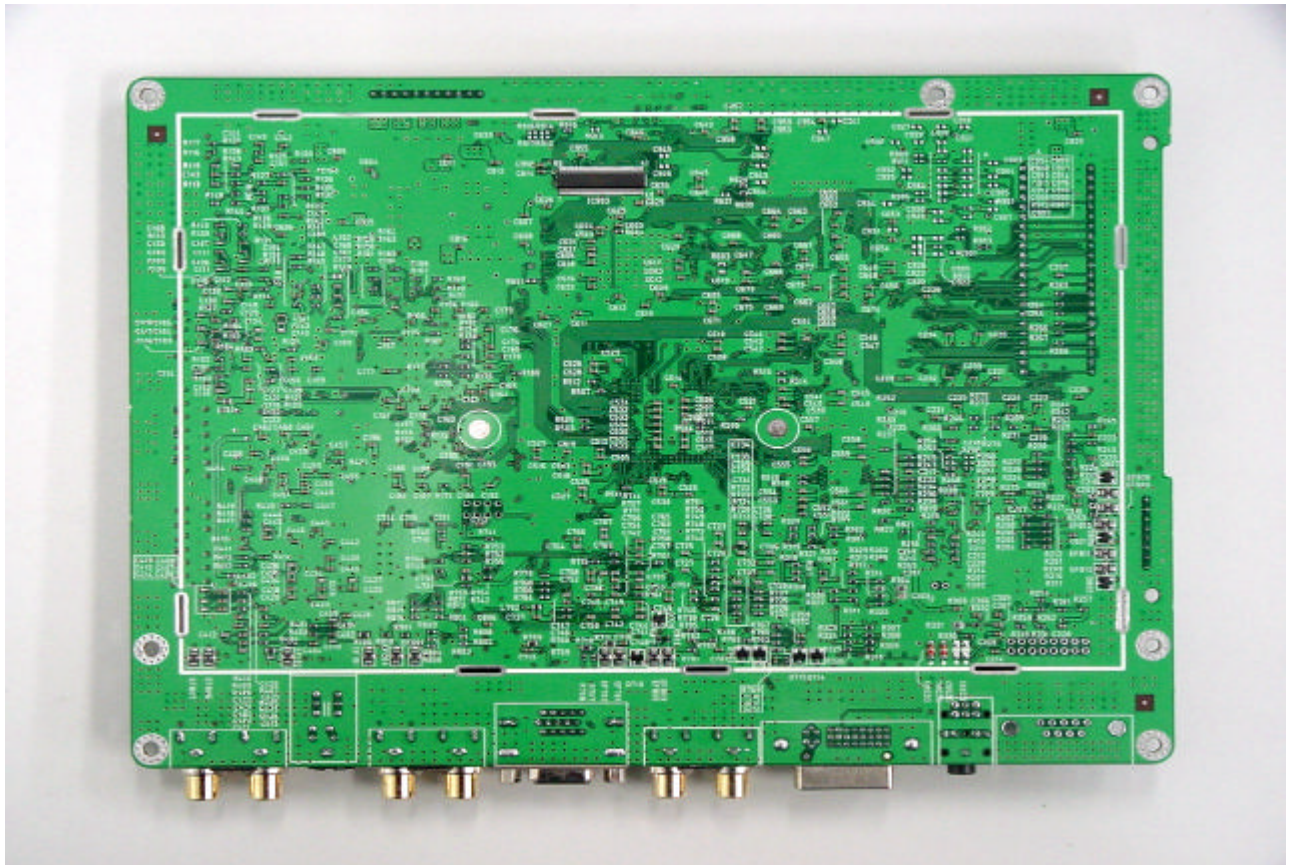


Logic buffer G board bottom view

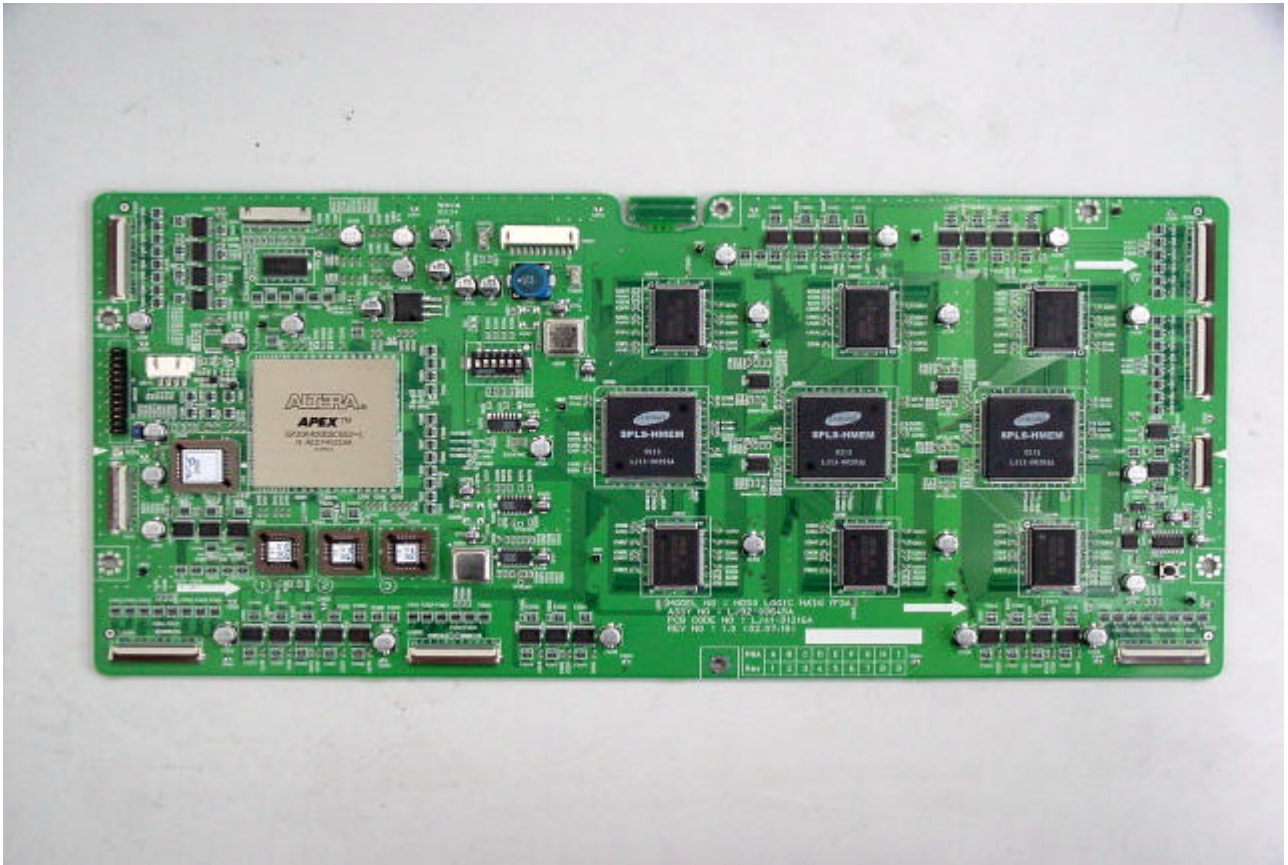




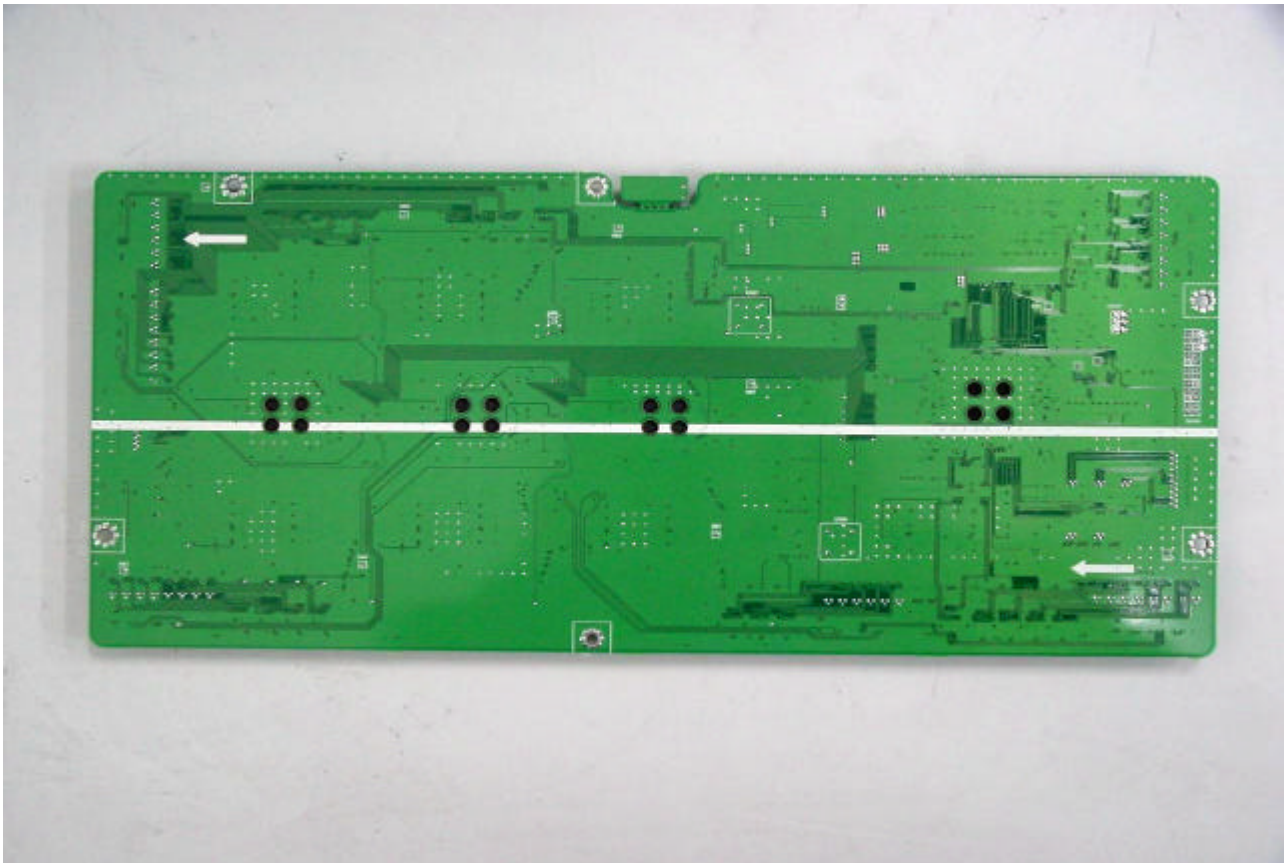
Digital board top view



Digital board bottom view



Logic board top view



Logic board bottom view