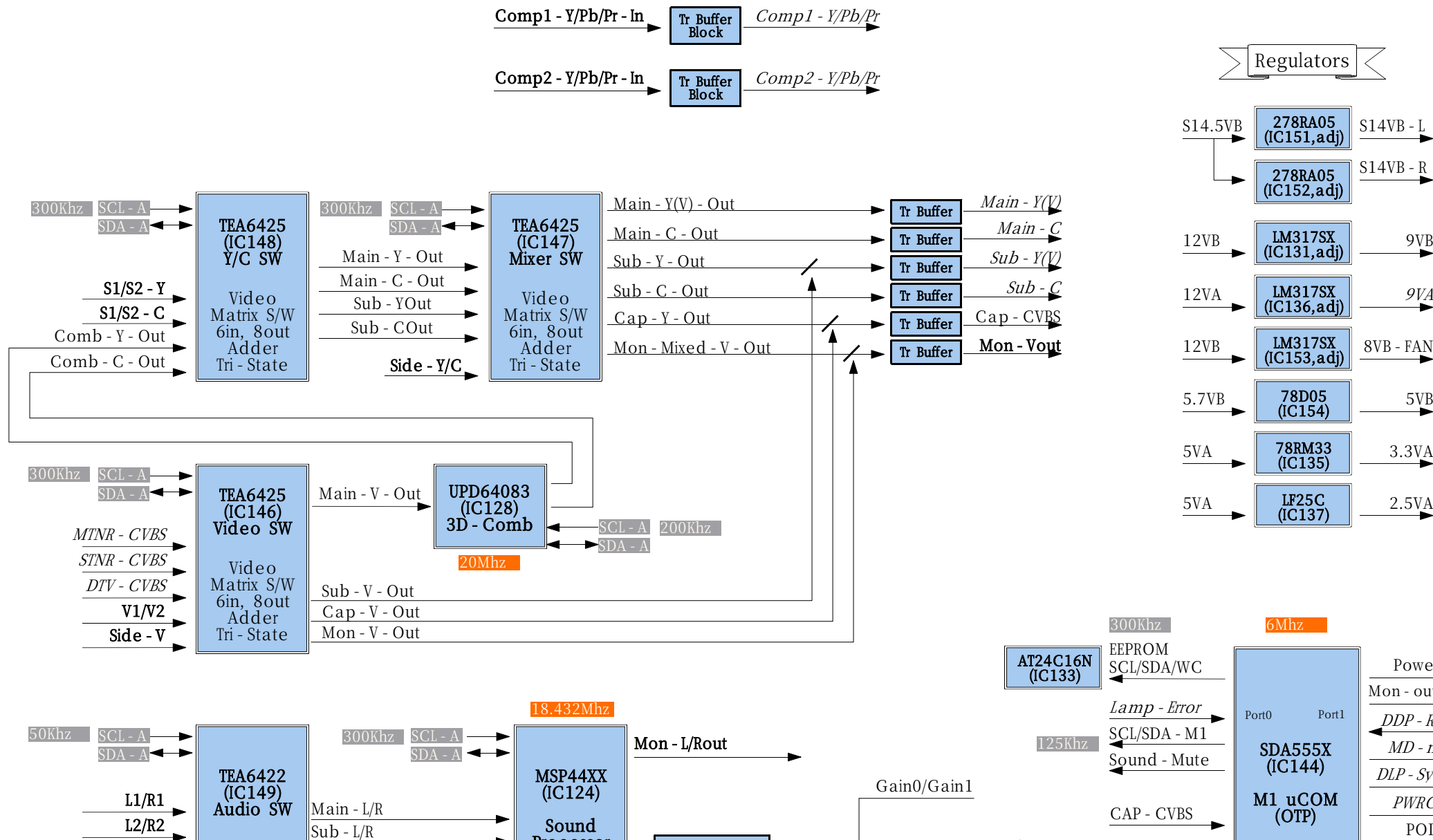


* Robocop Analog Board Blockdiagram *



POD Channel
(NT&ATSC&QAM)

TUNER
#1
(NT&ATSC)

TUNER
#2
(NT&ATSC)

TUNER SIGNAL
◁ M_CVBS
◁ S_CVBS
◁ M_SIF
◁ S_SIF

VIDEO-1
▶ 3D-Y
▶ 3D-C

VIDEO-2
▶ SUB-V/Y
▶ SUB-C

Componet-1
▶ COMP1_Y
▶ COMP1_Pb
▶ COMP1_Pr

Componet-2
▶ COMP2_Y
▶ COMP2_Pb
▶ COMP2_Pr

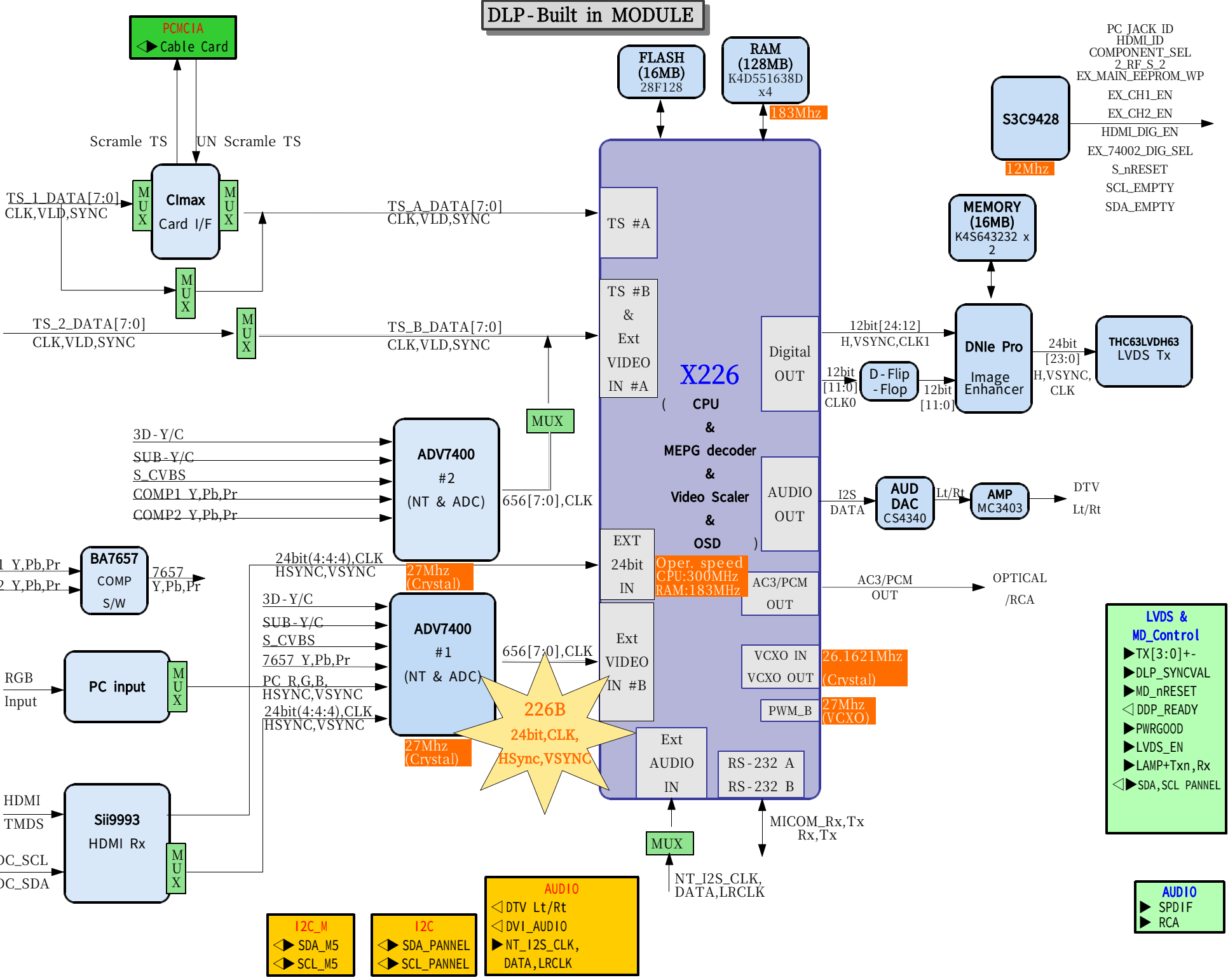
PC
▶ PC_R
▶ PC_R
▶ PC_R
▶ PC_HSYNC
▶ PC_VSYNC

DVI Rx
▶ TMDS
▶ DVI_AUDIO

HDMI Rx
▶ TMDS

RS-232
▶ MICOM_Rx
◁ MICOM_Tx

RS-232(Debug)
▶ Rx
◁ Tx



DLP-Built in MODULE

FLASH
(16MB)
28F128

RAM
(128MB)
K4D551638D
x4

183Mhz

S3C9428
12Mhz

MEMORY
(16MB)
K4S643232 x
2

DNle Pro
Image
Enhancer

THC63LVDP63
LVDS Tx

ADV7400
#2
(NT & ADC)

ADV7400
#1
(NT & ADC)

BA7657
COMP
S/W

PC input

Sii9993
HDMI Rx

X226
CPU
&
MPEG decoder
&
Video Scaler
&
OSD

AUD
DAC
CS4340

AMP
MC3403

LVDS &
MD_Control
▶ TX[3:0]±-
▶ DLP_SYNCVAL
▶ MD_nRESET
◁ DDP_READY
▶ PWRGOOD
▶ LVDS_EN
▶ LAMP+Txn, Rx
◁ SDA, SCL PANNEL

AUDIO
▶ SPDIF
▶ RCA

I2C_M
◁ SDA_M5
◁ SCL_M5

I2C
◁ SDA_PANNEL
◁ SCL_PANNEL

AUDIO
◁ DTV Lt/Rt
◁ DVI_AUDIO
▶ NT_I2S_CLK,
DATA, LRCLK

PC JACK ID
HDMI_ID
COMPONENT_SEL
2_RF_S_2
EX_MAIN_EEPROM_WP
EX_CH1_EN
EX_CH2_EN
HDMI_DIG_EN
EX_74002_DIG_SEL
S_nRESET
SCL_EMPTY
SDA_EMPTY