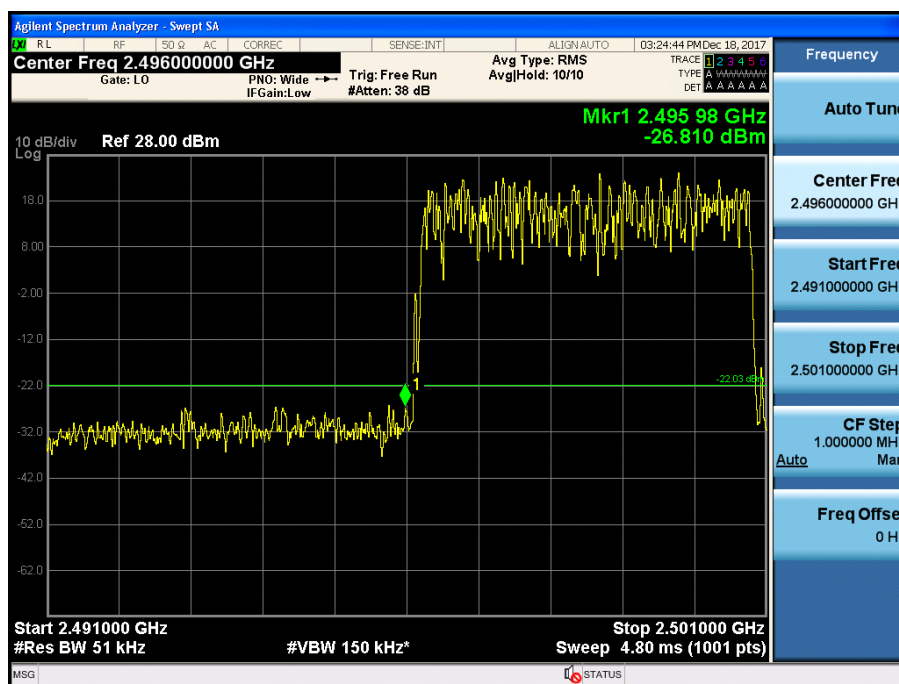
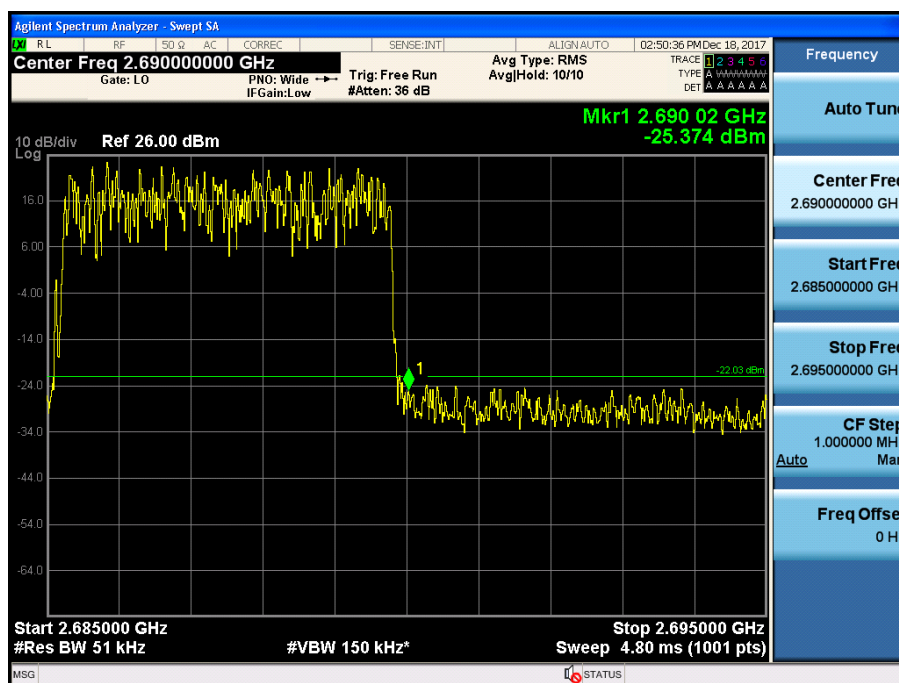


## Test Data at Output Port 7\_256QAM

### [Downlink Low]



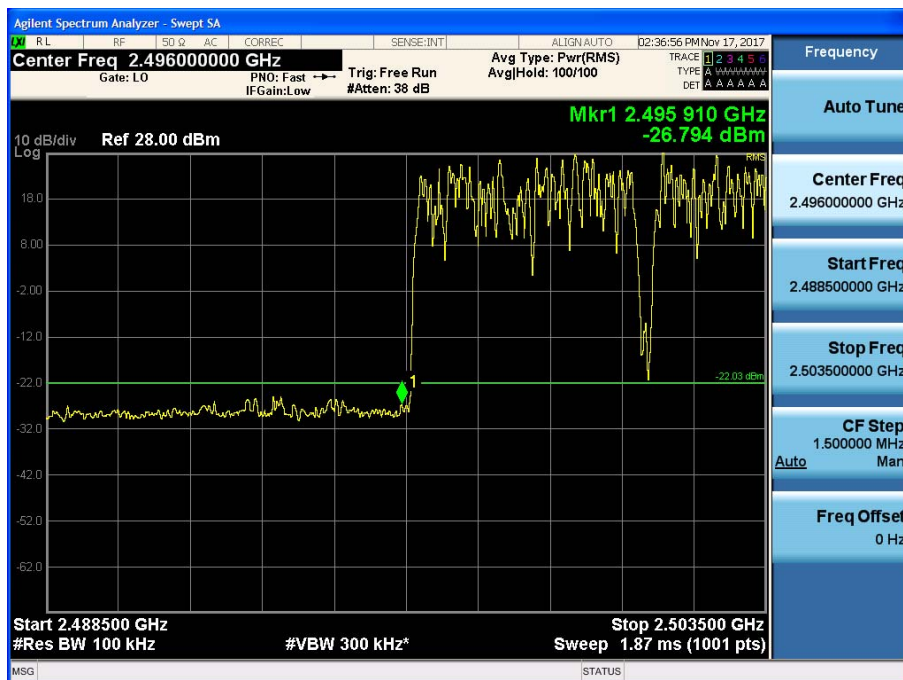
### [Downlink High]



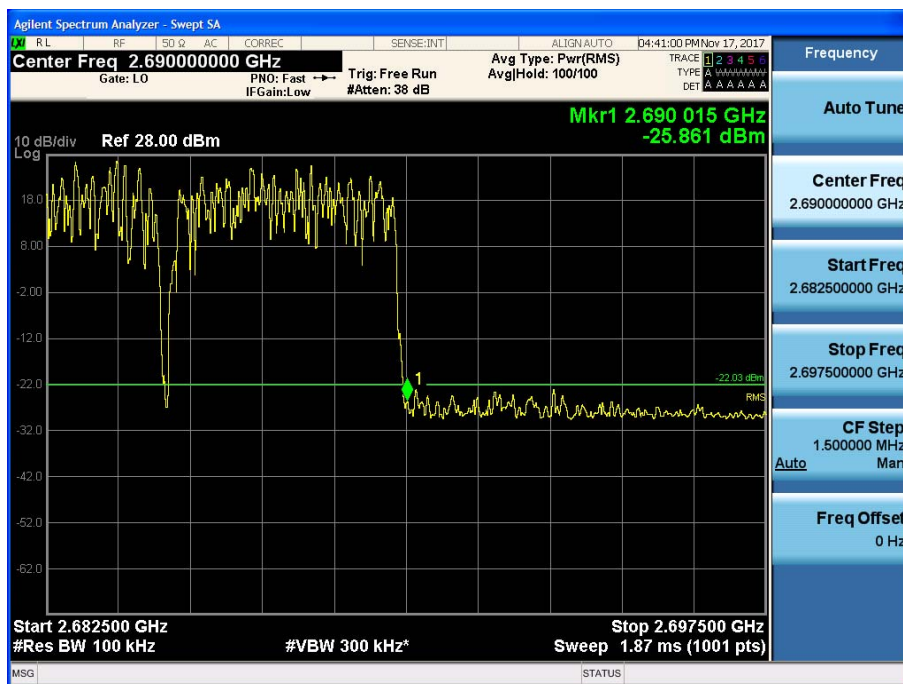
5 MHz + 5 MHz + 5 MHz / 3 Carriers (5 W + 5 W + 5 W)

Test Data at Output Port 0\_256QAM

[Downlink Low]

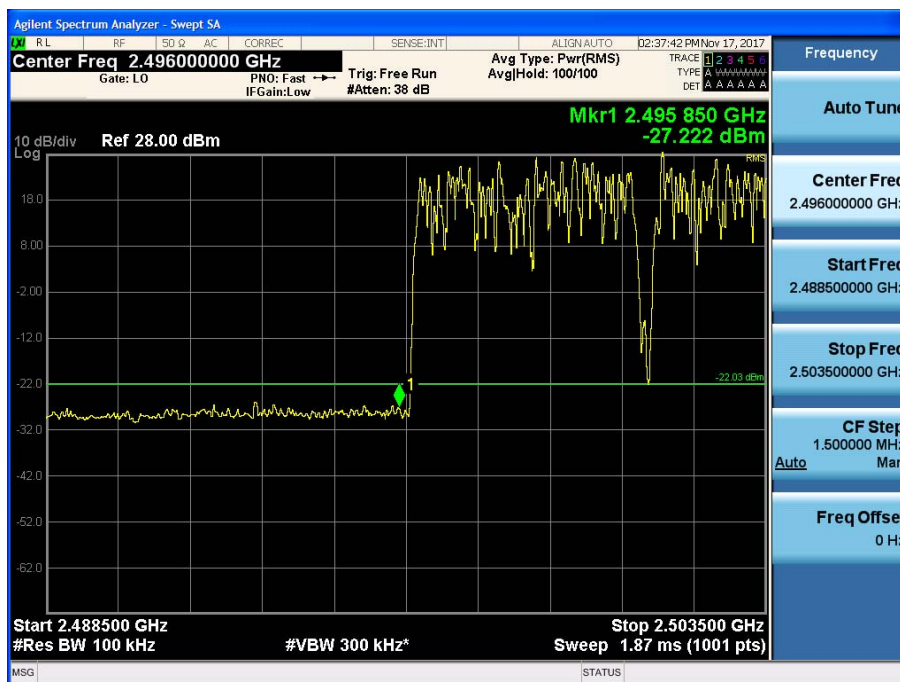


[Downlink High]

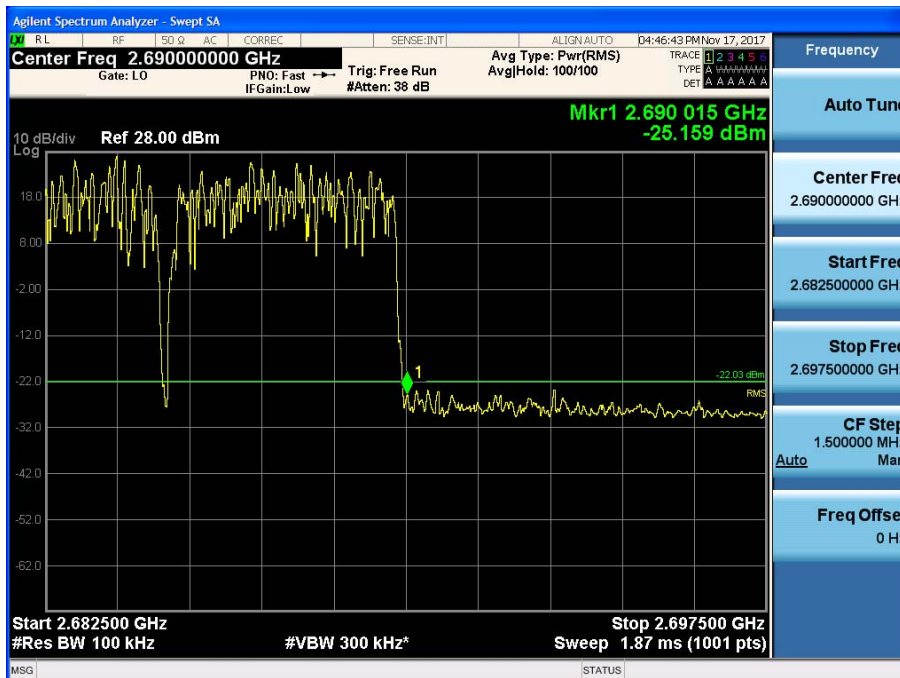


# Test Data at Output Port 1\_256QAM

## [Downlink Low]

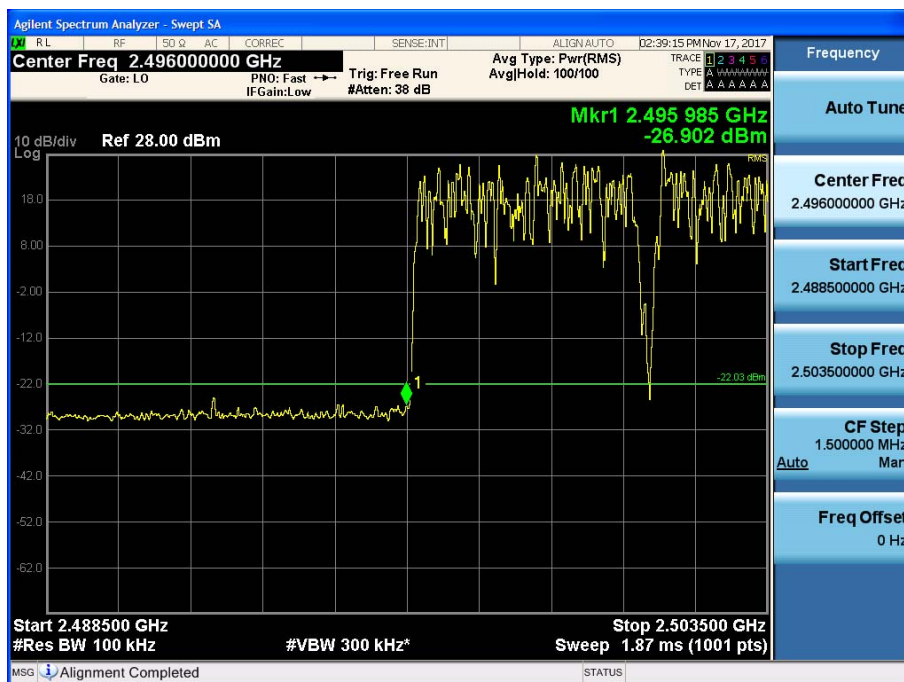


## [Downlink High]

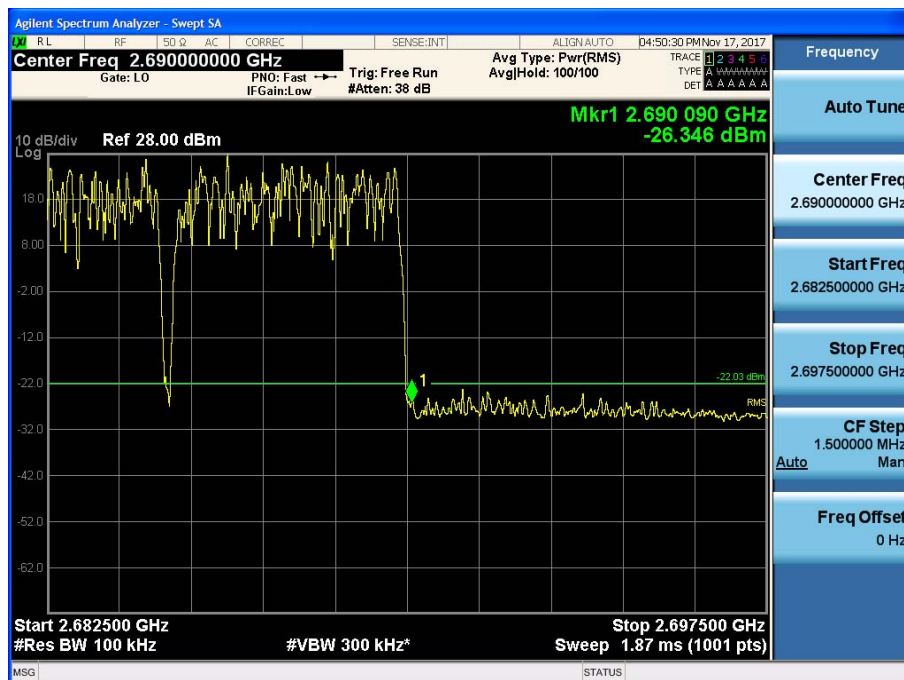


## Test Data at Output Port 2\_256QAM

### [Downlink Low]

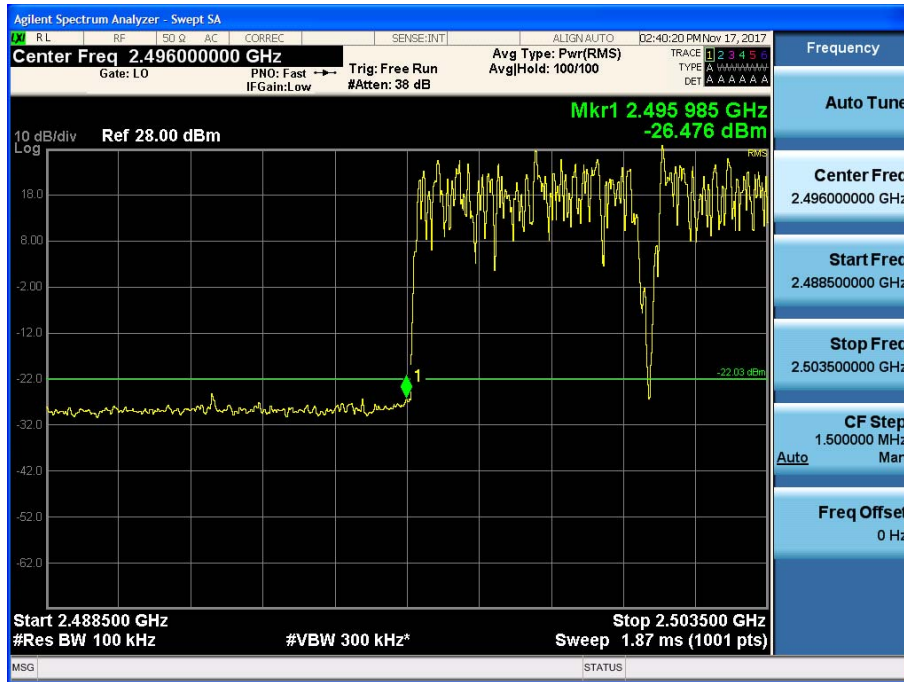


### [Downlink High]

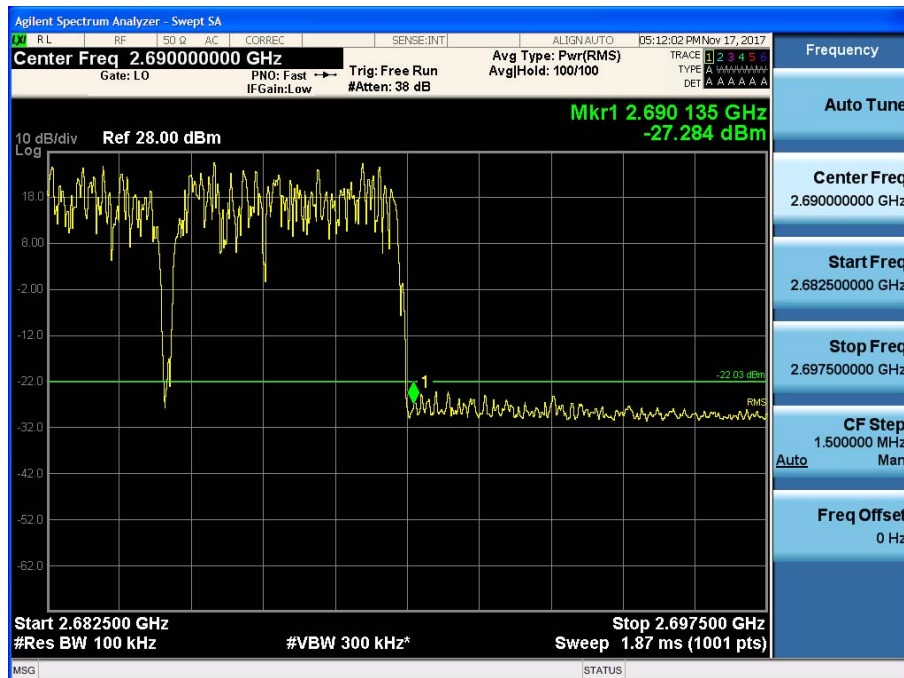


## Test Data at Output Port 3\_256QAM

### [Downlink Low]

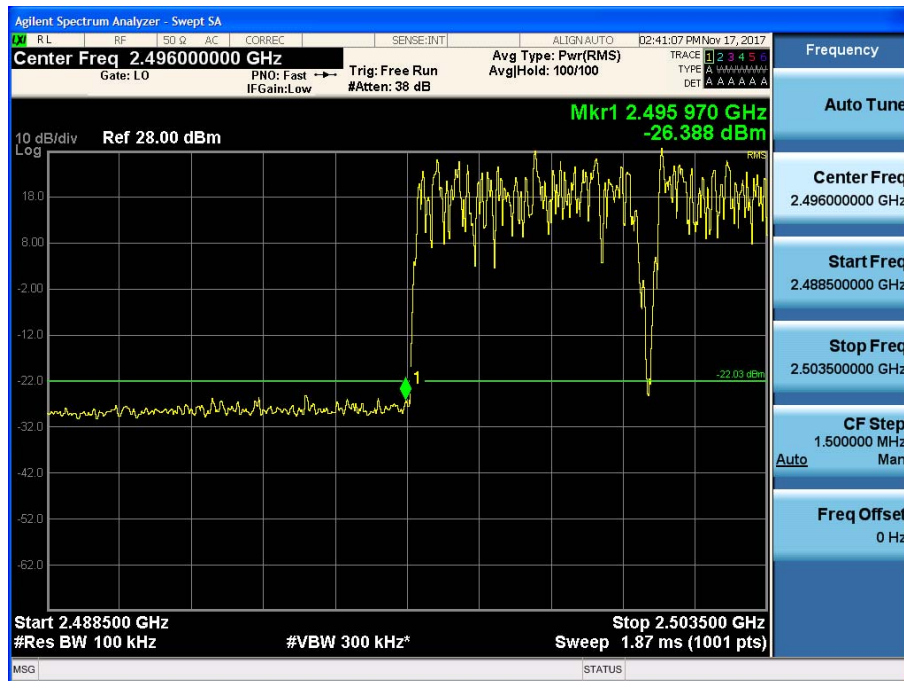


### [Downlink High]

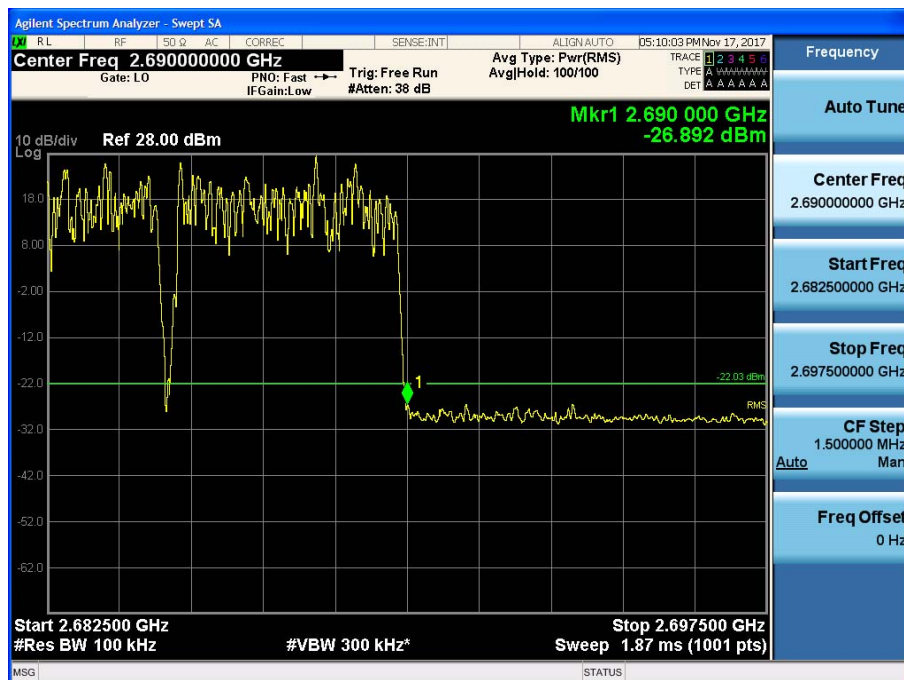


## Test Data at Output Port 4\_256QAM

### [Downlink Low]

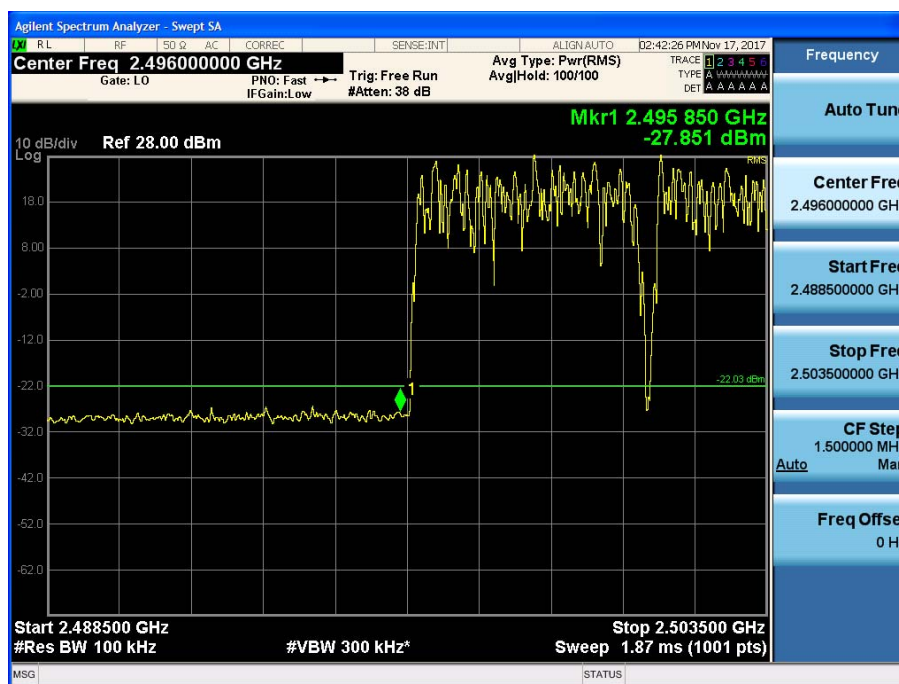


### [Downlink High]

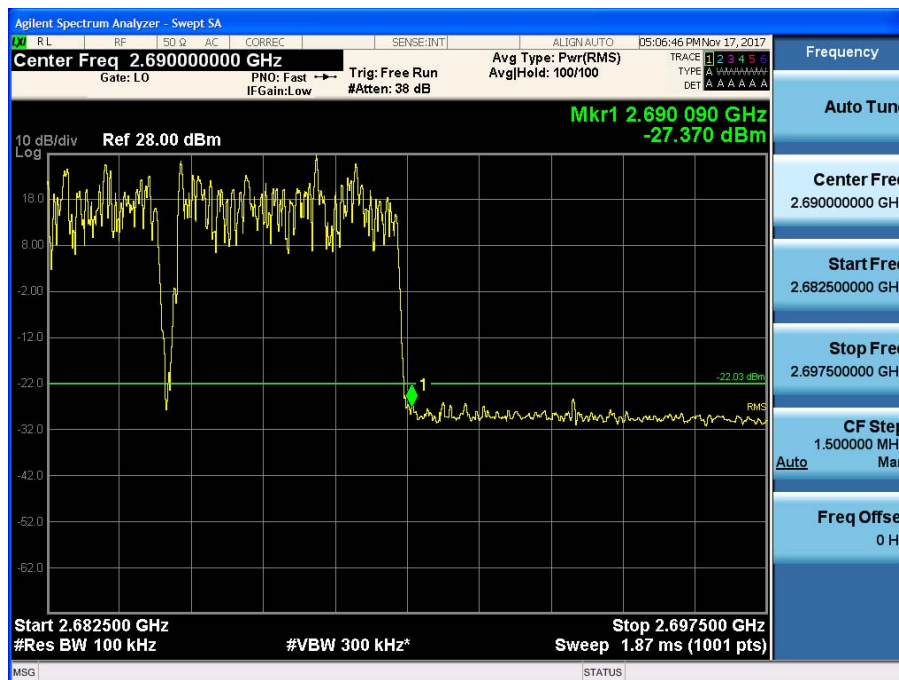


## Test Data at Output Port 5\_256QAM

### [Downlink Low]

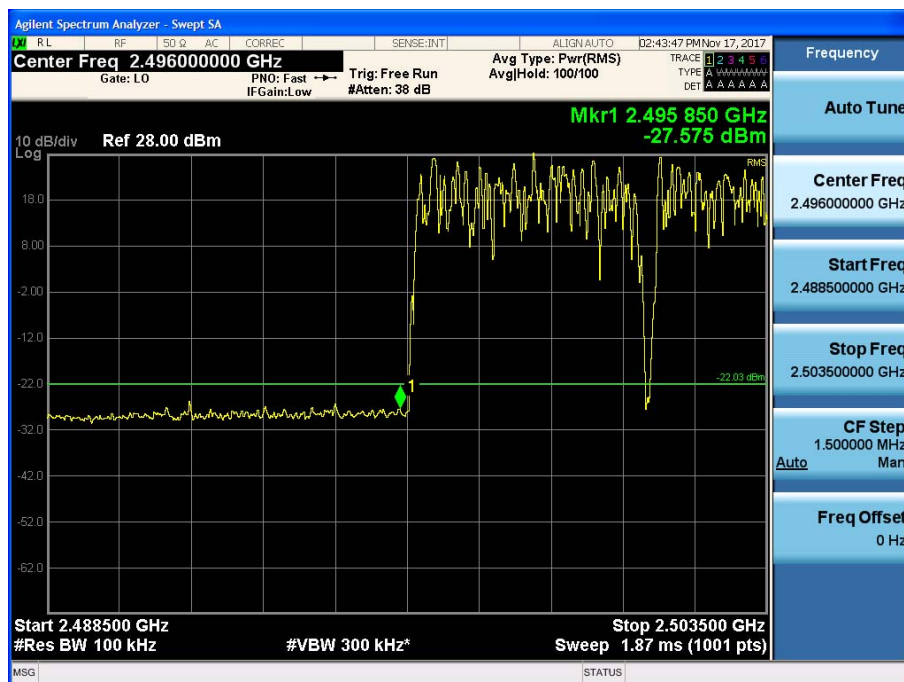


### [Downlink High]

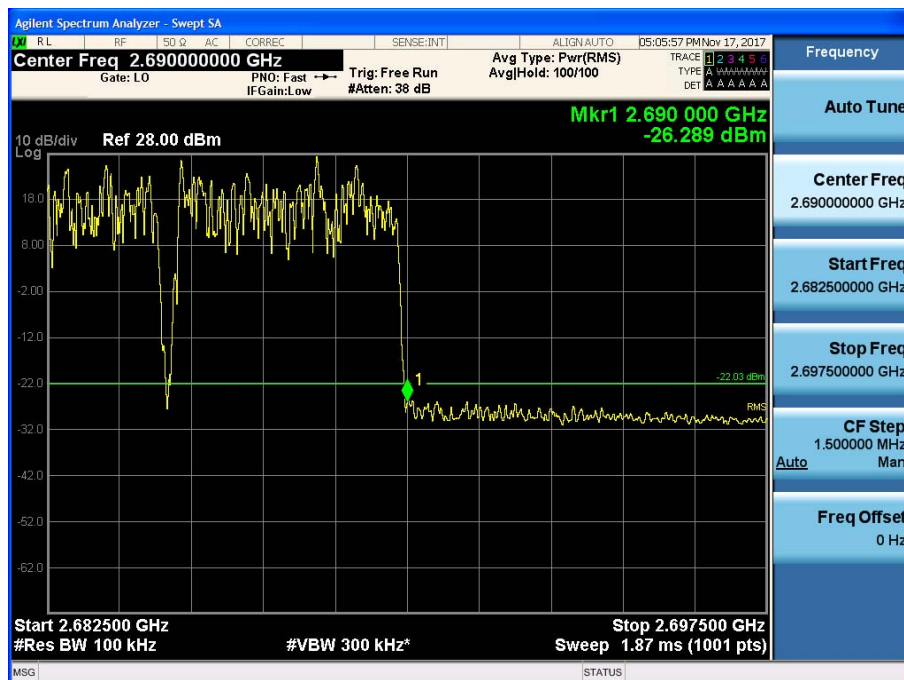


## Test Data at Output Port 6\_256QAM

### [Downlink Low]

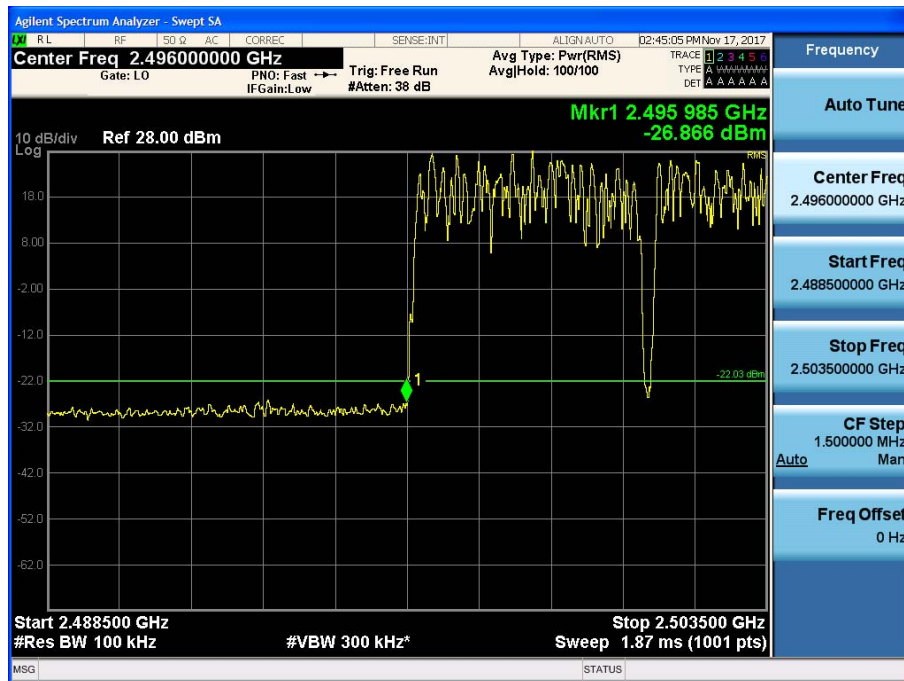


### [Downlink High]

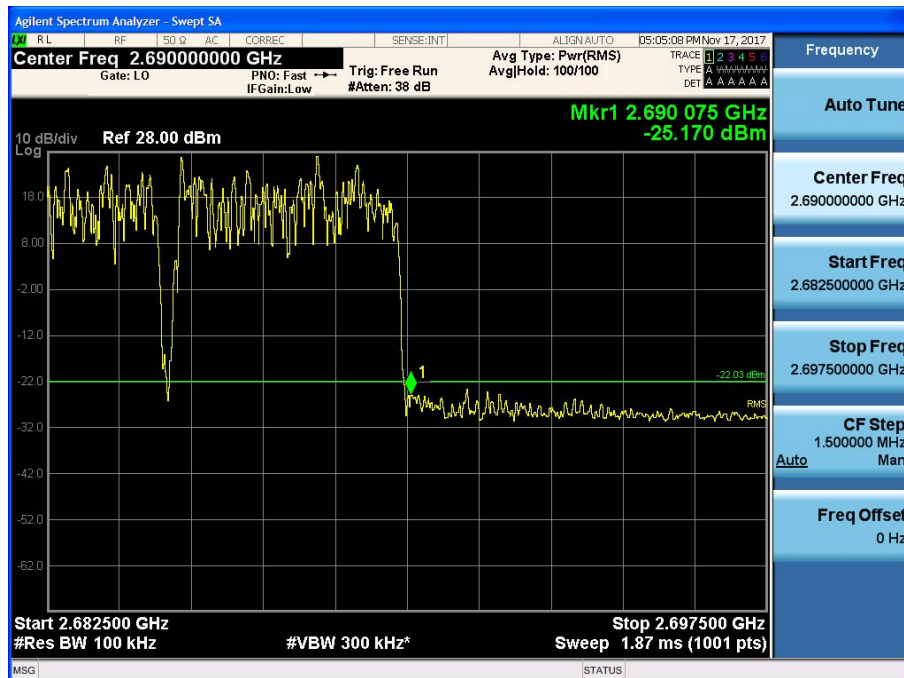


**Test Data at Output Port 7\_256QAM**

**[Downlink Low]**



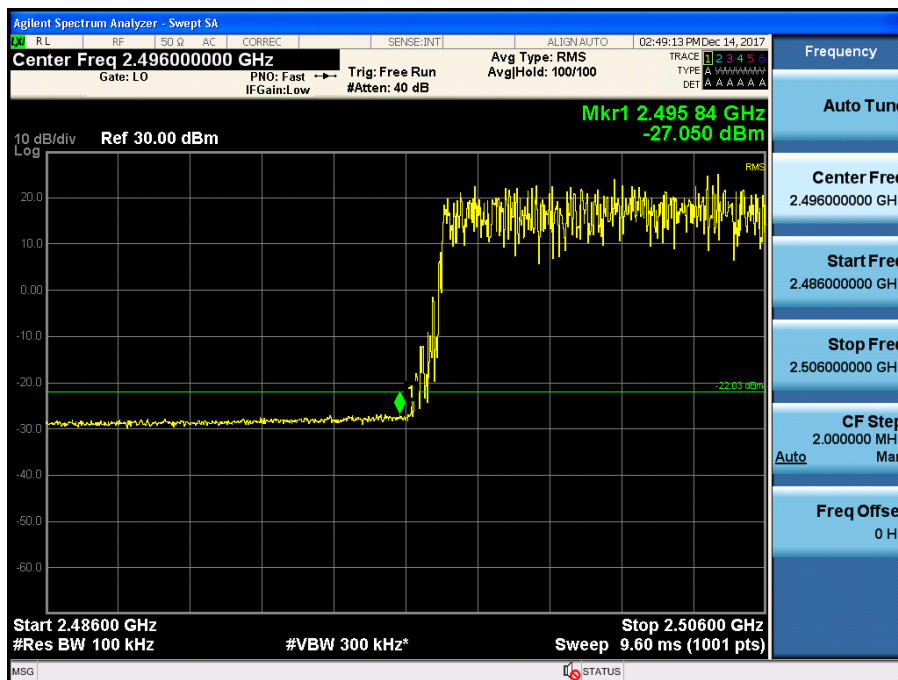
**[Downlink High]**



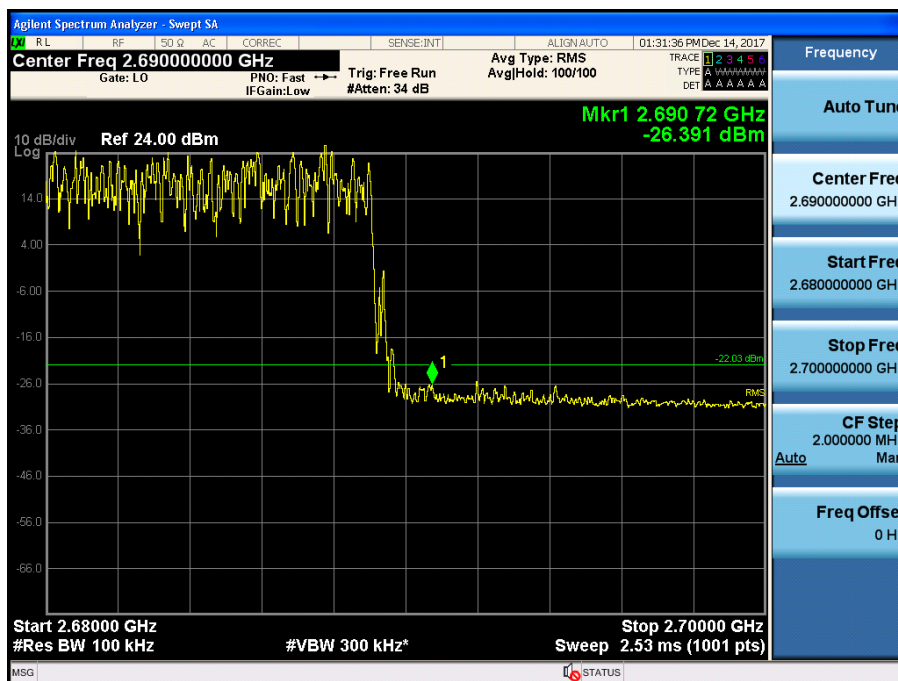
20 MHz / 1 Carrier (10 W)

Test Data at Output Port 0\_256QAM

[Downlink Low]

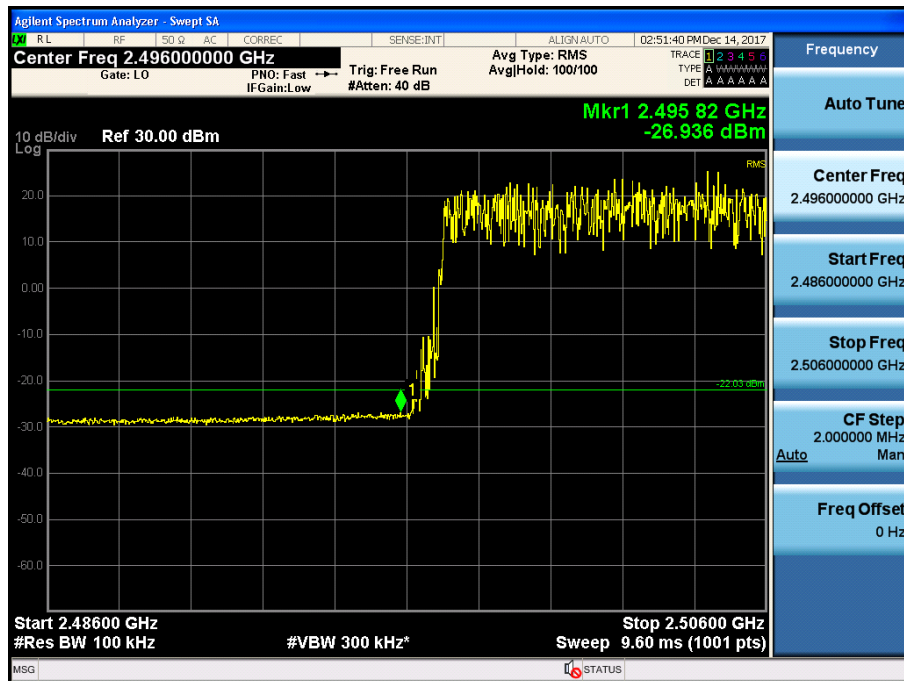


[Downlink High]



## Test Data at Output Port 1\_256QAM

### [Downlink Low]

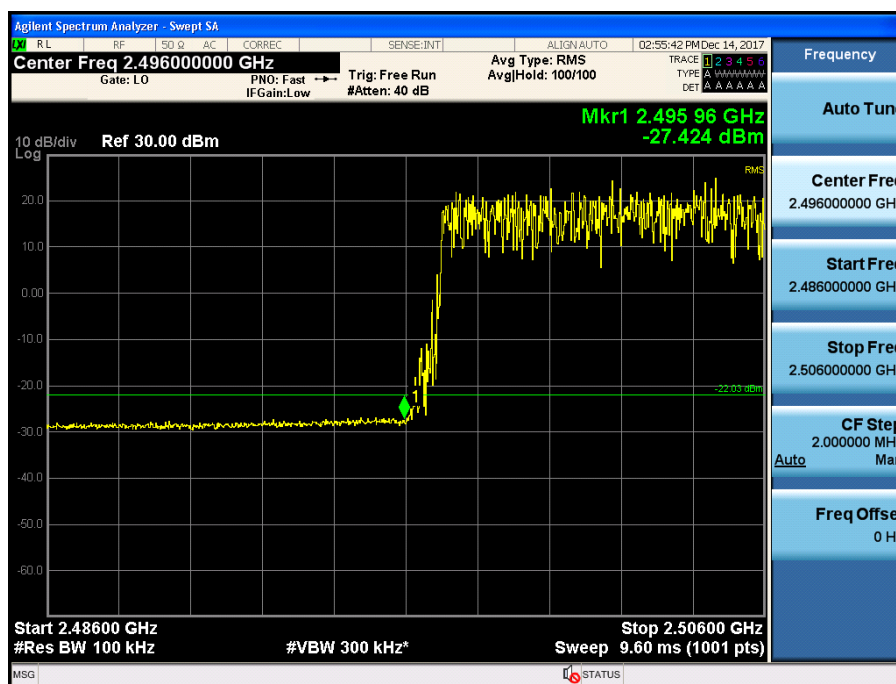


### [Downlink High]

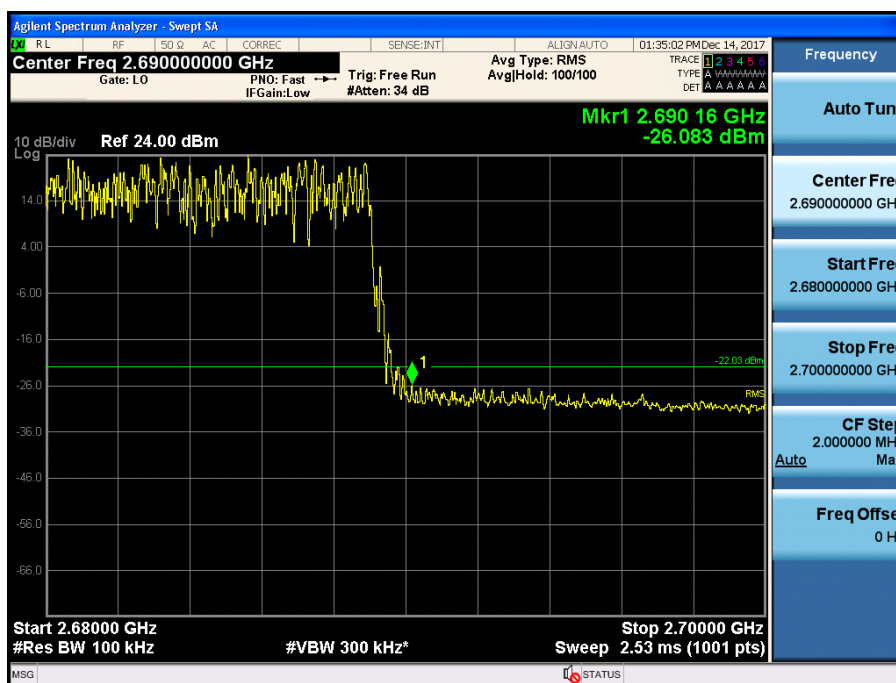


## Test Data at Output Port 2\_256QAM

### [Downlink Low]

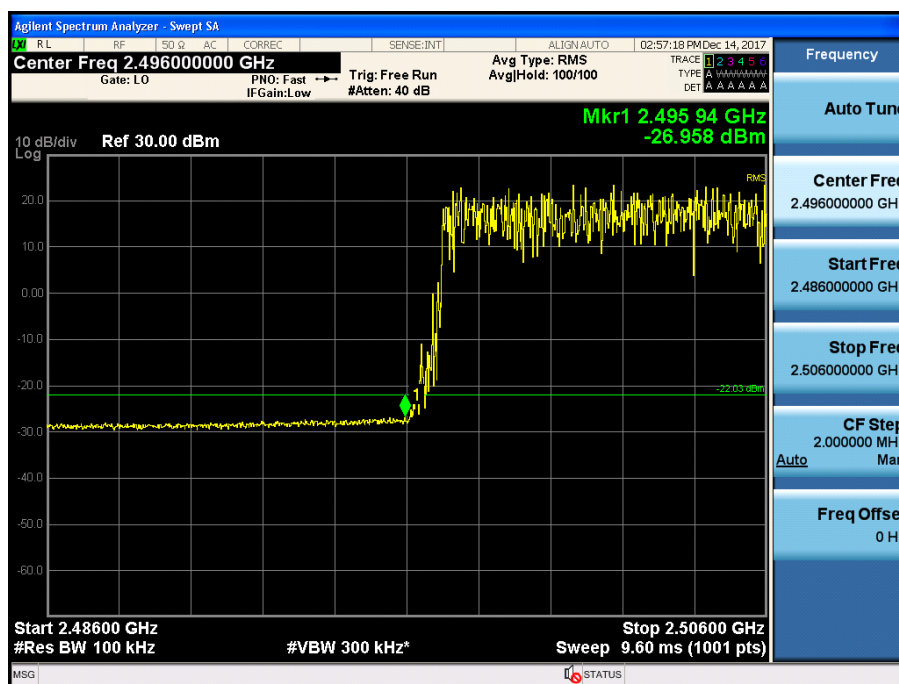


### [Downlink High]

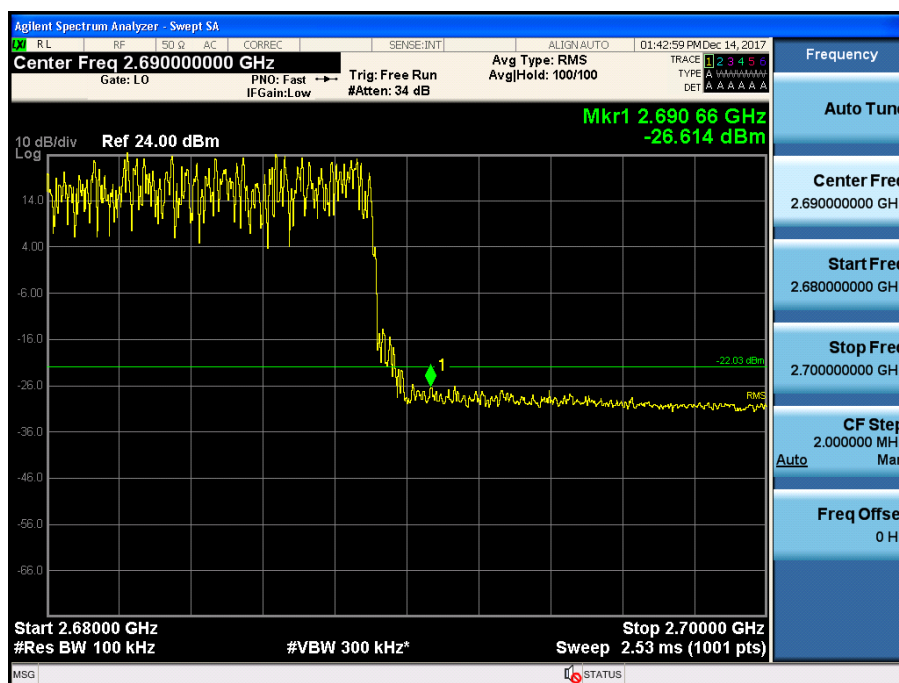


## Test Data at Output Port 3\_256QAM

### [Downlink Low]

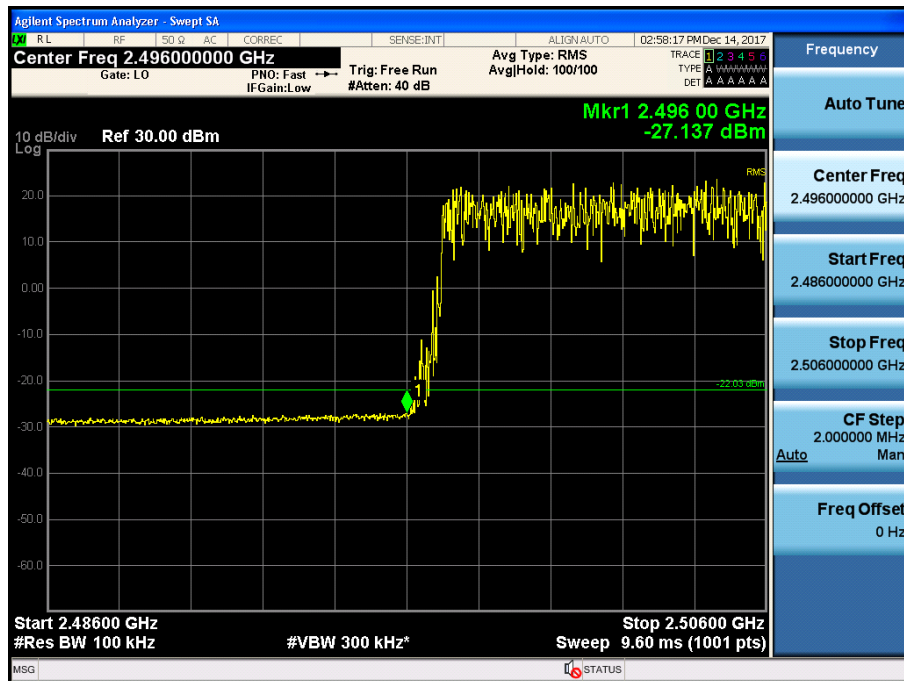


### [Downlink High]

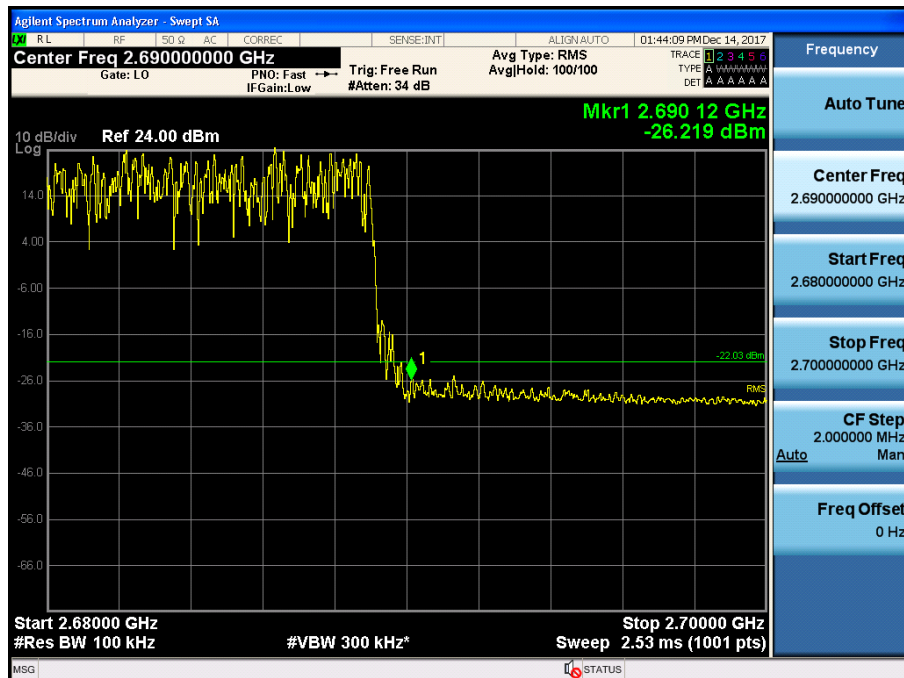


Test Data at Output Port 4\_256QAM

[Downlink Low]

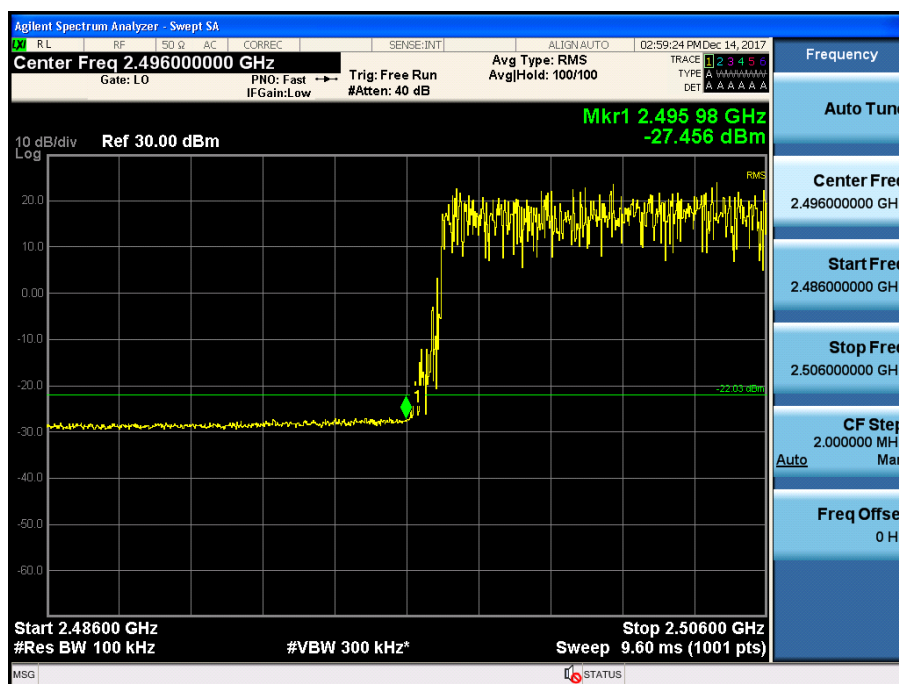


[Downlink High]

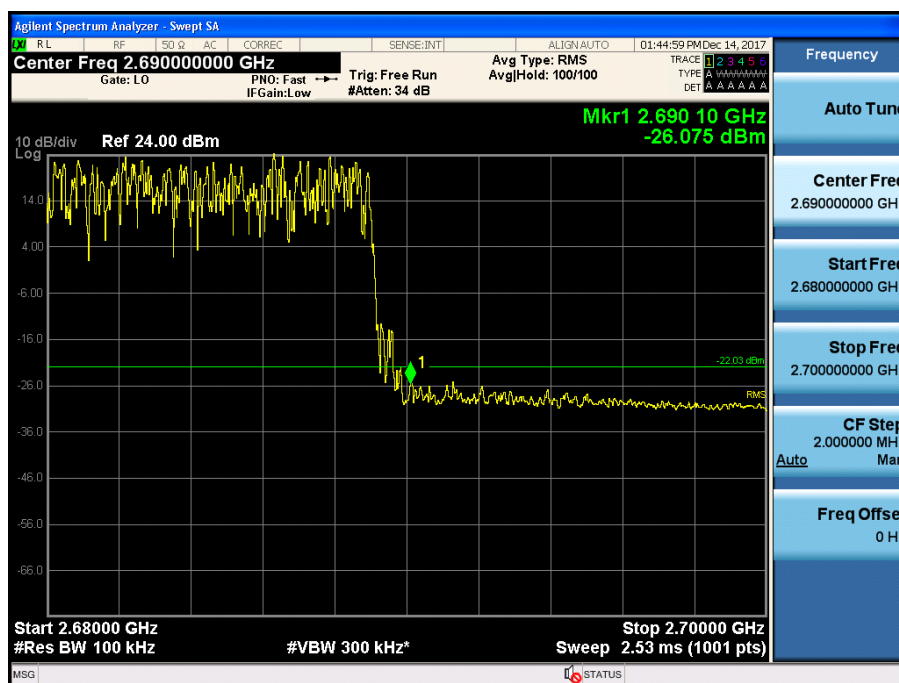


## Test Data at Output Port 5\_256QAM

### [Downlink Low]

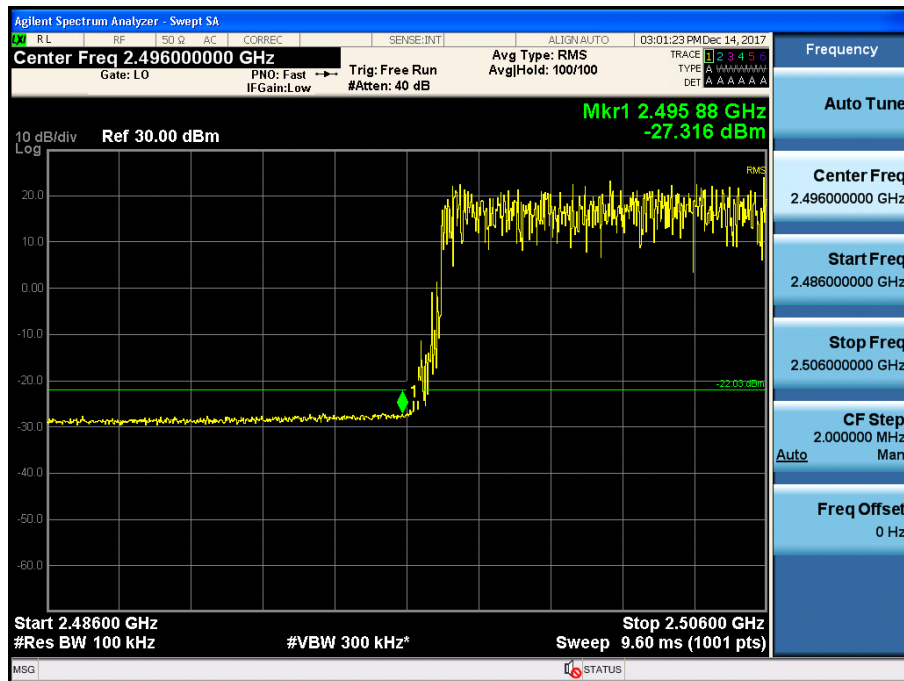


### [Downlink High]

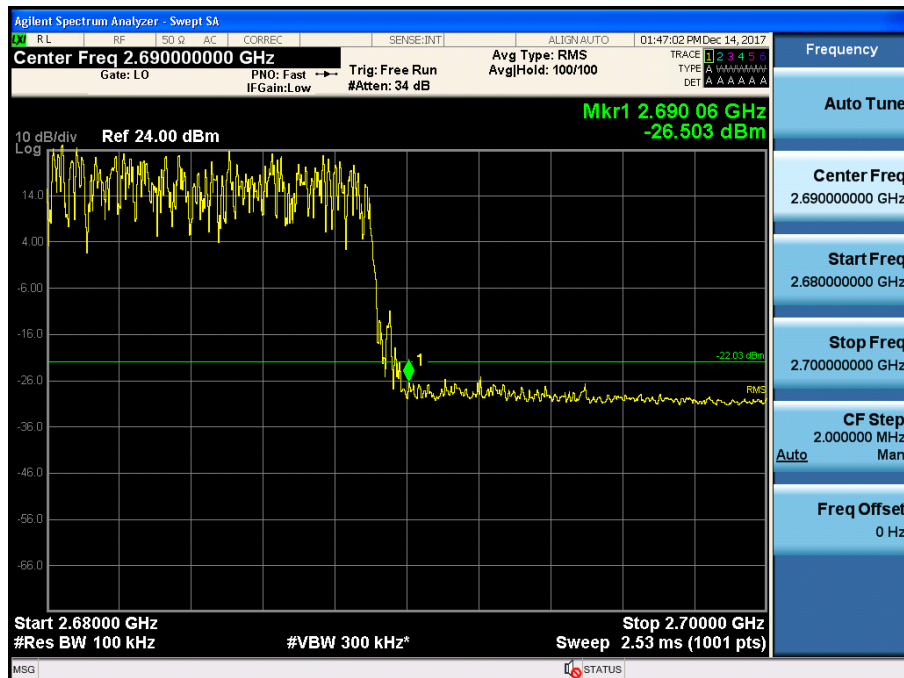


Test Data at Output Port 6\_256QAM

[Downlink Low]

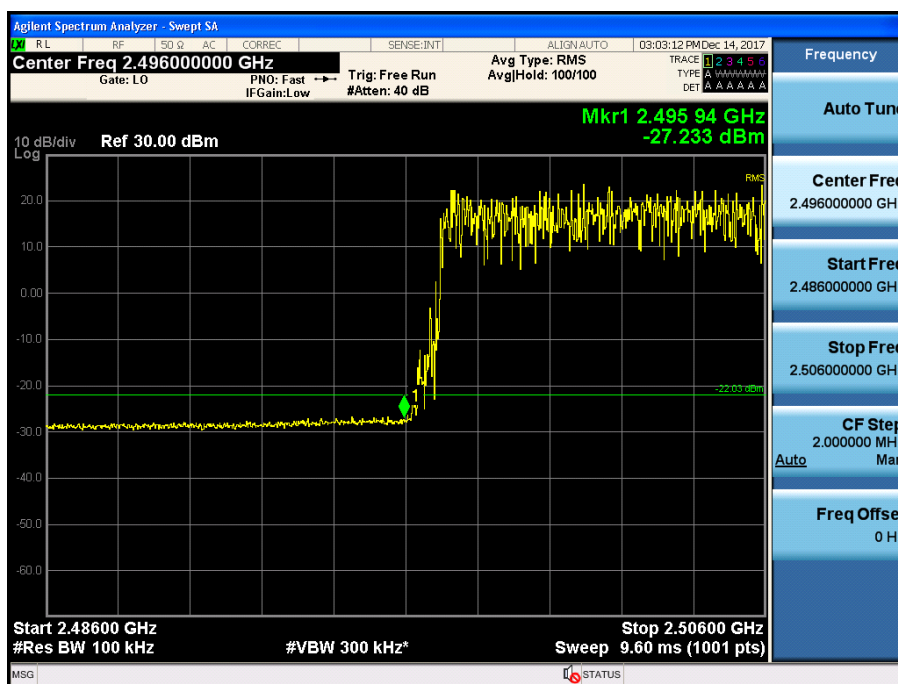


[Downlink High]

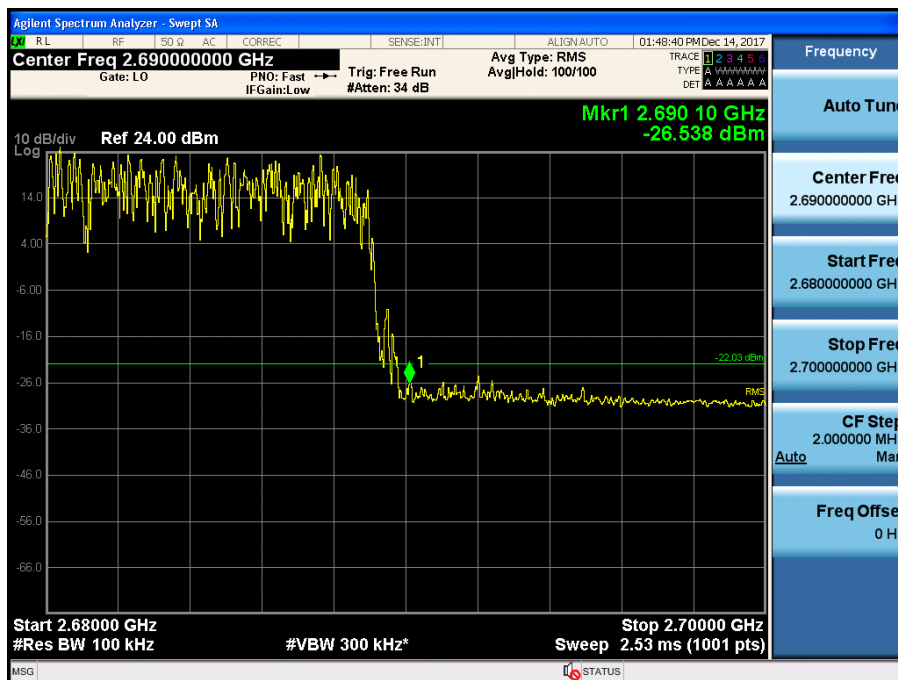


## Test Data at Output Port 7\_256QAM

### [Downlink Low]



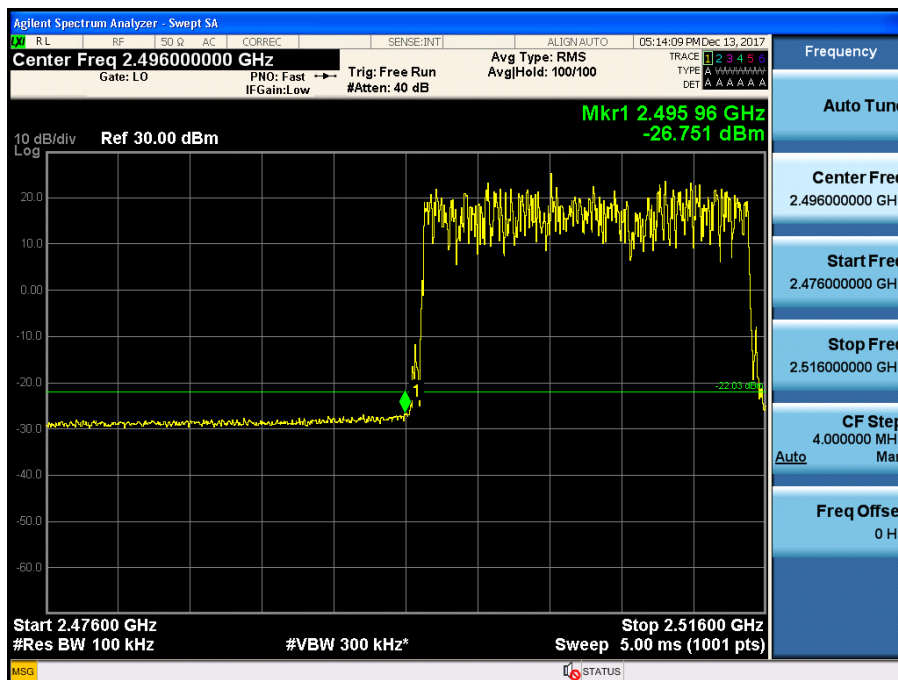
### [Downlink High]



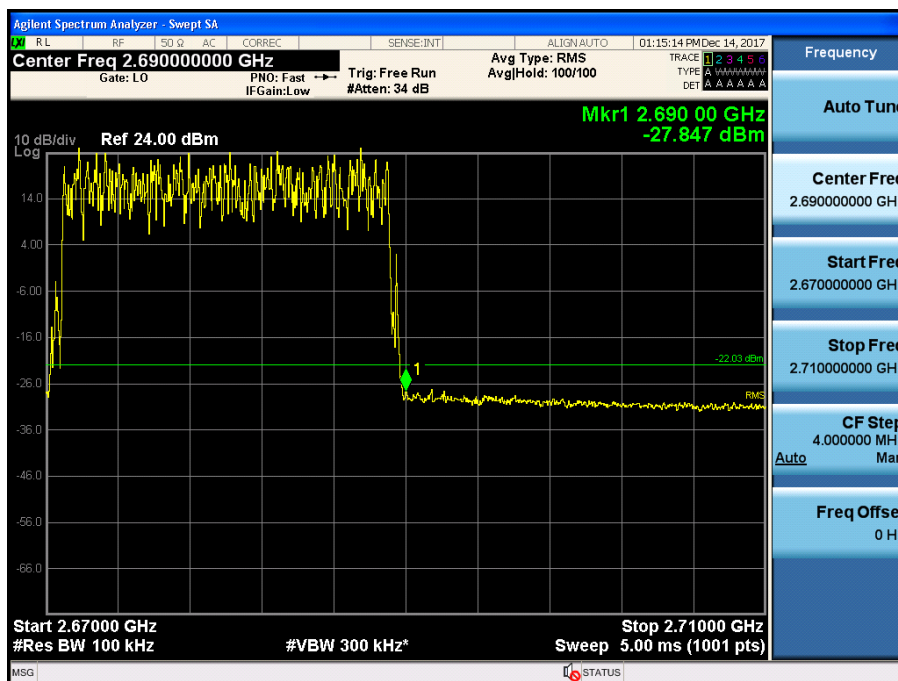
20 MHz + 20 MHz / 2 Carriers (10 W + 10 W)

Test Data at Output Port 0\_256QAM

[Downlink Low]

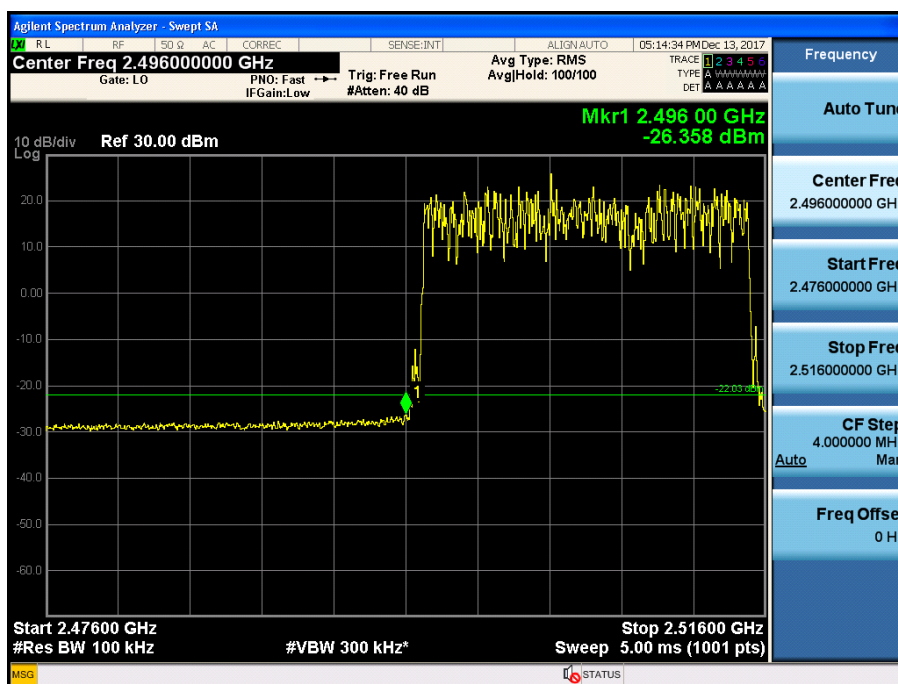


[Downlink High]



## Test Data at Output Port 1\_256QAM

### [Downlink Low]

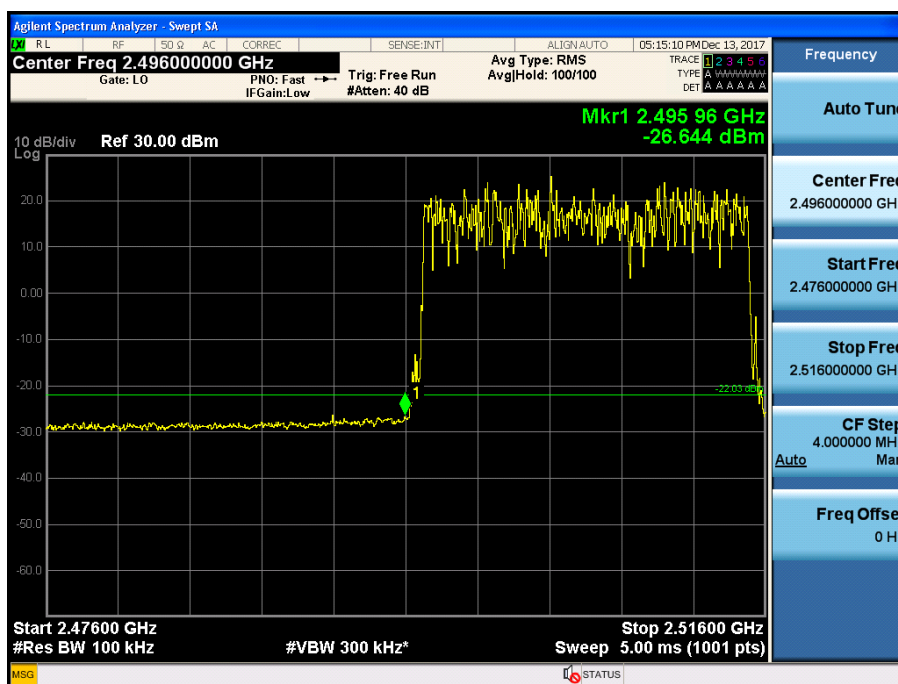


### [Downlink High]

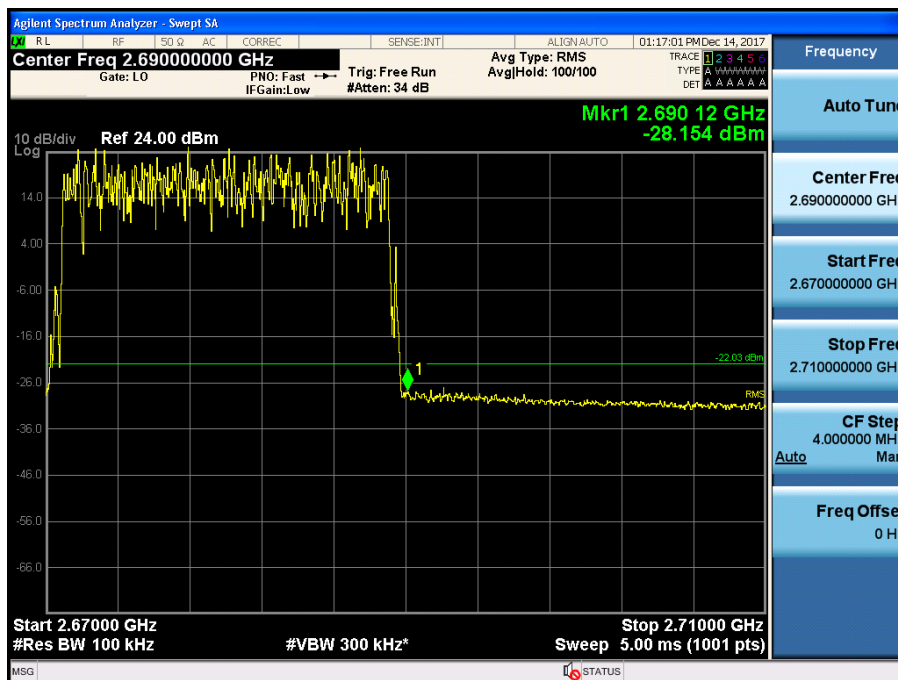


## Test Data at Output Port 2\_256QAM

### [Downlink Low]

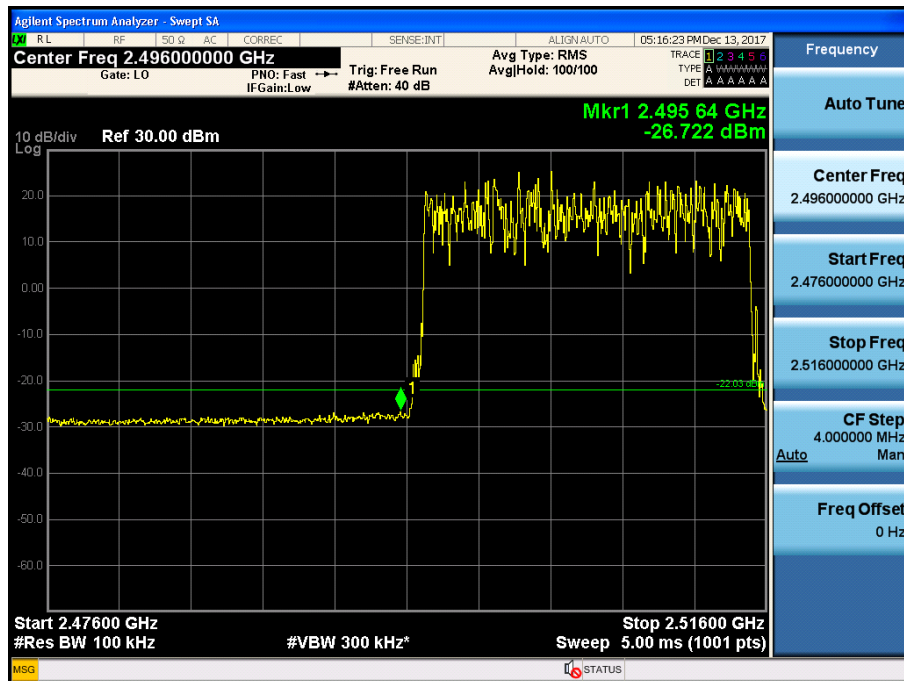


### [Downlink High]

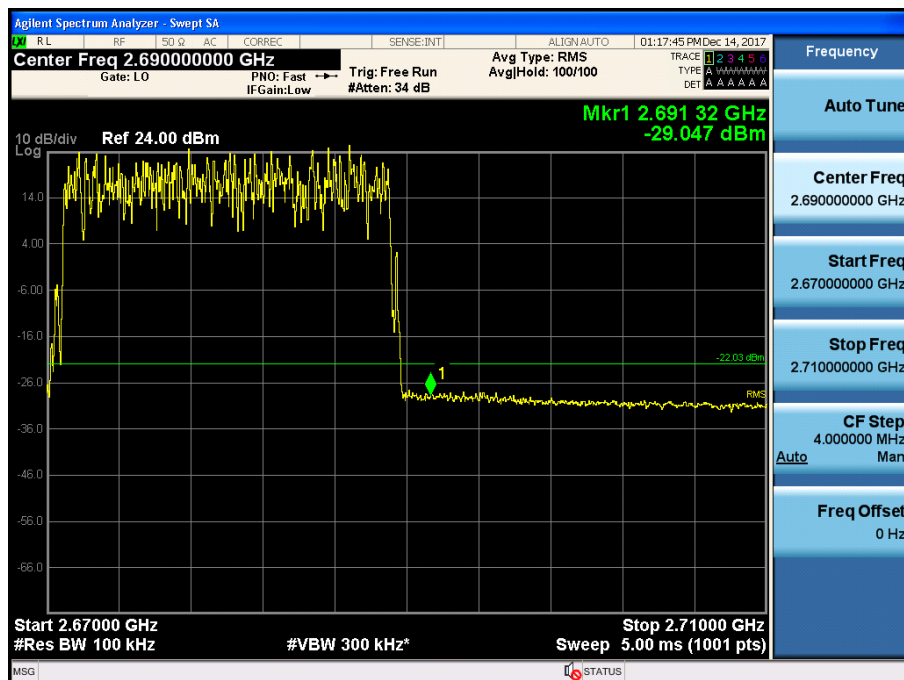


## Test Data at Output Port 3\_256QAM

### [Downlink Low]

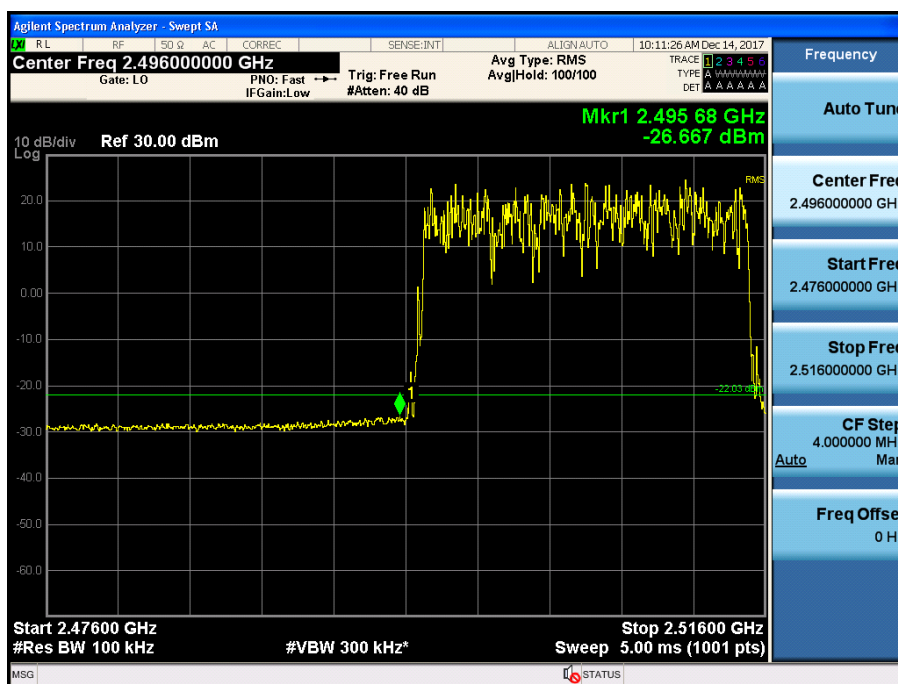


### [Downlink High]

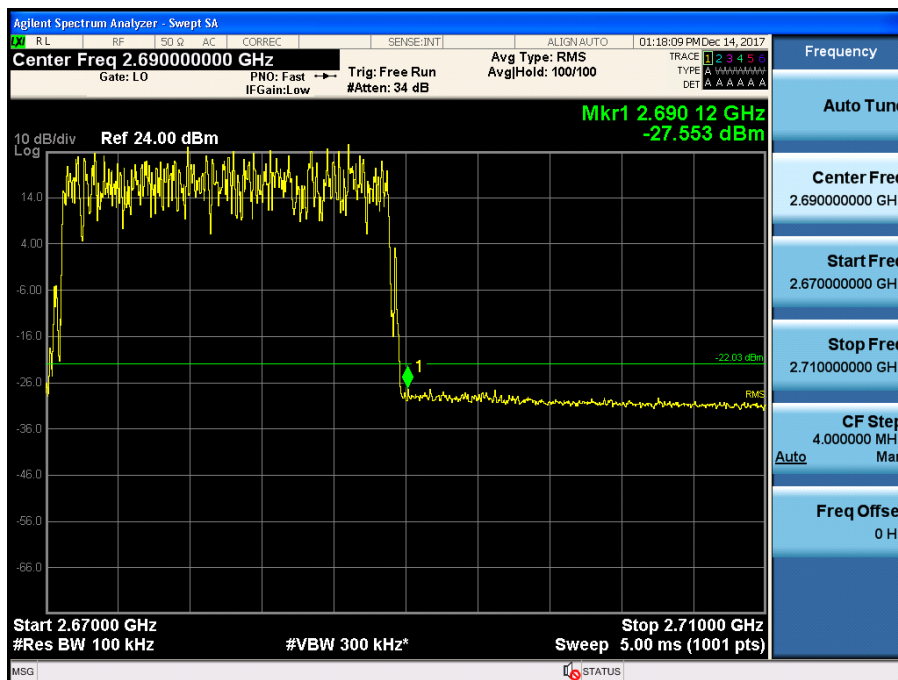


## Test Data at Output Port 4\_256QAM

### [Downlink Low]

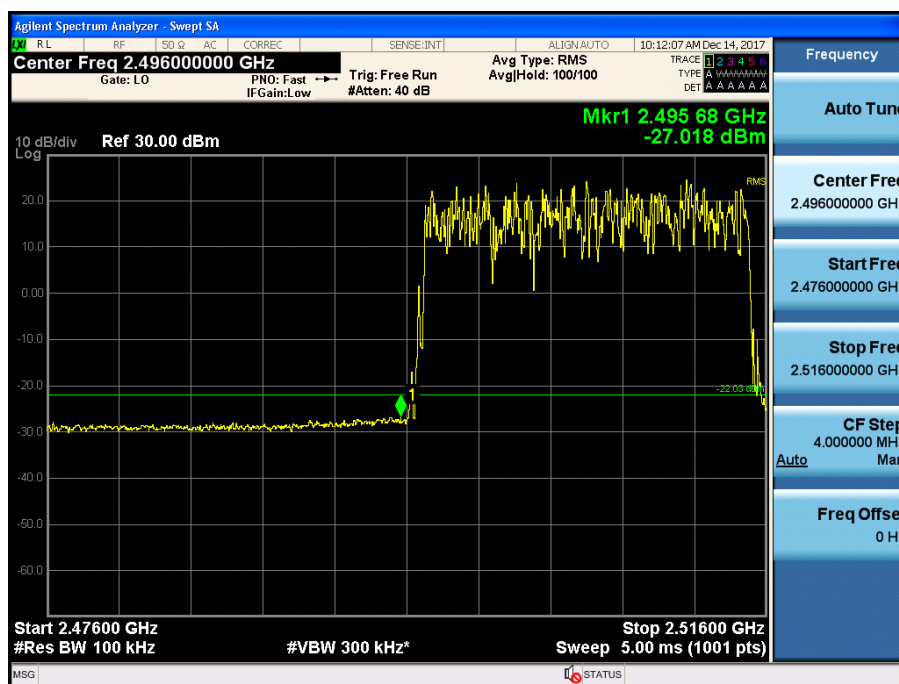


### [Downlink High]

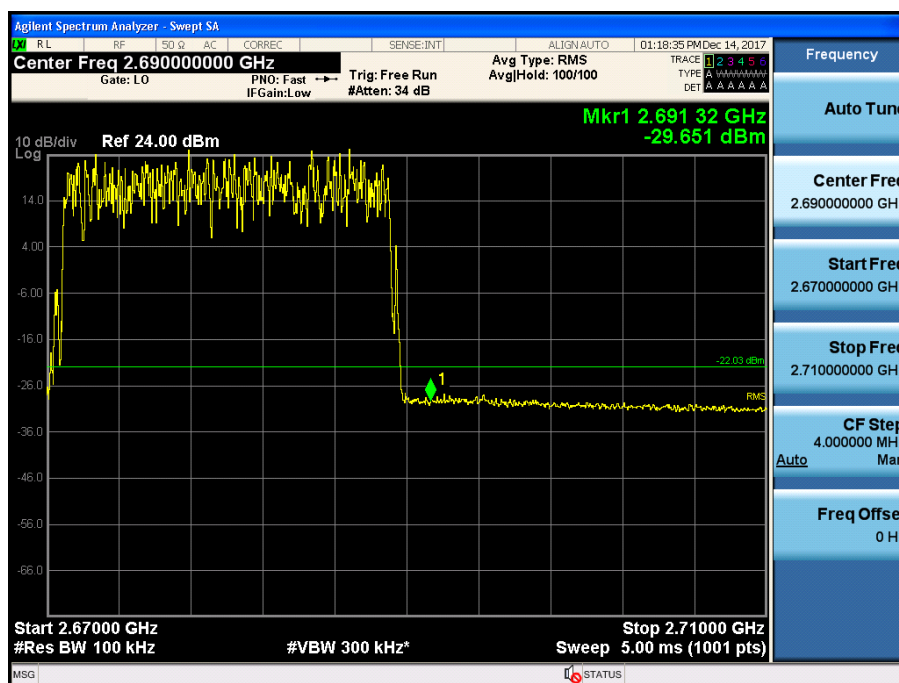


## Test Data at Output Port 5\_256QAM

### [Downlink Low]

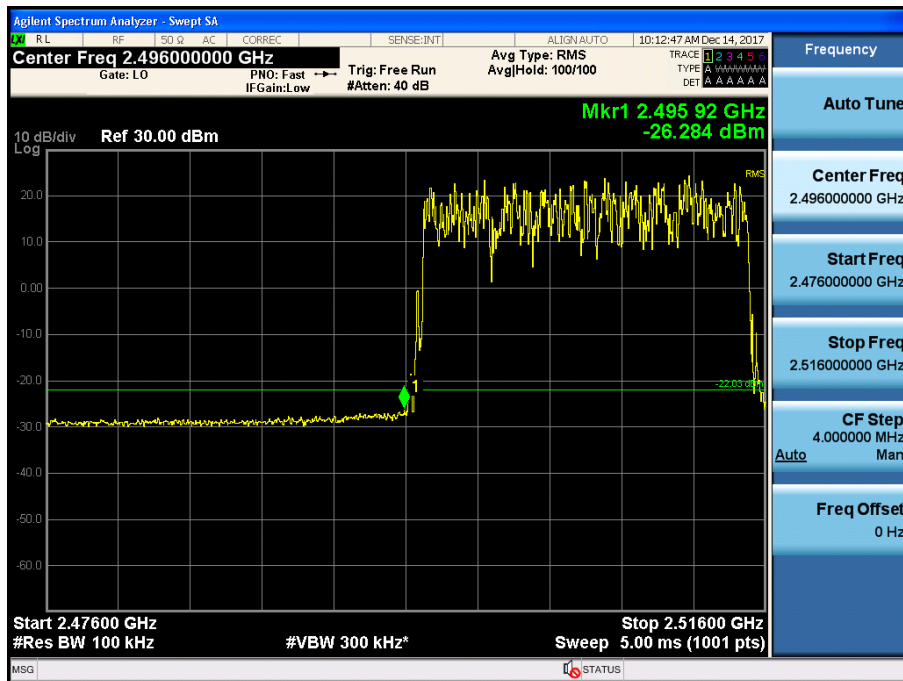


### [Downlink High]

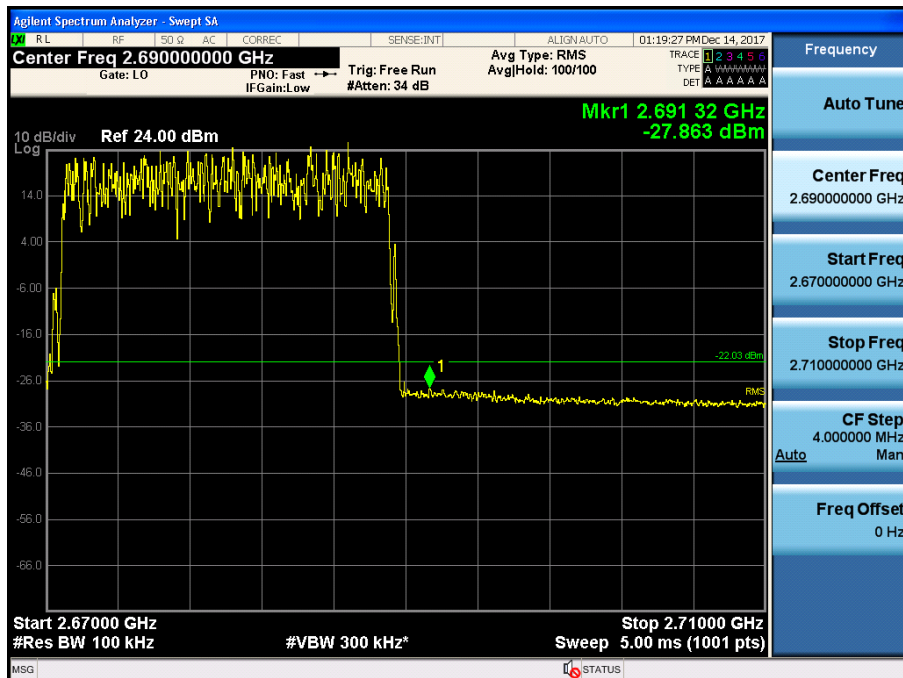


Test Data at Output Port 6\_256QAM

[Downlink Low]

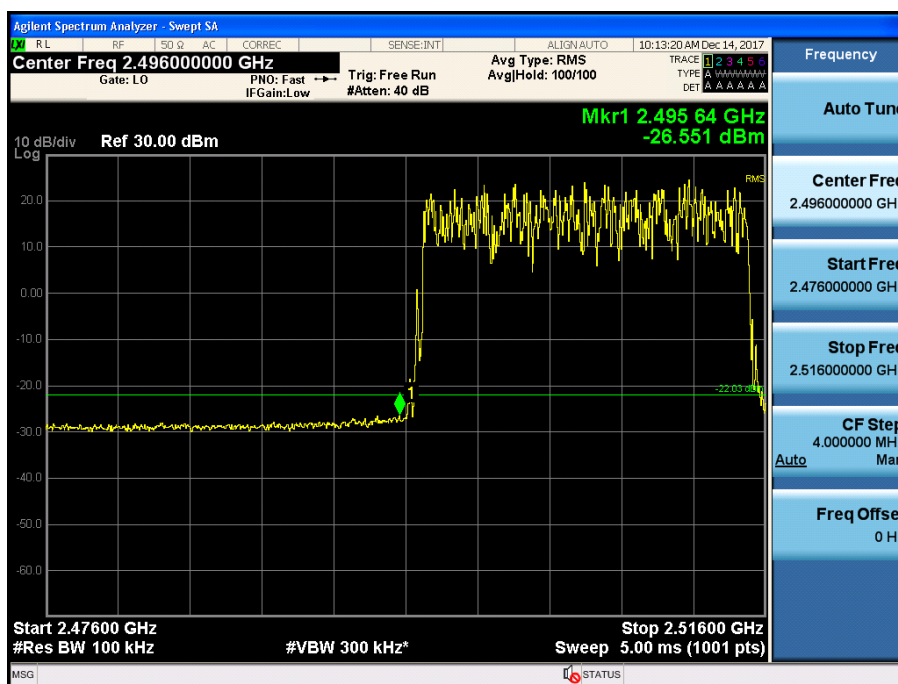


[Downlink High]

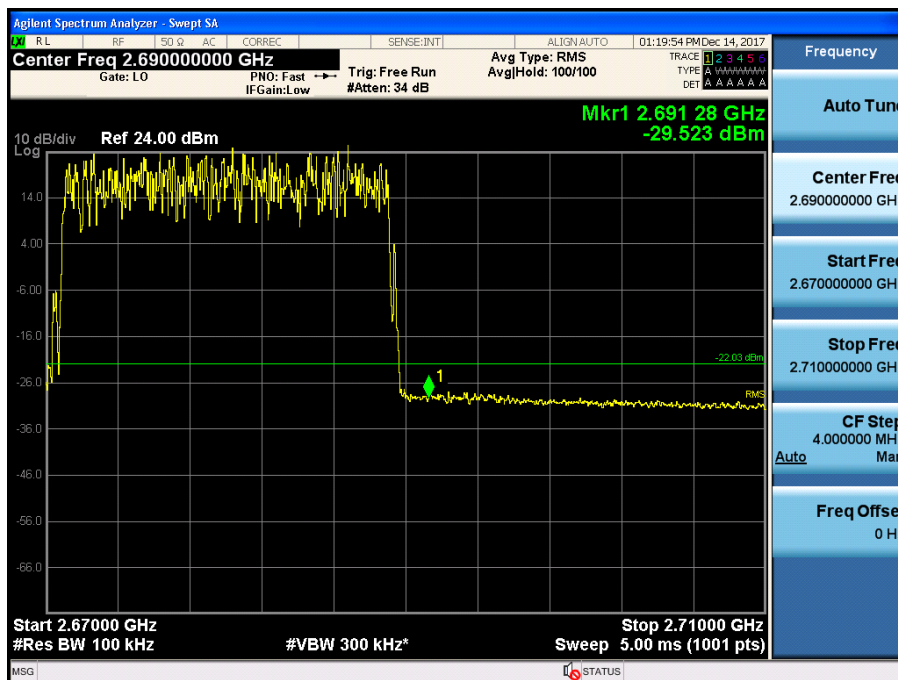


## Test Data at Output Port 7\_256QAM

### [Downlink Low]



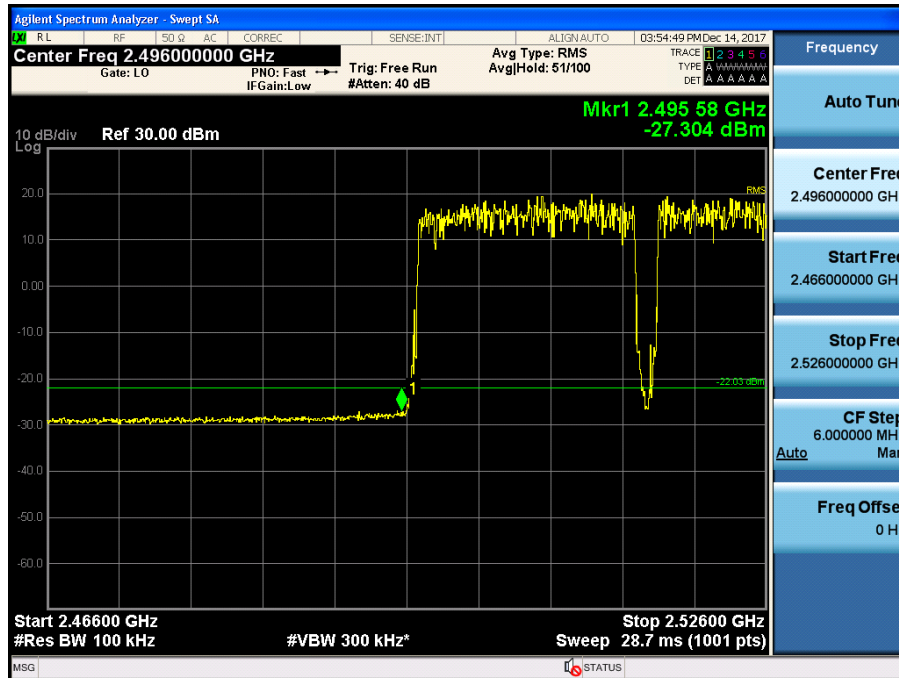
### [Downlink High]



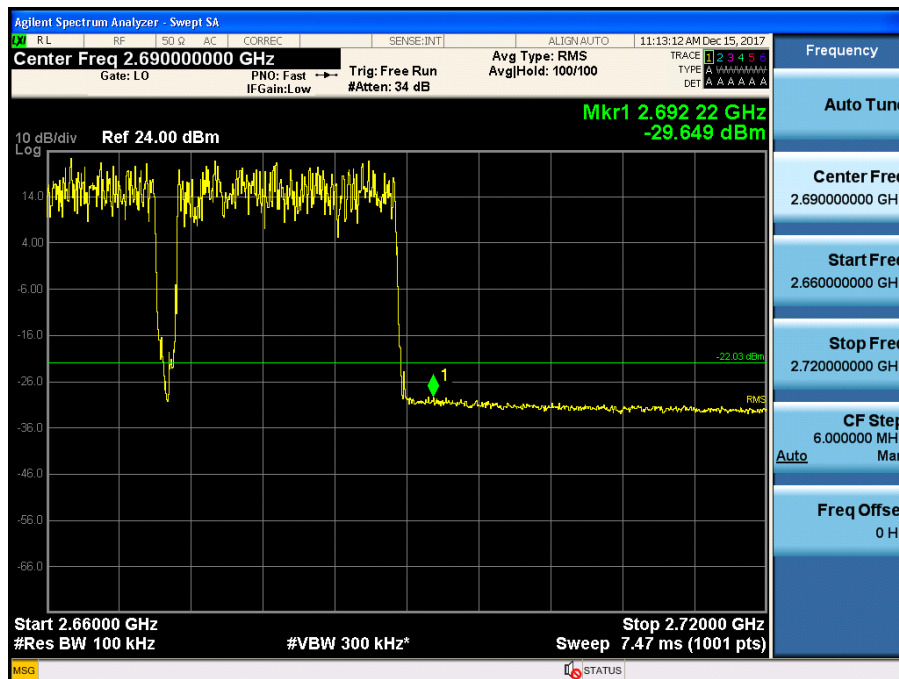
20 MHz + 20 MHz + 20 MHz / 3 Carriers (6.67 W + 6.67 W + 6.67 W)

Test Data at Output Port 0\_256QAM

[Downlink Low]

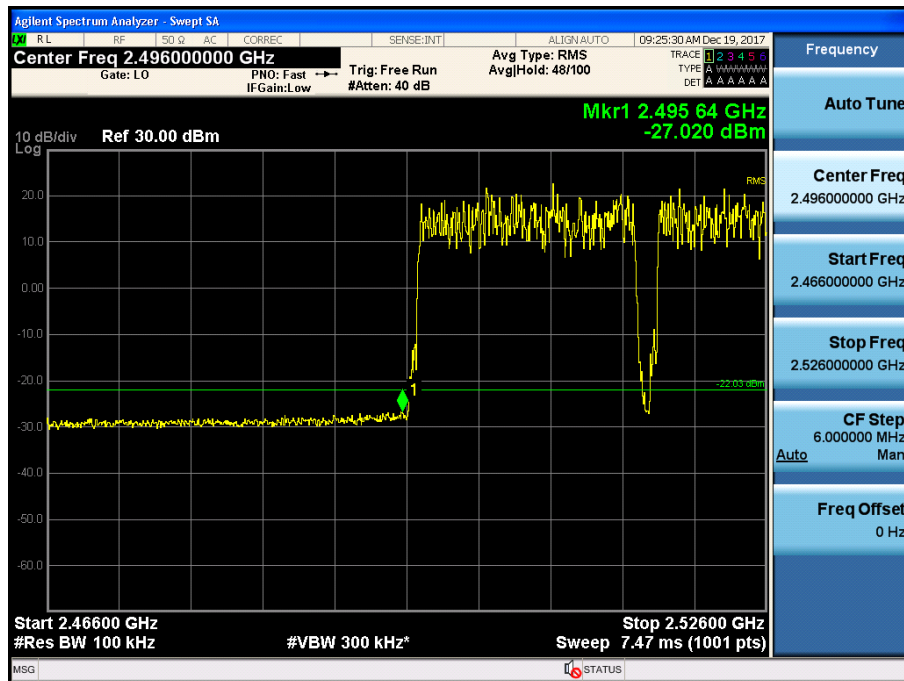


[Downlink High]

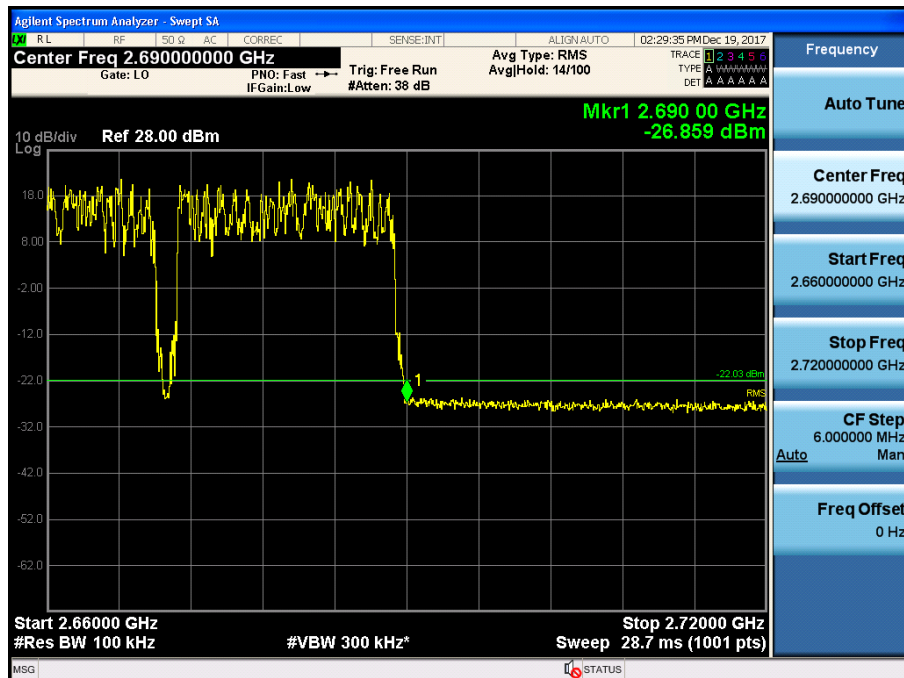


Test Data at Output Port 1\_QPSK

[Downlink Low]

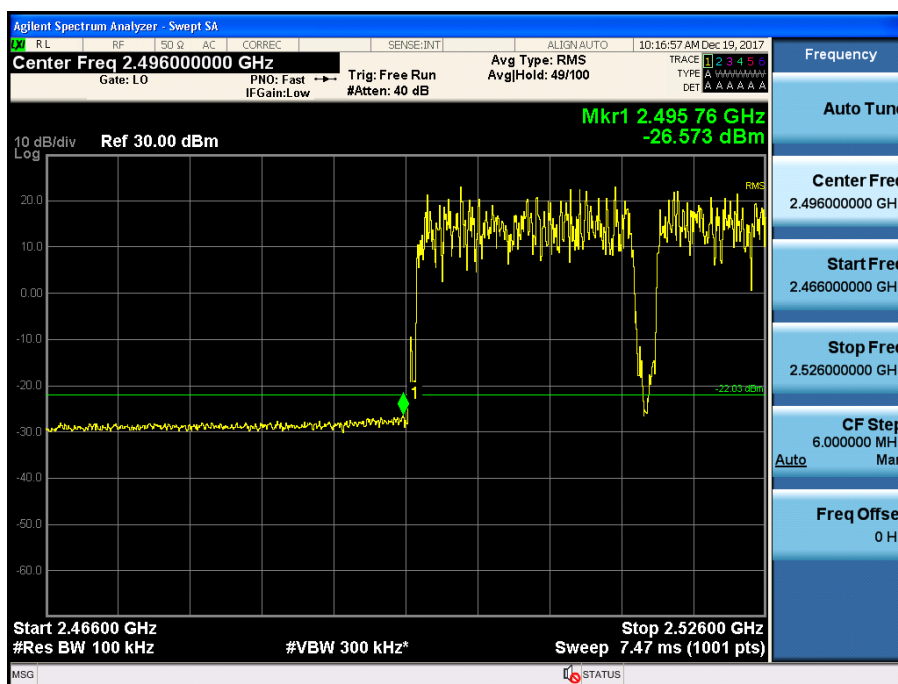


[Downlink High]

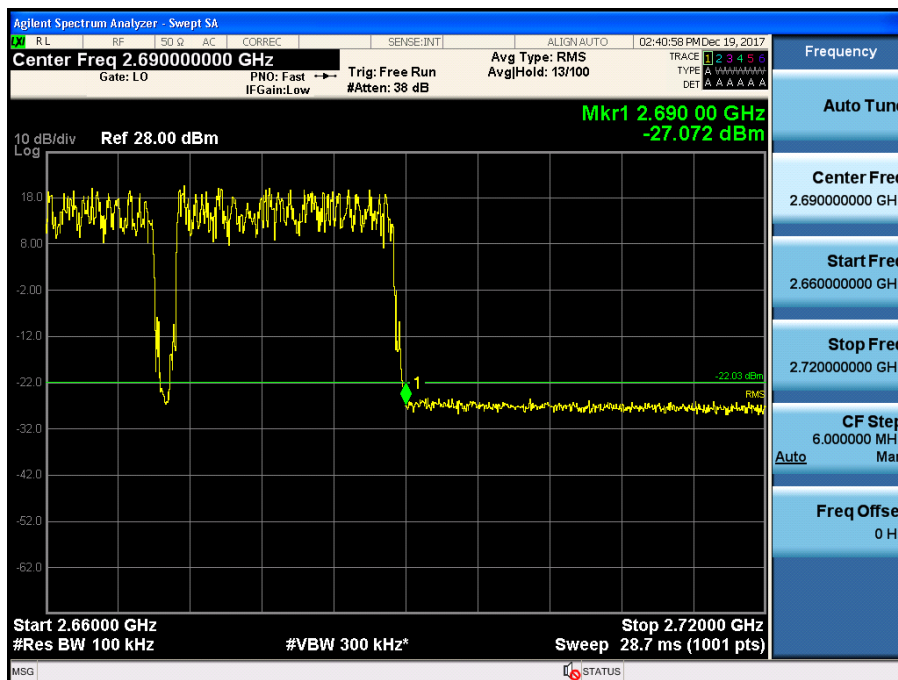


## Test Data at Output Port 1\_16QAM

### [Downlink Low]

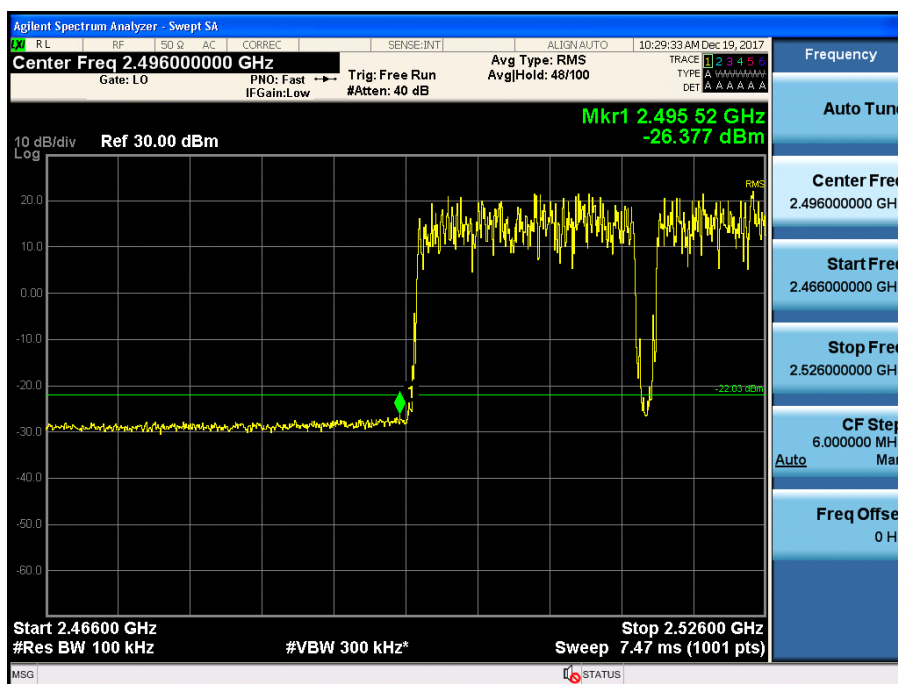


### [Downlink High]

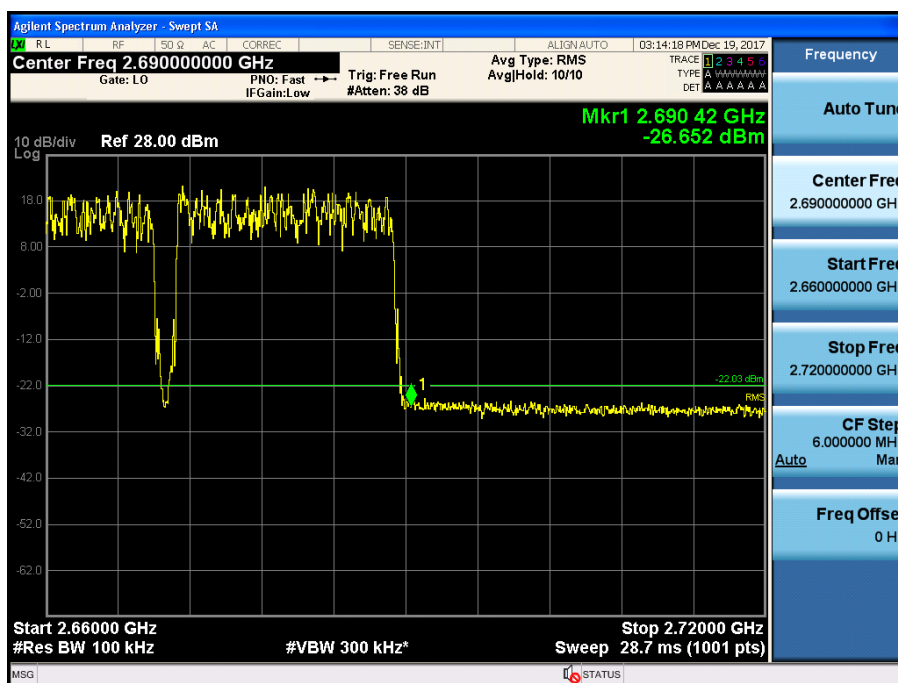


## Test Data at Output Port 1\_64QAM

### [Downlink Low]

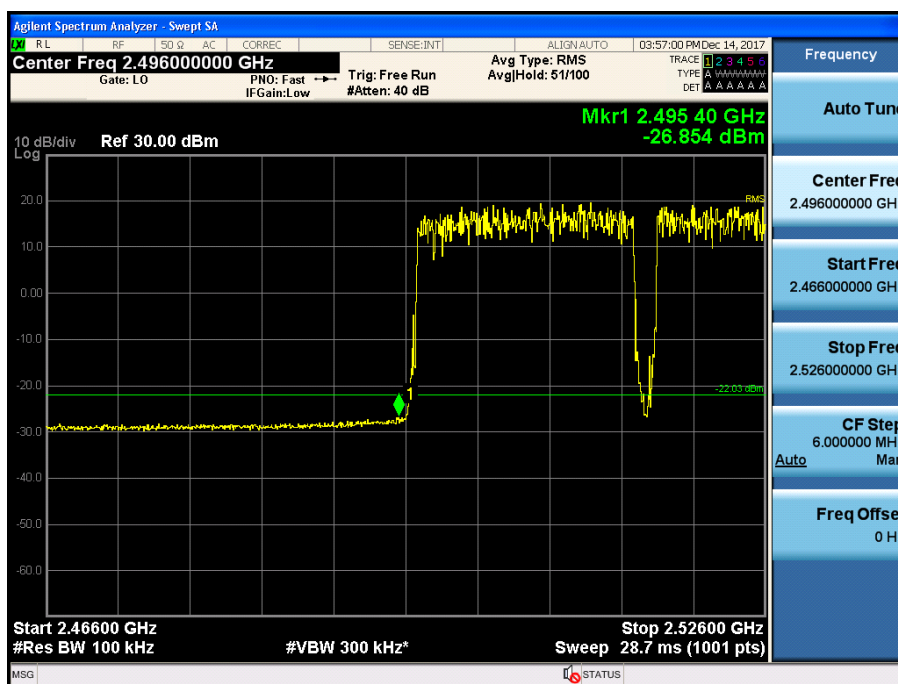


### [Downlink High]

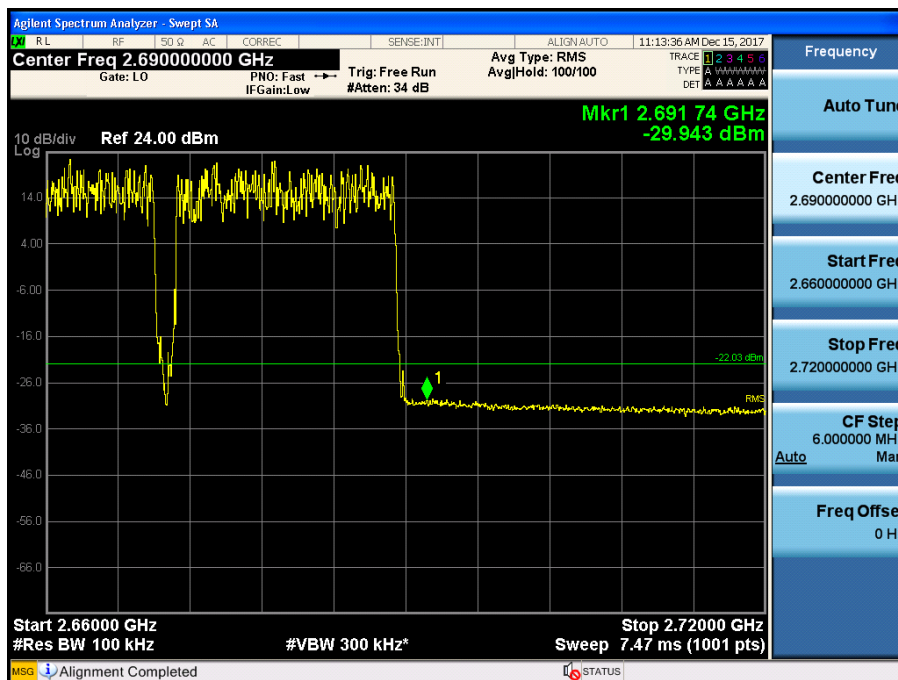


## Test Data at Output Port 1\_256QAM

### [Downlink Low]

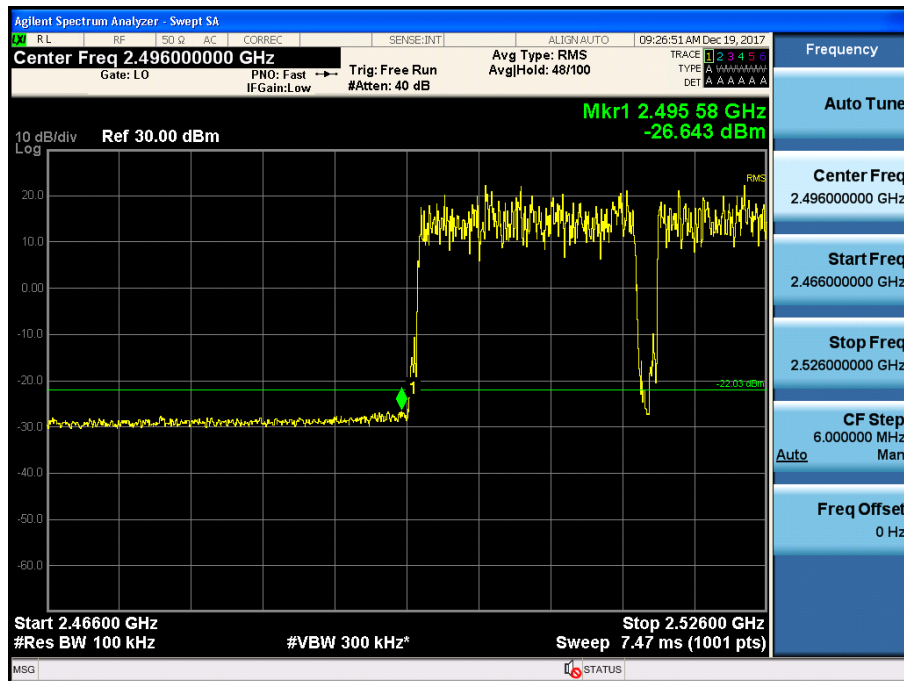


### [Downlink High]

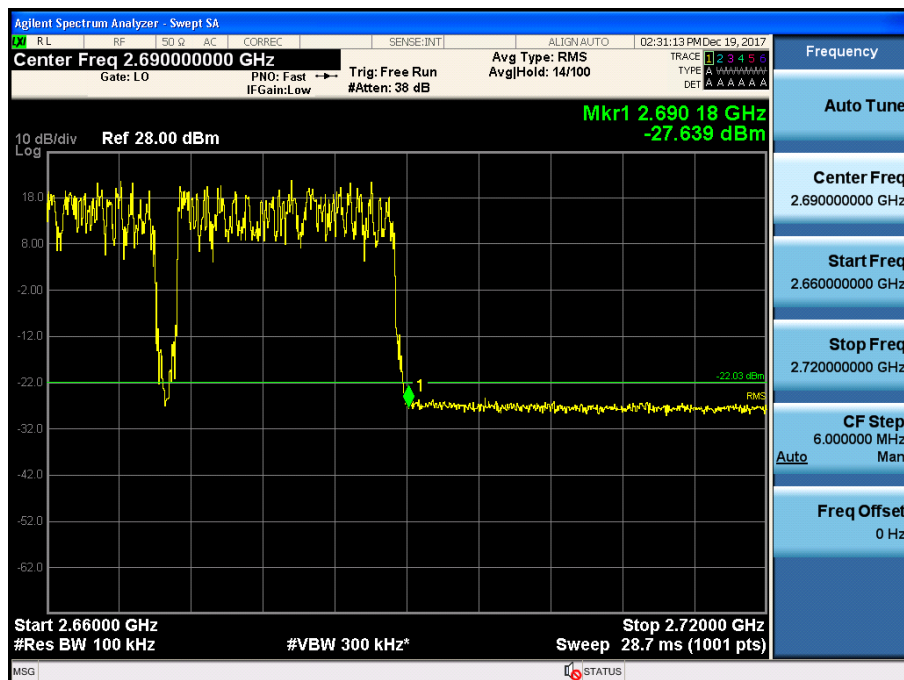


Test Data at Output Port 2\_QPSK

[Downlink Low]

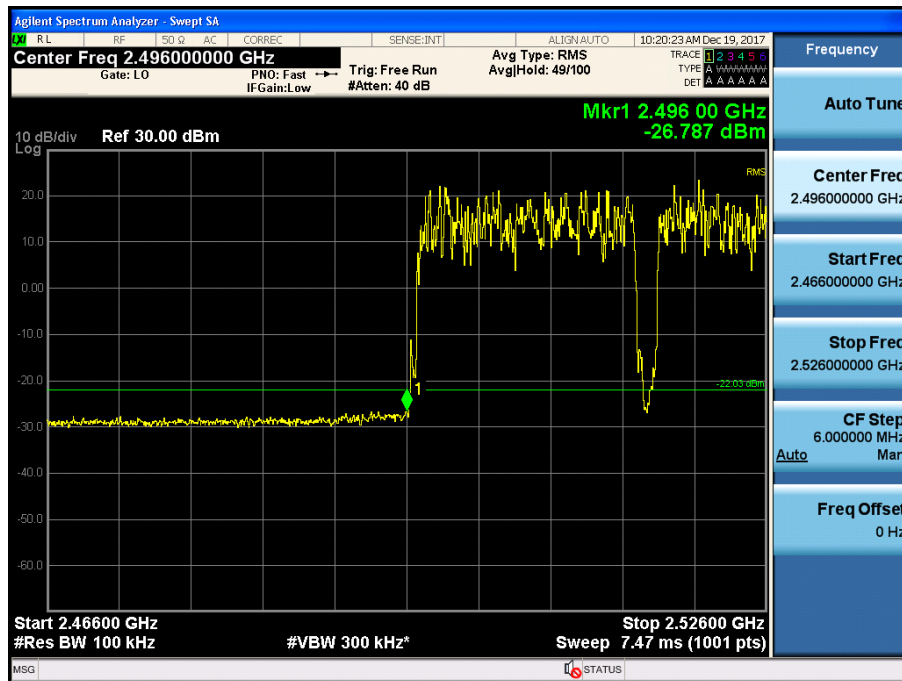


[Downlink High]

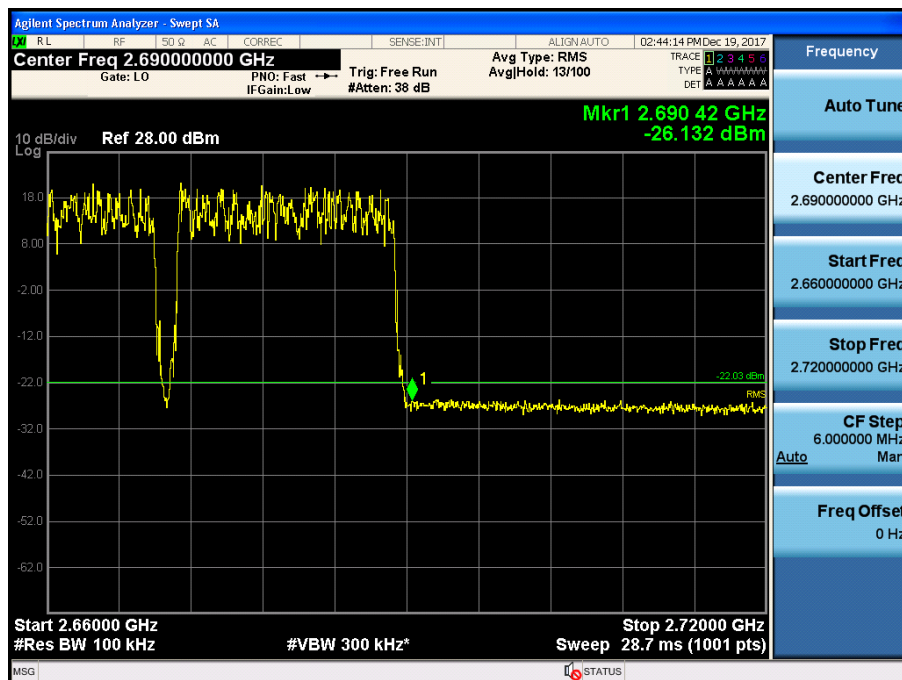


## Test Data at Output Port 2\_16QAM

### [Downlink Low]

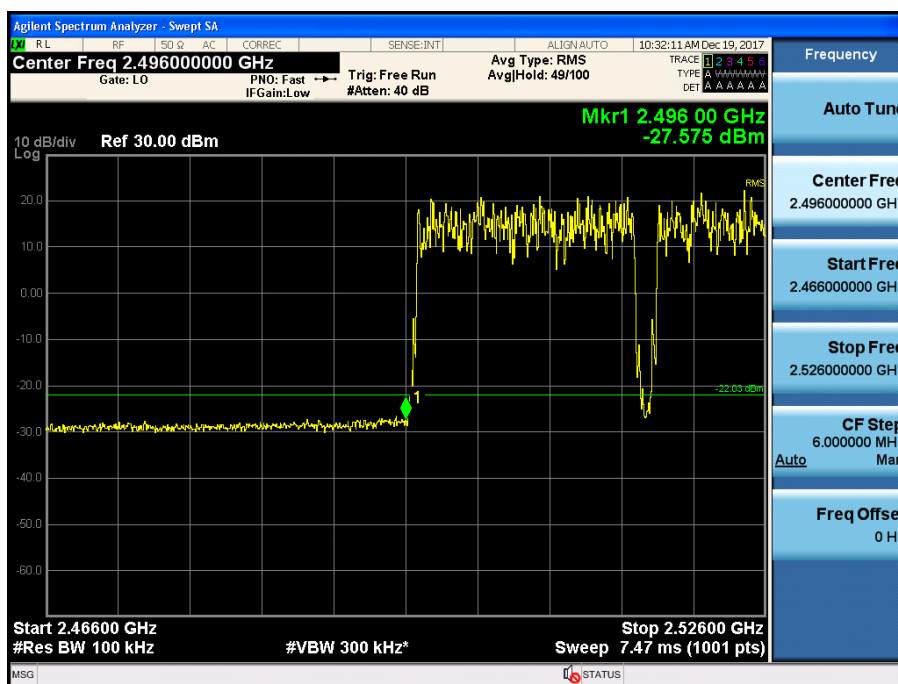


### [Downlink High]

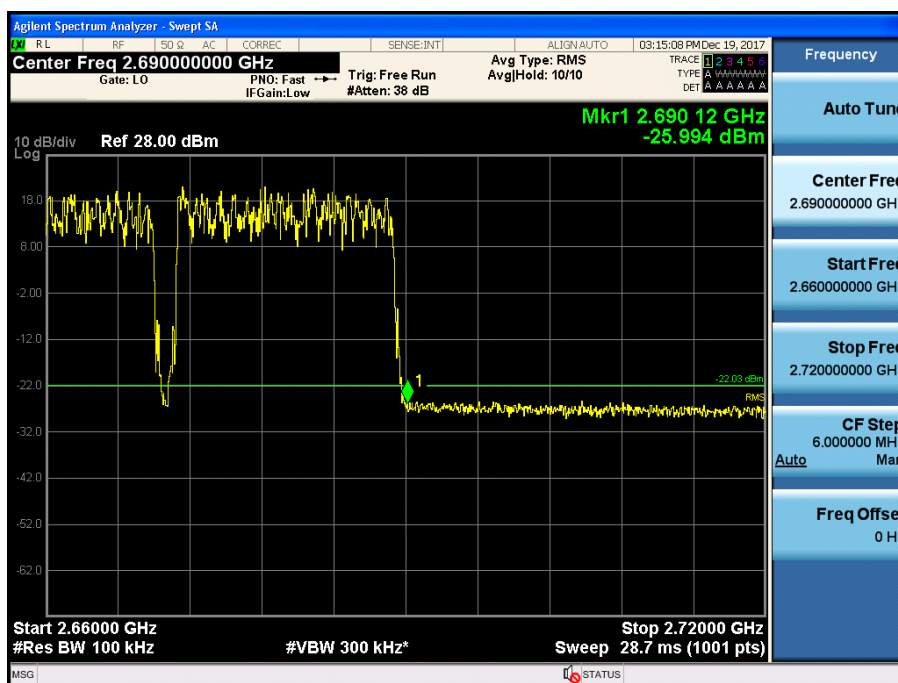


## Test Data at Output Port 2\_64QAM

### [Downlink Low]

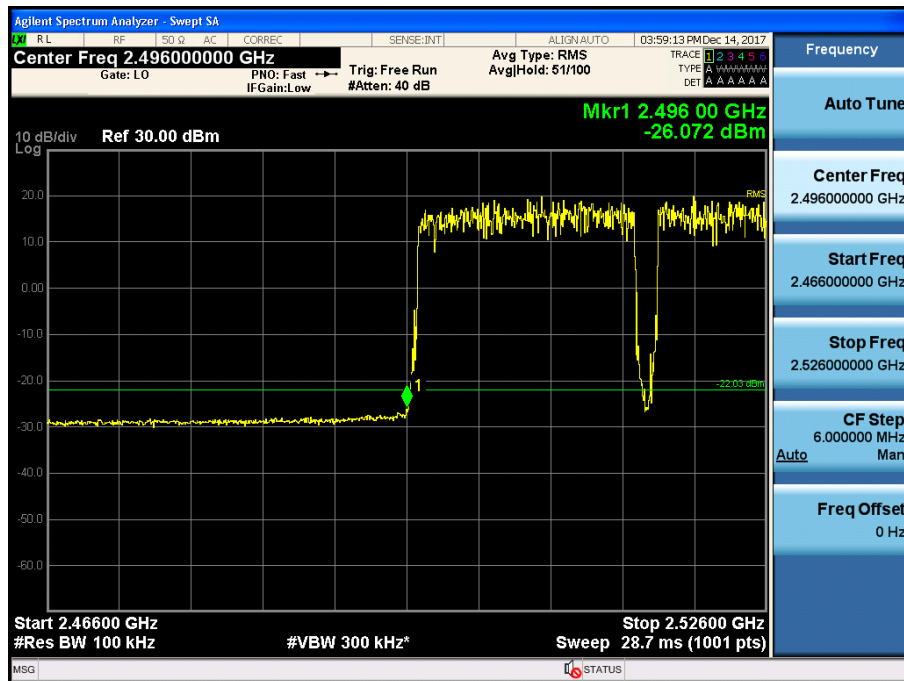


### [Downlink High]

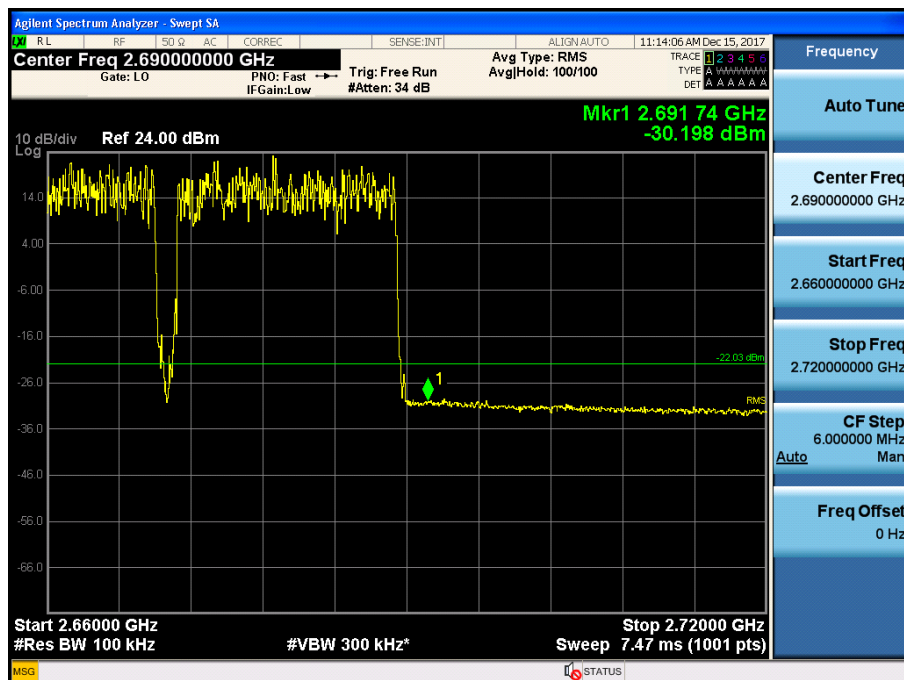


## Test Data at Output Port 2\_256QAM

### [Downlink Low]

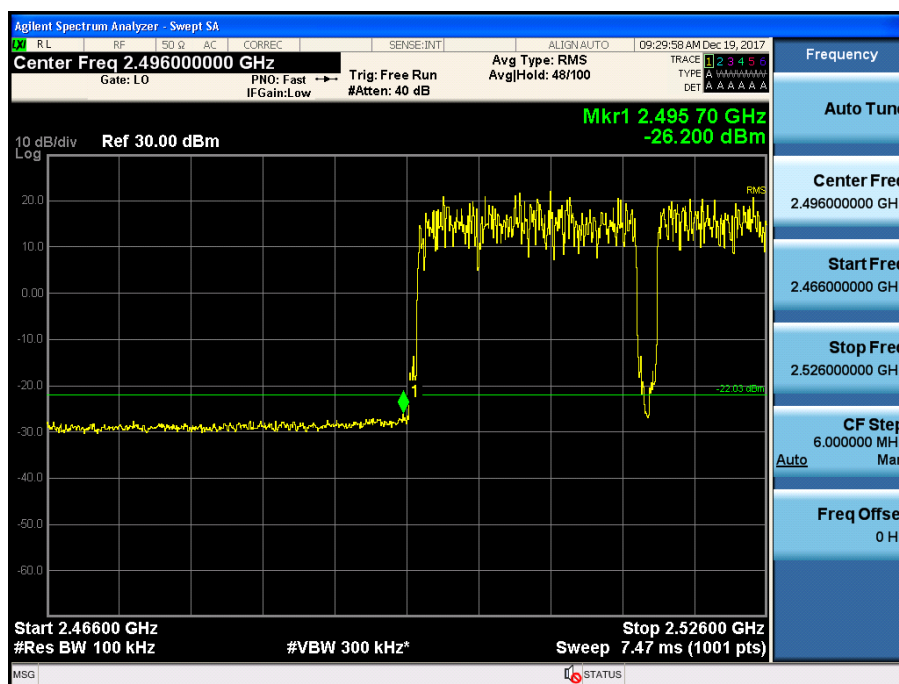


### [Downlink High]

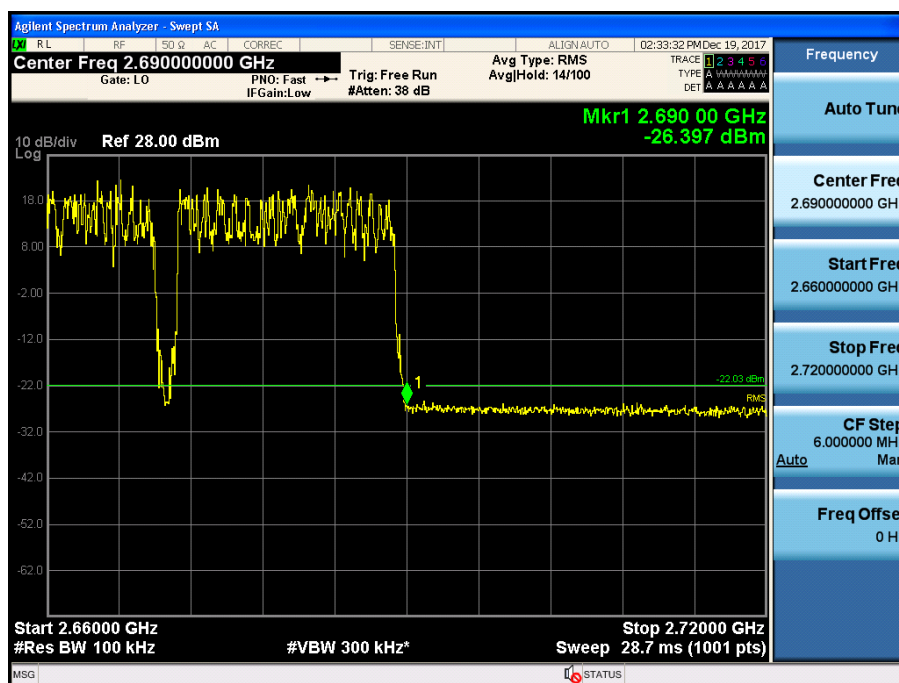


## Test Data at Output Port 3\_QPSK

### [Downlink Low]

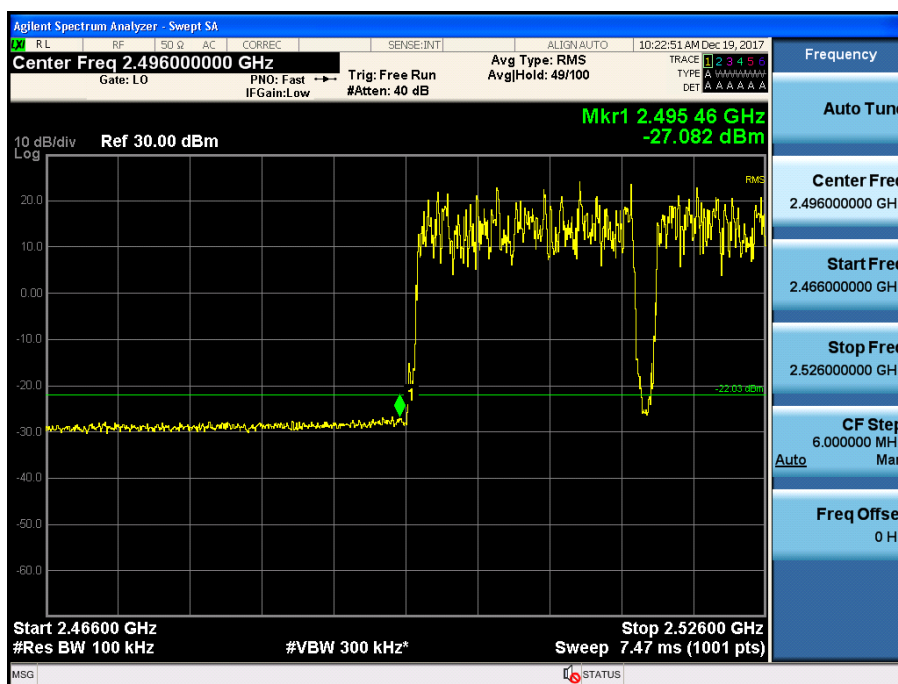


### [Downlink High]

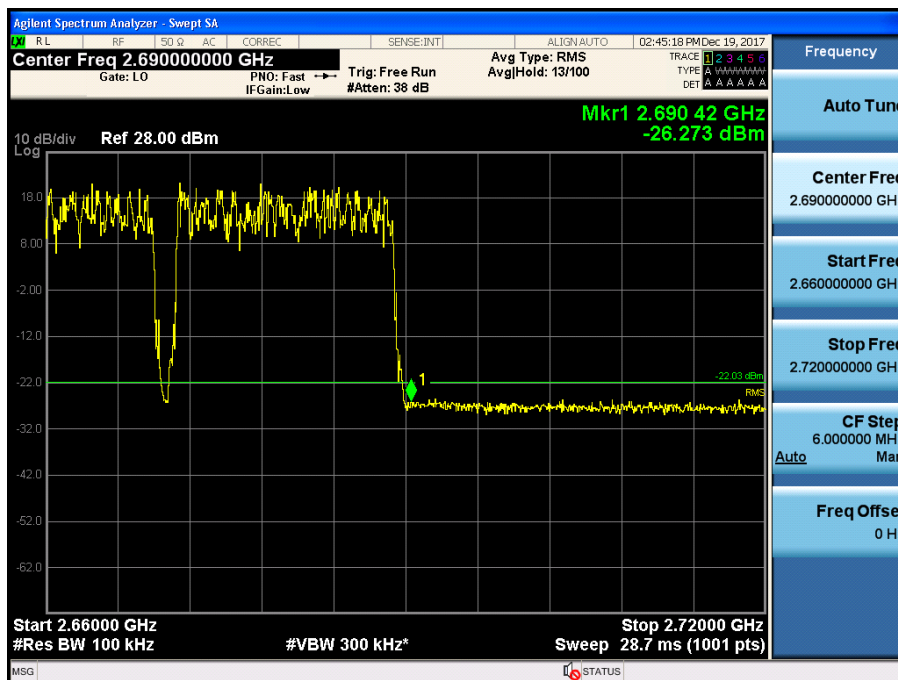


## Test Data at Output Port 3\_16QAM

### [Downlink Low]

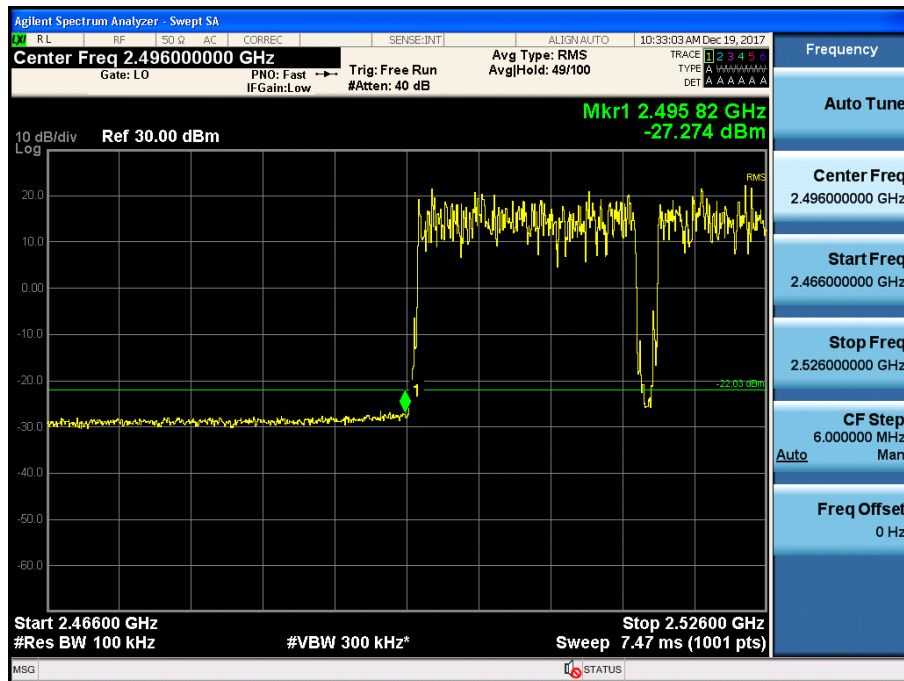


### [Downlink High]

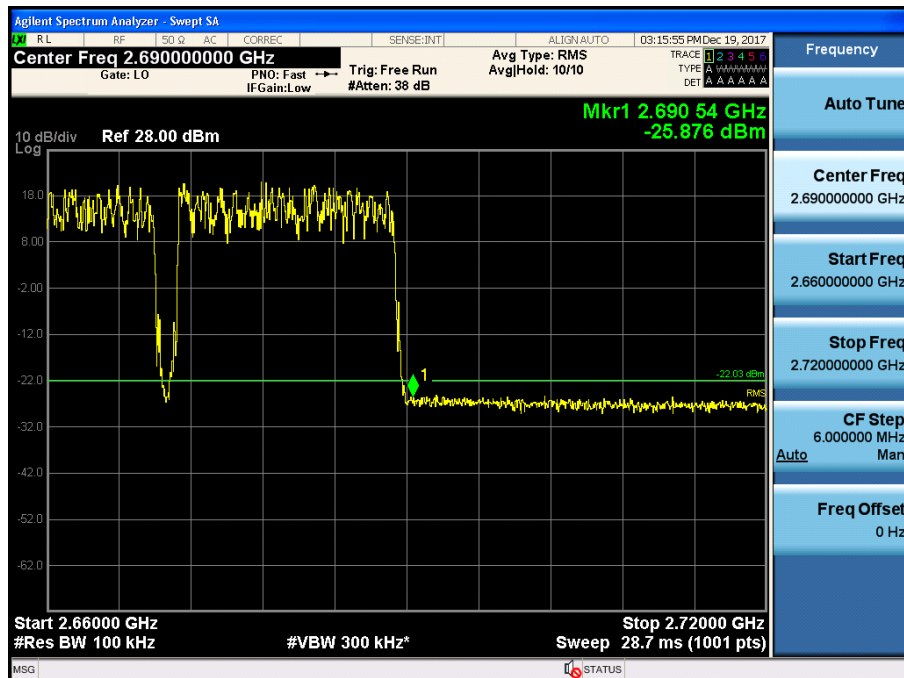


Test Data at Output Port 3\_64QAM

[Downlink Low]

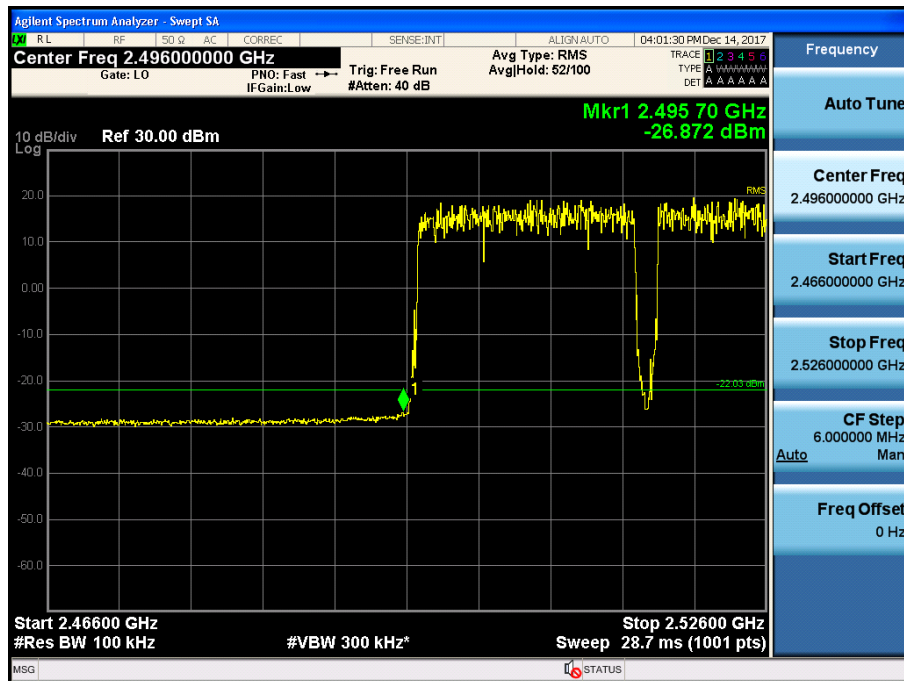


[Downlink High]

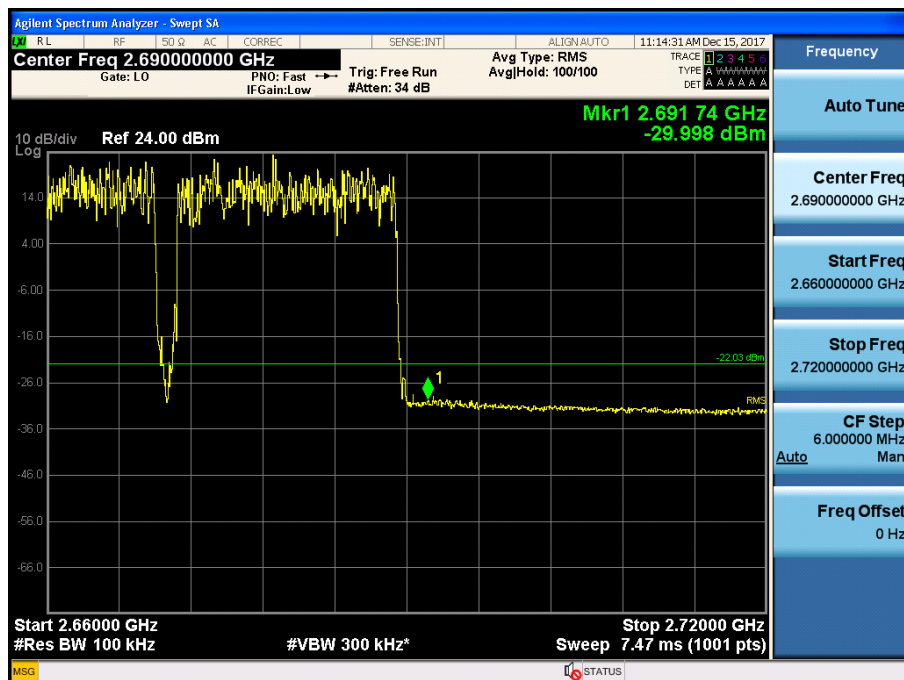


Test Data at Output Port 3\_256QAM

[Downlink Low]

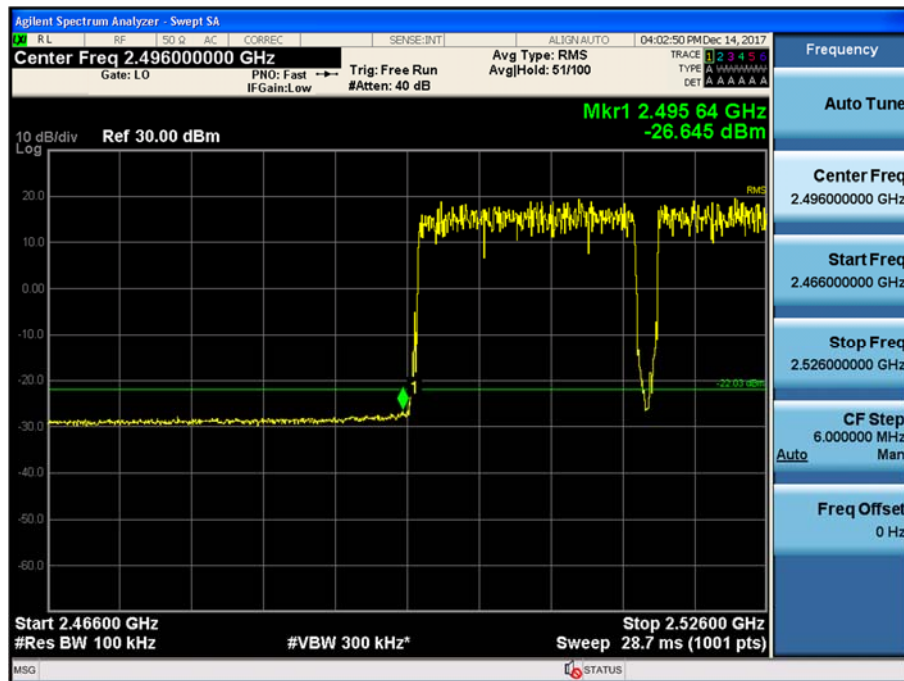


[Downlink High]

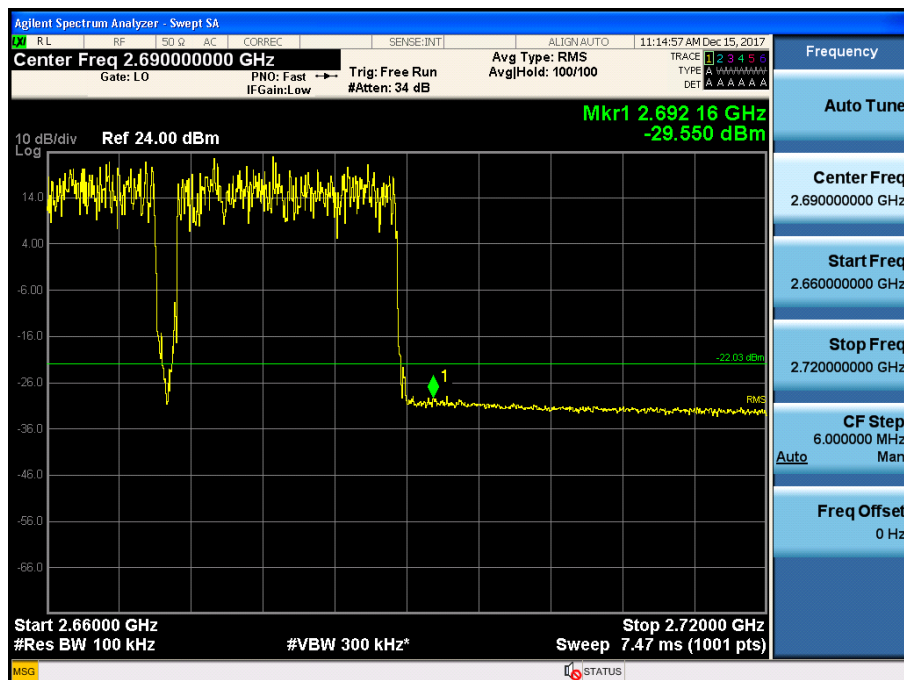


Test Data at Output Port 4\_256QAM

[Downlink Low]

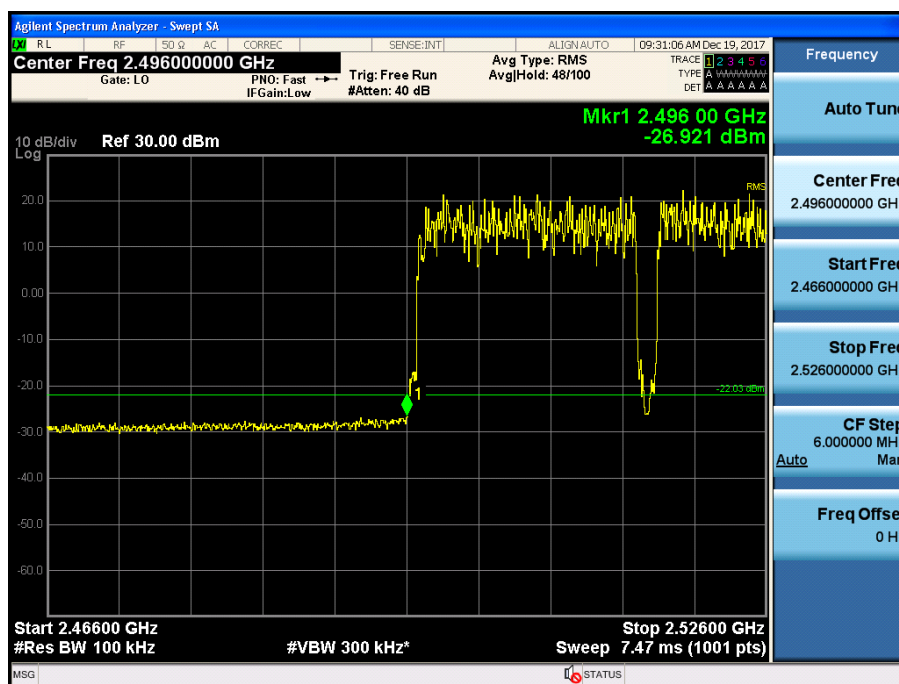


[Downlink High]

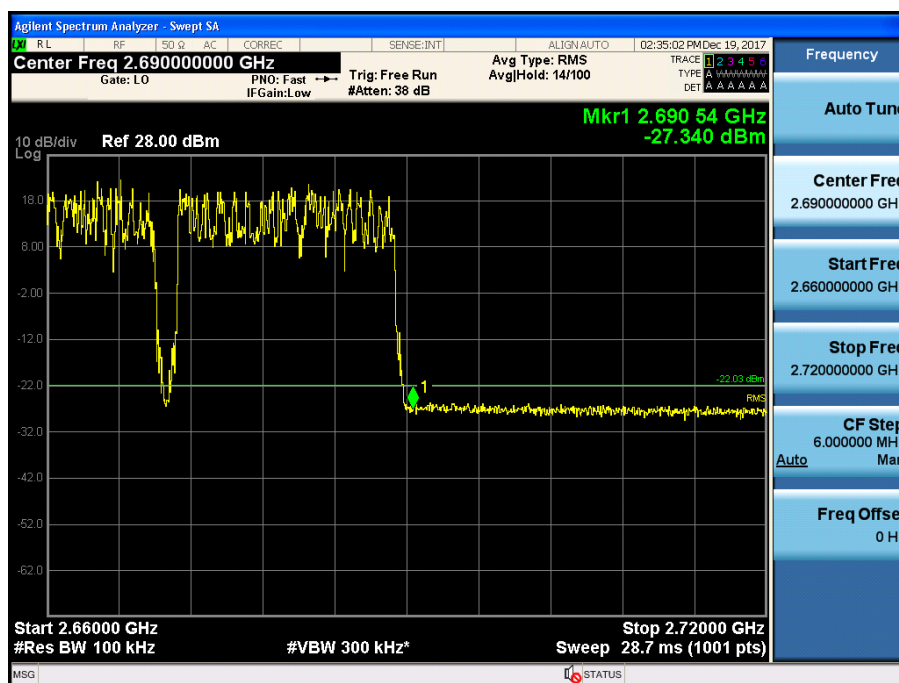


## Test Data at Output Port 5\_QPSK

### [Downlink Low]

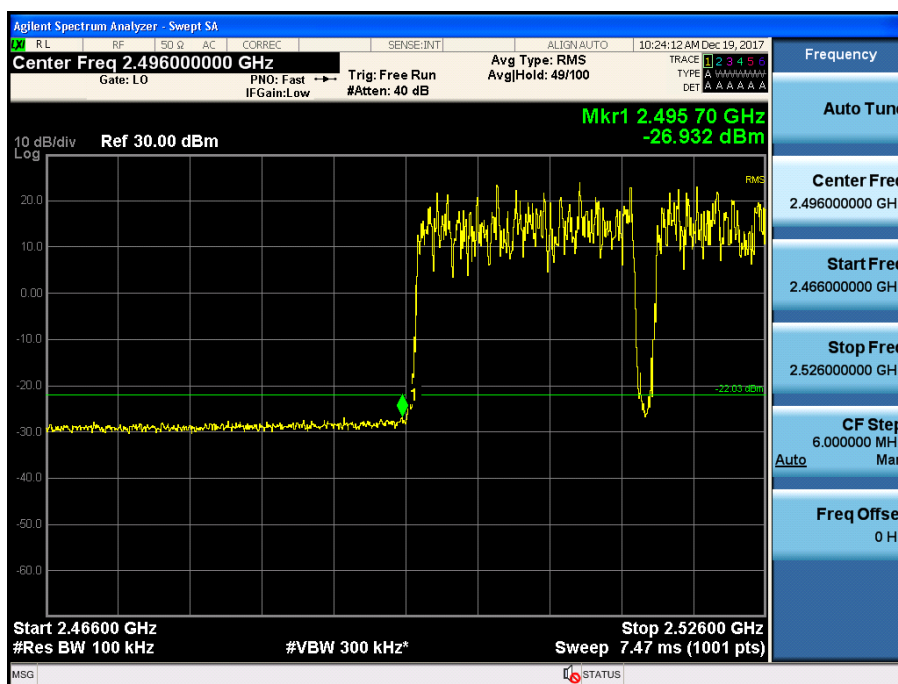


### [Downlink High]

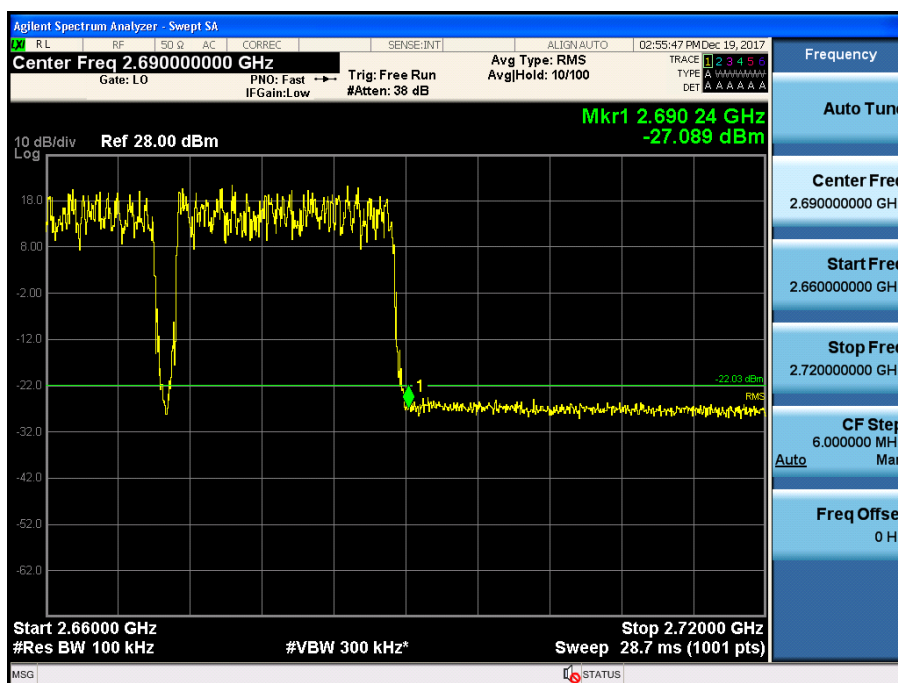


## Test Data at Output Port 5\_16QAM

### [Downlink Low]

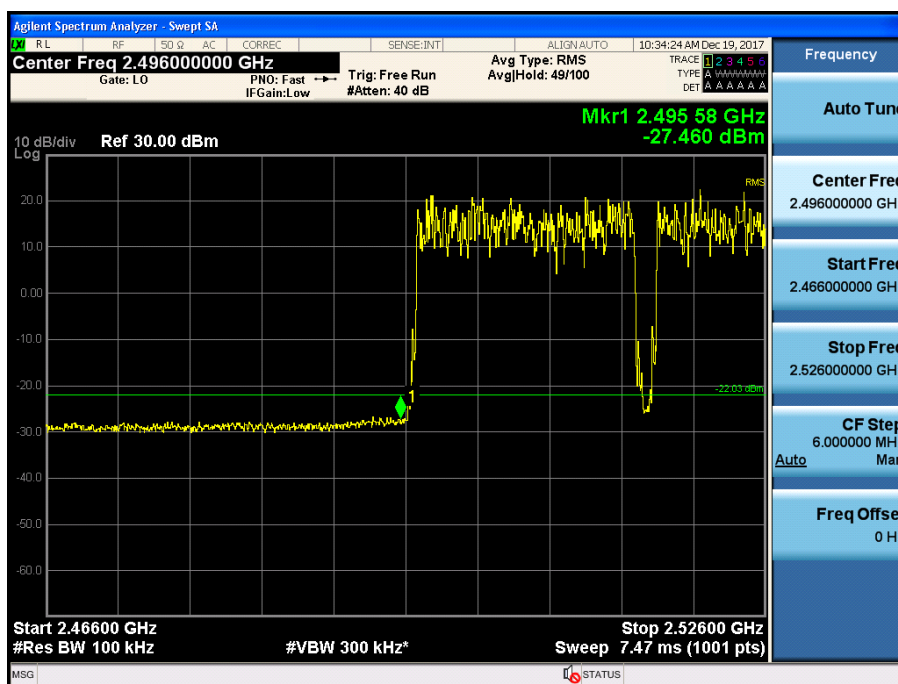


### [Downlink High]

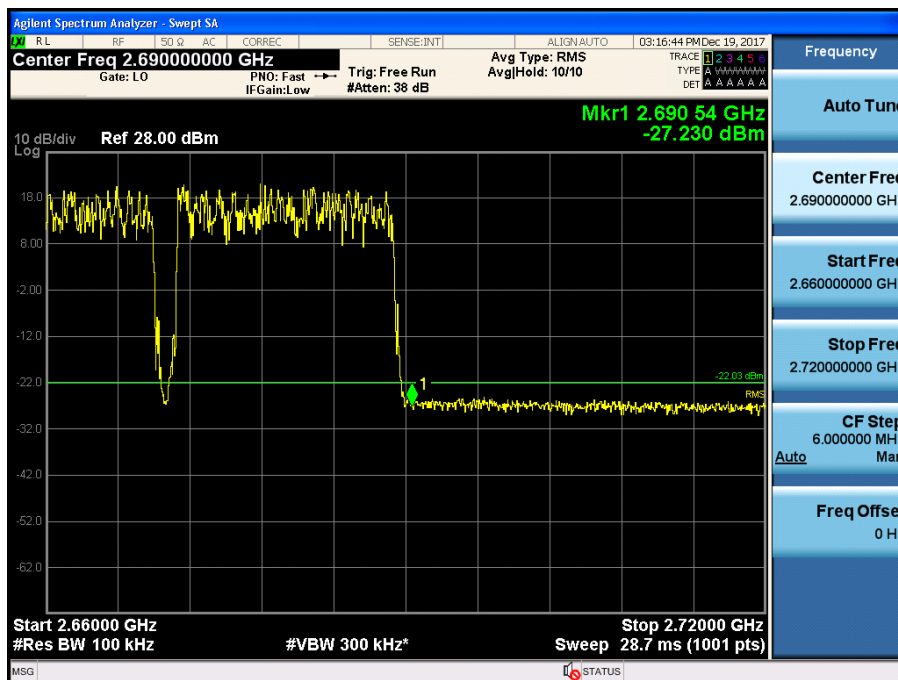


## Test Data at Output Port 5\_64QAM

### [Downlink Low]

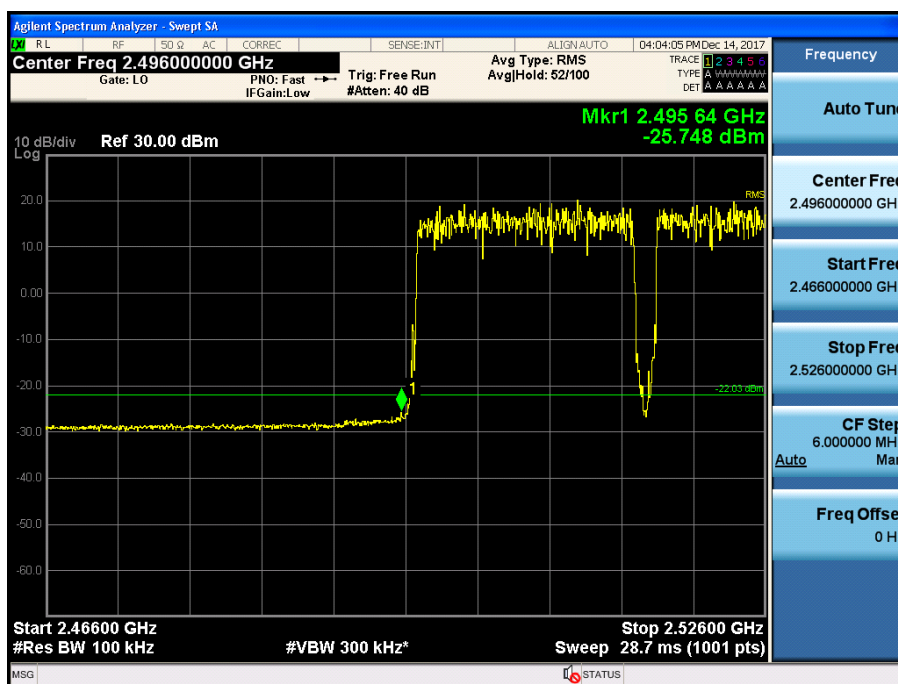


### [Downlink High]

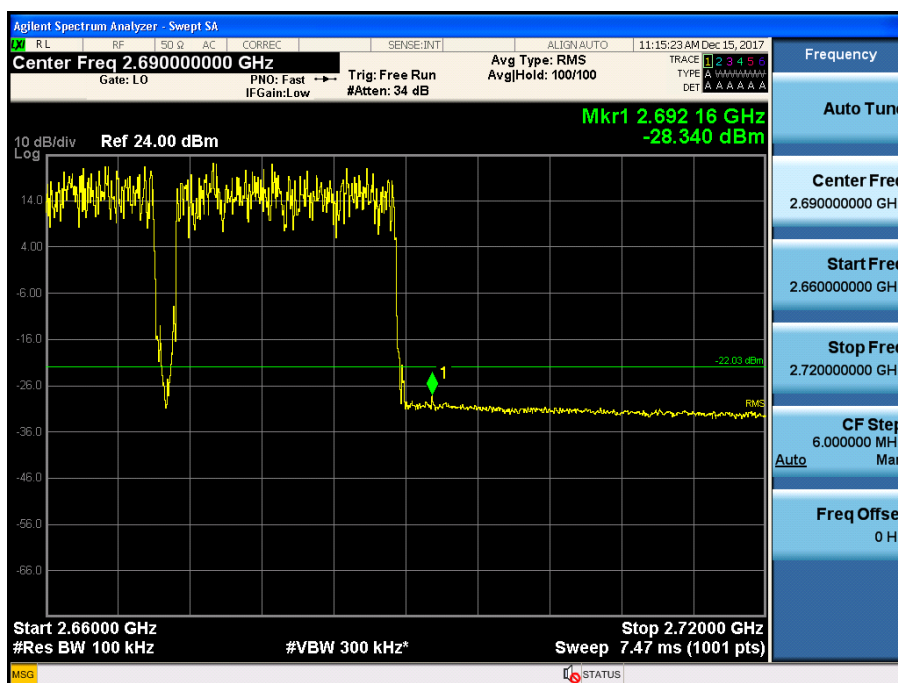


Test Data at Output Port 5\_256QAM

[Downlink Low]

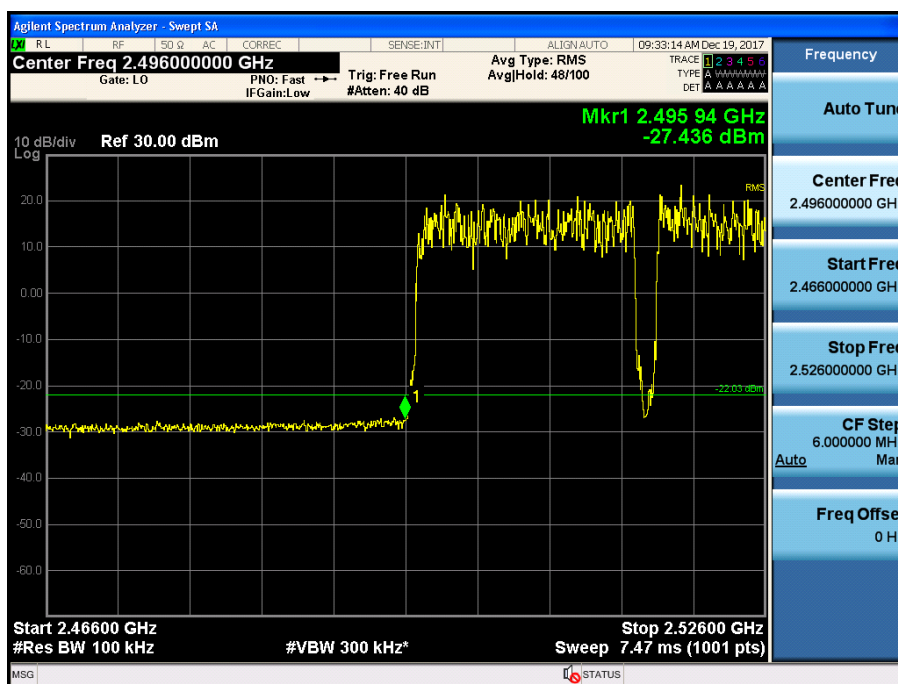


[Downlink High]

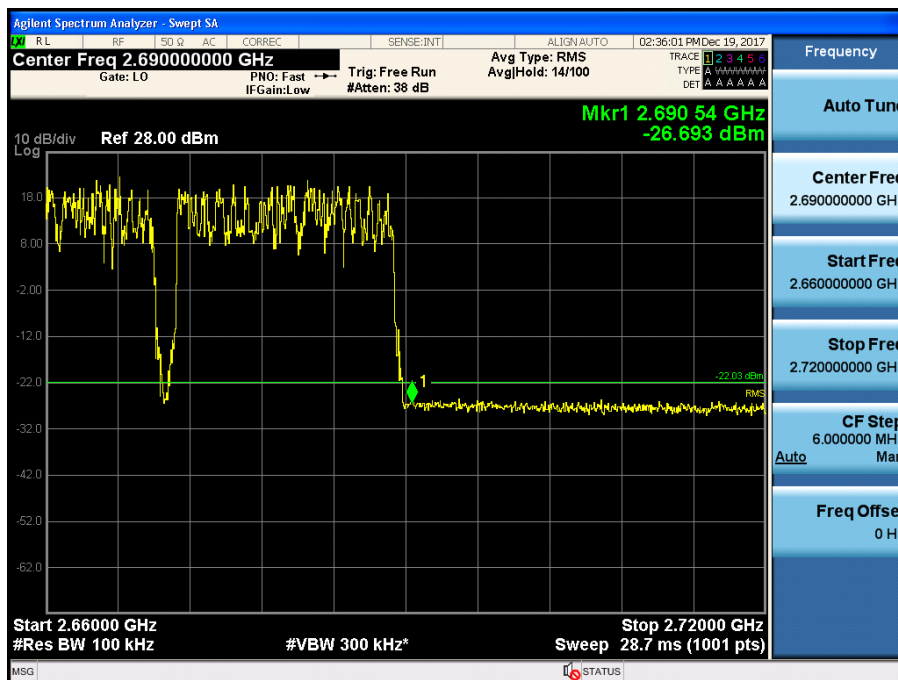


## Test Data at Output Port 6\_QPSK

### [Downlink Low]

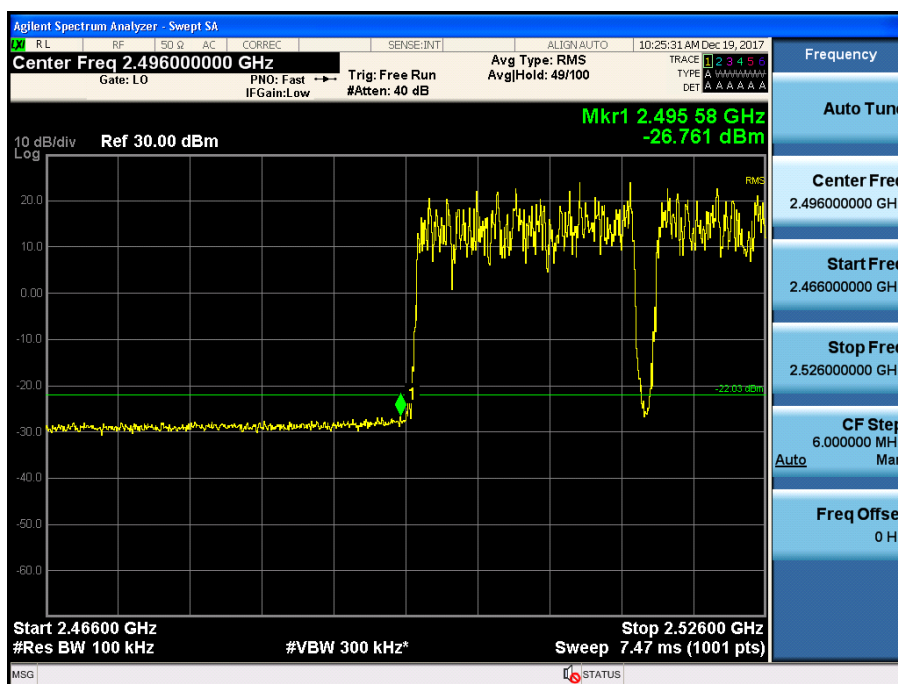


### [Downlink High]

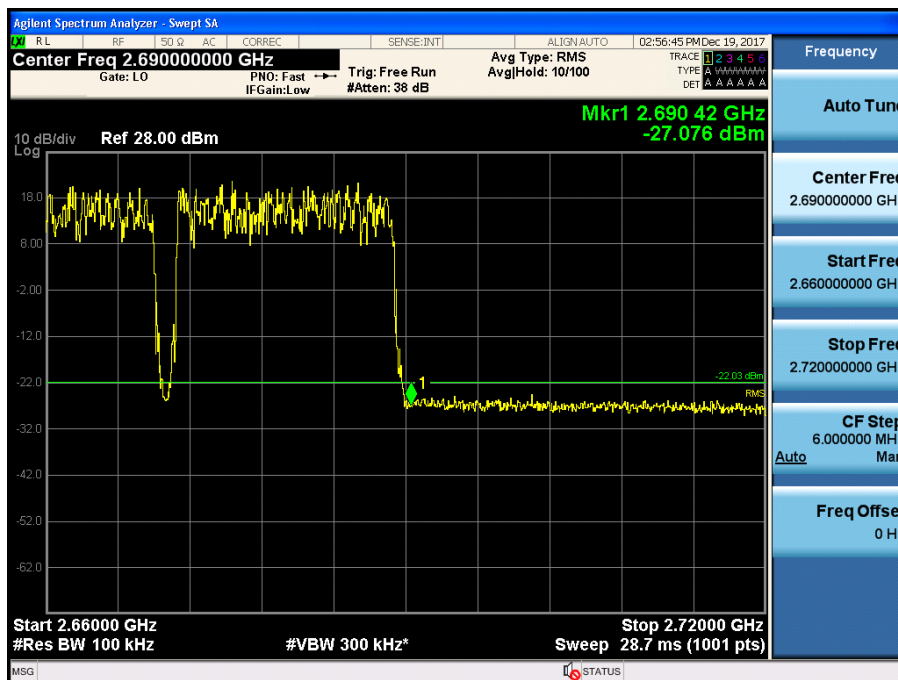


## Test Data at Output Port 6\_16QAM

### [Downlink Low]

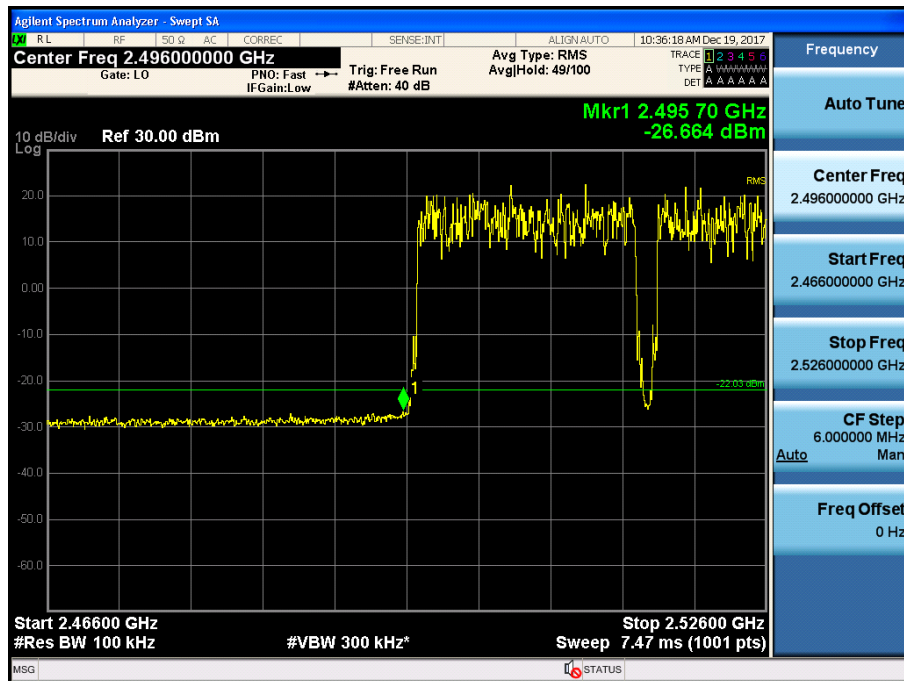


### [Downlink High]

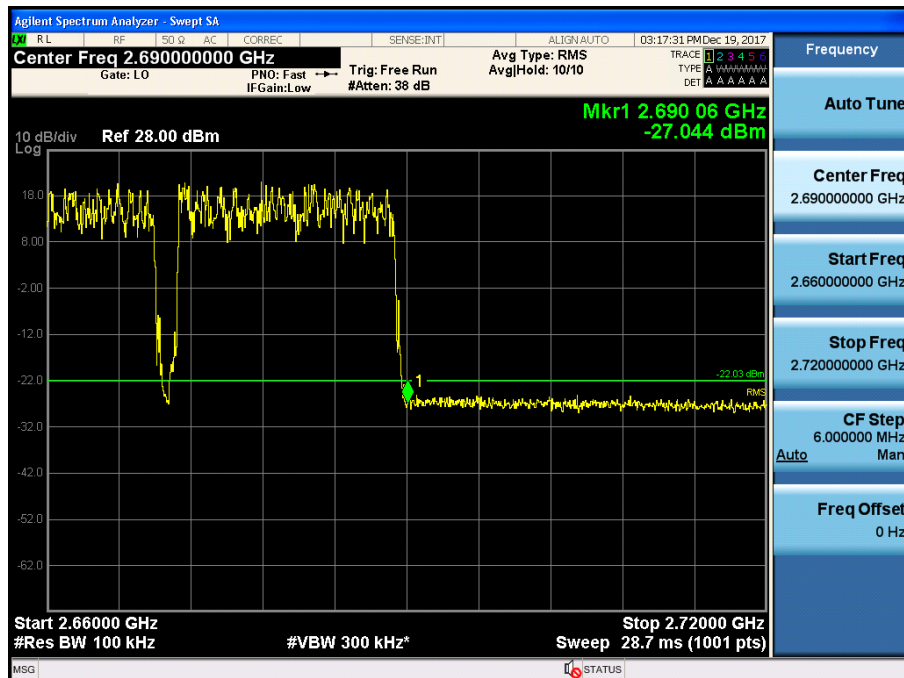


## Test Data at Output Port 6\_64QAM

### [Downlink Low]

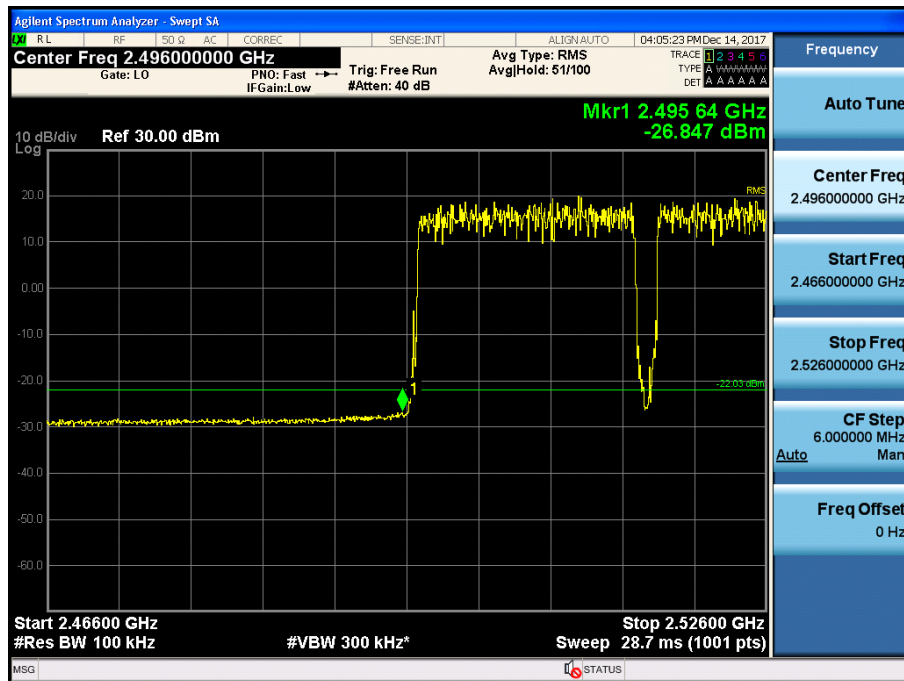


### [Downlink High]

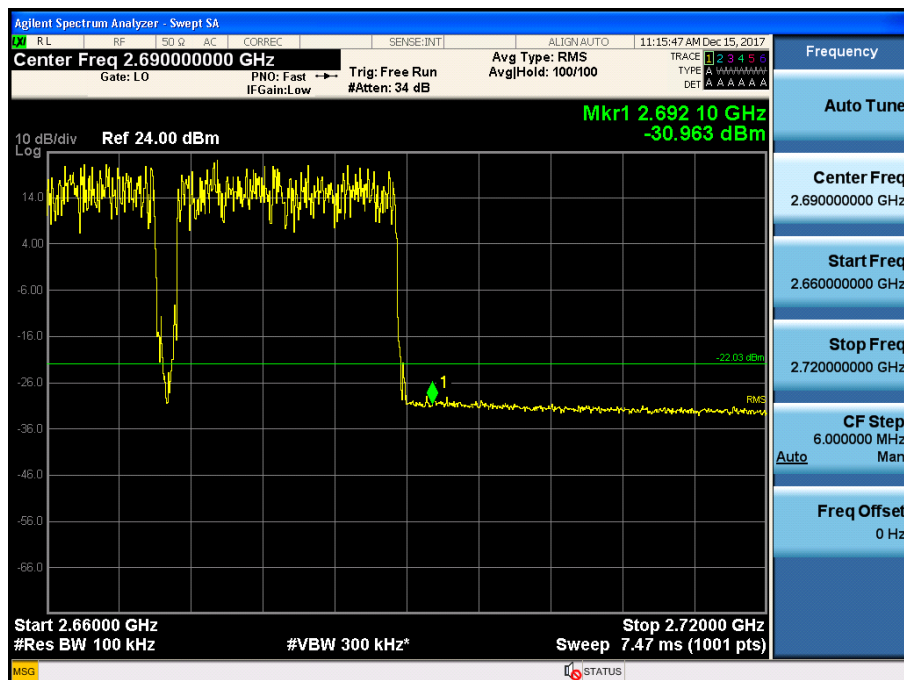


Test Data at Output Port 6\_256QAM

[Downlink Low]

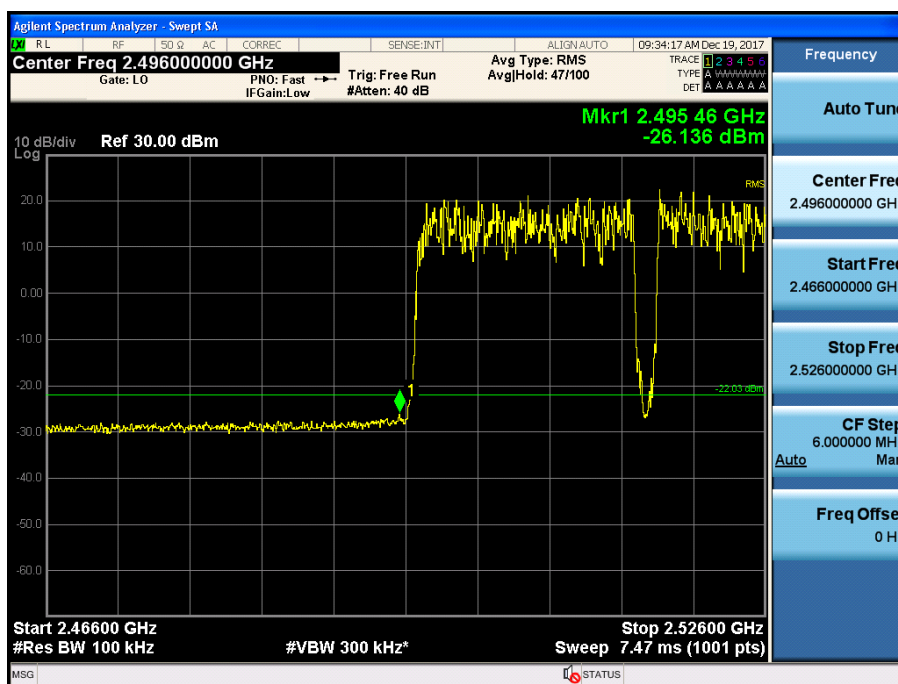


[Downlink High]



## Test Data at Output Port 7\_QPSK

### [Downlink Low]



### [Downlink High]

