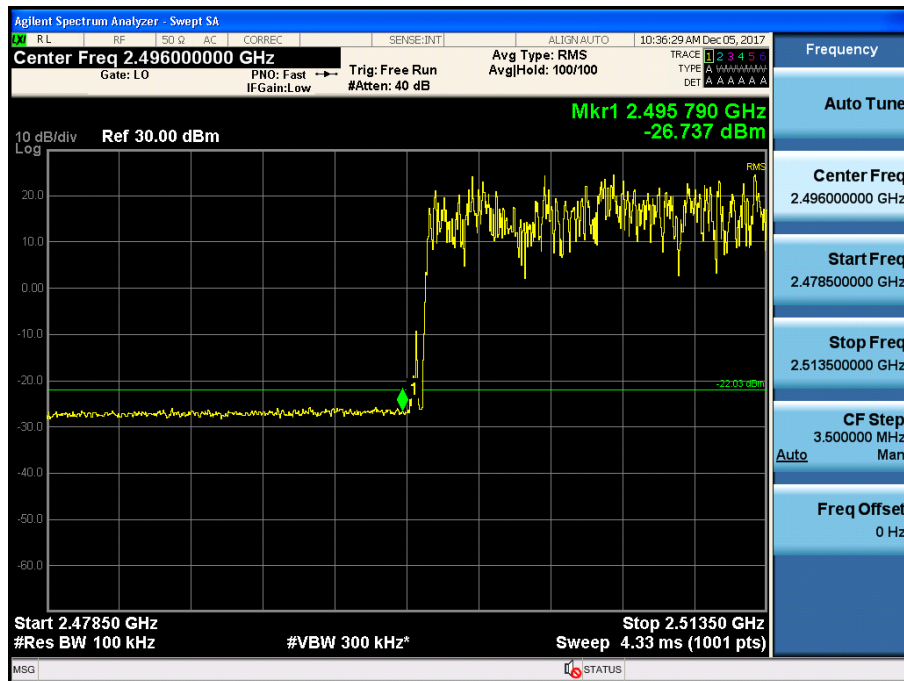
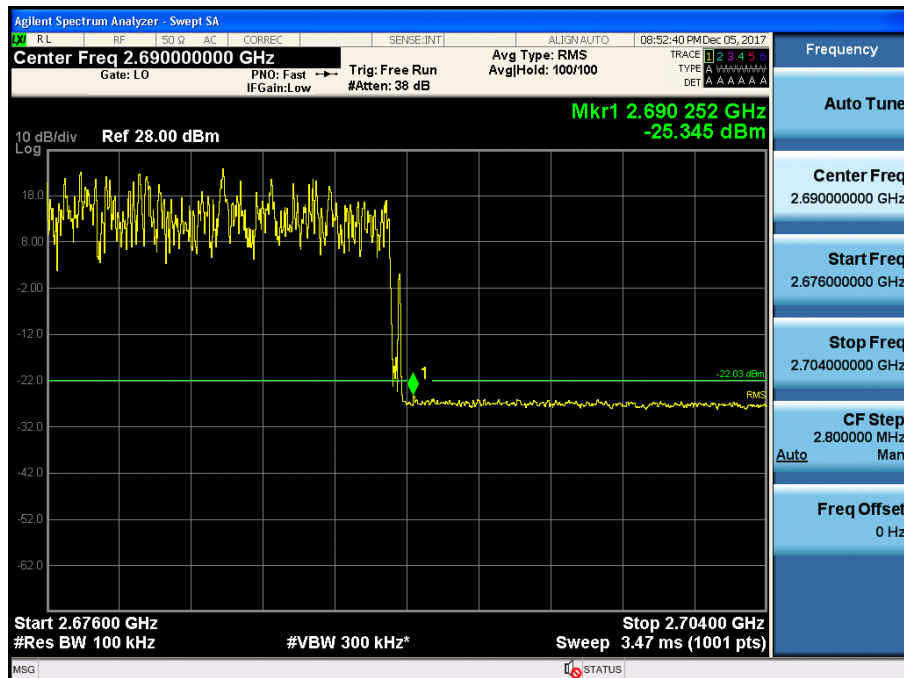


Test Data at Output Port 3_16QAM

[Downlink Low]

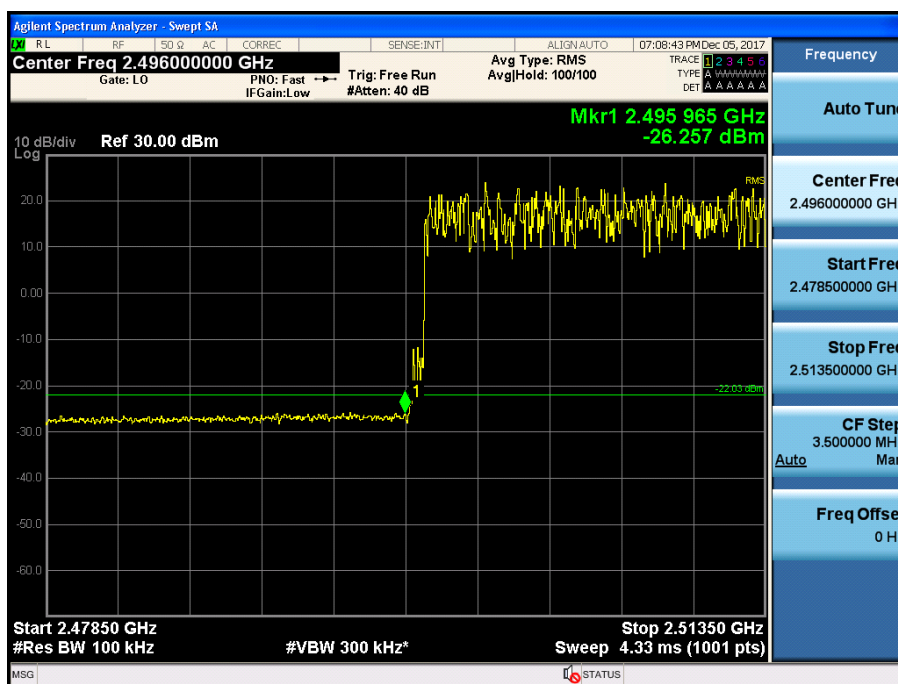


[Downlink High]

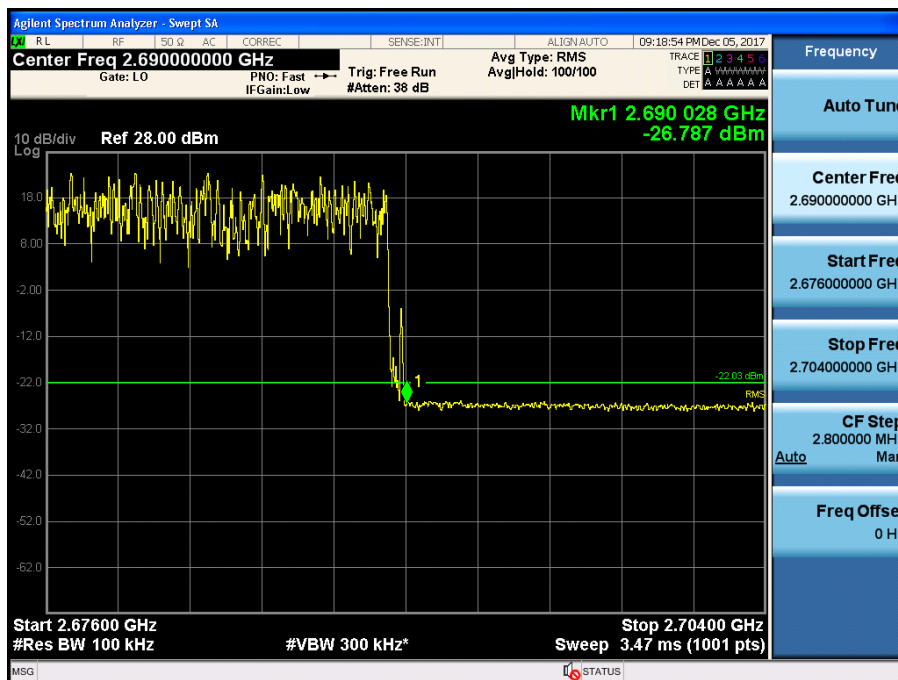


Test Data at Output Port 3_64QAM

[Downlink Low]

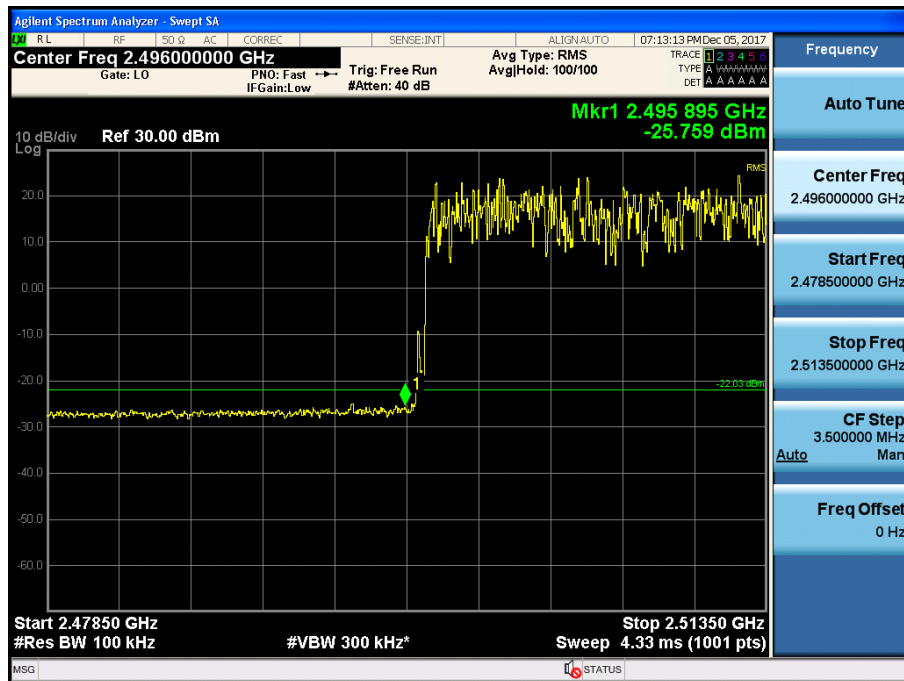


[Downlink High]

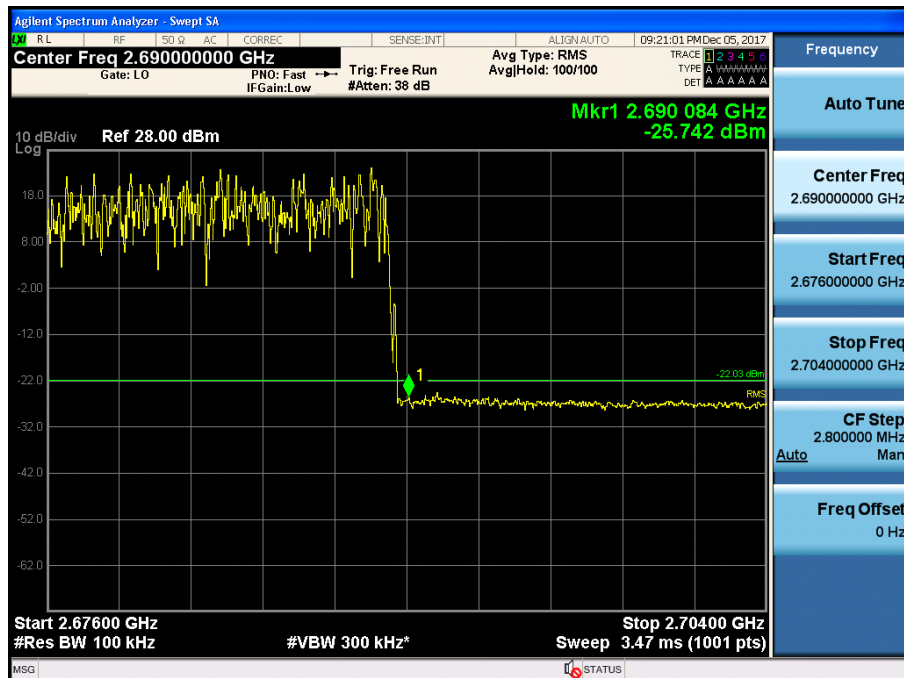


Test Data at Output Port 3_256QAM

[Downlink Low]

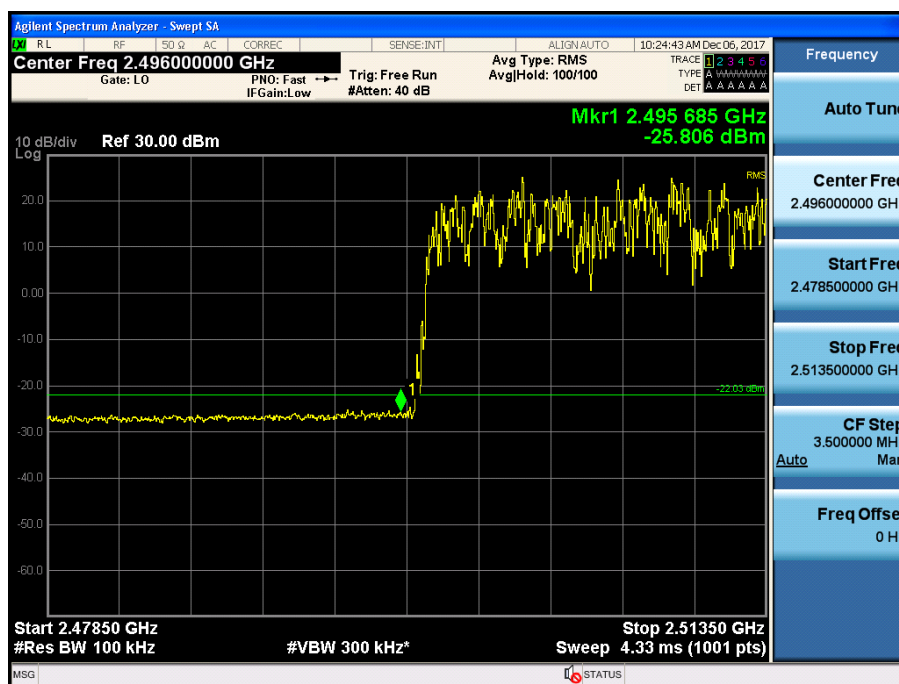


[Downlink High]



Test Data at Output Port 4_QPSK

[Downlink Low]

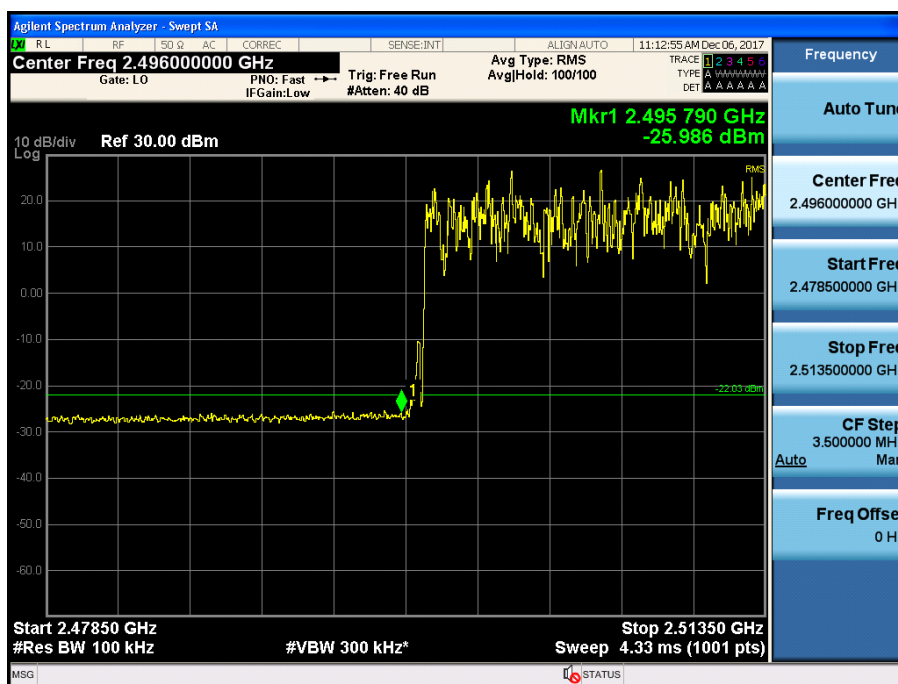


[Downlink High]



Test Data at Output Port 4_16QAM

[Downlink Low]

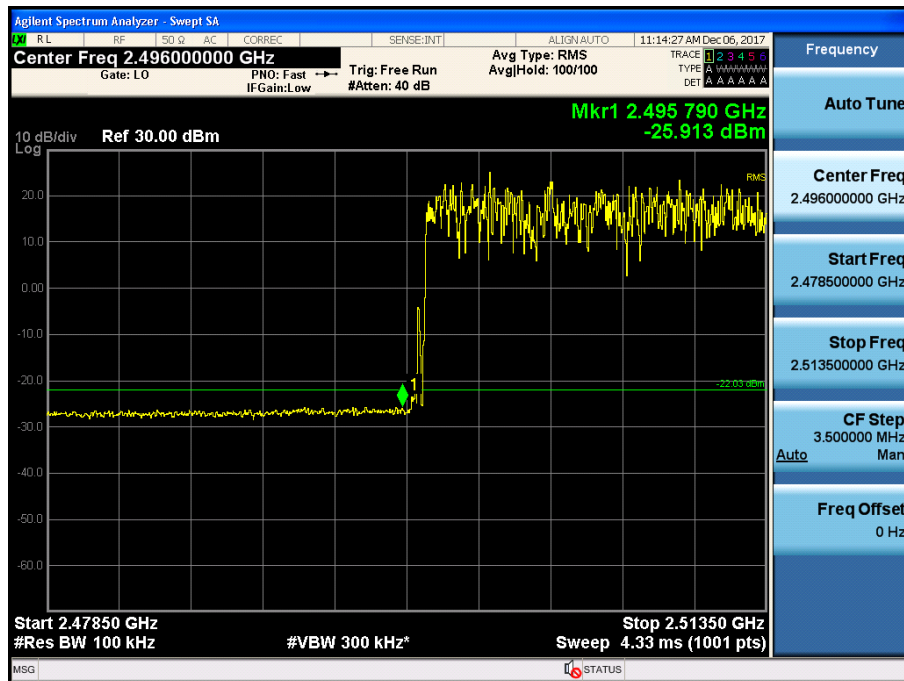


[Downlink High]

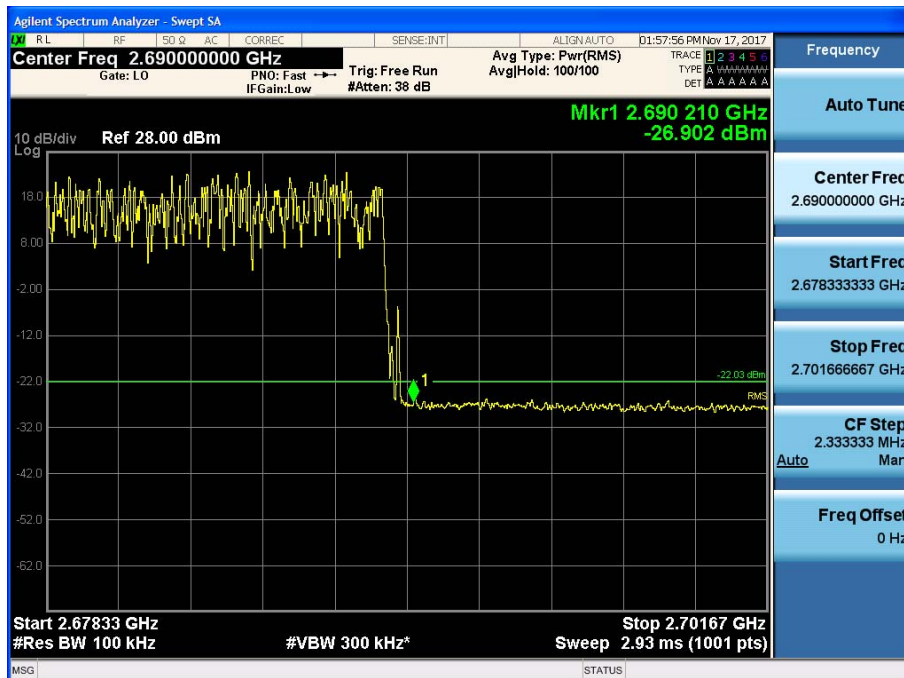


Test Data at Output Port 4_64QAM

[Downlink Low]

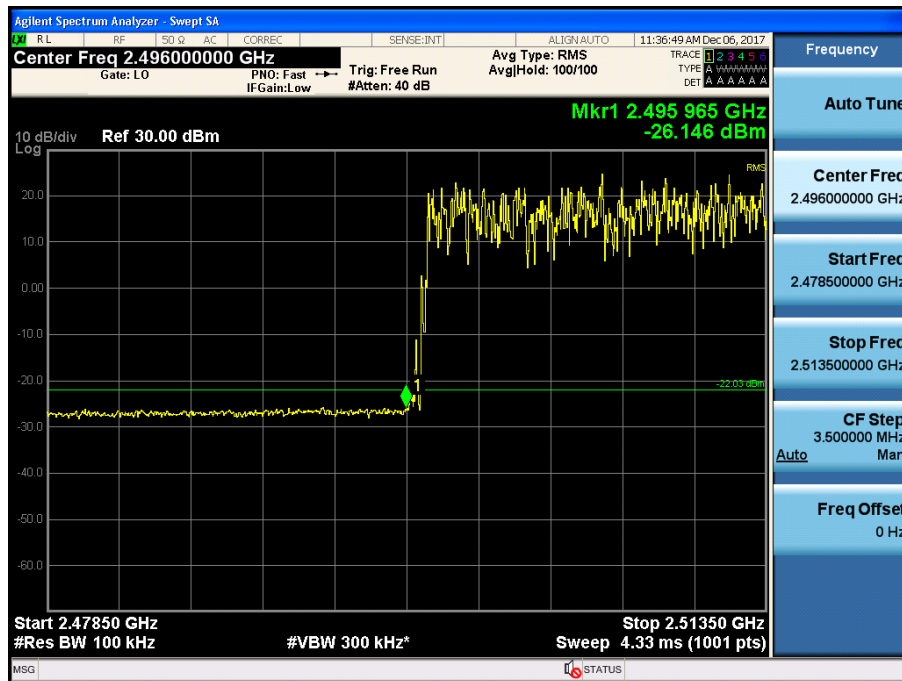


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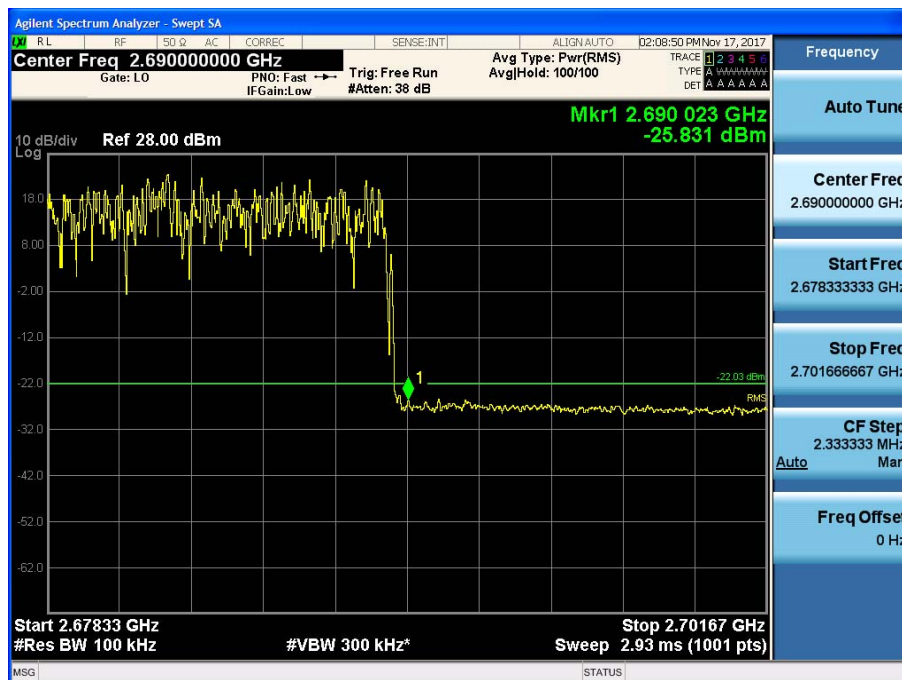


Test Data at Output Port 4_256QAM

[Downlink Low]

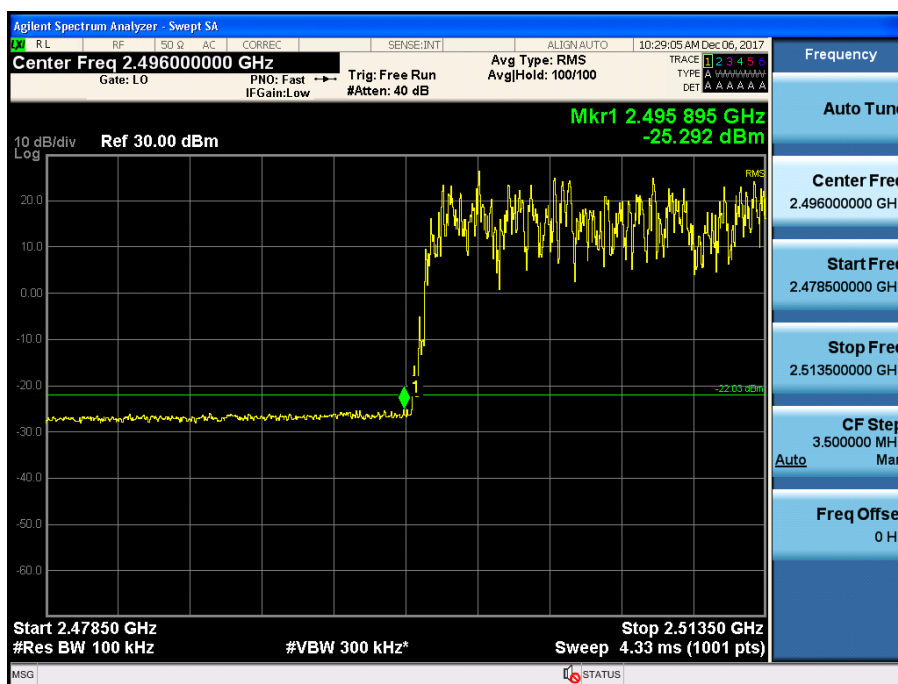


[Downlink High]



Test Data at Output Port 5_QPSK

[Downlink Low]

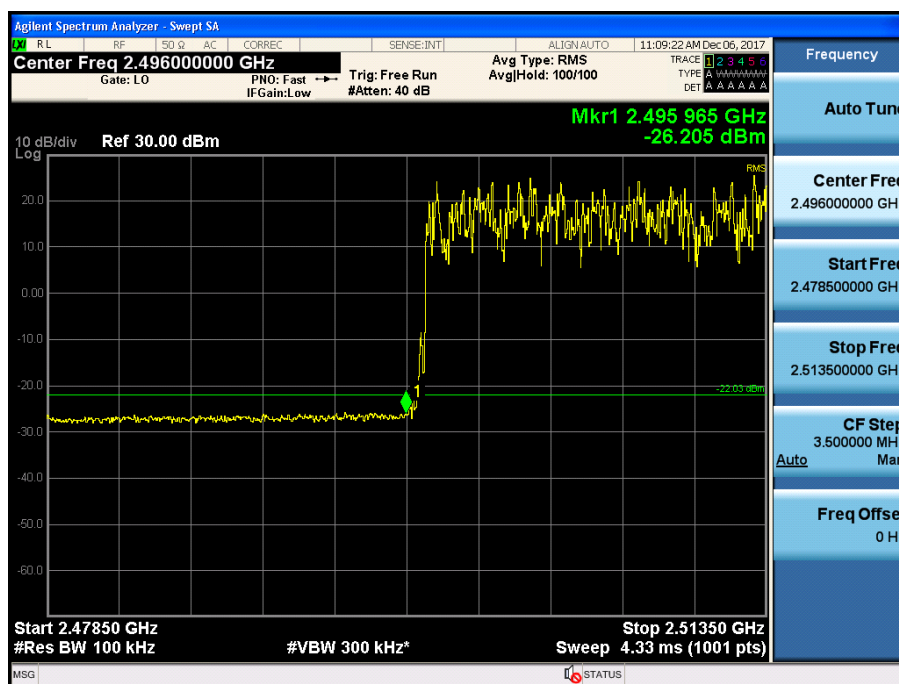


[Downlink High]



Test Data at Output Port 5_16QAM

[Downlink Low]

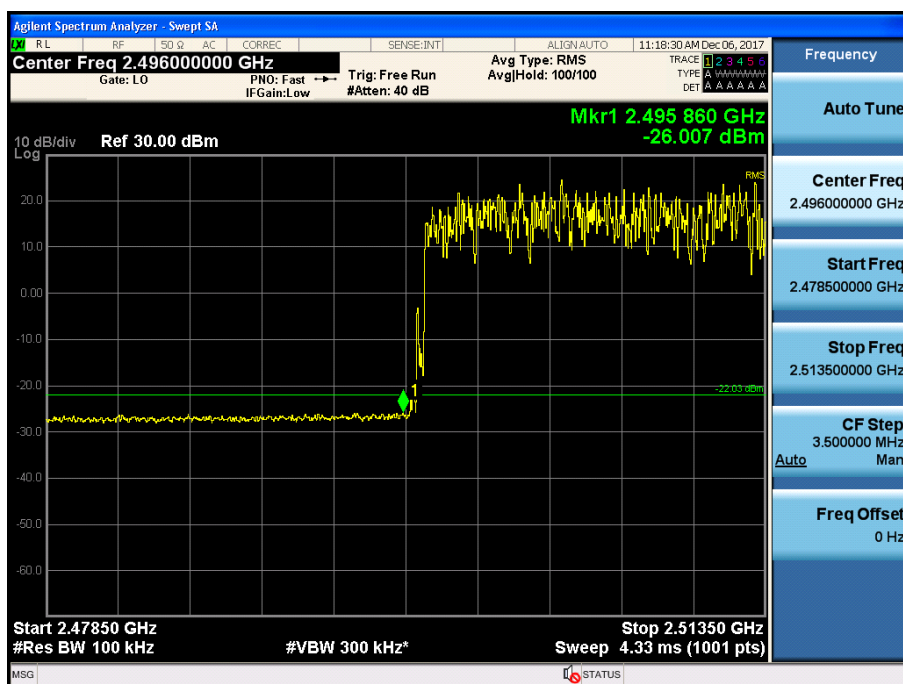


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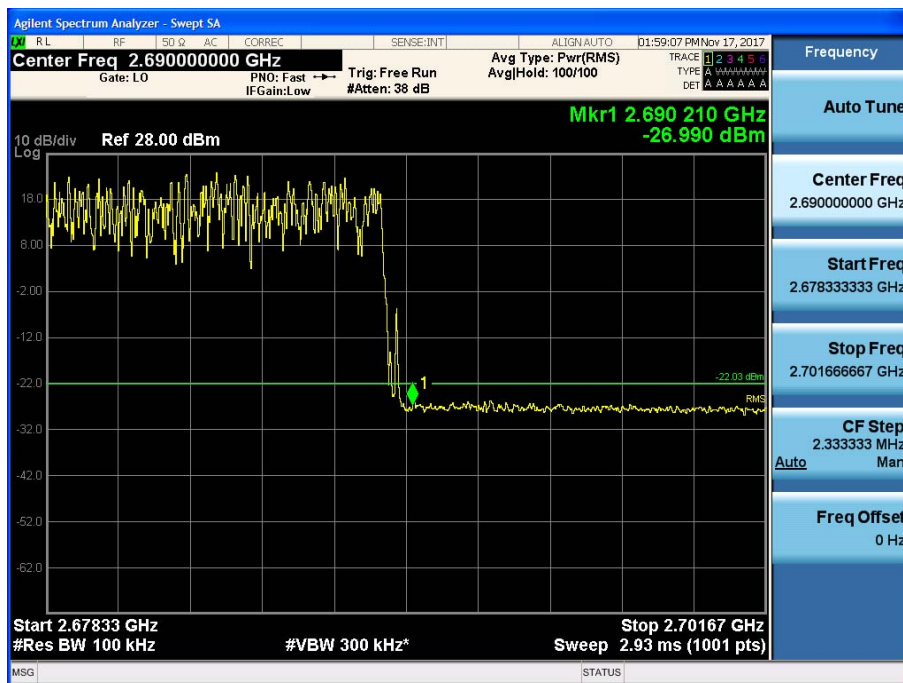


Test Data at Output Port 5_64QAM

[Downlink Low]

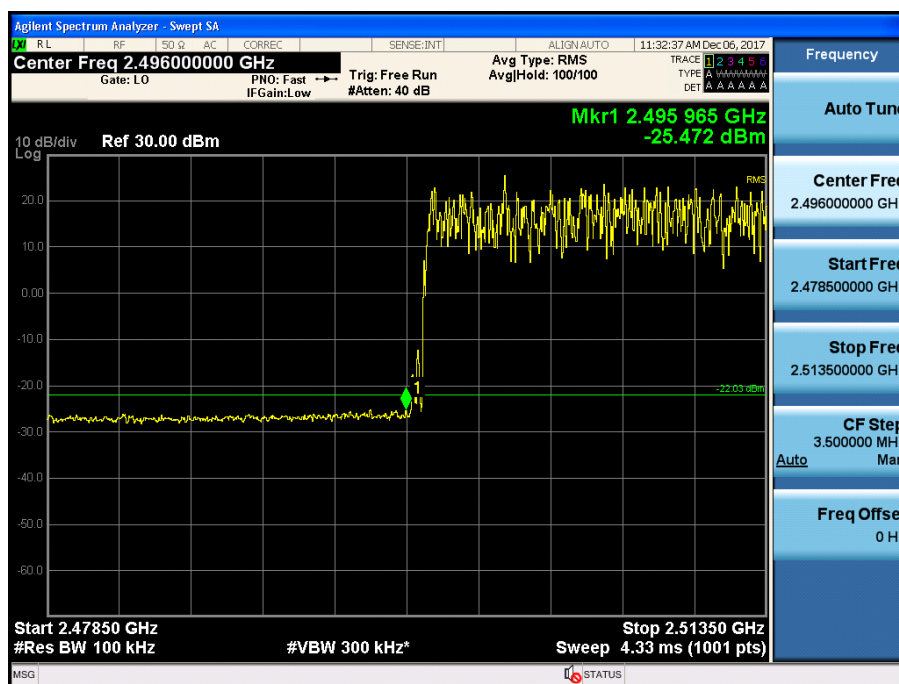


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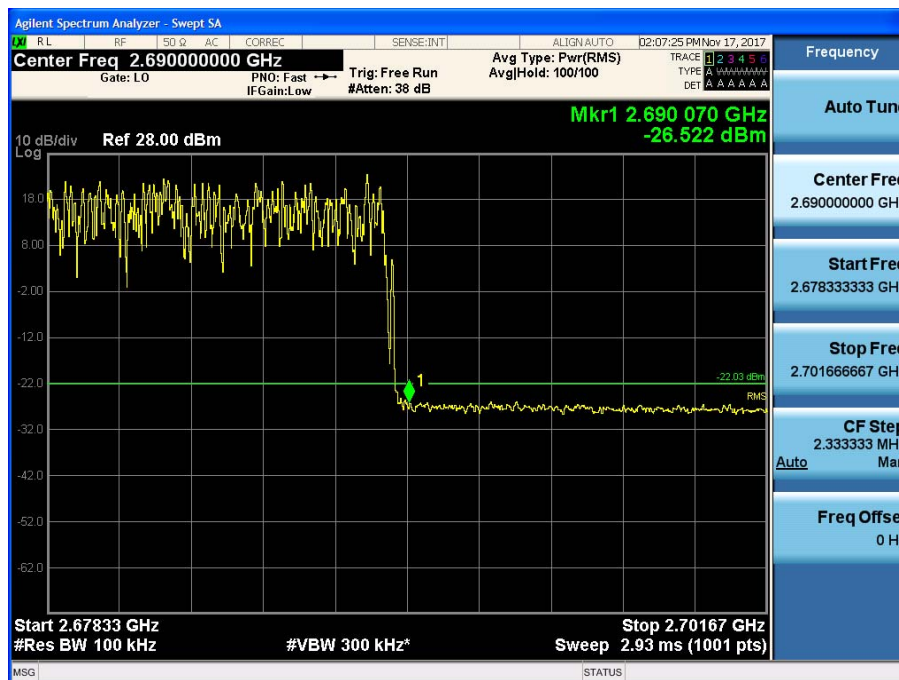


Test Data at Output Port 5_256QAM

[Downlink Low]

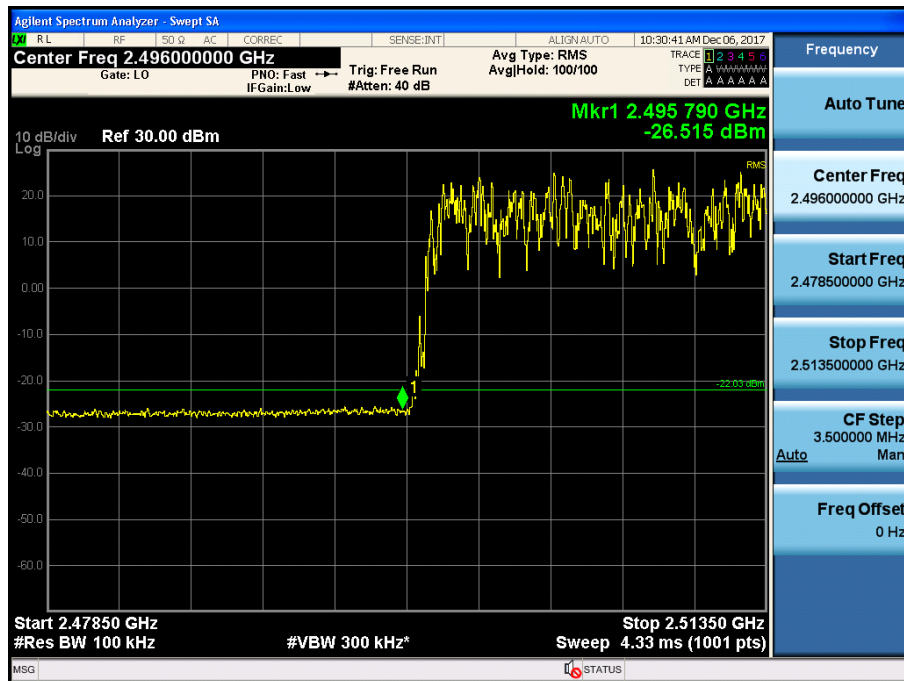


[Downlink High]



Test Data at Output Port 6_QPSK

[Downlink Low]

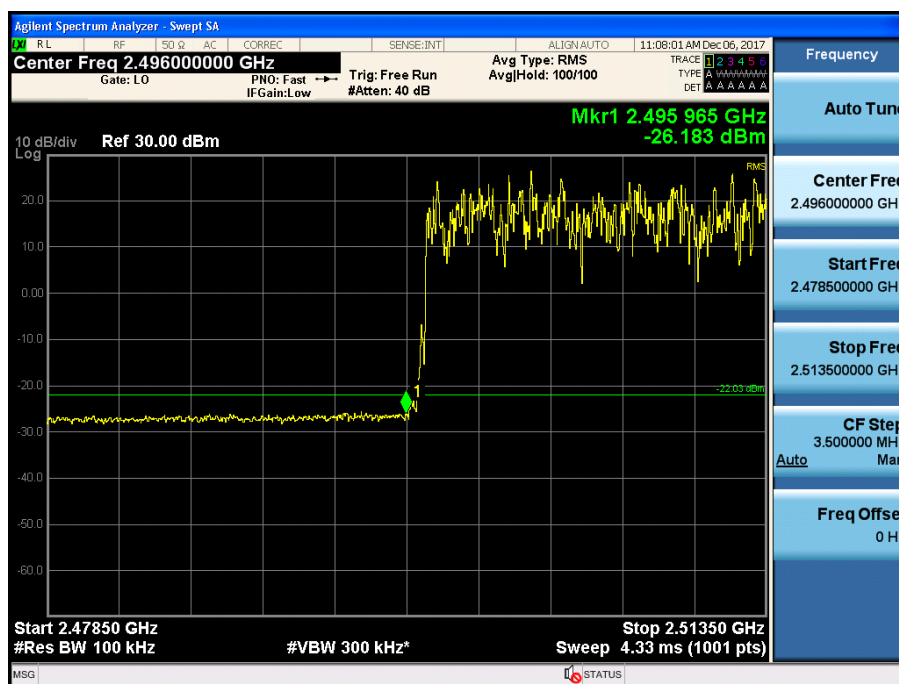


[Downlink High]



Test Data at Output Port 6_16QAM

[Downlink Low]

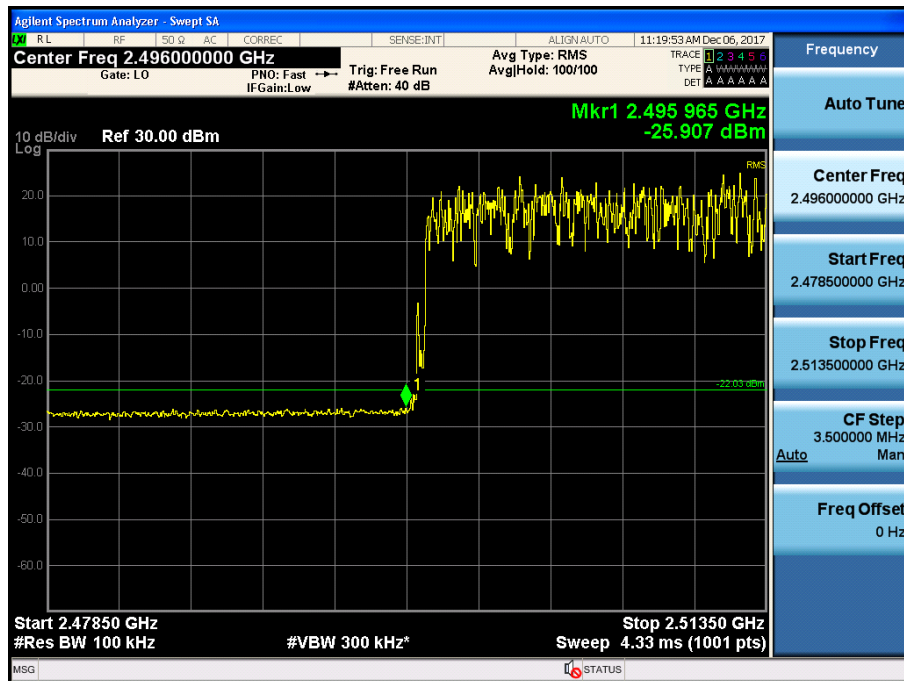


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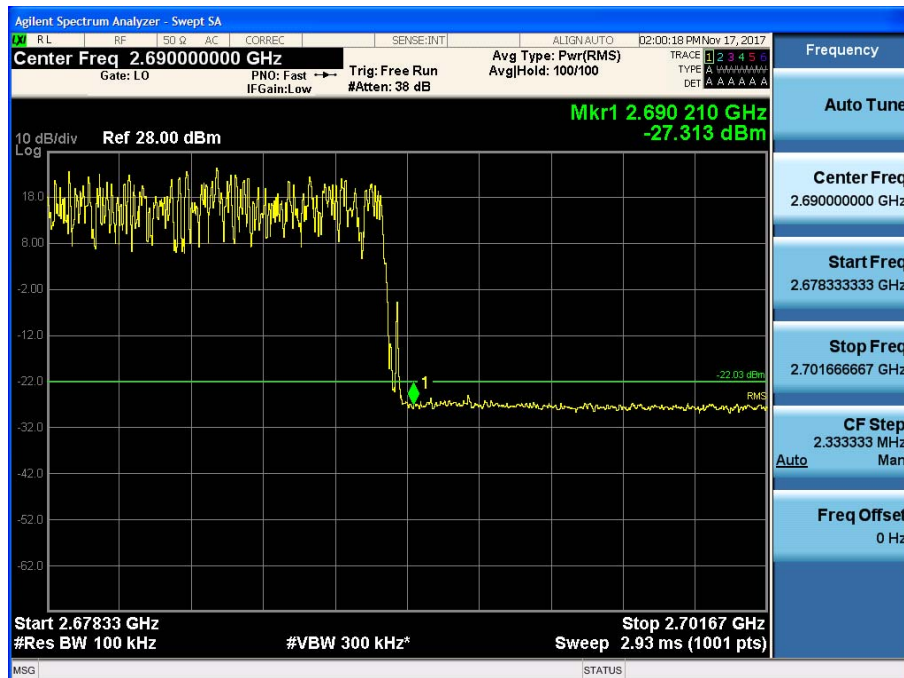


Test Data at Output Port 6_64QAM

[Downlink Low]

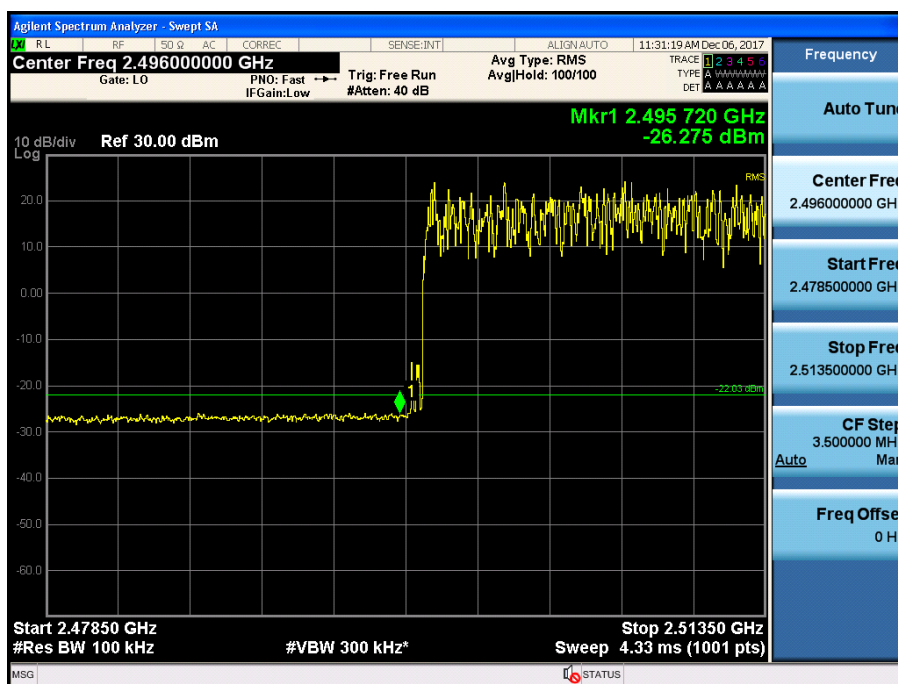


[Downlink High]



Test Data at Output Port 6_256QAM

[Downlink Low]

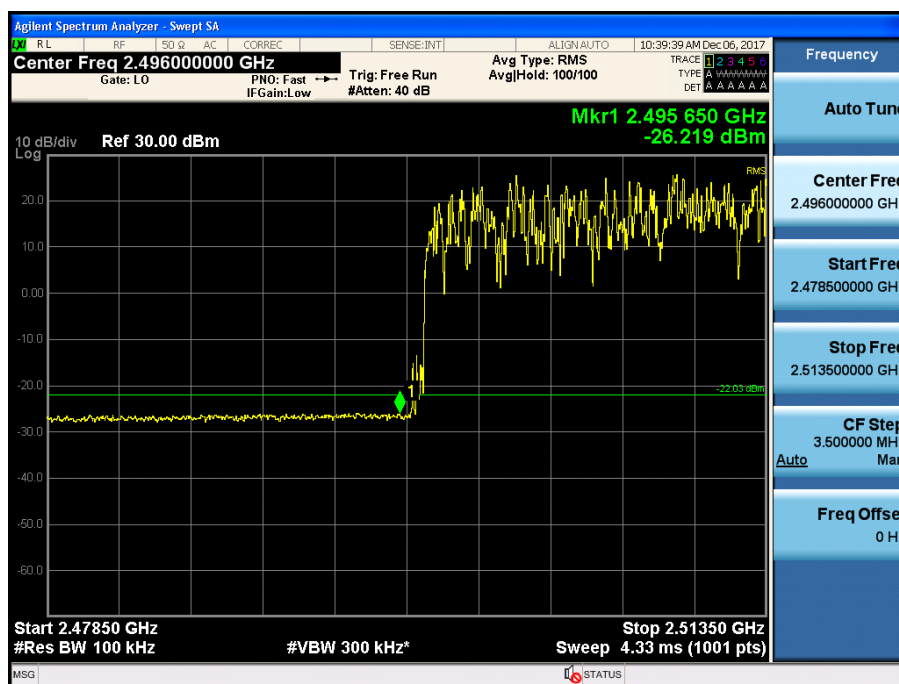


[Downlink High]



Test Data at Output Port 7_QPSK

[Downlink Low]

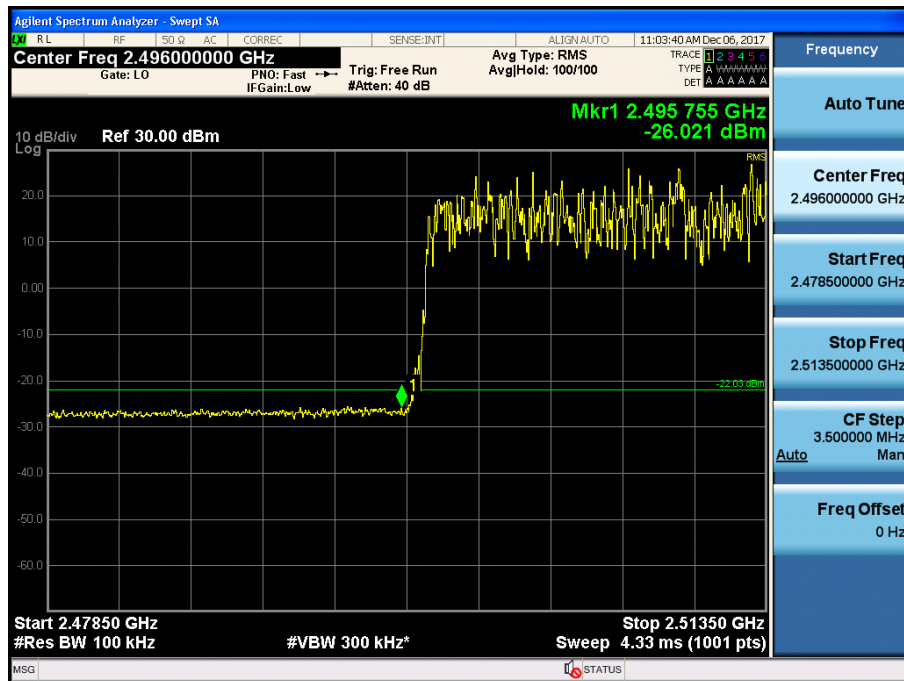


[Downlink High]



Test Data at Output Port 7_16QAM

[Downlink Low]

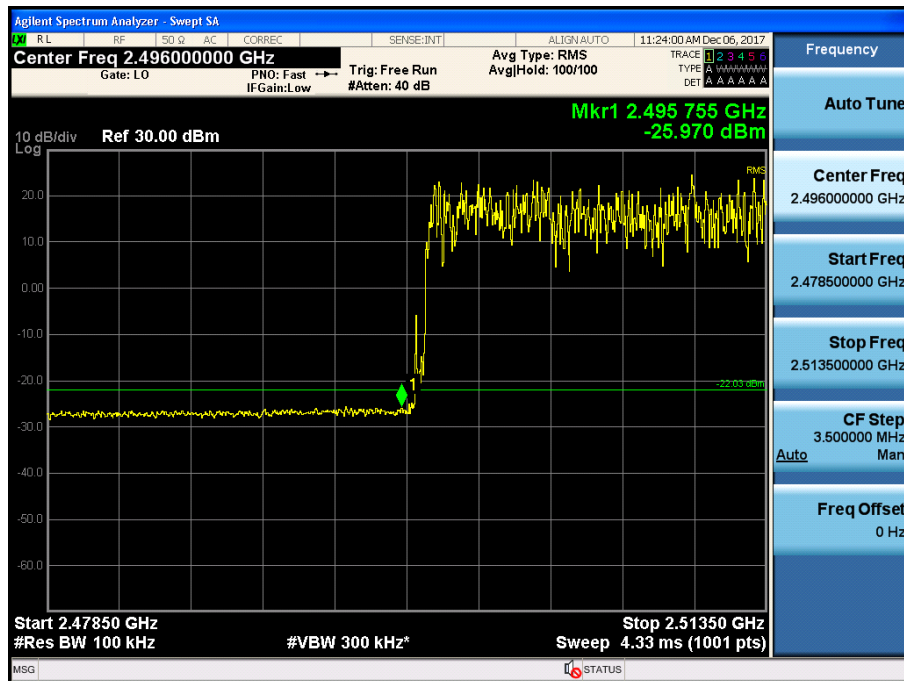


[Downlink High]



Test Data at Output Port 7_64QAM

[Downlink Low]

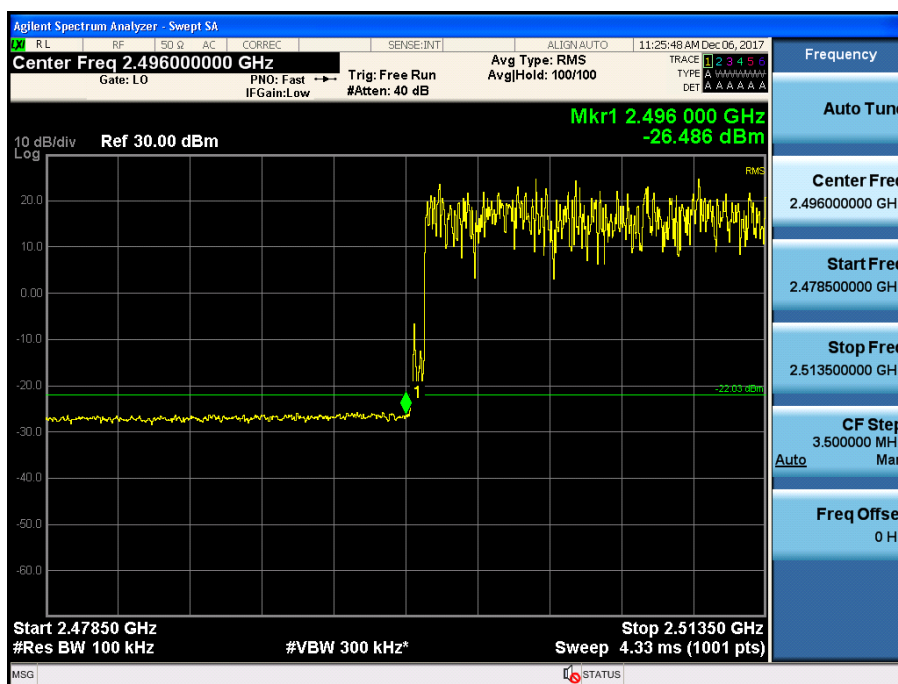


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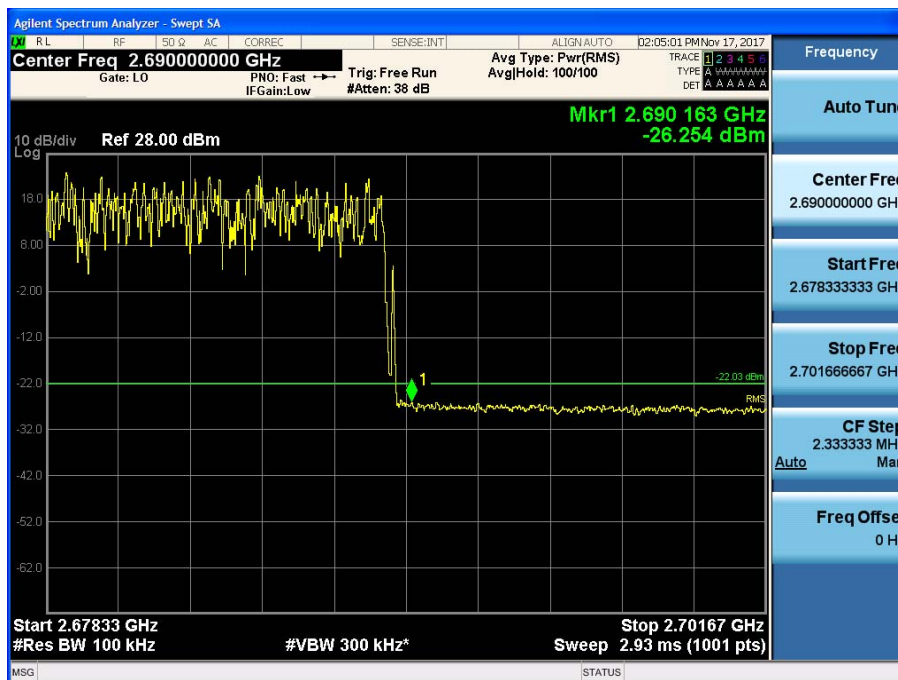


Test Data at Output Port 7_256QAM

[Downlink Low]



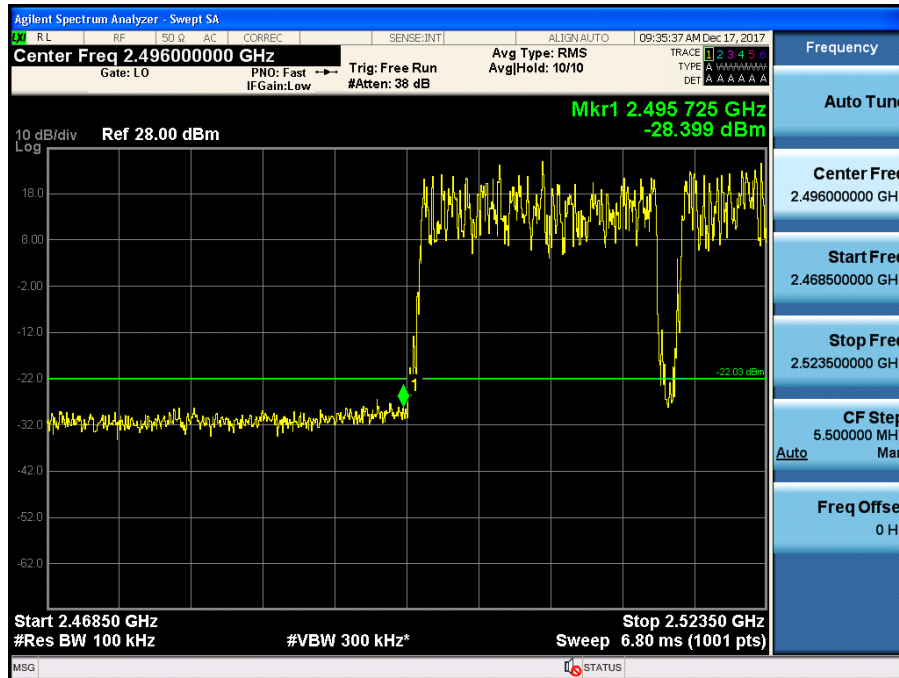
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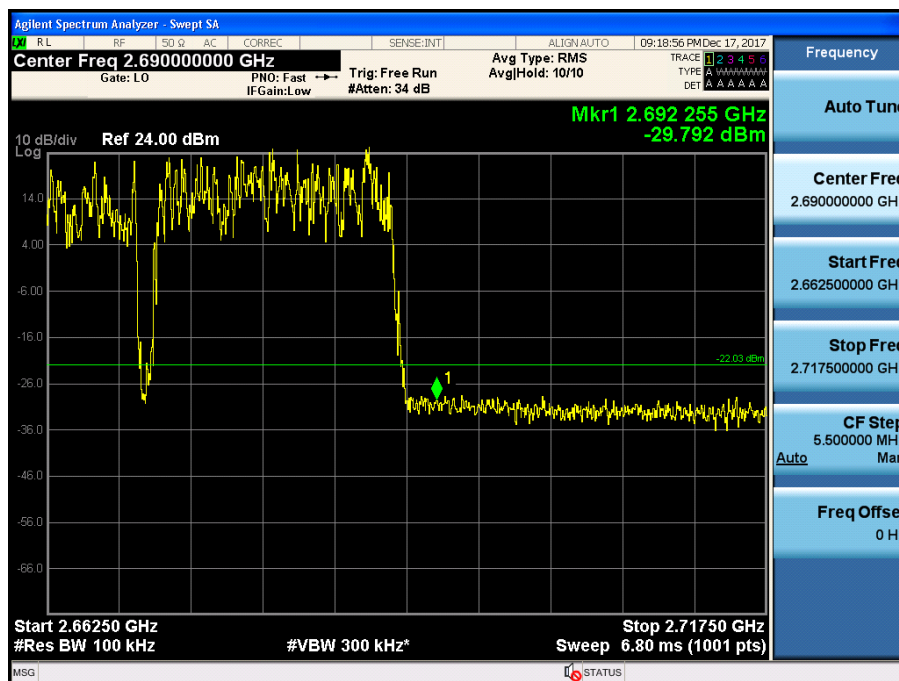
20 MHz +15 MHz +20 MHz / 3 Carriers (6.67 W + 5 W + 6.67 W)

Test Data at Output Port 0_QPSK

[Downlink Low]

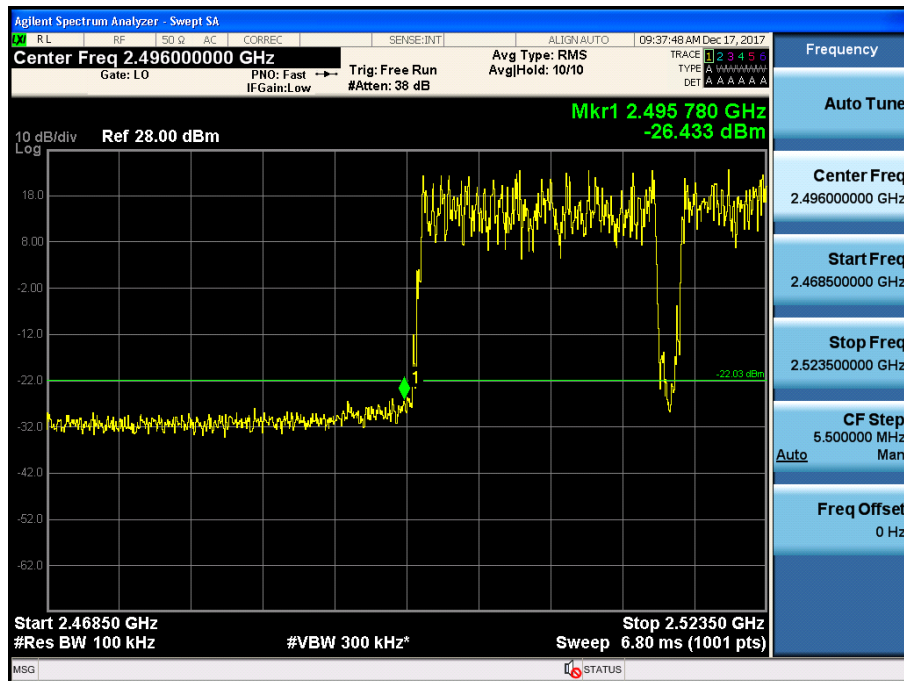


[Downlink High]

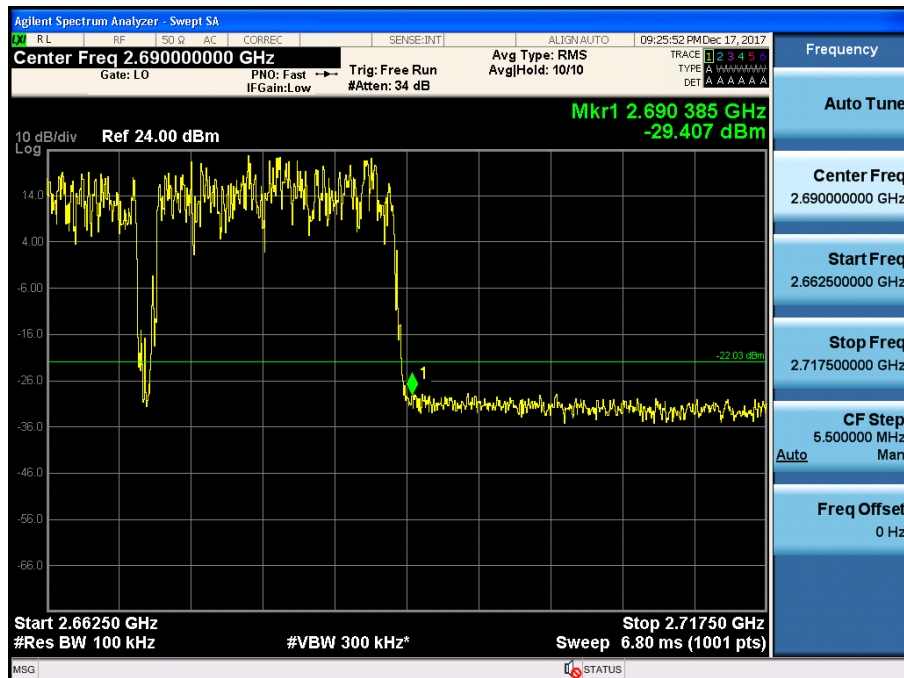


Test Data at Output Port 0_16QAM

[Downlink Low]

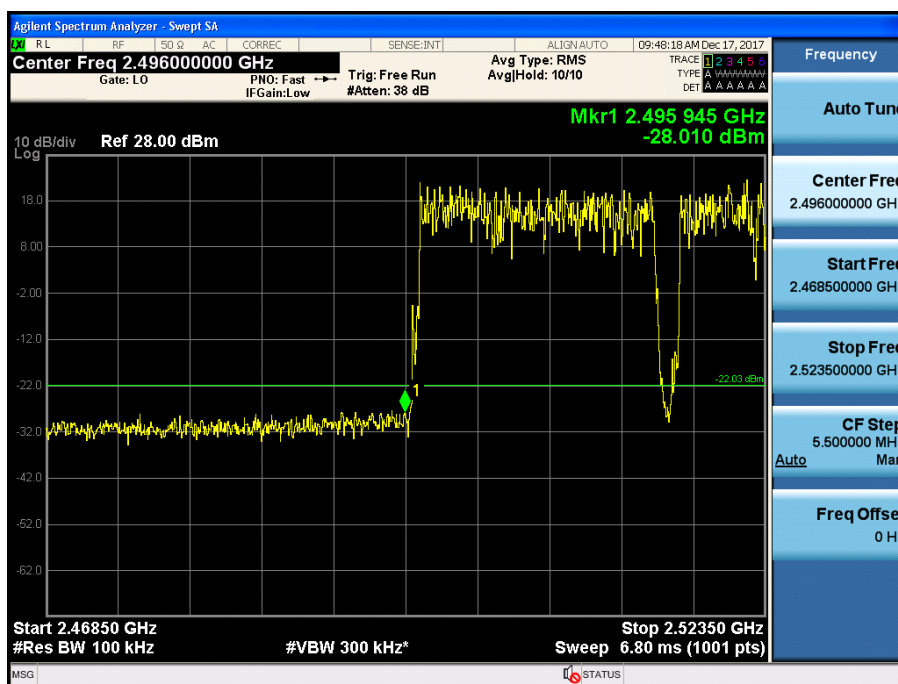


[Downlink High]

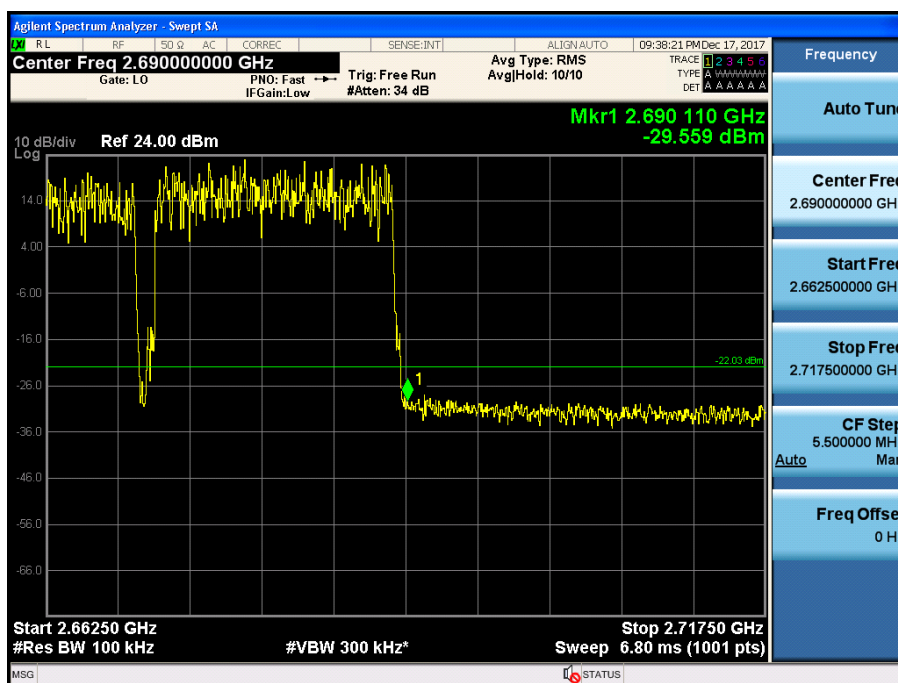


Test Data at Output Port 0_64QAM

[Downlink Low]

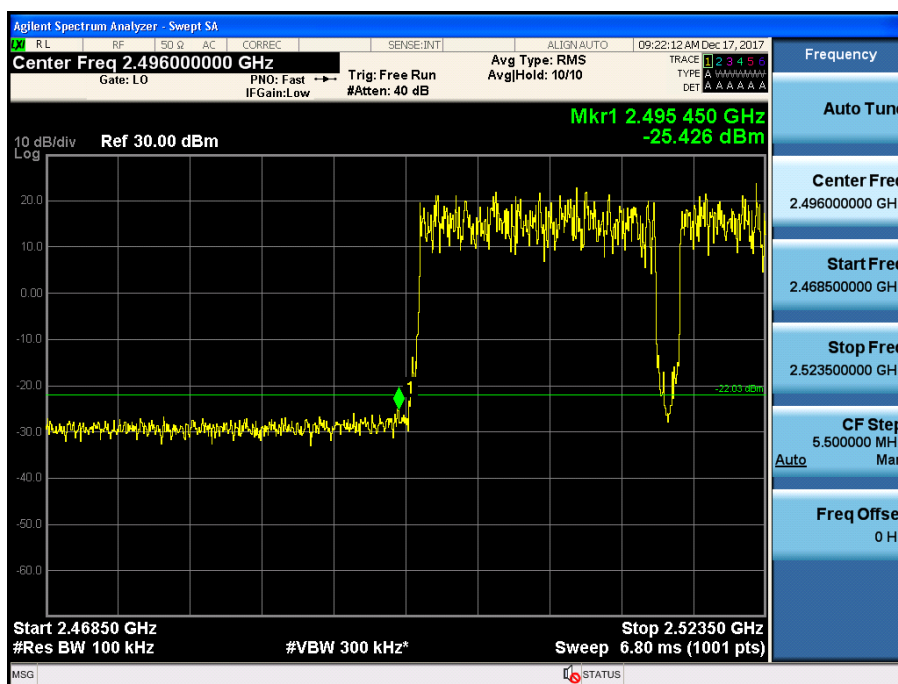


[Downlink High]

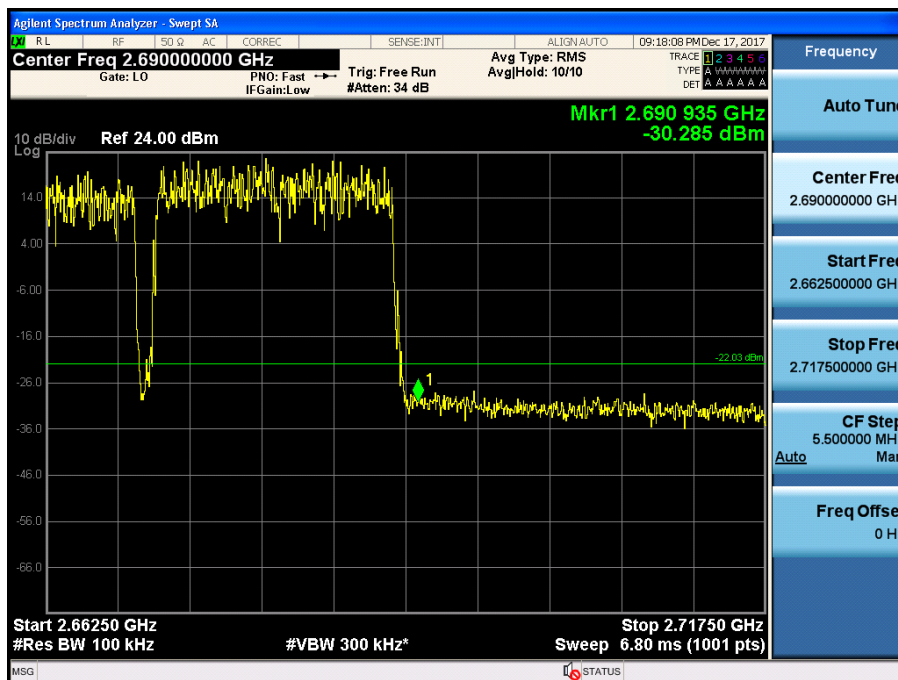


Test Data at Output Port 0_256QAM

[Downlink Low]

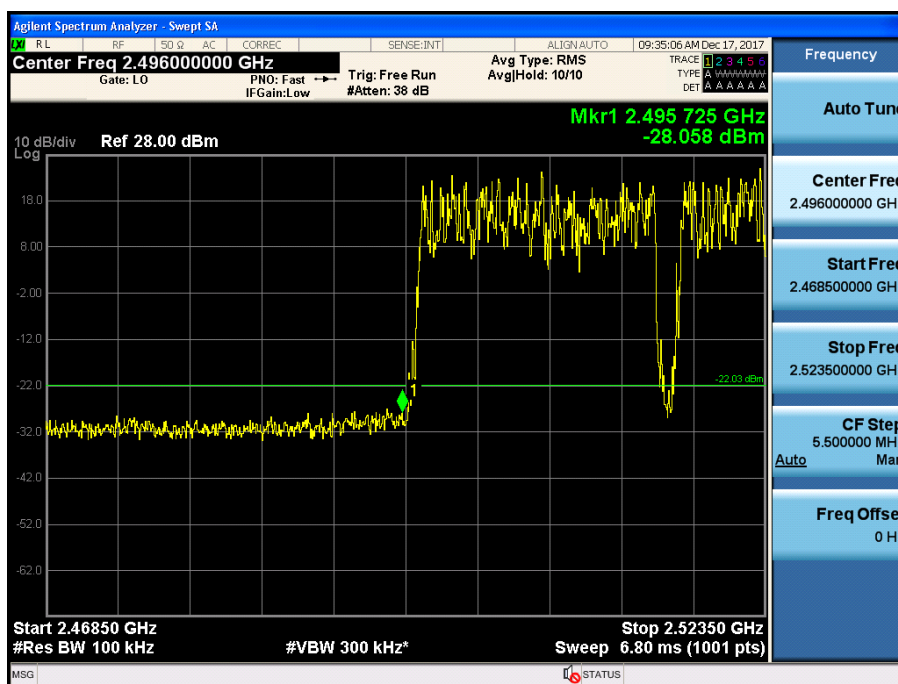


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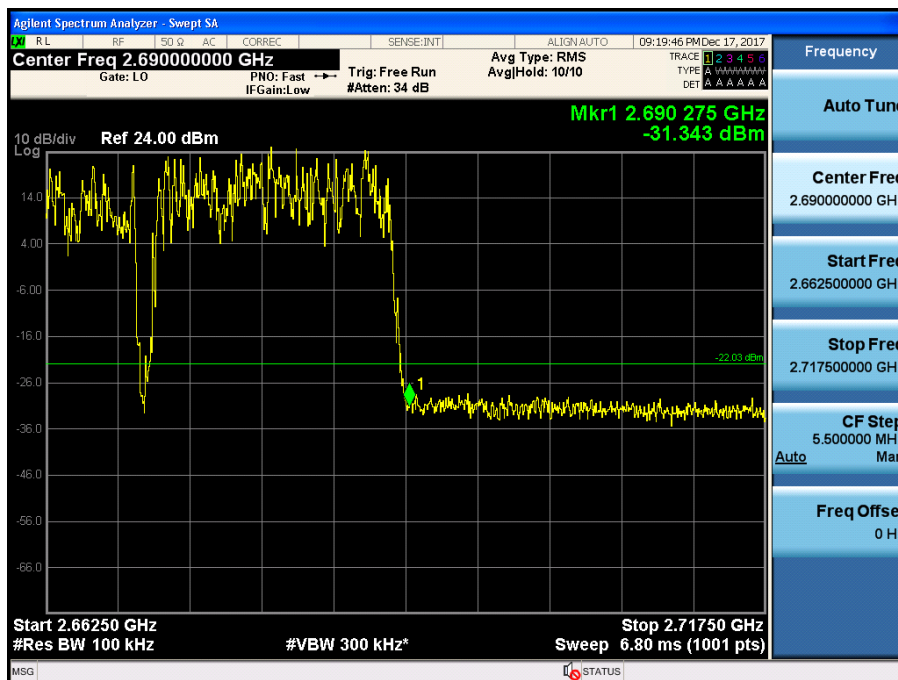


Test Data at Output Port 1_QPSK

[Downlink Low]

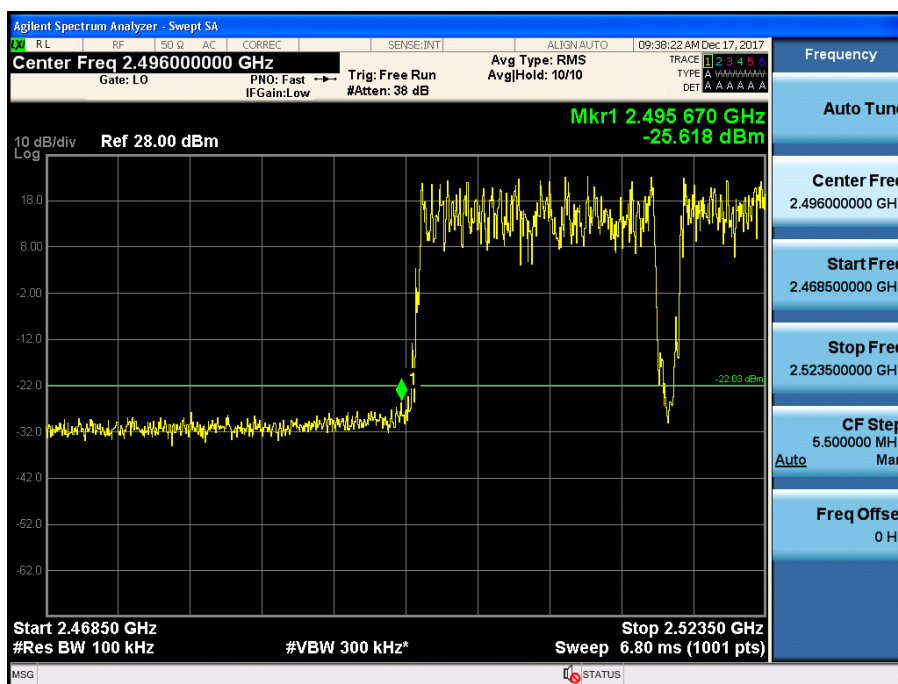


[Downlink High]

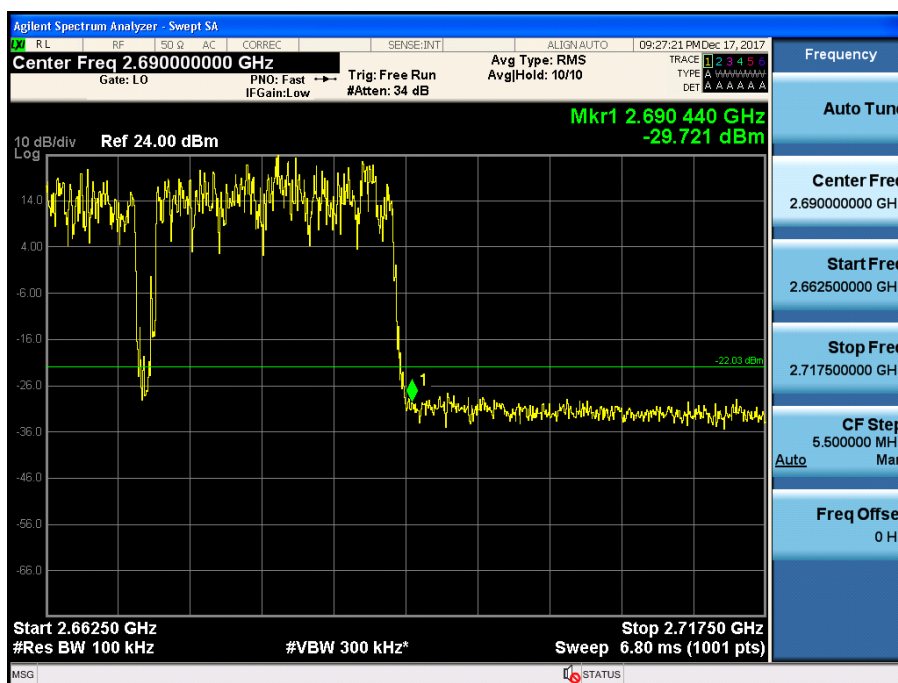


Test Data at Output Port 1_16QAM

[Downlink Low]

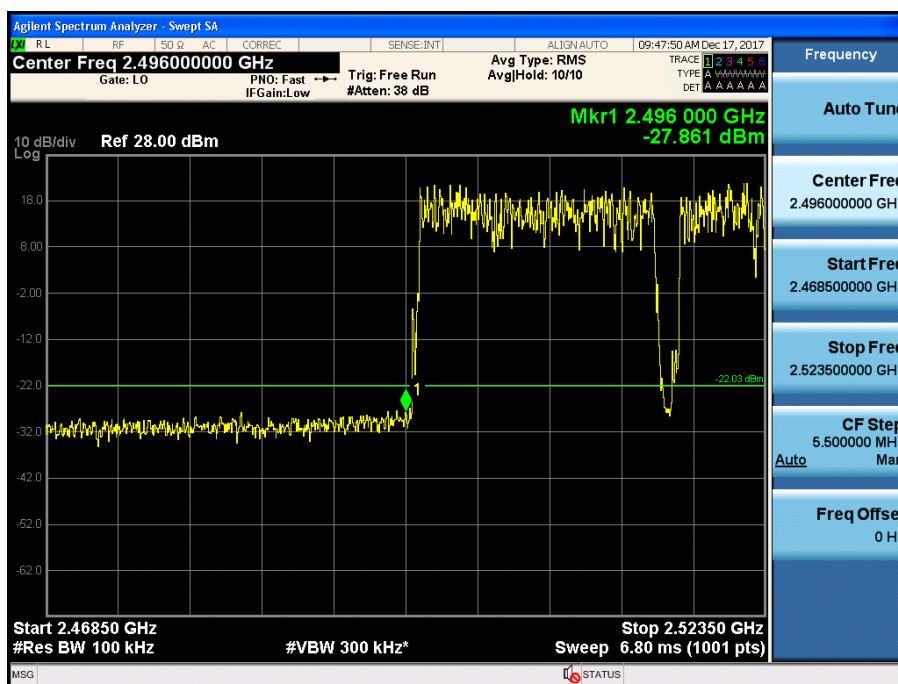


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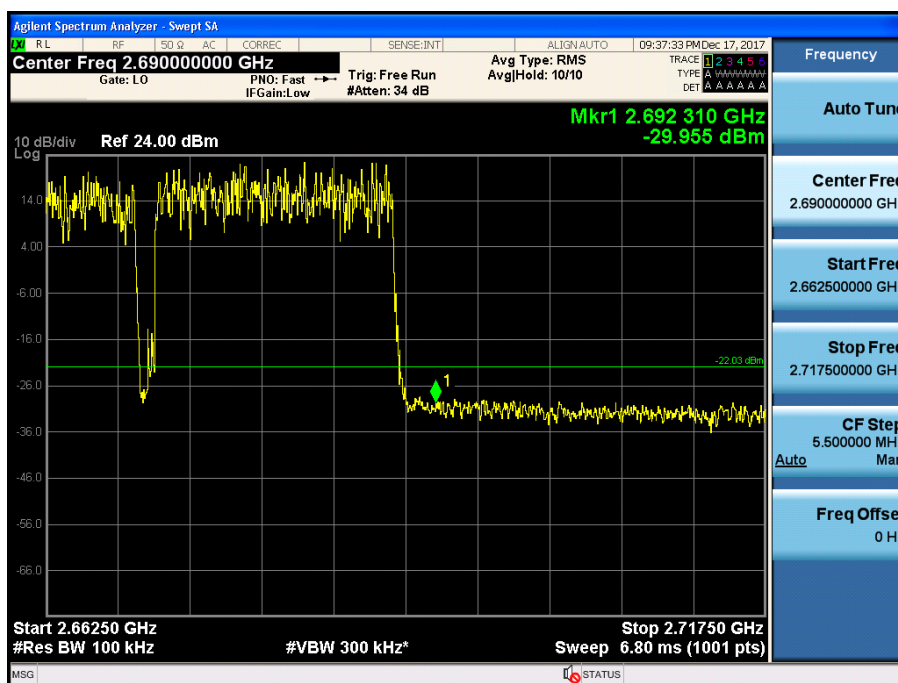


Test Data at Output Port 1_64QAM

[Downlink Low]

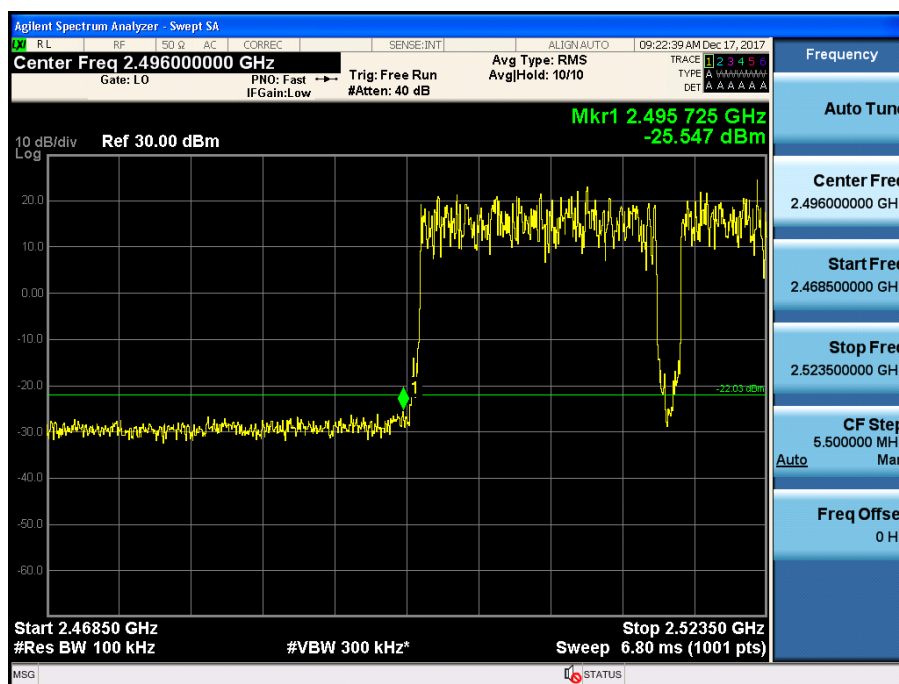


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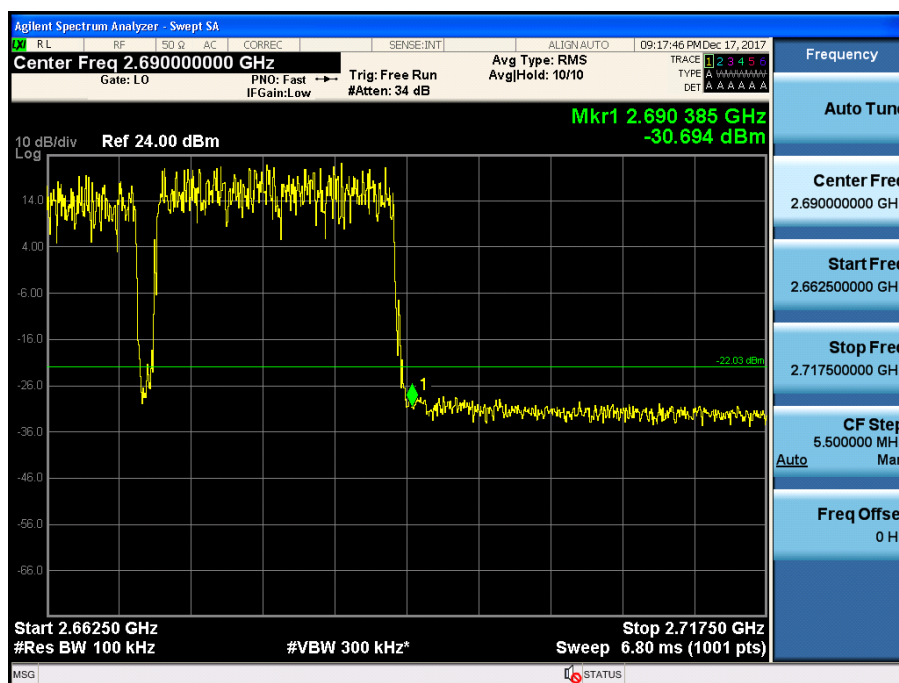


Test Data at Output Port 1_256QAM

[Downlink Low]

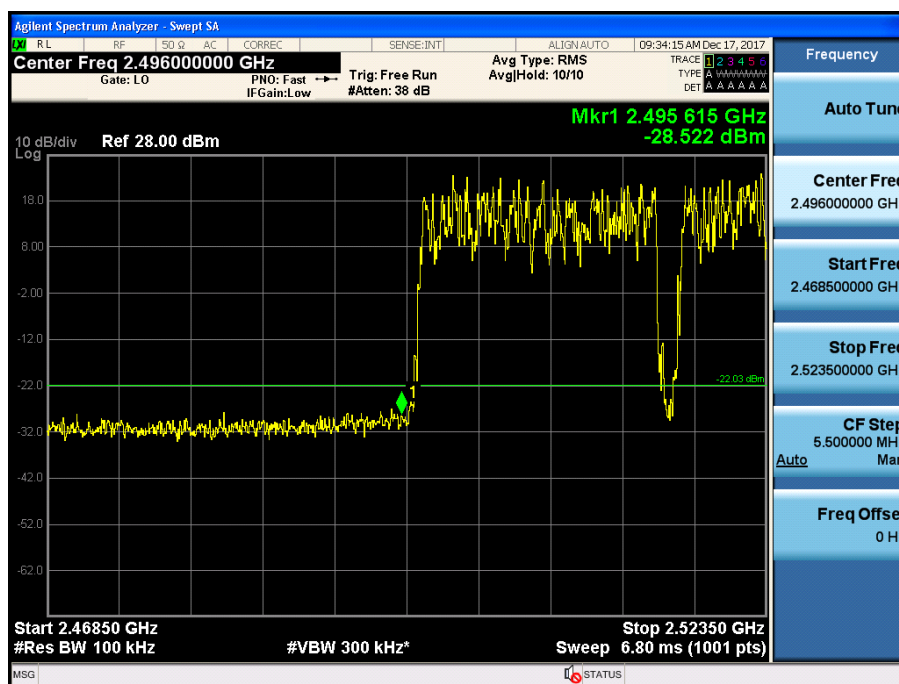


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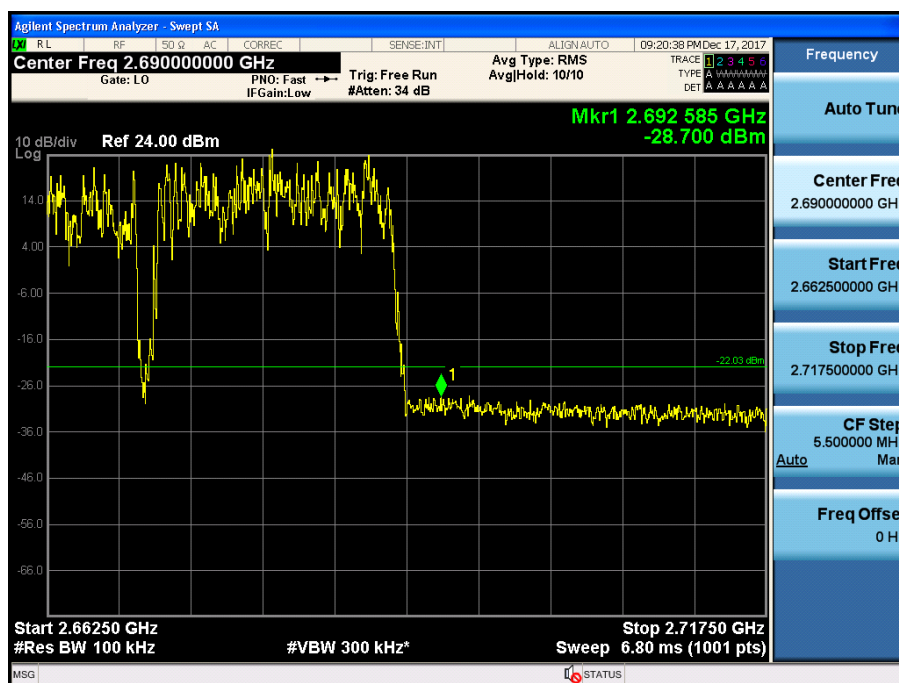


Test Data at Output Port 2_QPSK

[Downlink Low]

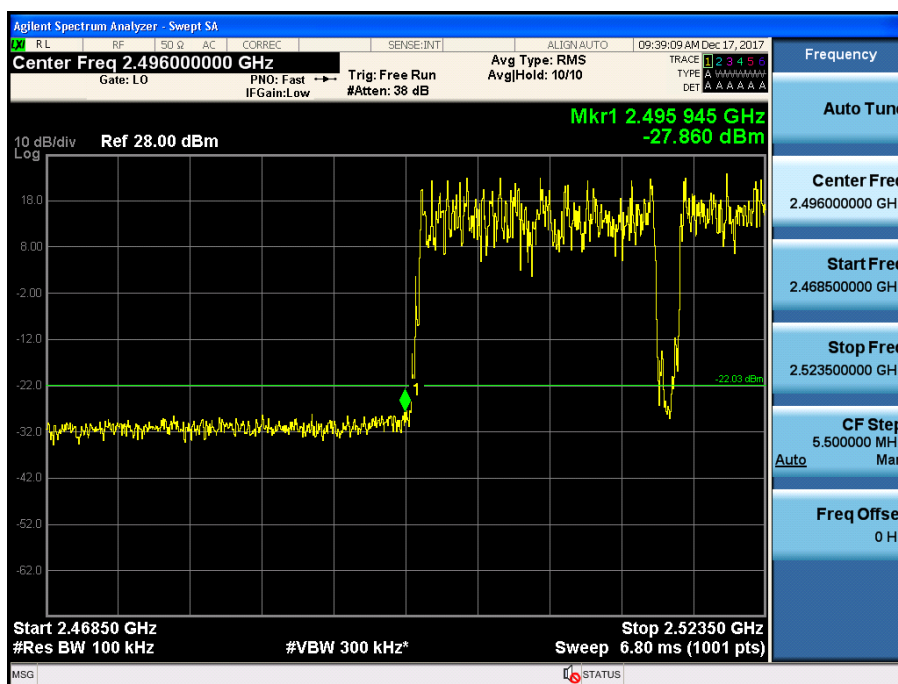


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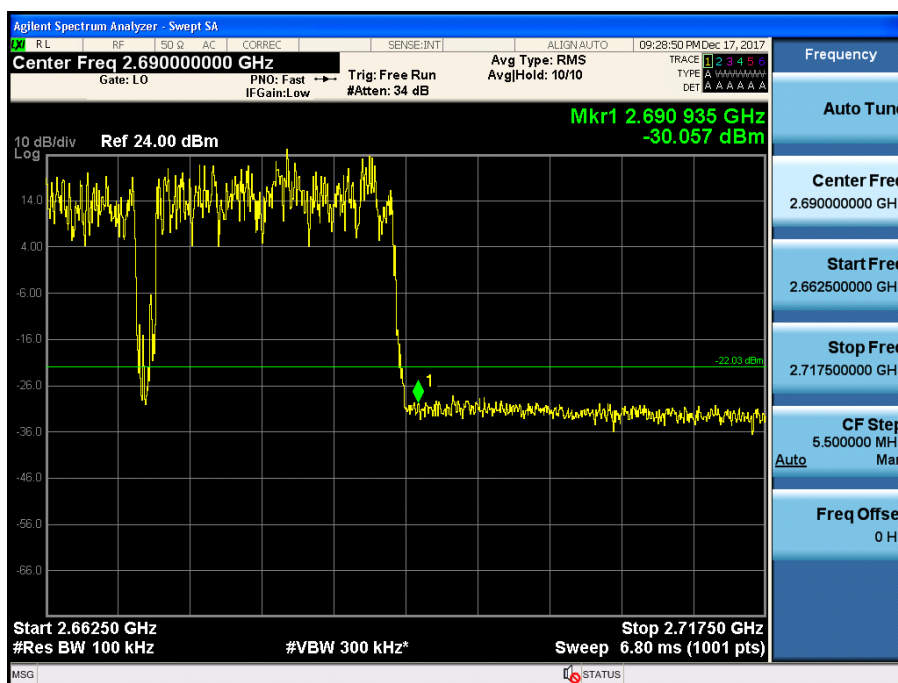


Test Data at Output Port 2_16QAM

[Downlink Low]

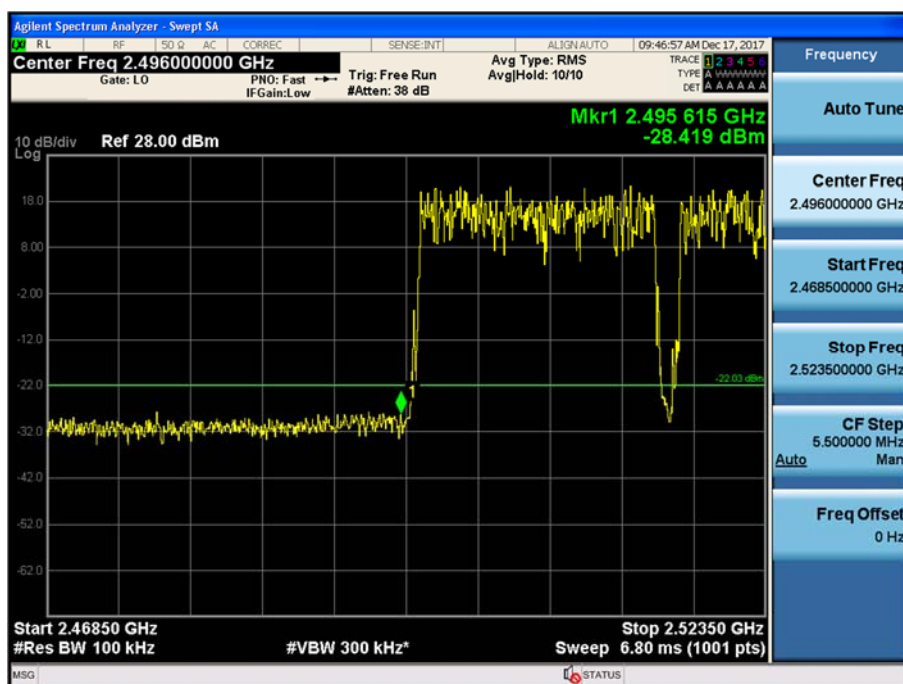


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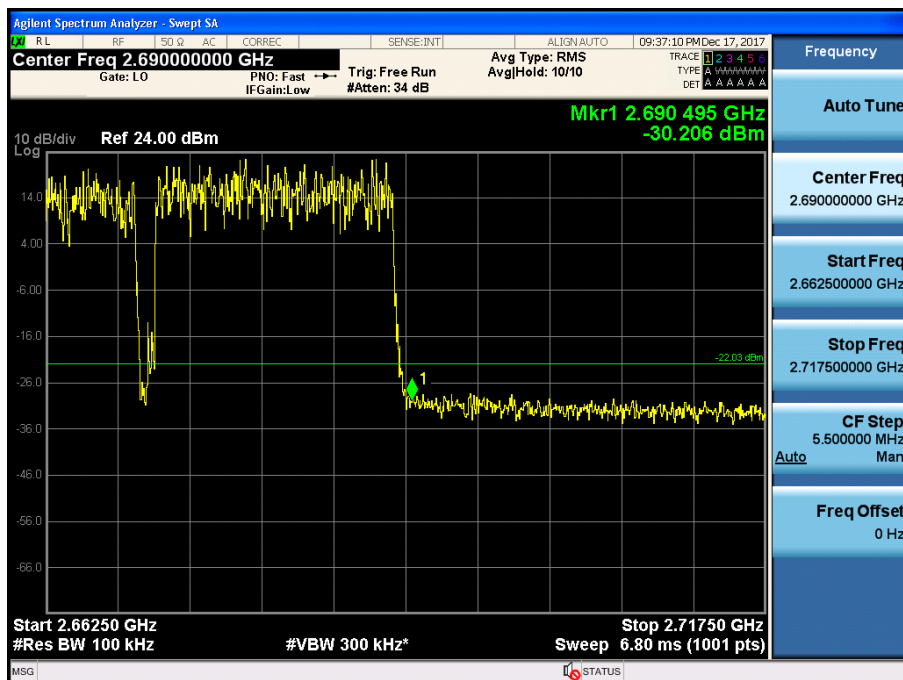


Test Data at Output Port 2_64QAM

[Downlink Low]

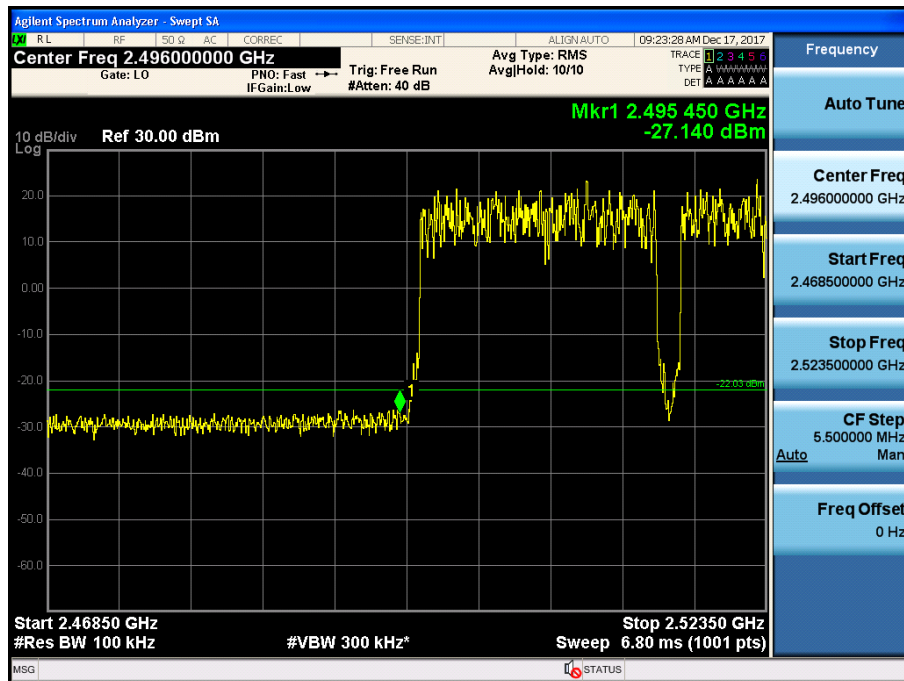


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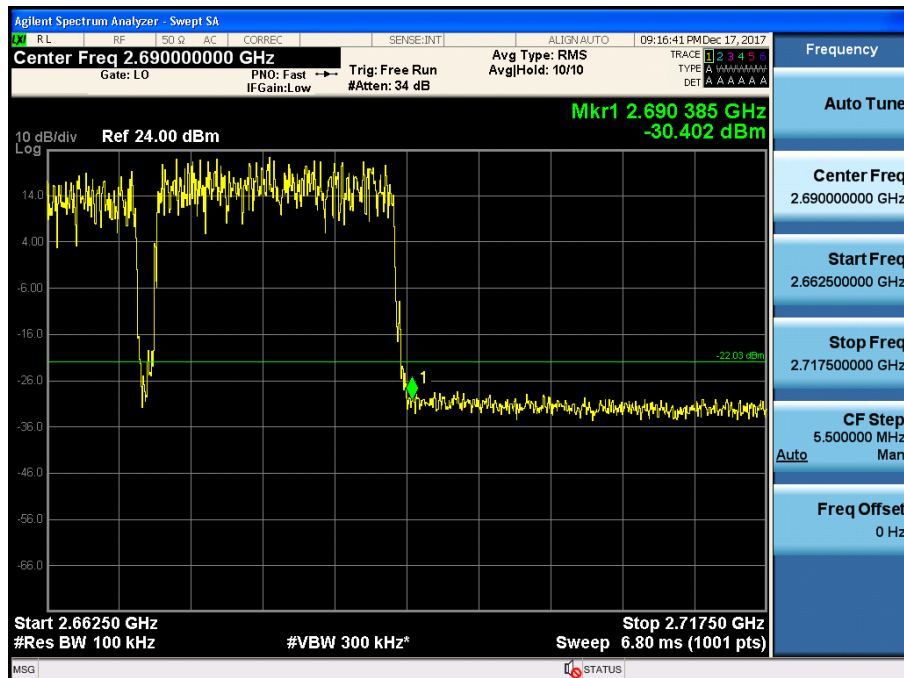


Test Data at Output Port 2_256QAM

[Downlink Low]

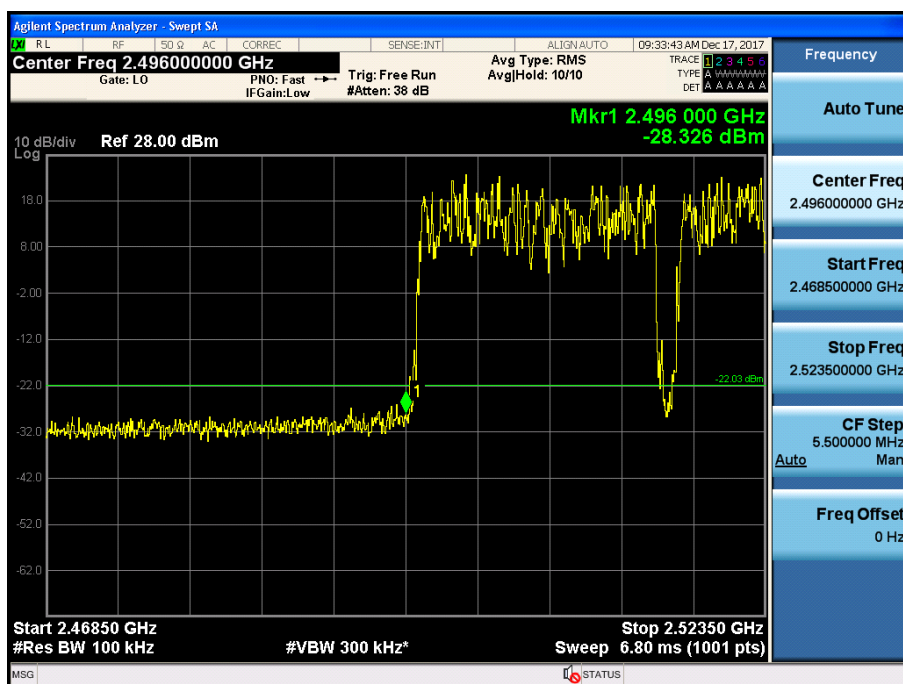


[Downlink High]



Test Data at Output Port 3_QPSK

[Downlink Low]



[Downlink High]

