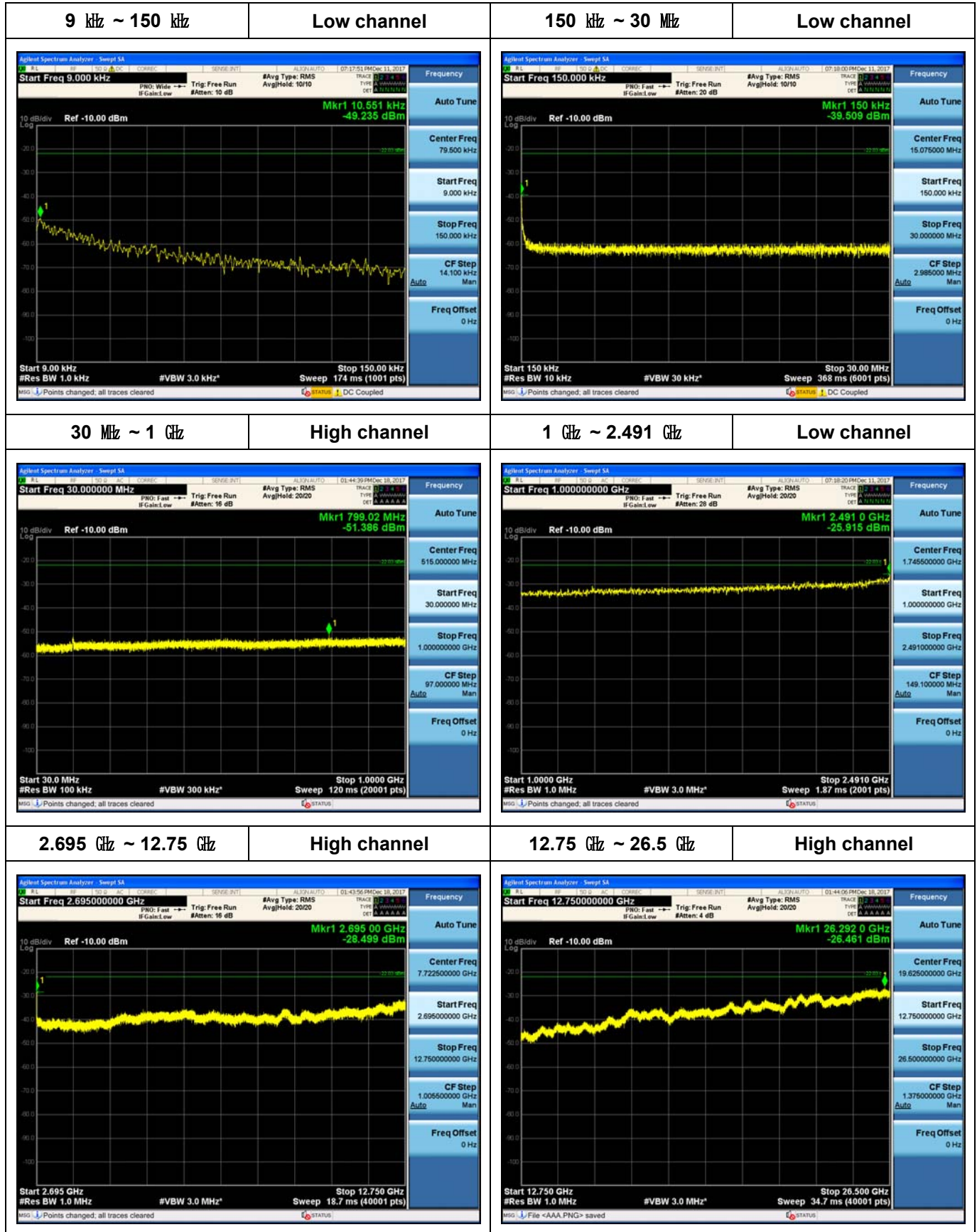
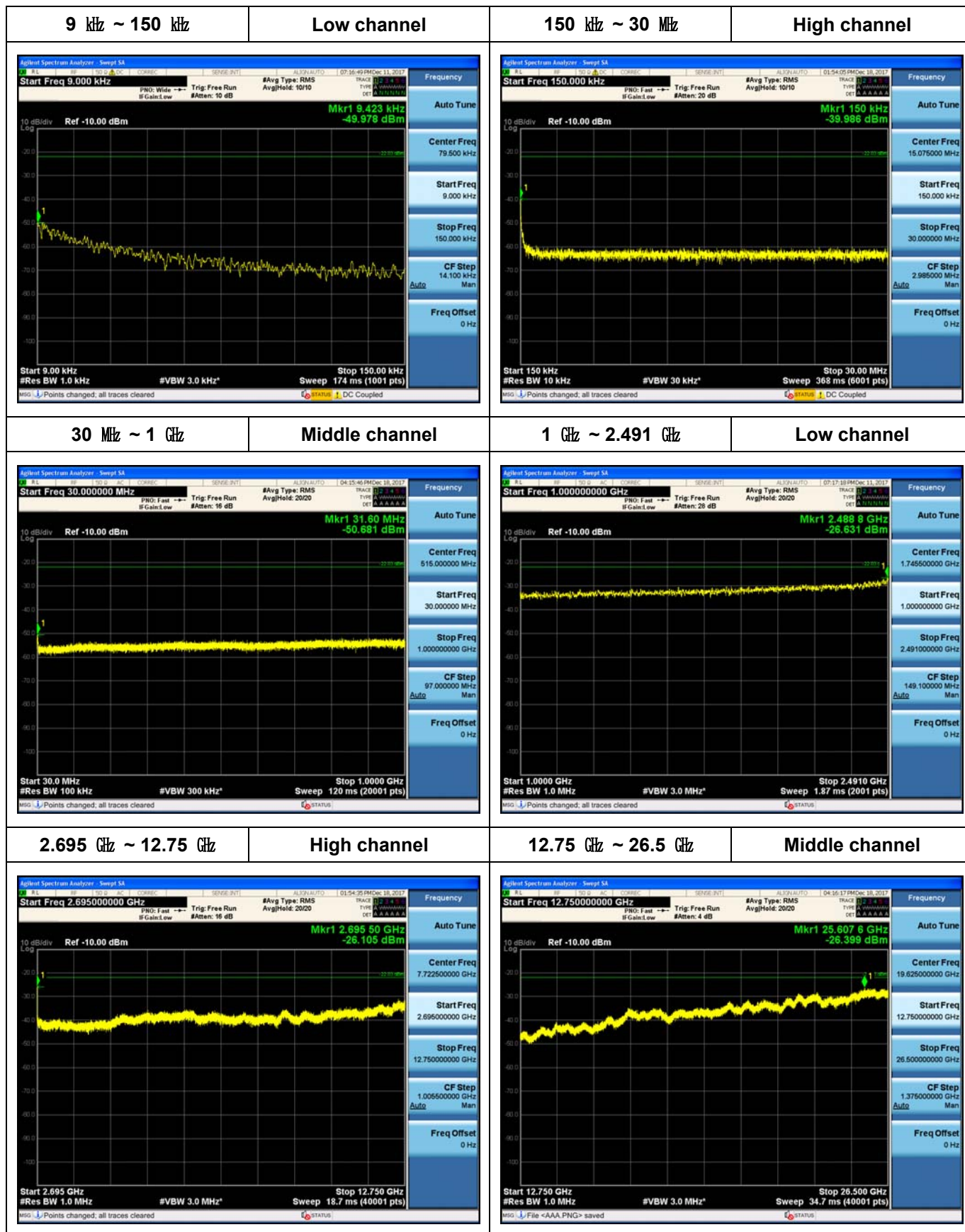


Plot Data for Output Port 6_256QAM

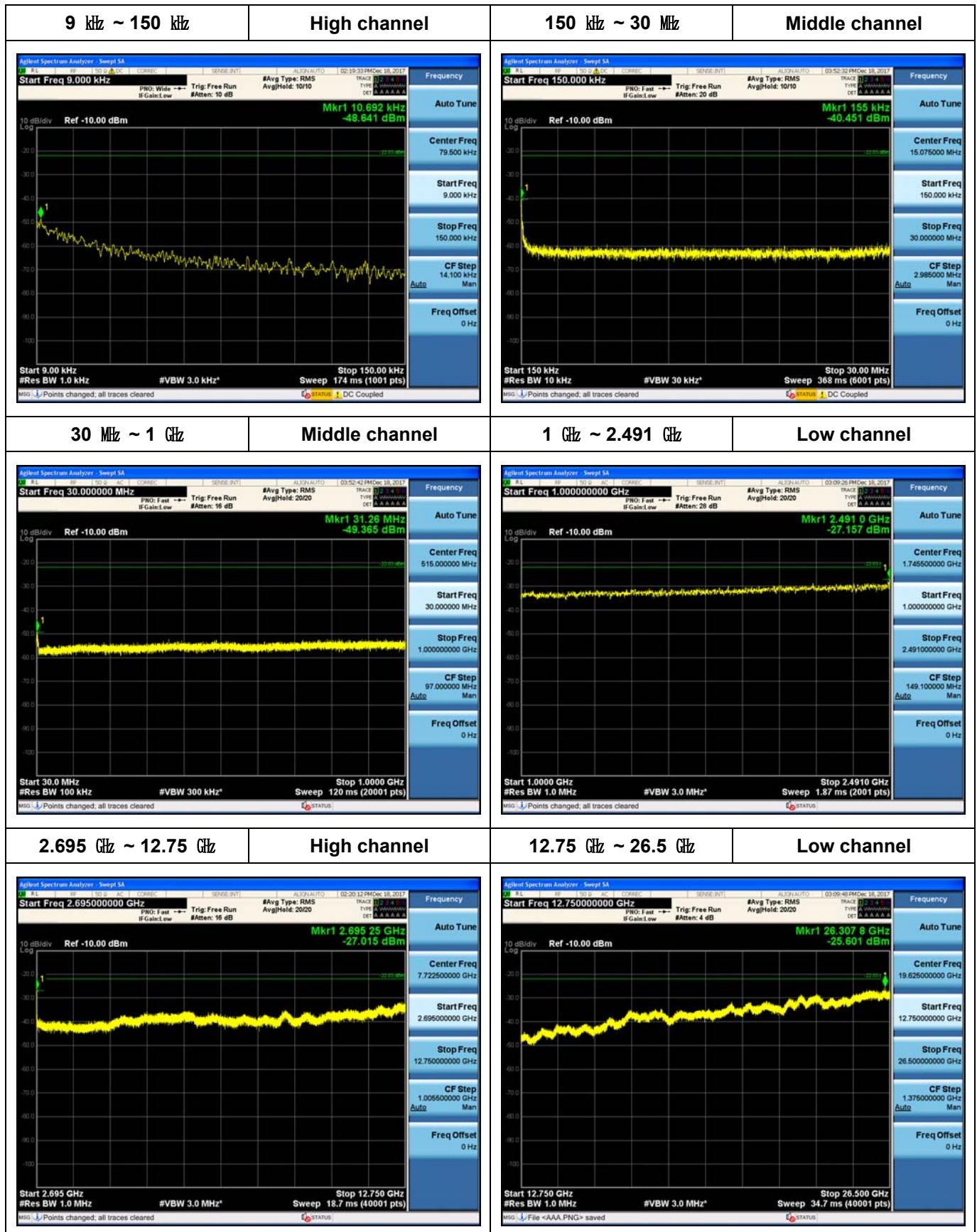


Plot Data for Output Port 7_256QAM

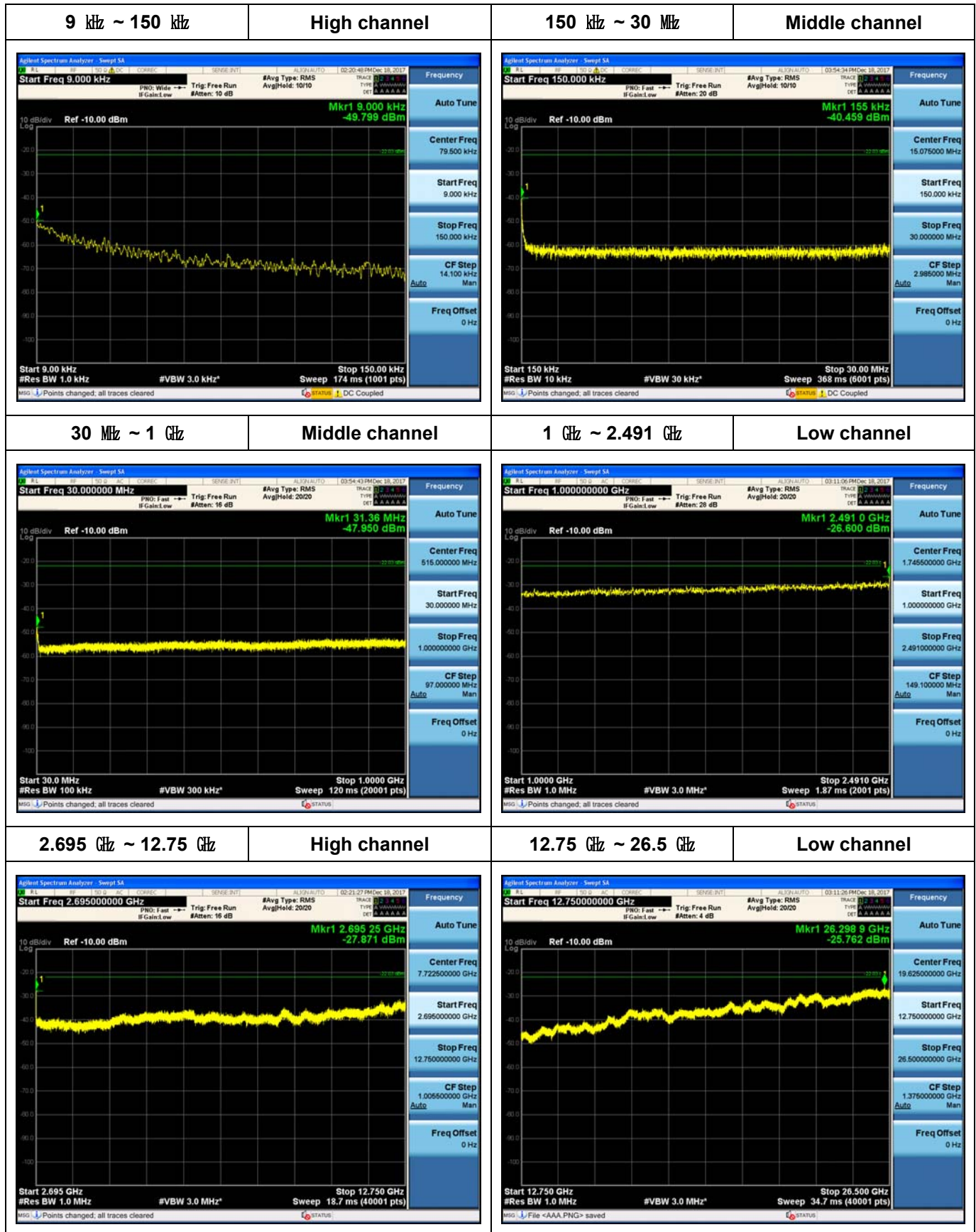


5 MHz + 5 MHz / 2 Carriers (5 W + 5 W)

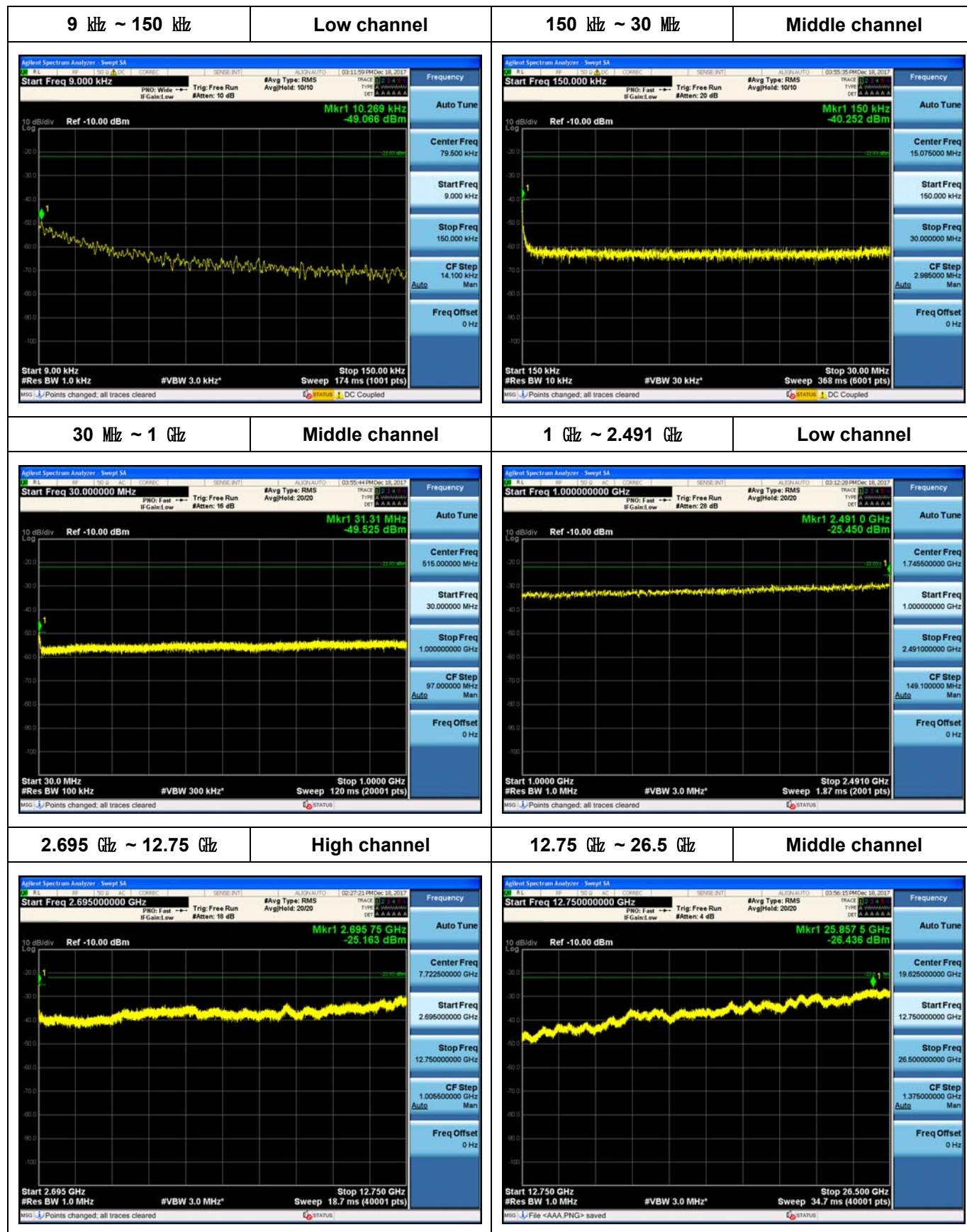
Plot Data for Output Port 0_256QAM



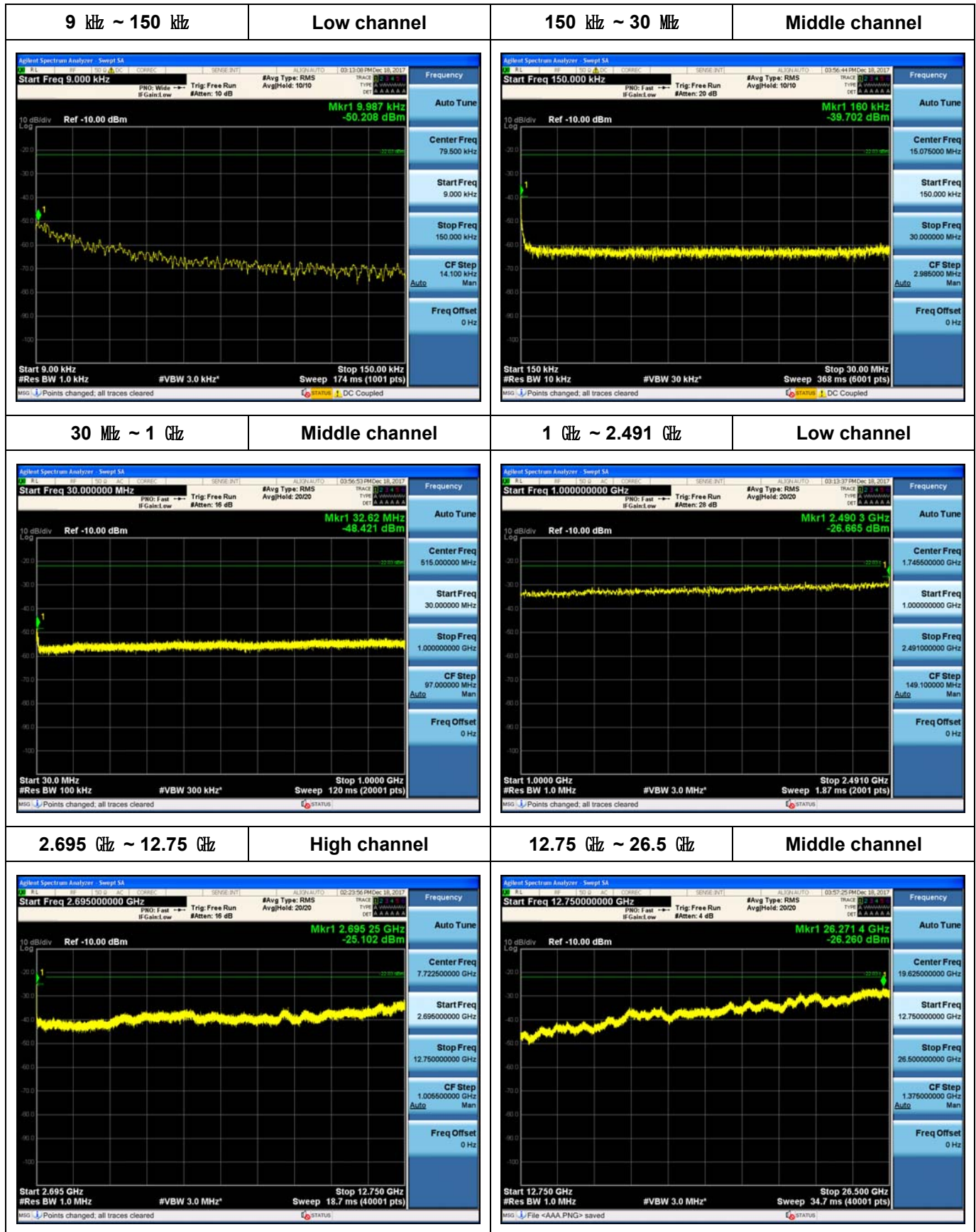
Plot Data for Output Port 1_256QAM



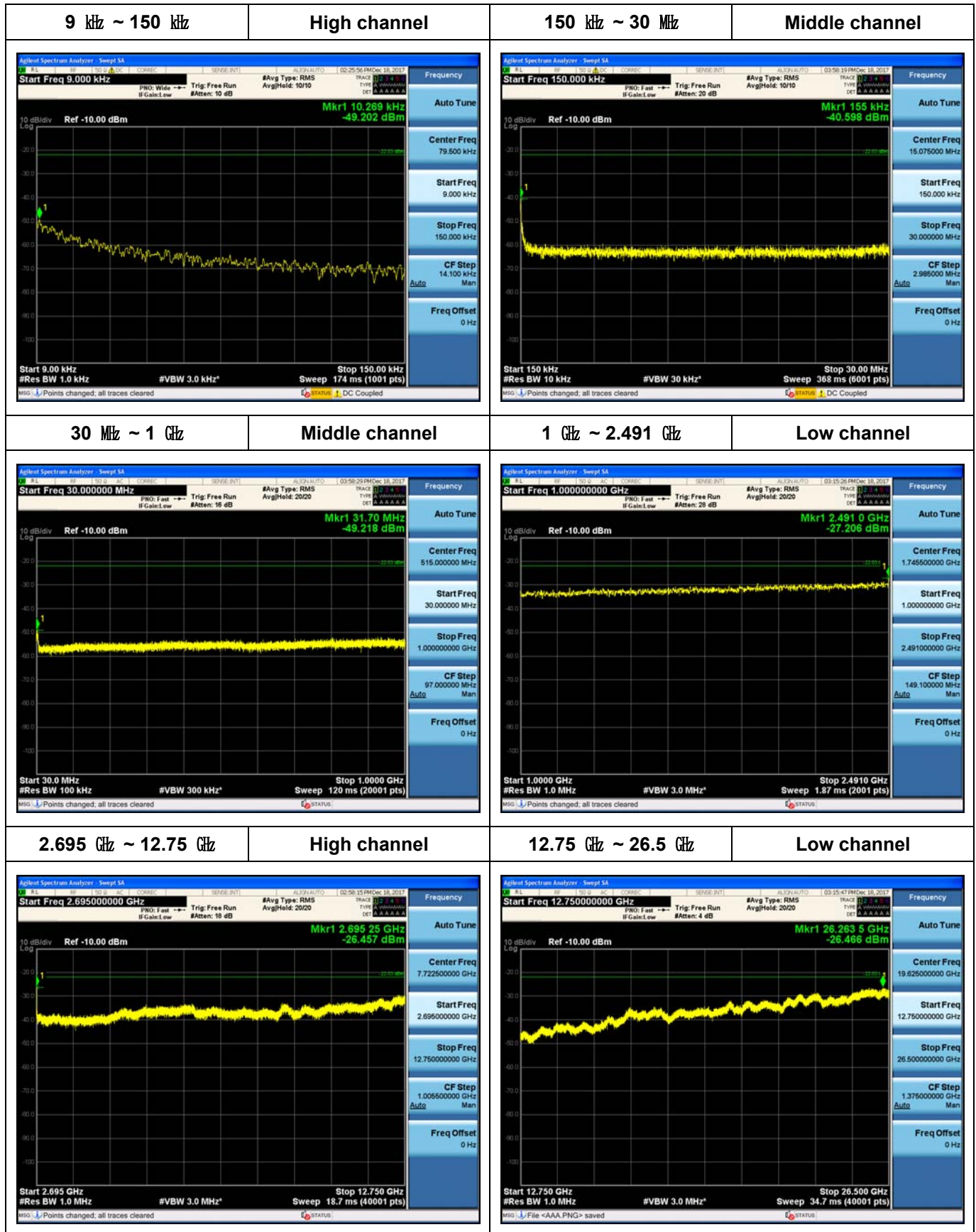
Plot Data for Output Port 2_256QAM



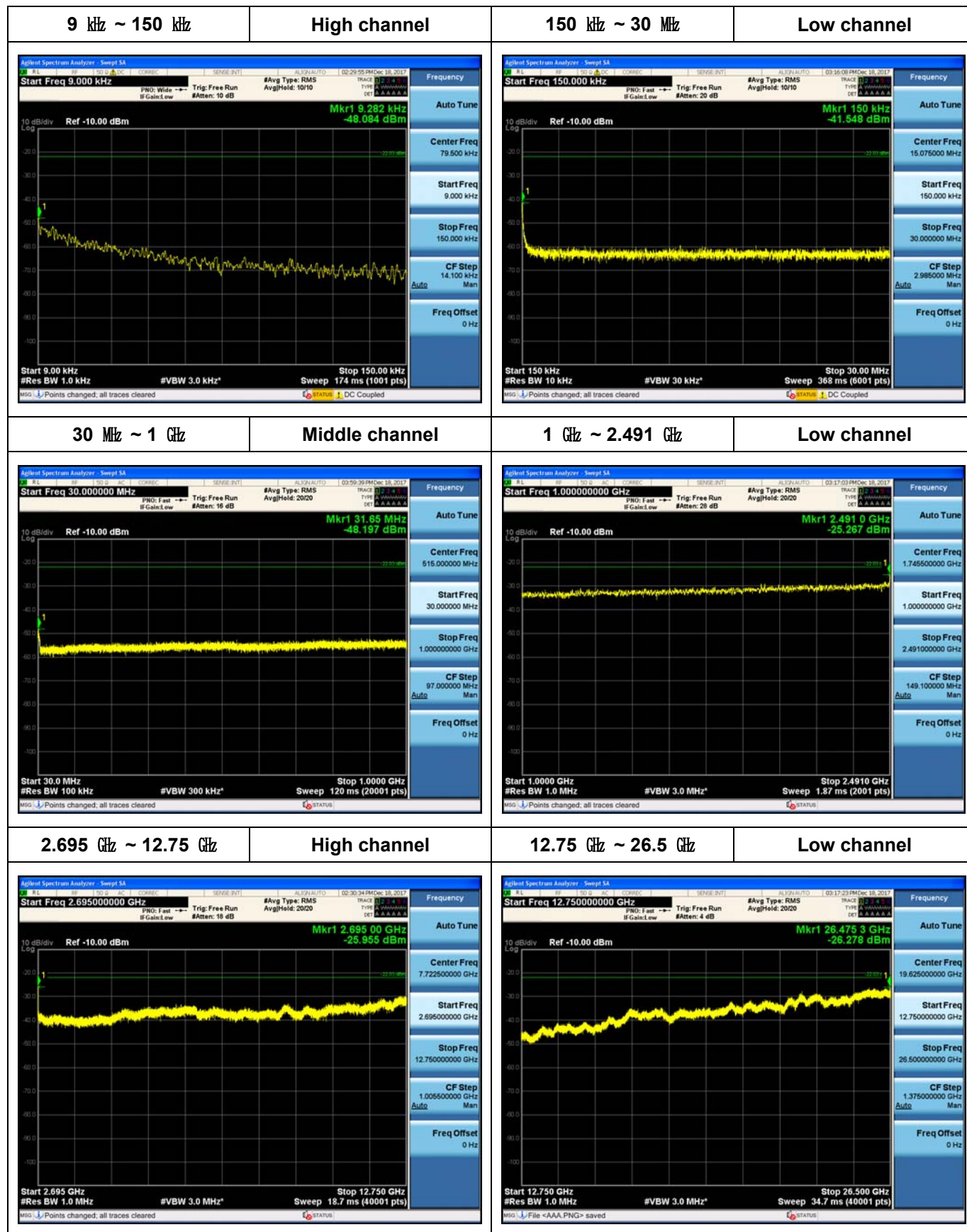
Plot Data for Output Port 3_256QAM



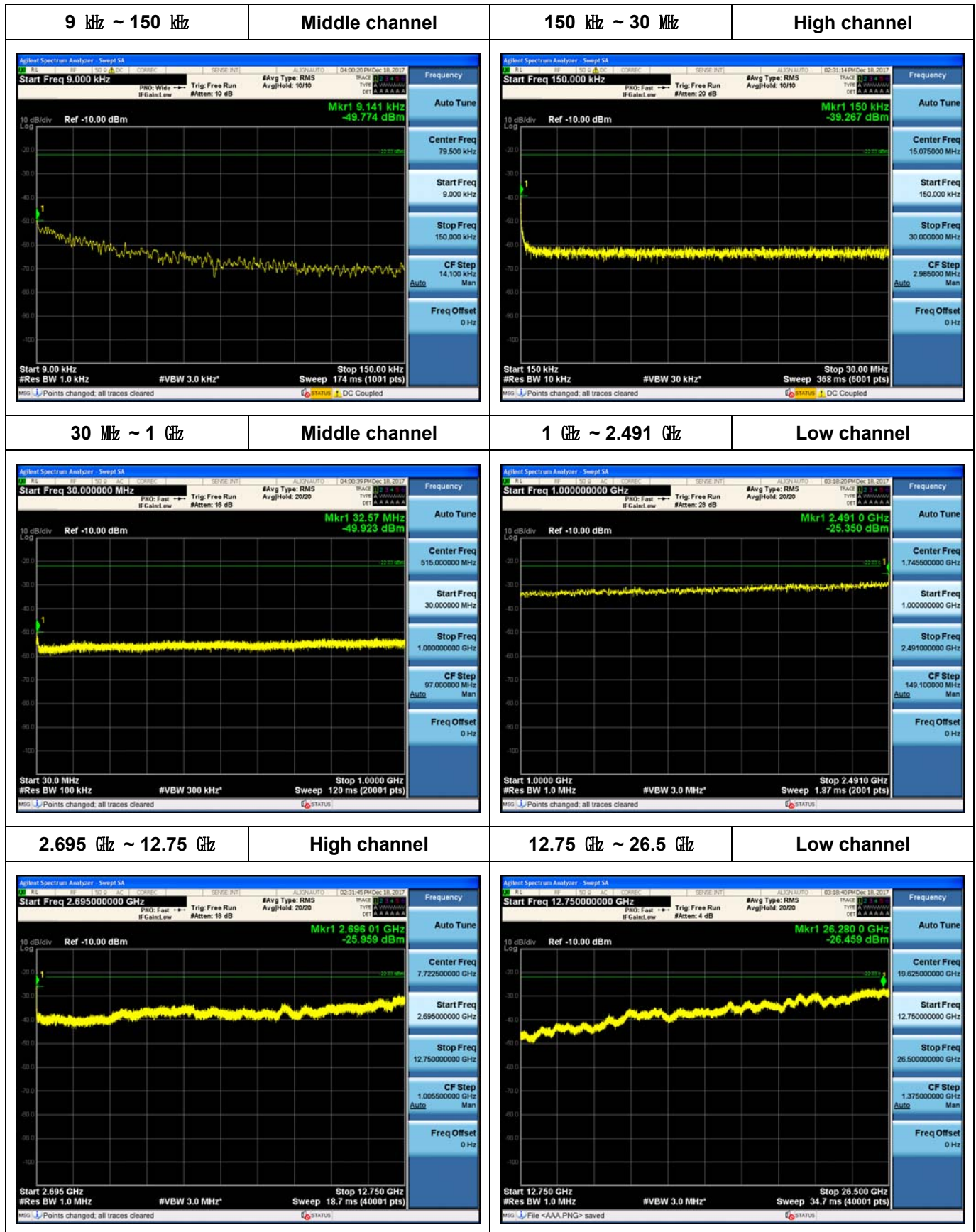
Plot Data for Output Port 4_256QAM



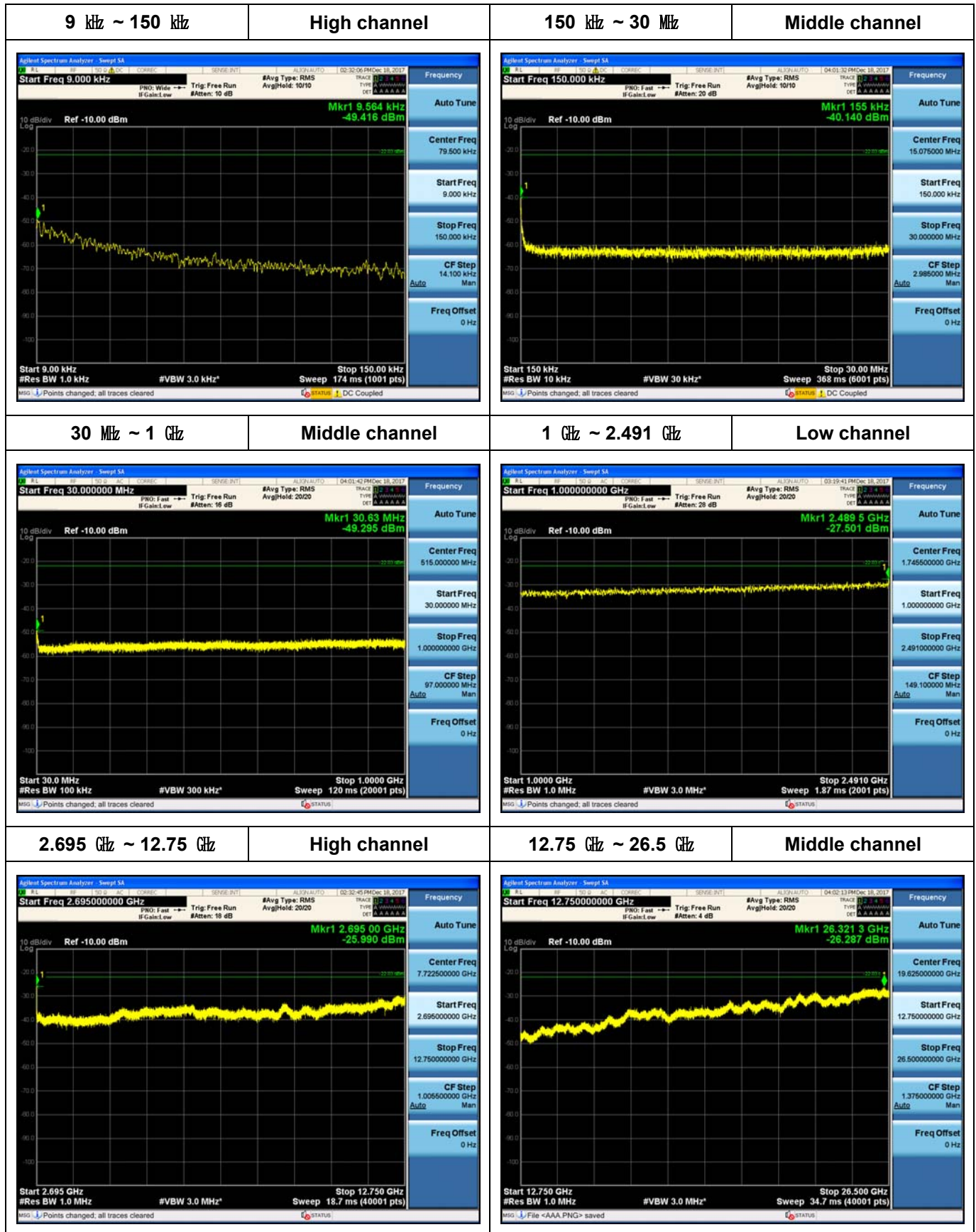
Plot Data for Output Port 5_256QAM



Plot Data for Output Port 6_256QAM

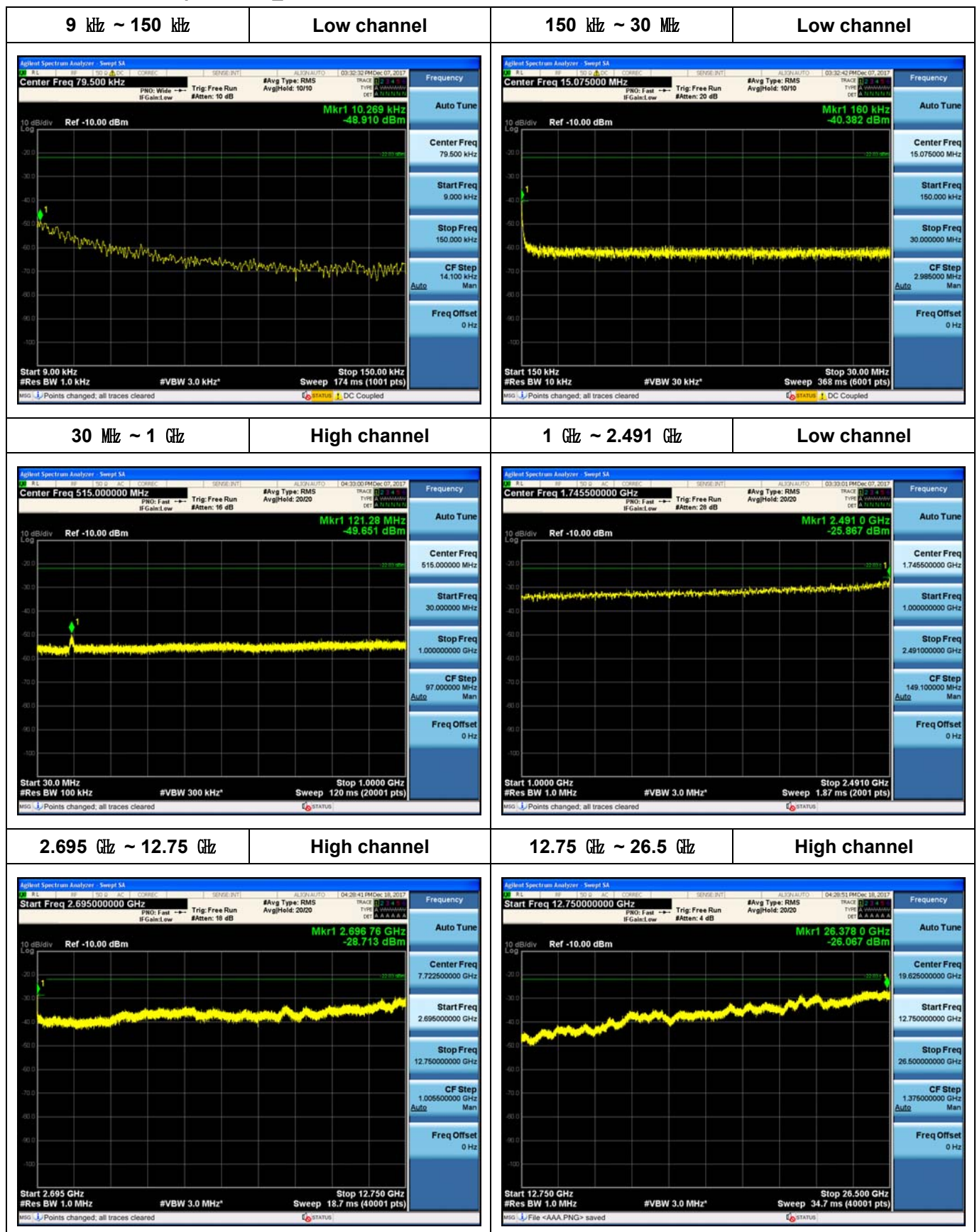


Plot Data for Output Port 7_256QAM

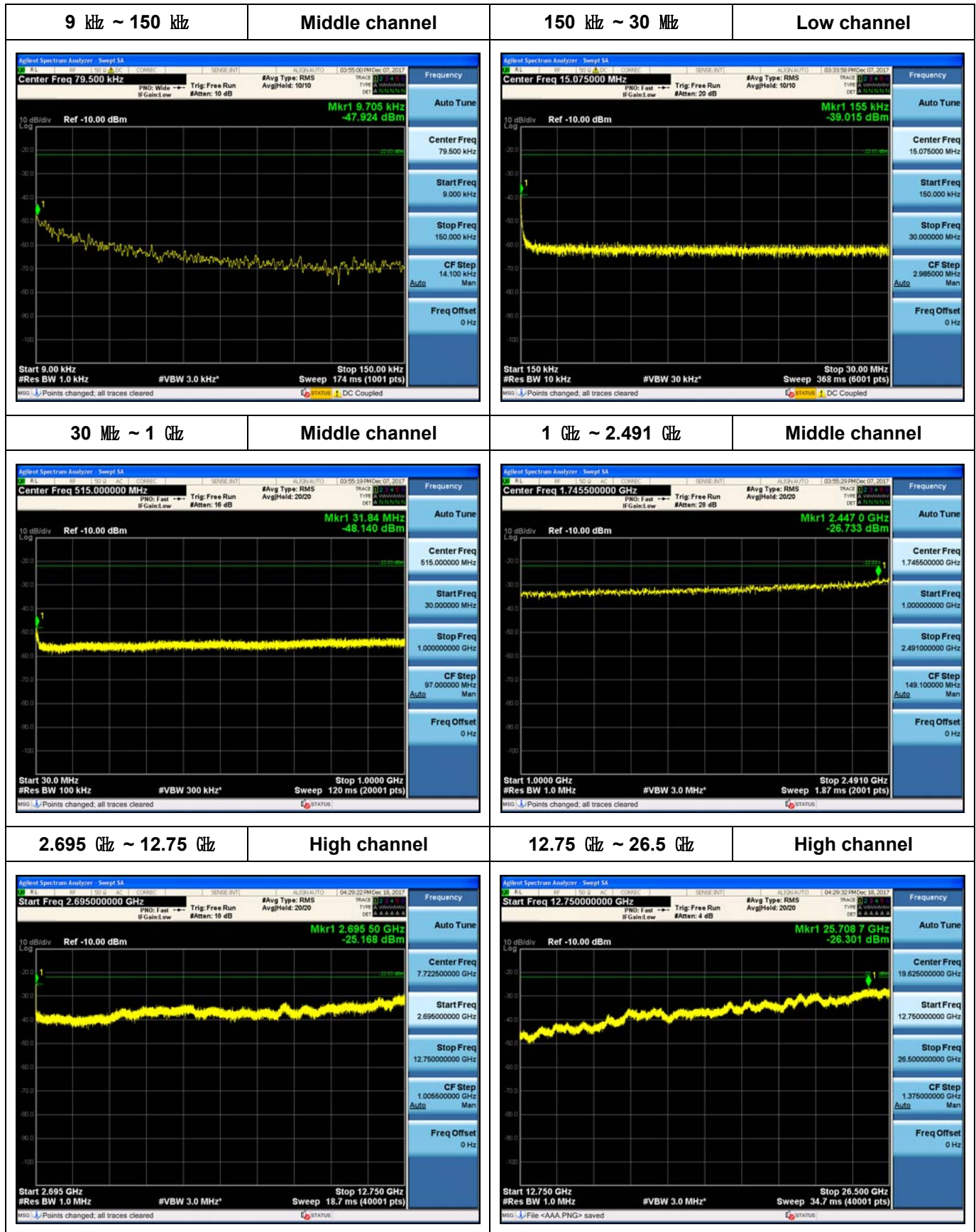


5 MHz + 5 MHz + 5 MHz / 3 Carriers (5 W + 5 W + 5 W)

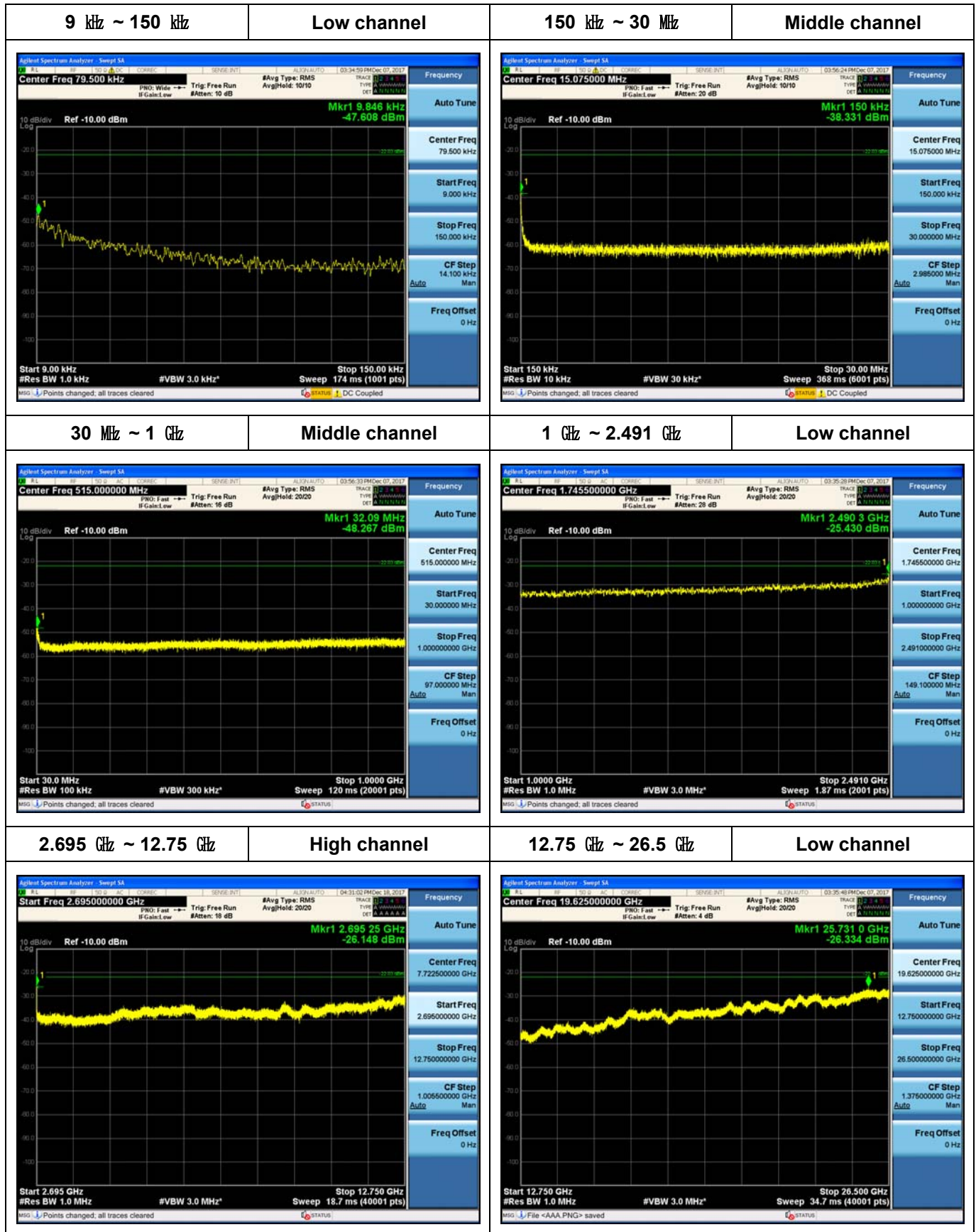
Plot Data for Output Port 0_256QAM



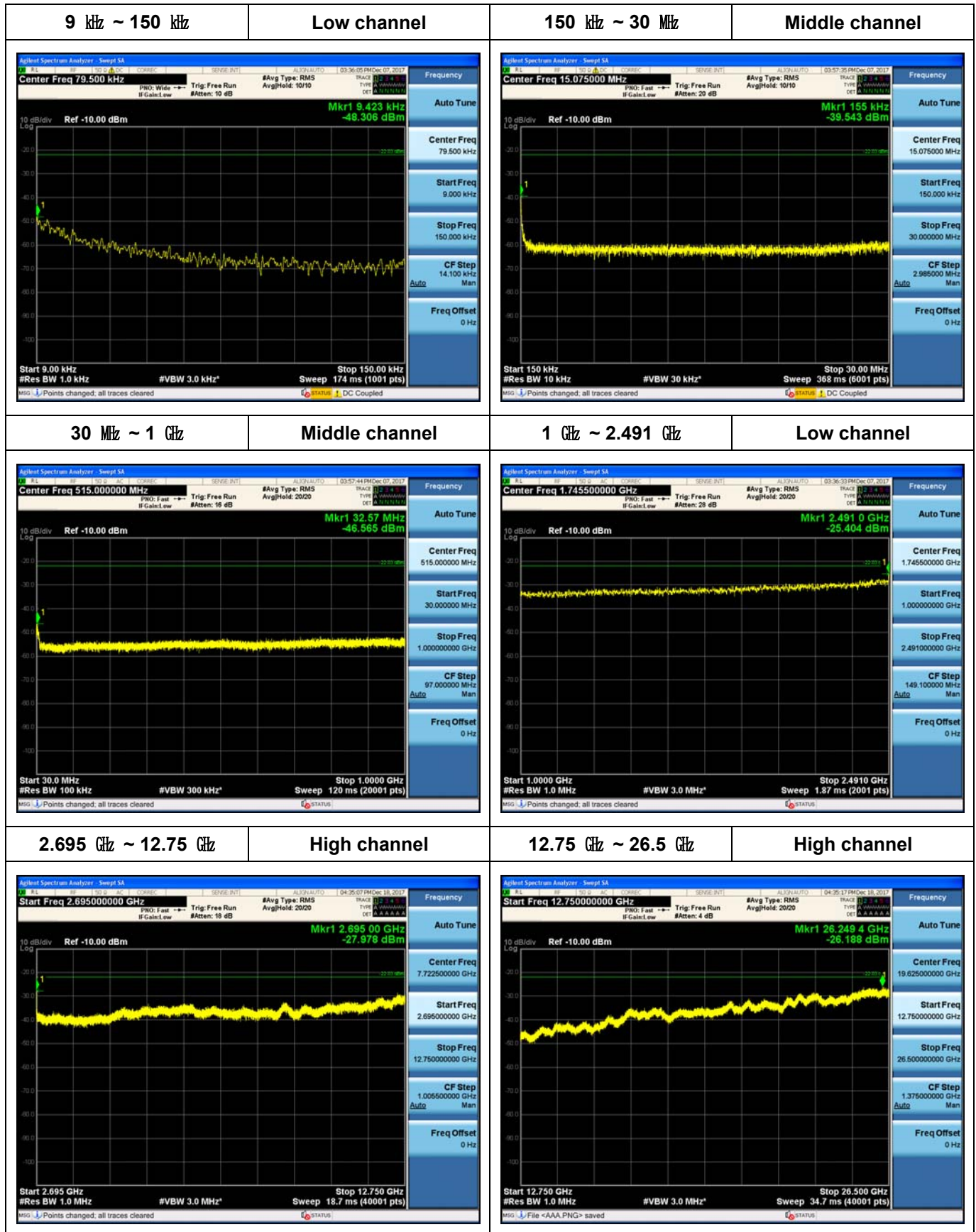
Plot Data for Output Port 1_256QAM



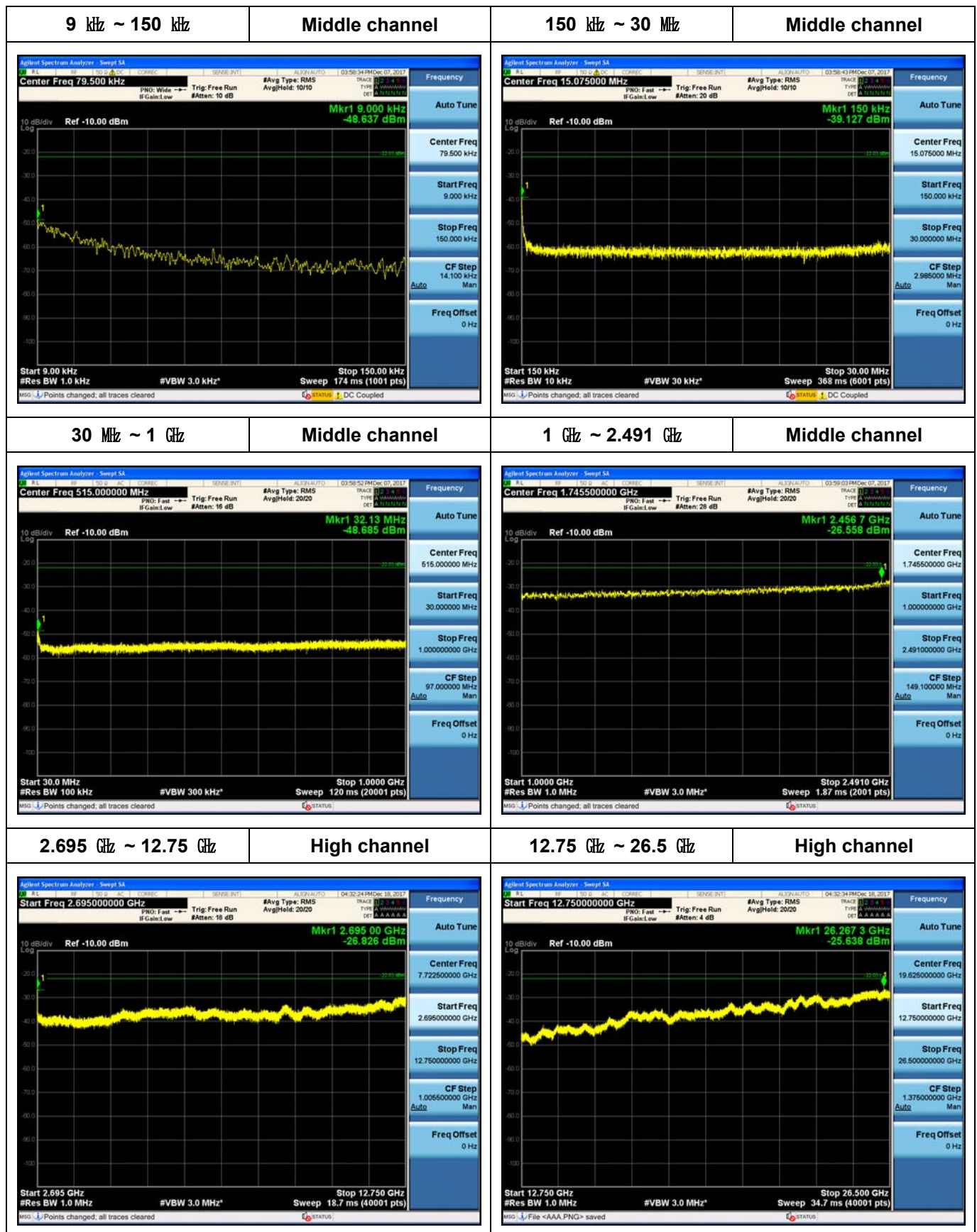
Plot Data for Output Port 2_256QAM



Plot Data for Output Port 3_256QAM



Plot Data for Output Port 4_256QAM



Plot Data for Output Port 5_256QAM

