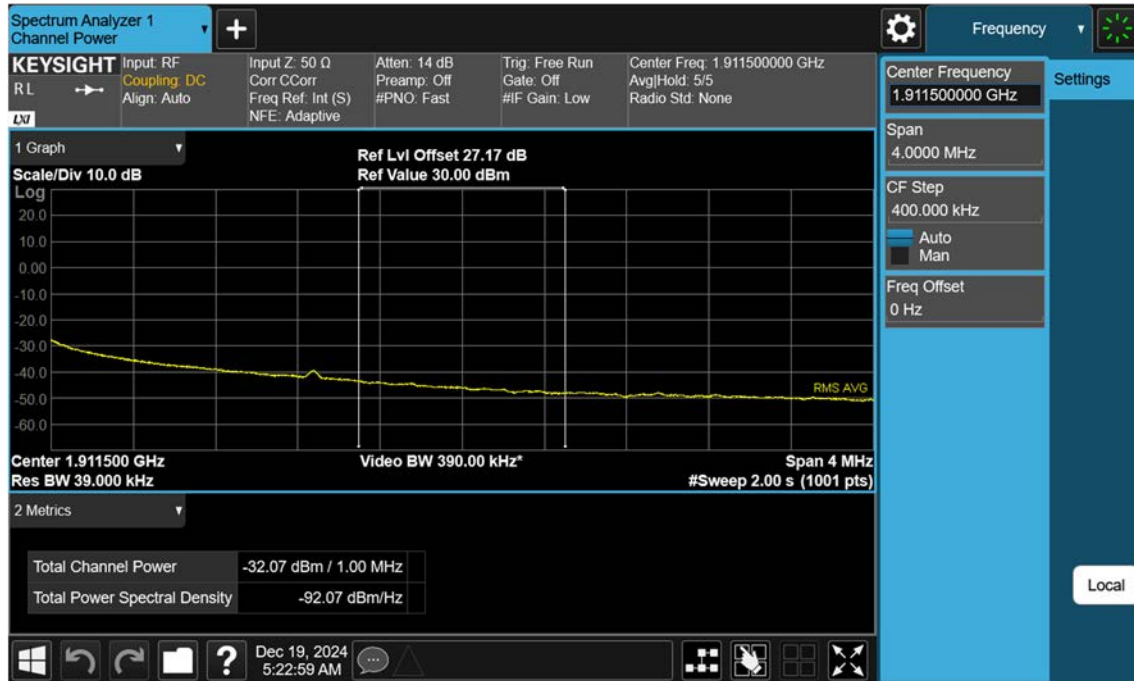


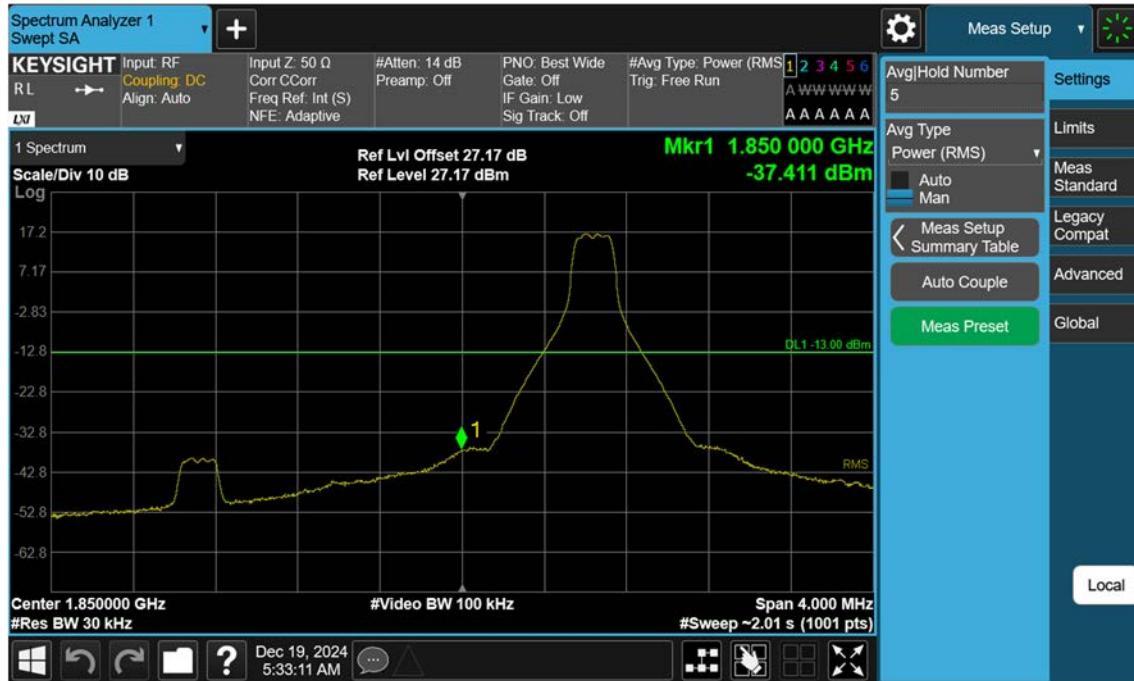
NR2_30 M_Band Edge_High_BPSK_FullRB



NR2_30 M_Extended Band Edge_High_BPSK_FullRB



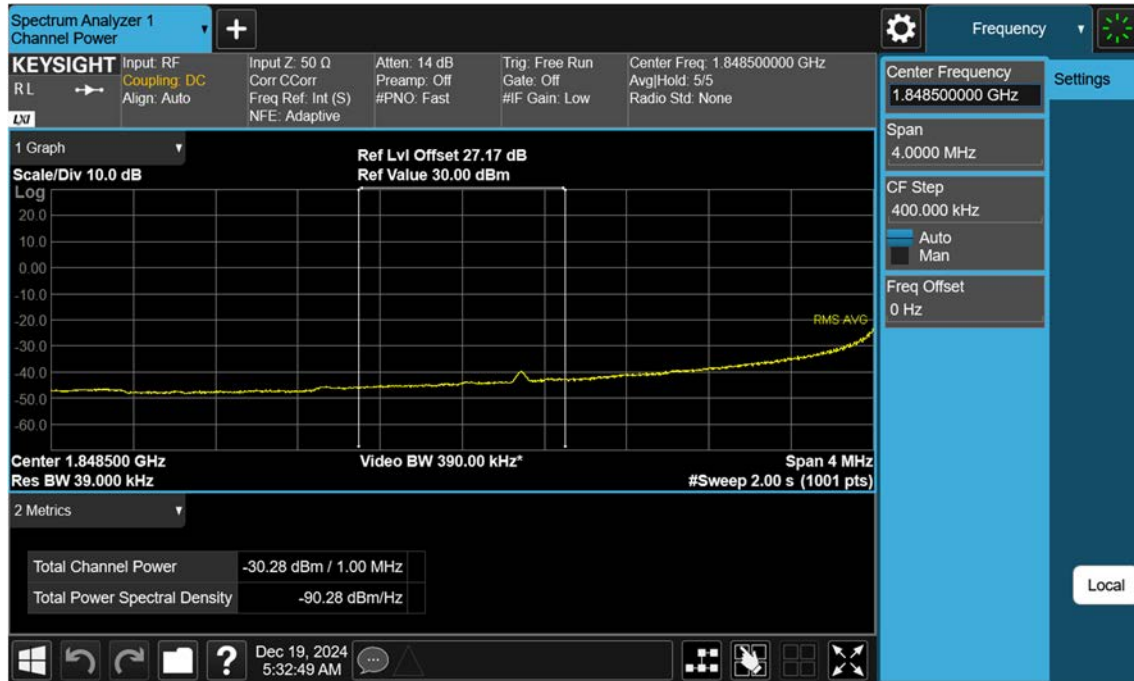
NR2_40 M_Band Edge_Low_BPSK_1RB



NR2_40 M_Band Edge_Low_BPSK_FullRB



NR2_40 M_Extended Band Edge_Low_BPSK_FullRB



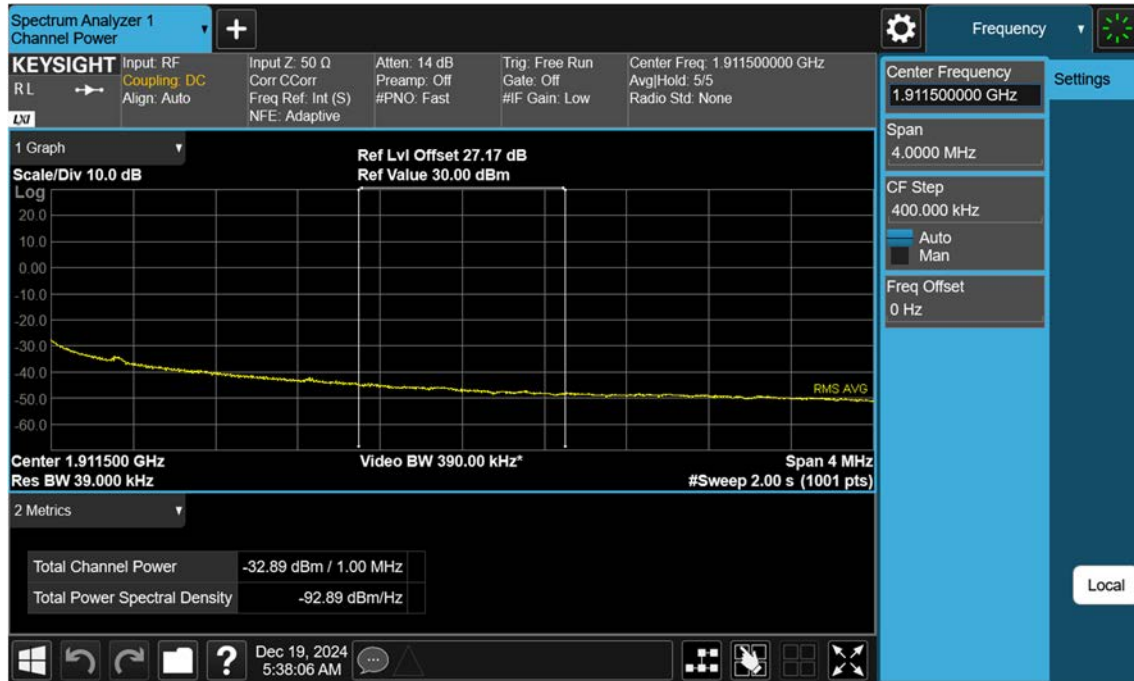
NR2_40 M_Band Edge_High_BPSK_1RB



NR2_40 M_Band Edge_High_BPSK_FullRB

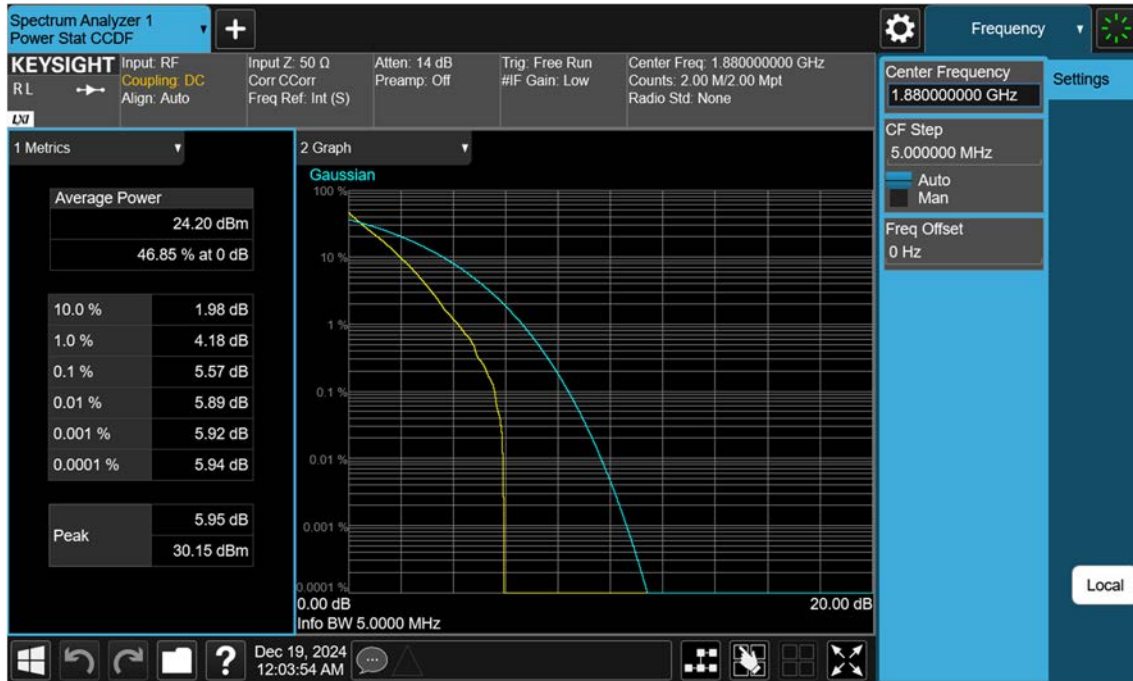


NR2_40 M_Extended Band Edge_High_BPSK_FullRB

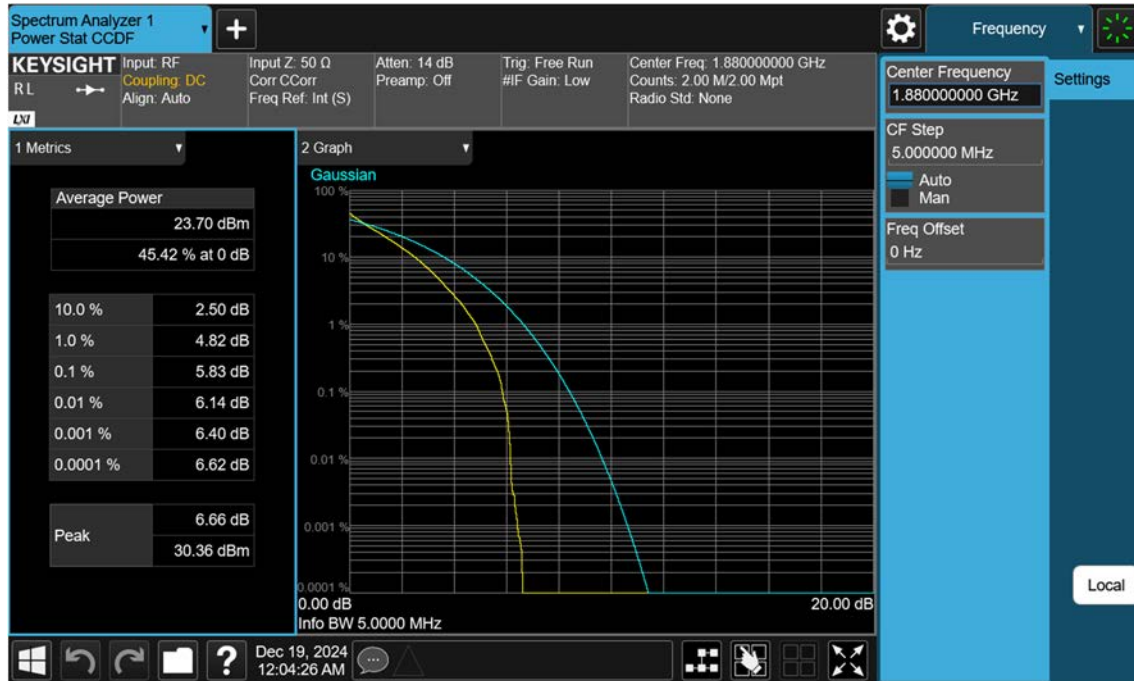


11. TEST PLOTS(Sub2)

NR2_5 M_PAR_Mid_BPSK_FullRB



NR2_5 M_PAR_Mid_QPSK_FullRB



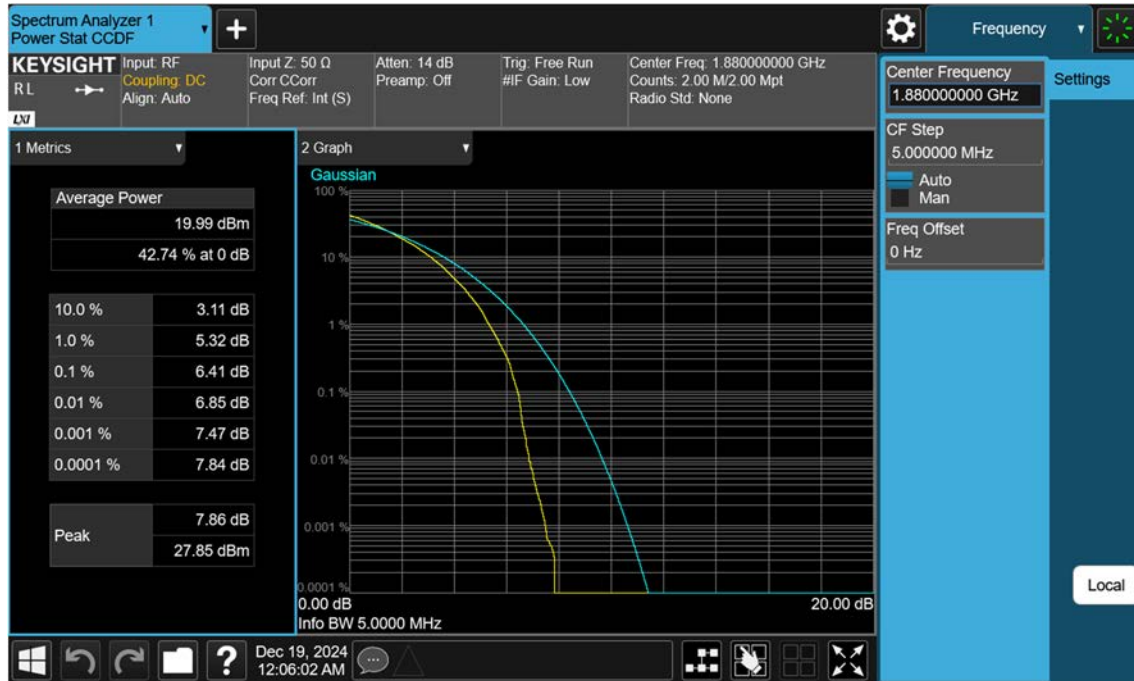
NR2_5 M_PAR_Mid_16QAM_FullIRB



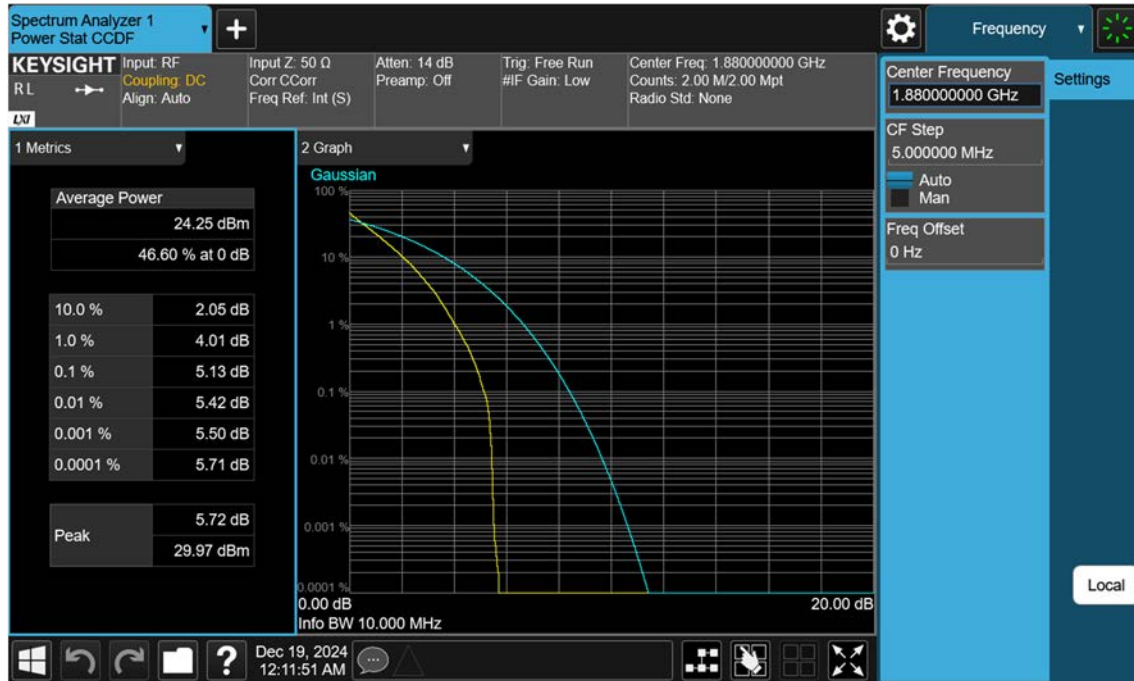
NR2_5 M_PAR_Mid_64QAM_FullIRB



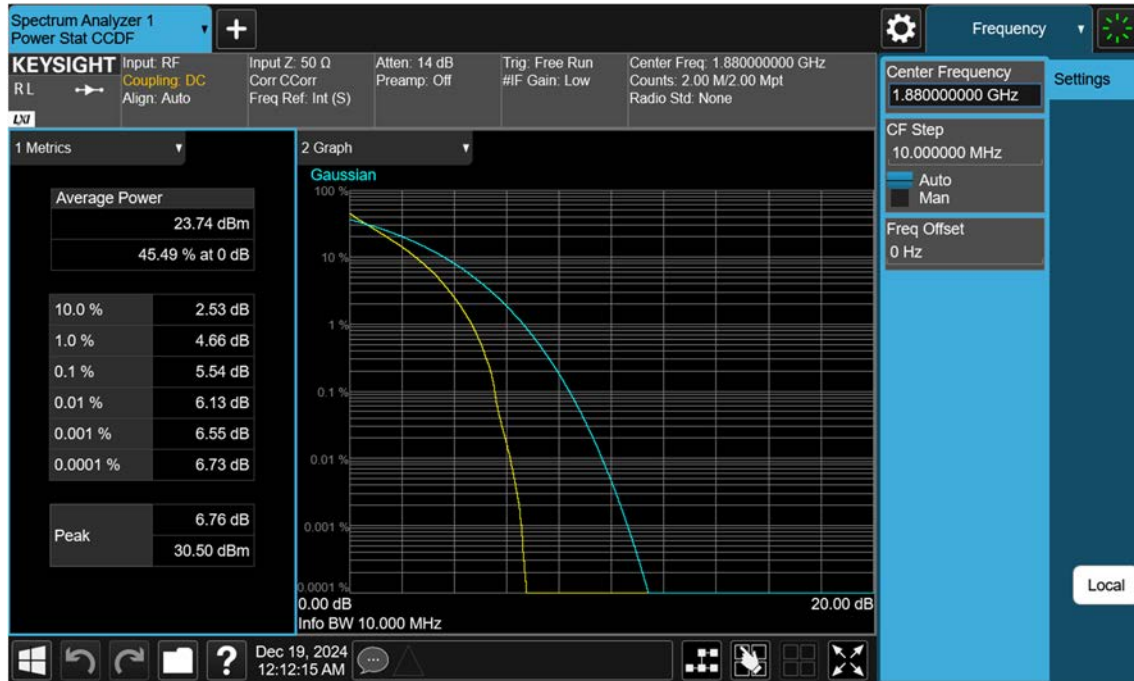
NR2_5 M_PAR_Mid_256QAM_FullRB



NR2_10 M_PAR_Mid_BPSK_FullRB



NR2_10 M_PAR_Mid_QPSK_FullRB



NR2_10 M_PAR_Mid_16QAM_FullRB



NR2_10 M_PAR_Mid_64QAM_FullRB



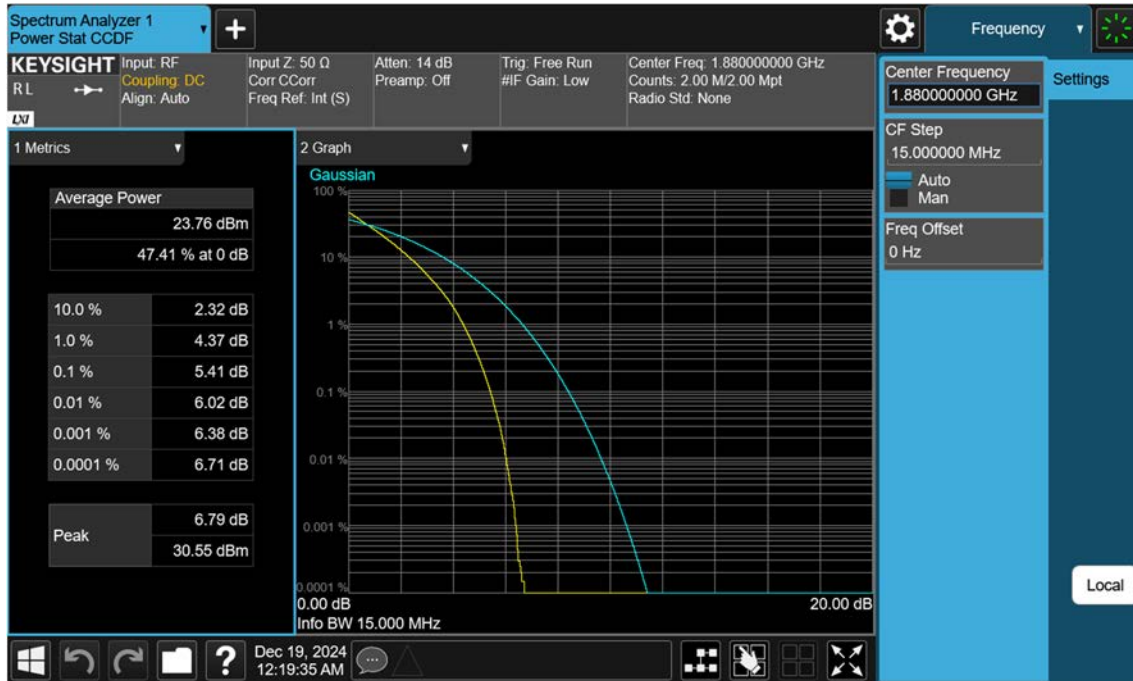
NR2_10 M_PAR_Mid_256QAM_FullRB



NR2_15 M_PAR_Mid_BPSK_FullIRB



NR2_15 M_PAR_Mid_QPSK_FullRB



NR2_15 M_PAR_Mid_16QAM_FullRB



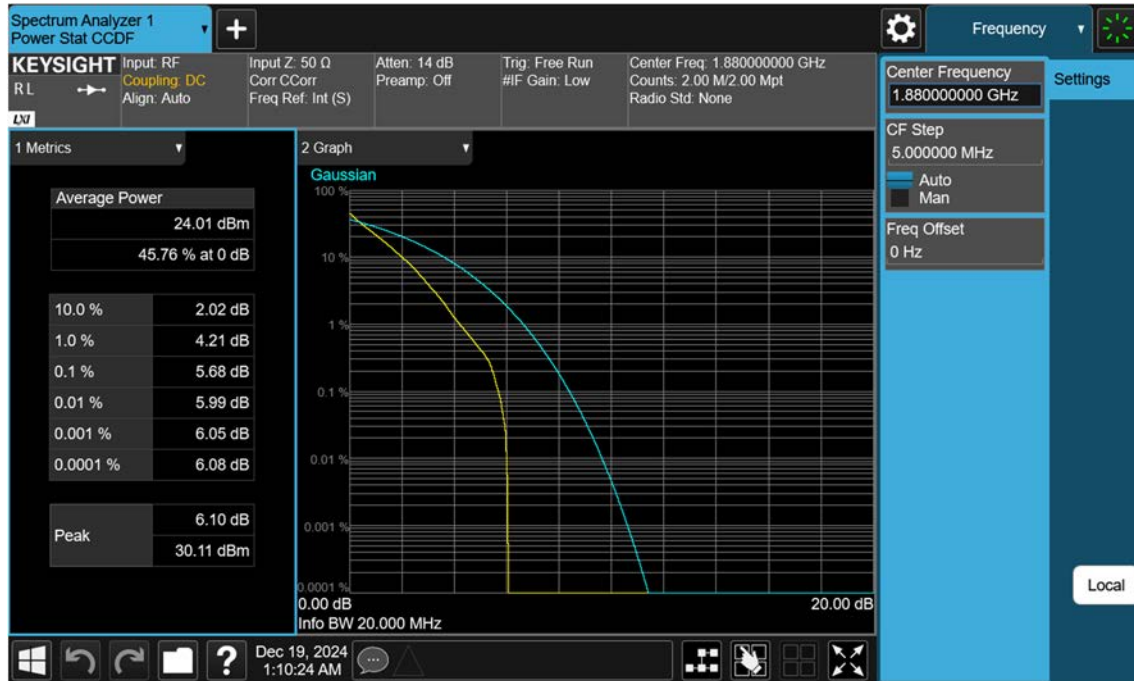
NR2_15 M_PAR_Mid_64QAM_FullRB



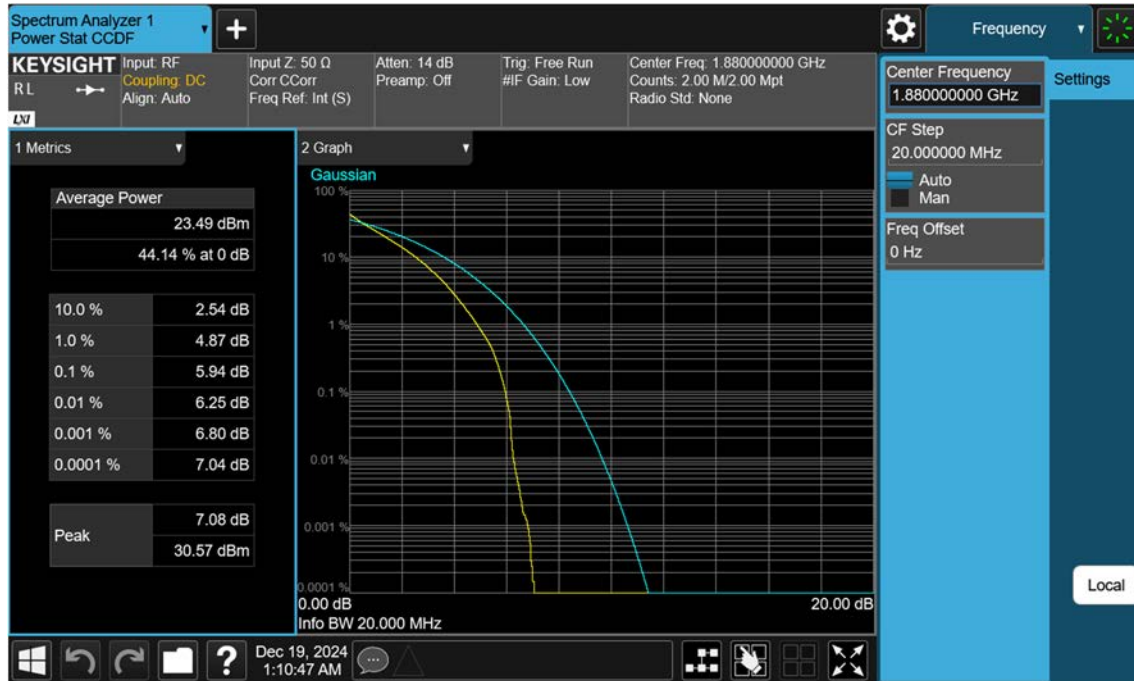
NR2_15 M_PAR_Mid_256QAM_FullRB



NR2_20 M_PAR_Mid_BPSK_FullRB



NR2_20 M_PAR_Mid_QPSK_FullRB



NR2_20 M_PAR_Mid_16QAM_FullRB



NR2_20 M_PAR_Mid_64QAM_FullRB



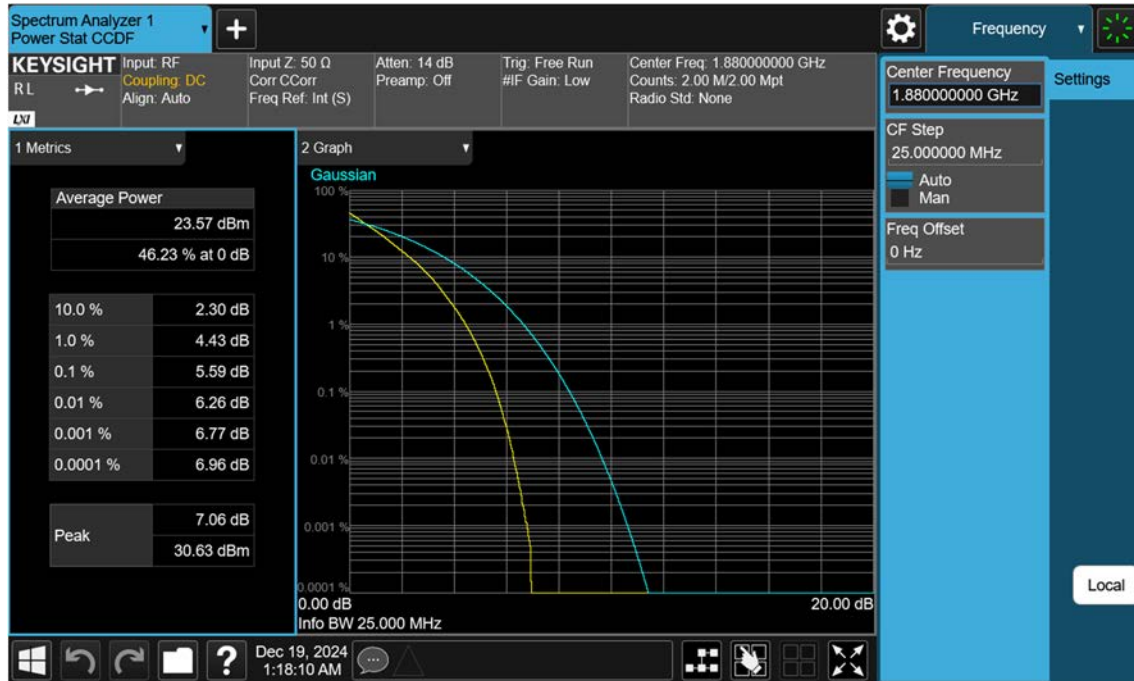
NR2_20 M_PAR_Mid_256QAM_FullRB



NR2_25 M_PAR_Mid_BPSK_FullRB



NR2_25 M_PAR_Mid_QPSK_FullRB



NR2_25 M_PAR_Mid_16QAM_FullRB



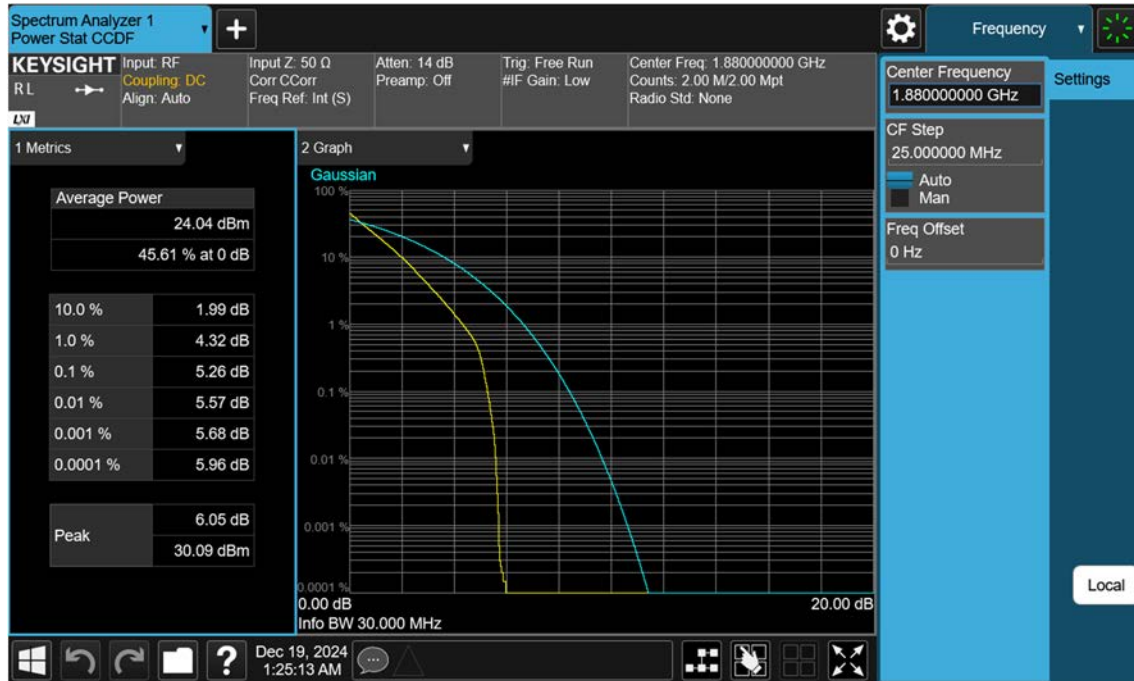
NR2_25 M_PAR_Mid_64QAM_FullRB



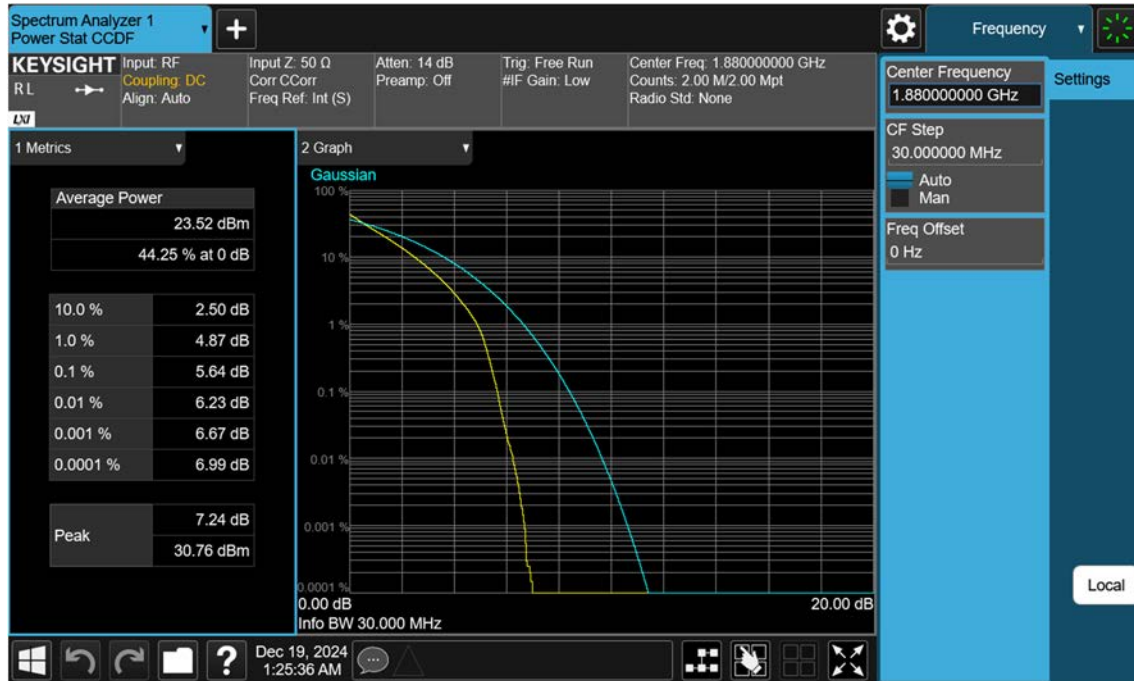
NR2_25 M_PAR_Mid_256QAM_FullRB



NR2_30 M_PAR_Mid_BPSK_FullIRB



NR2_30 M_PAR_Mid_QPSK_FullRB



NR2_30 M_PAR_Mid_16QAM_FullRB



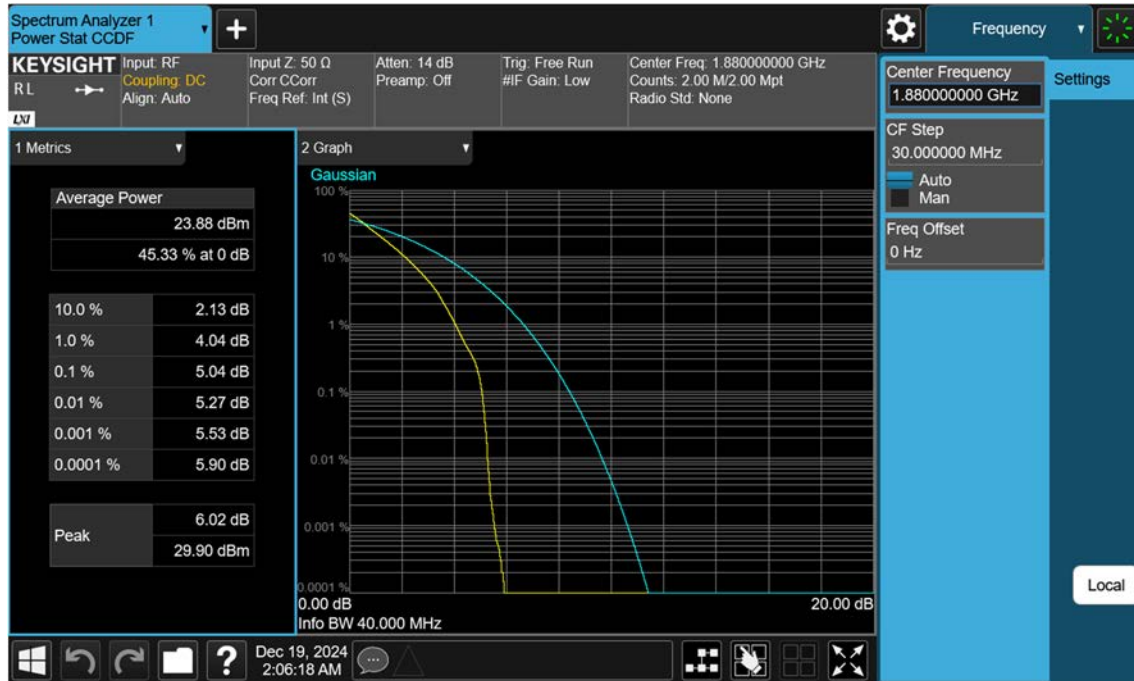
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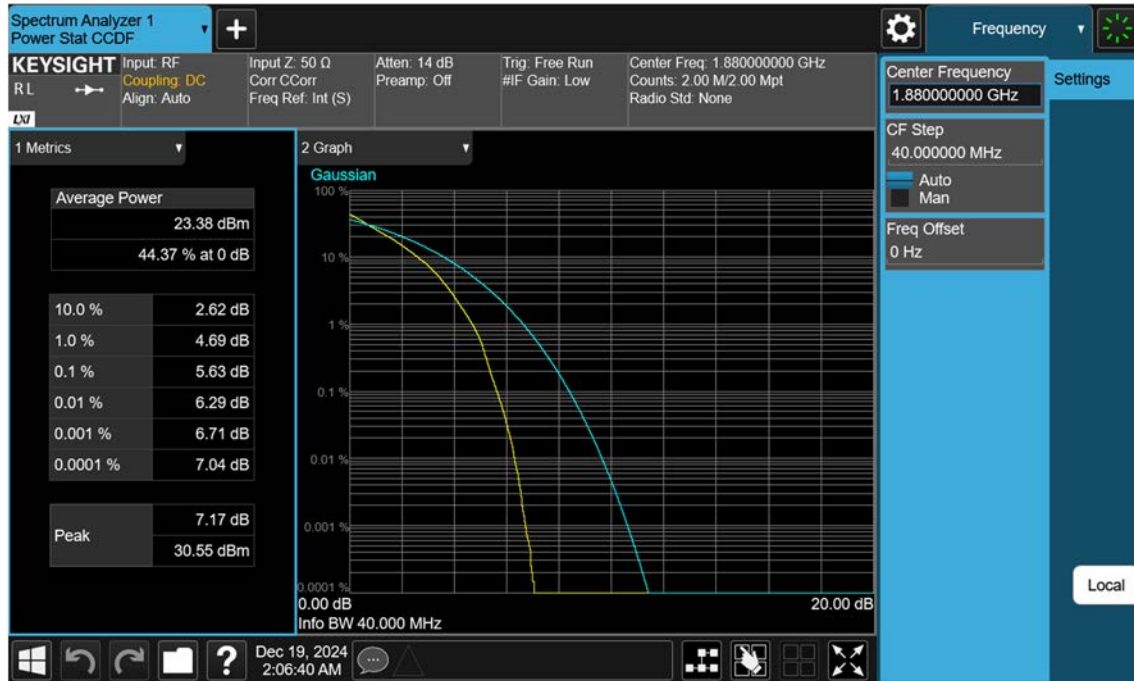
NR2_30 M_PAR_Mid_256QAM_FullRB



NR2_40 M_PAR_Mid_BPSK_FullRB



NR2_40 M_PAR_Mid_QPSK_FullRB



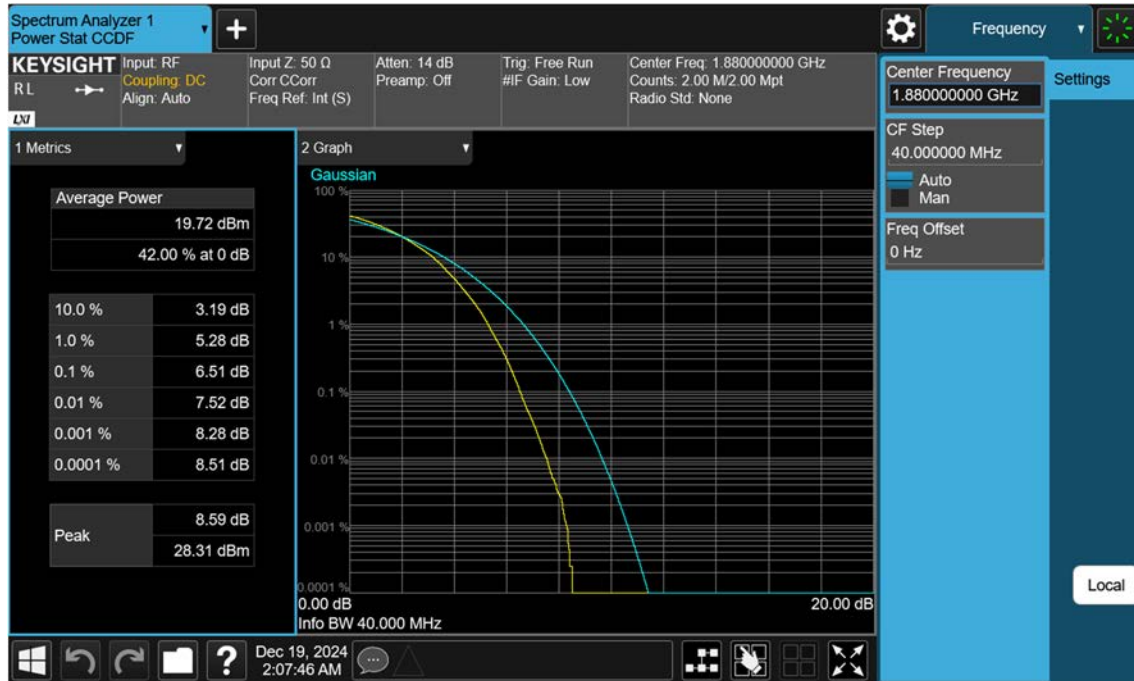
NR2_40 M_PAR_Mid_16QAM_FullRB



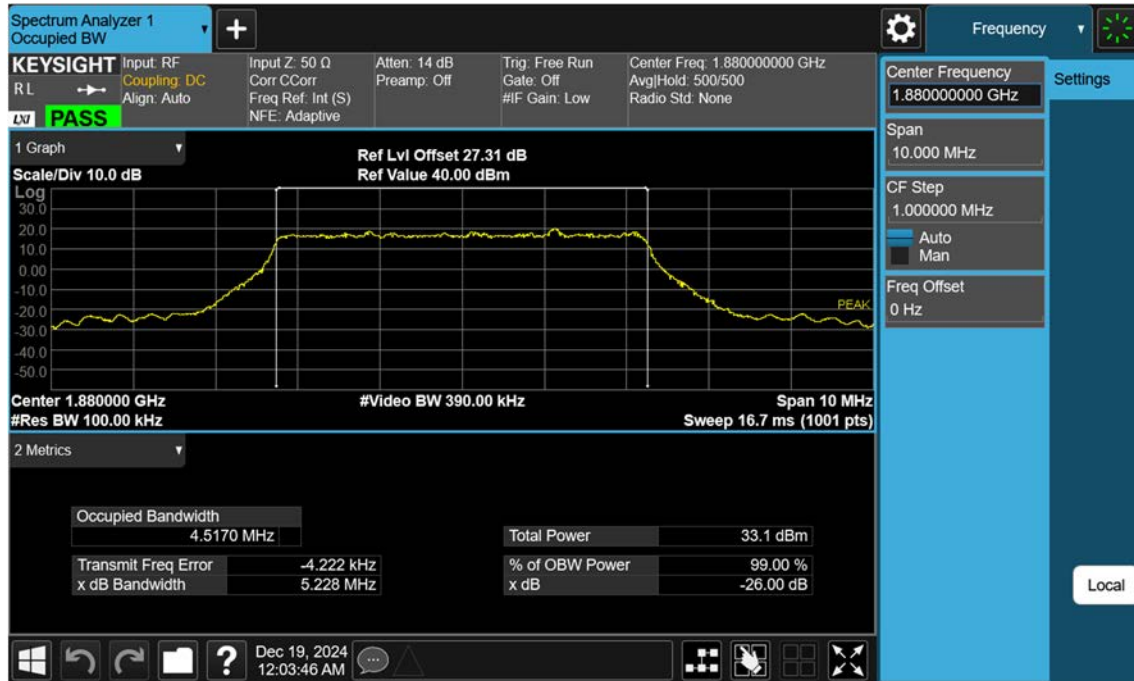
NR2_40 M_PAR_Mid_64QAM_FullRB



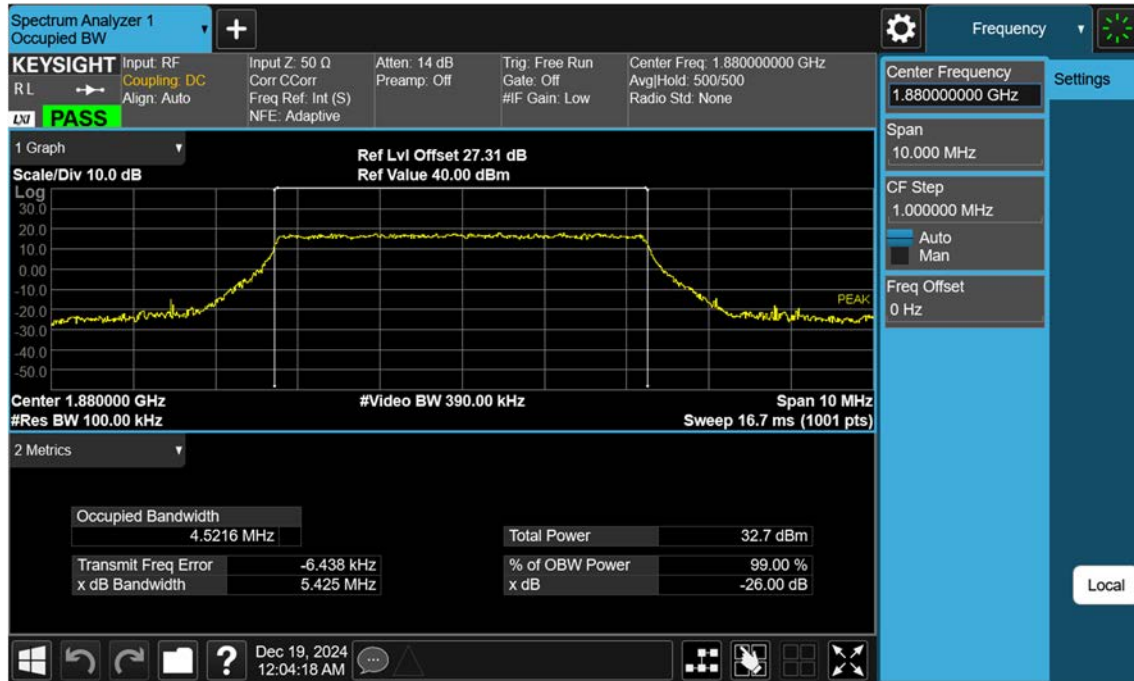
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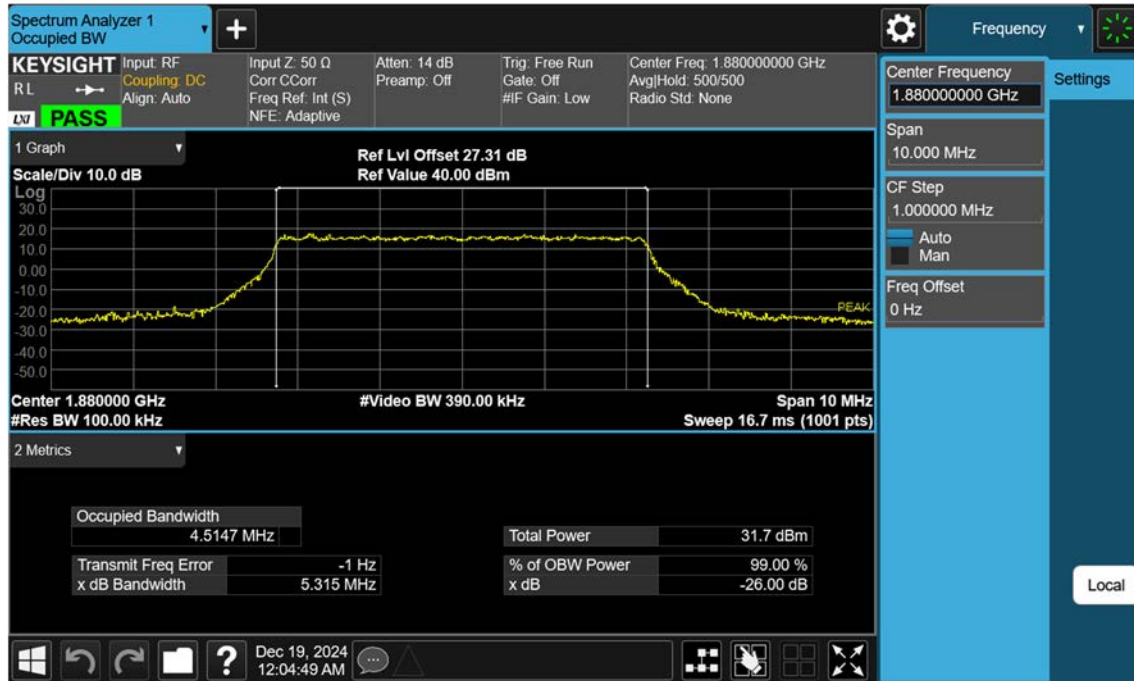
NR2_5 M_OBW_Mid_BPSK_FullIRB



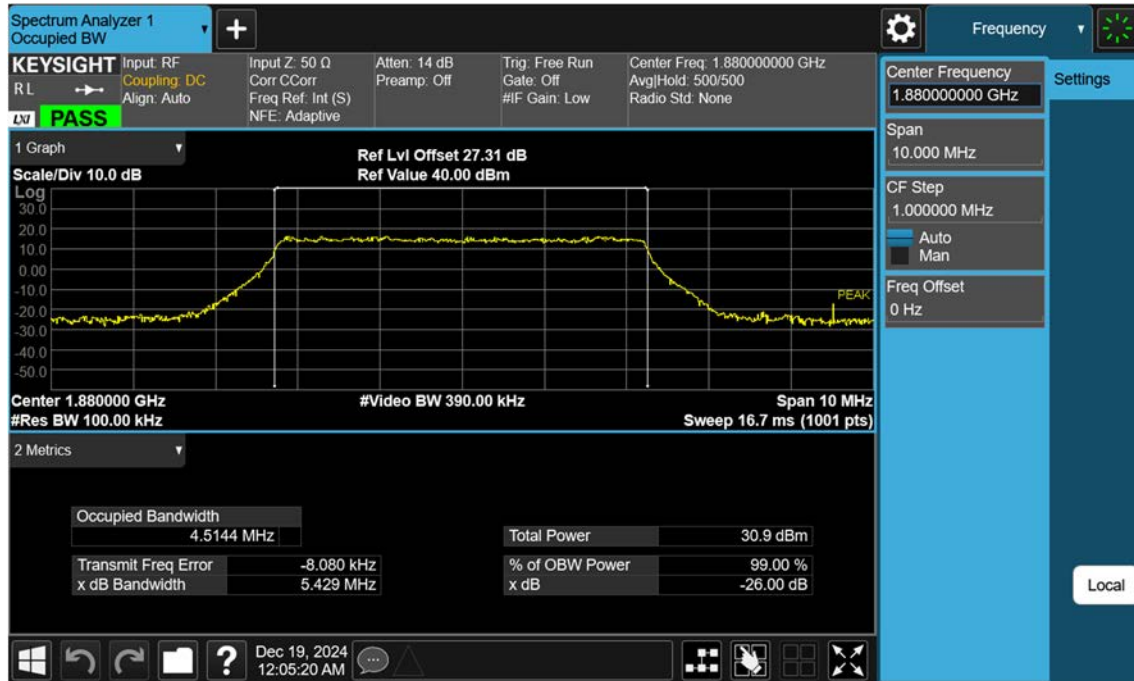
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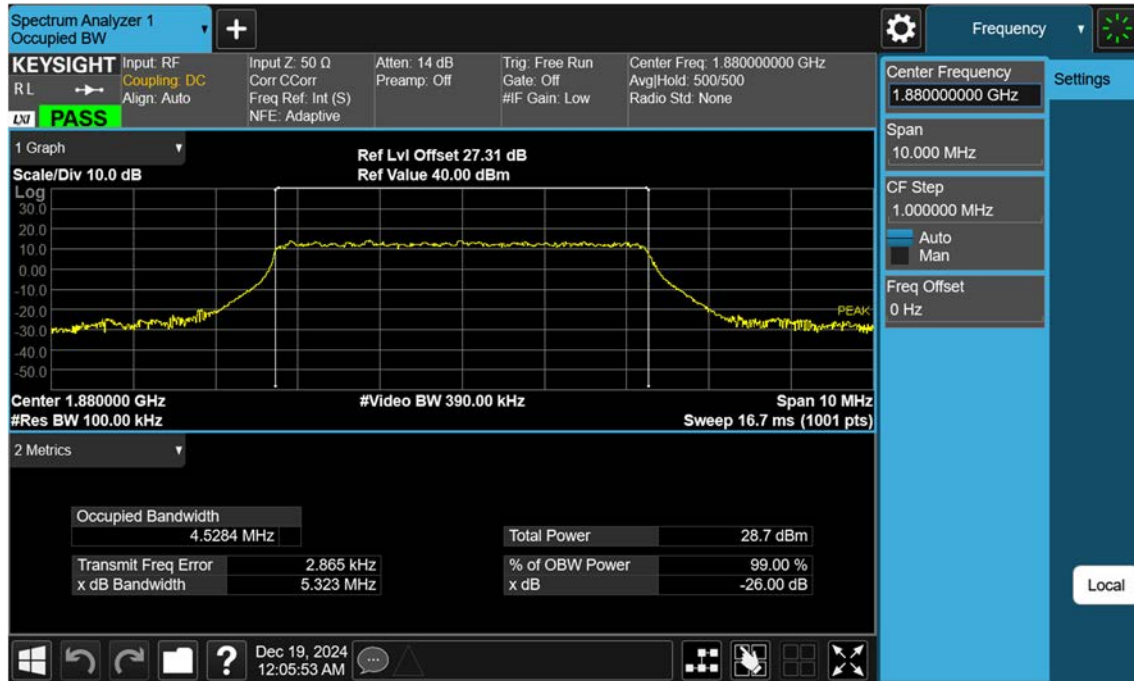
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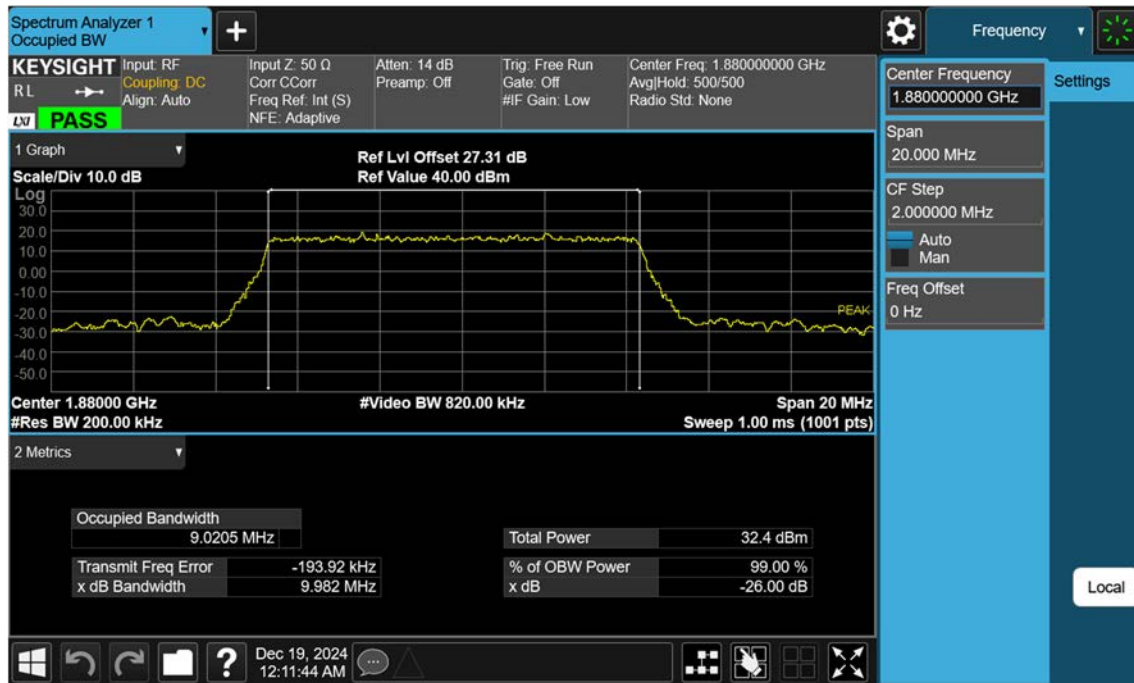
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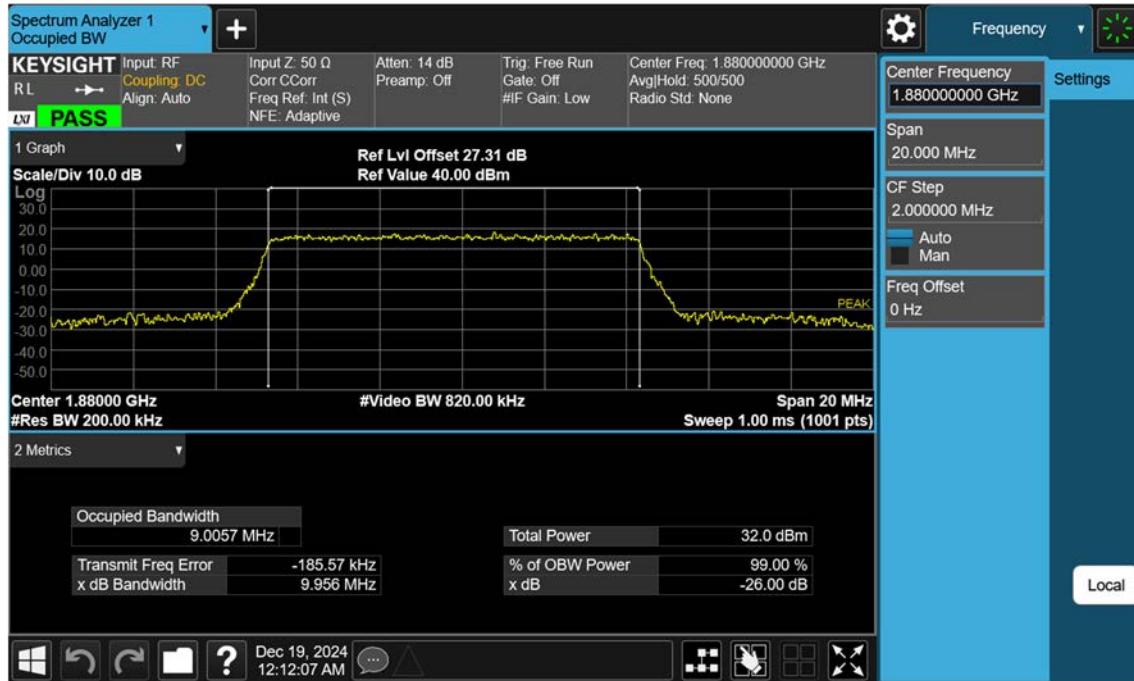
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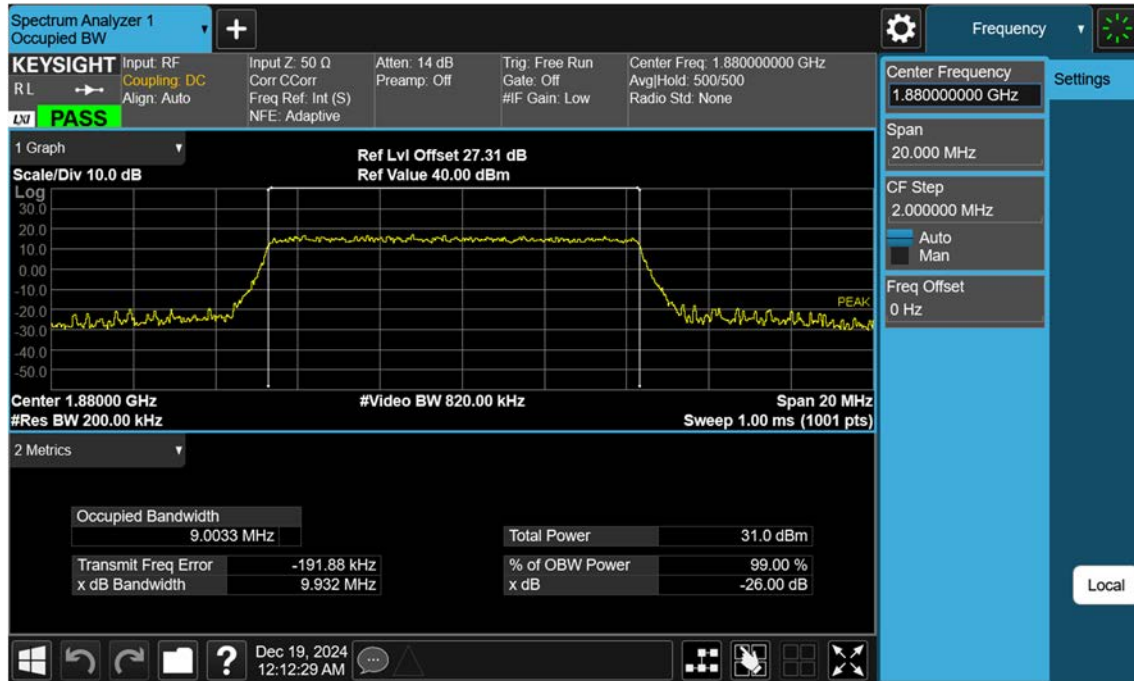
NR2_10 M_OBW_Mid_BPSK_FullIRB



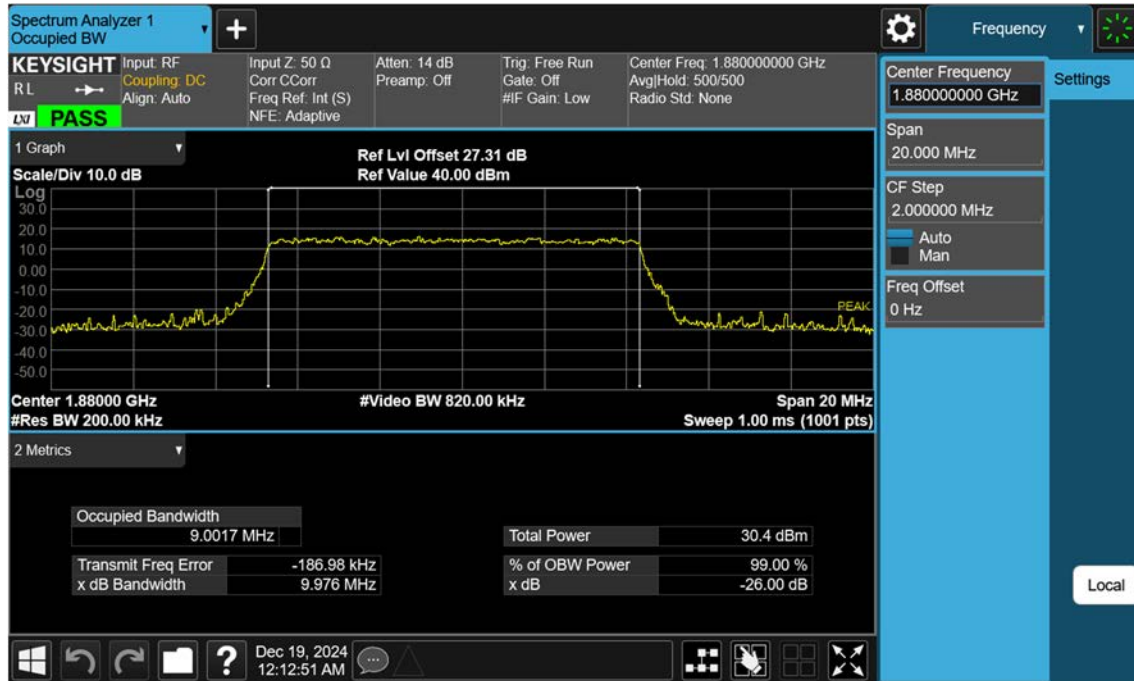
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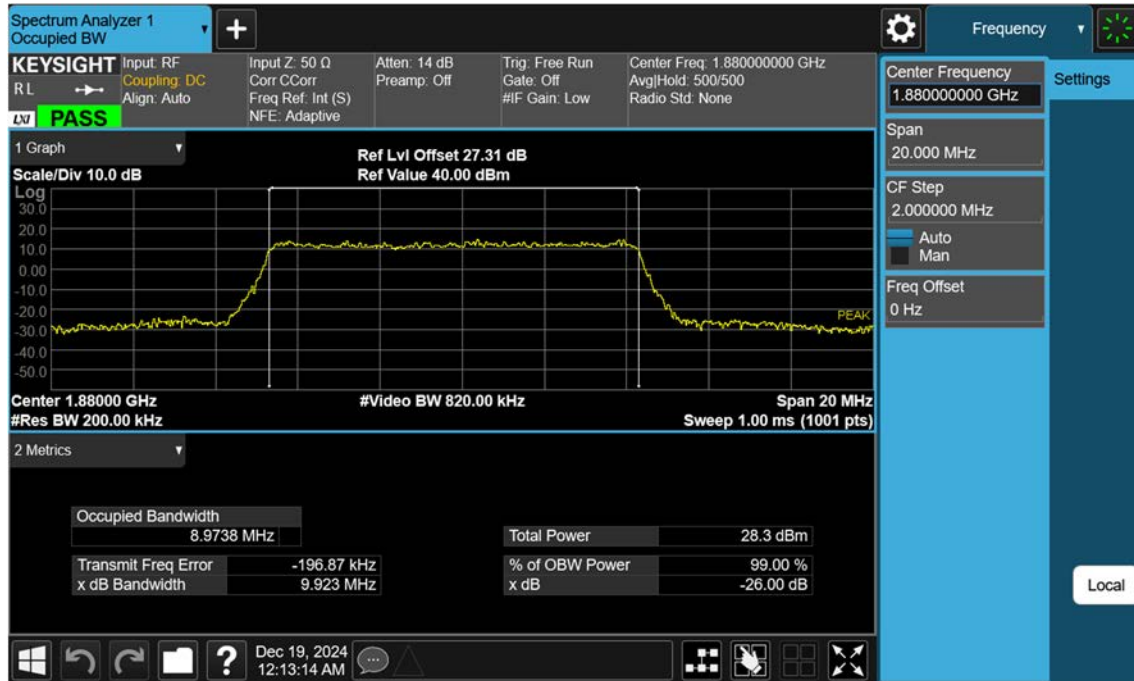
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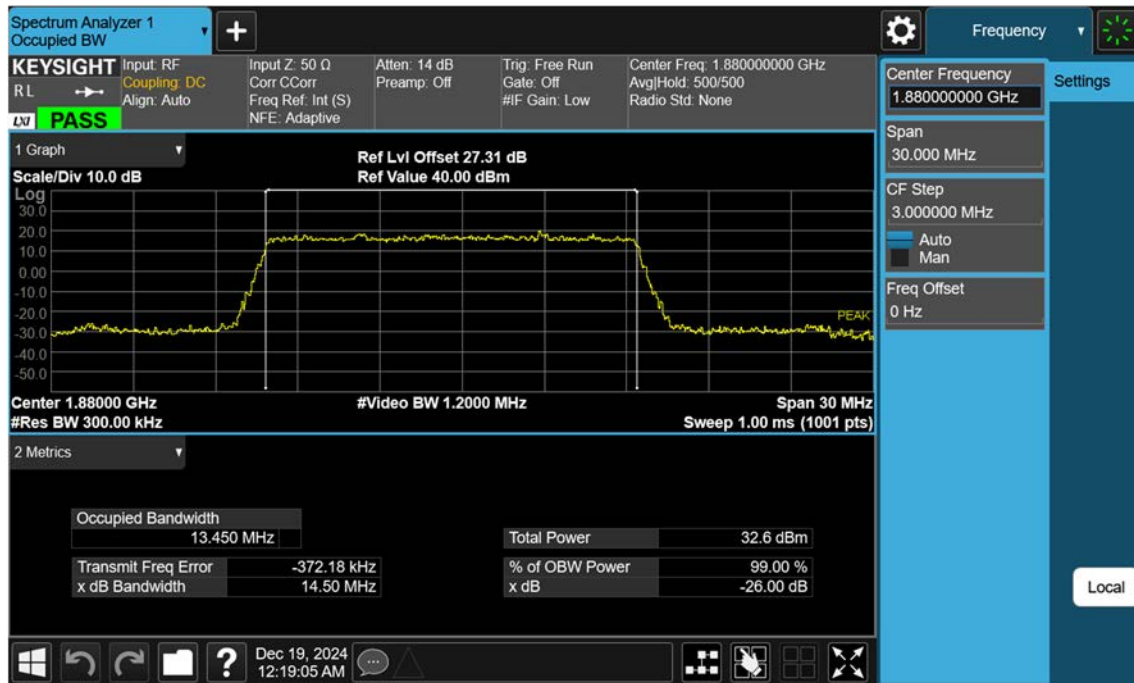
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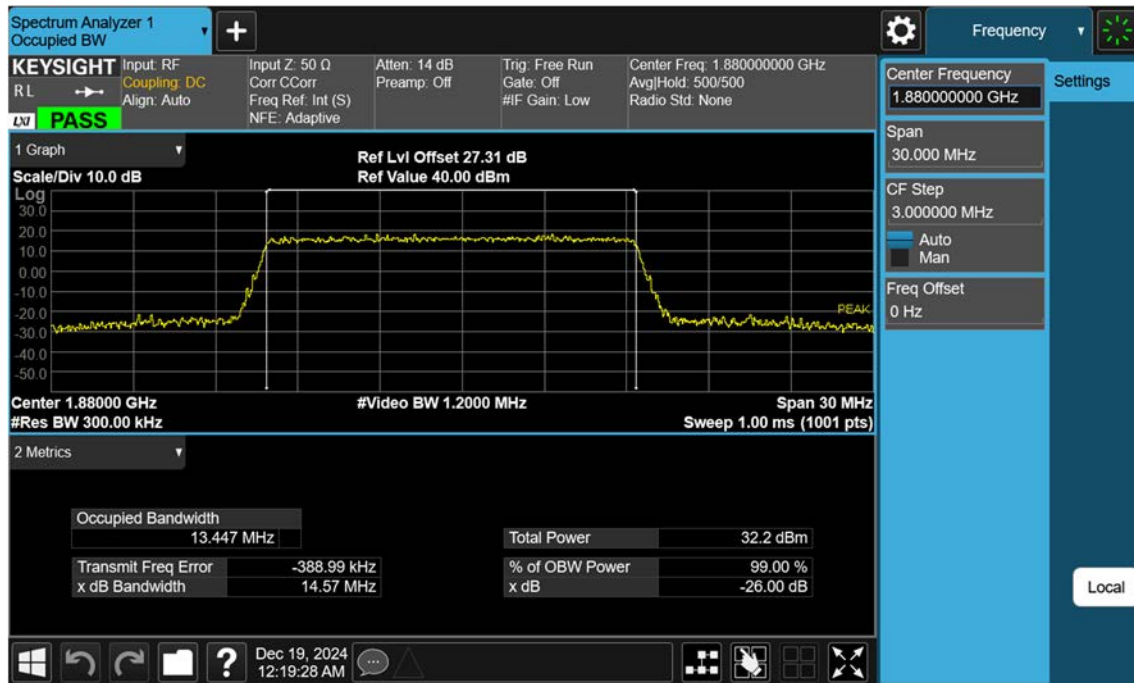
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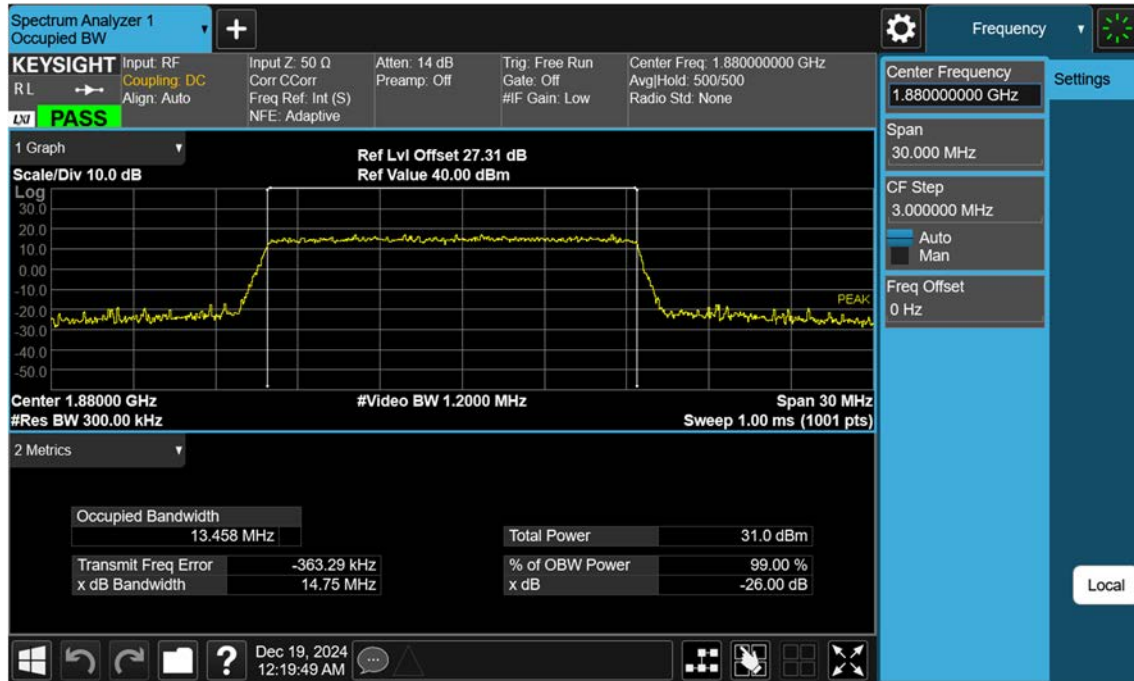
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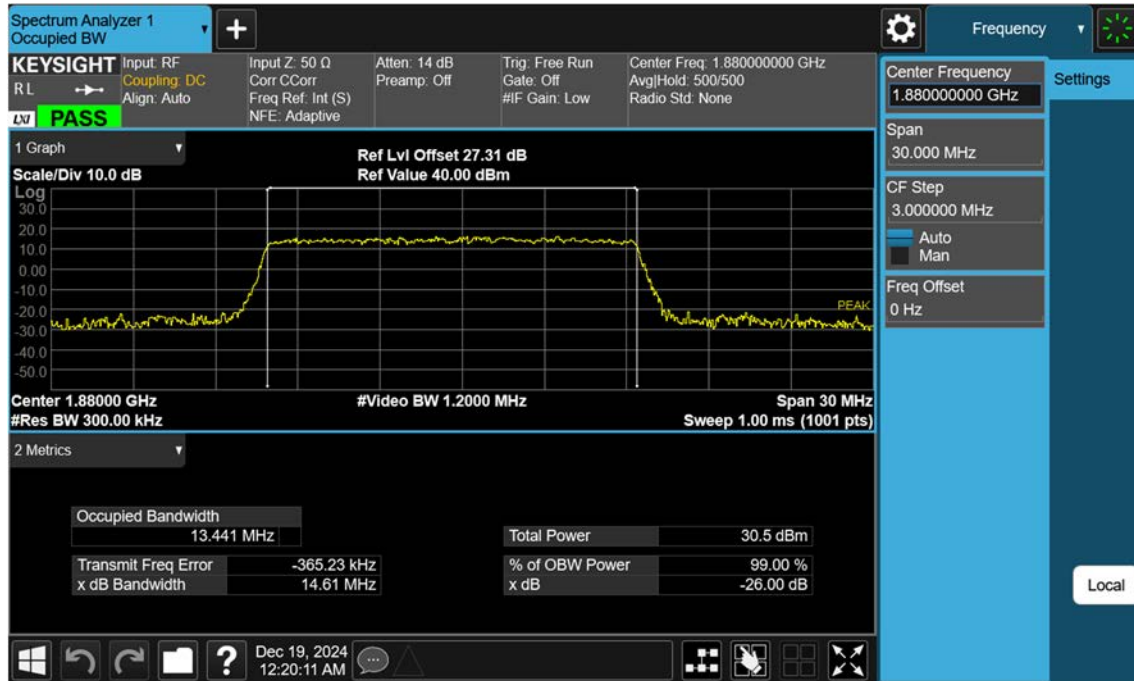
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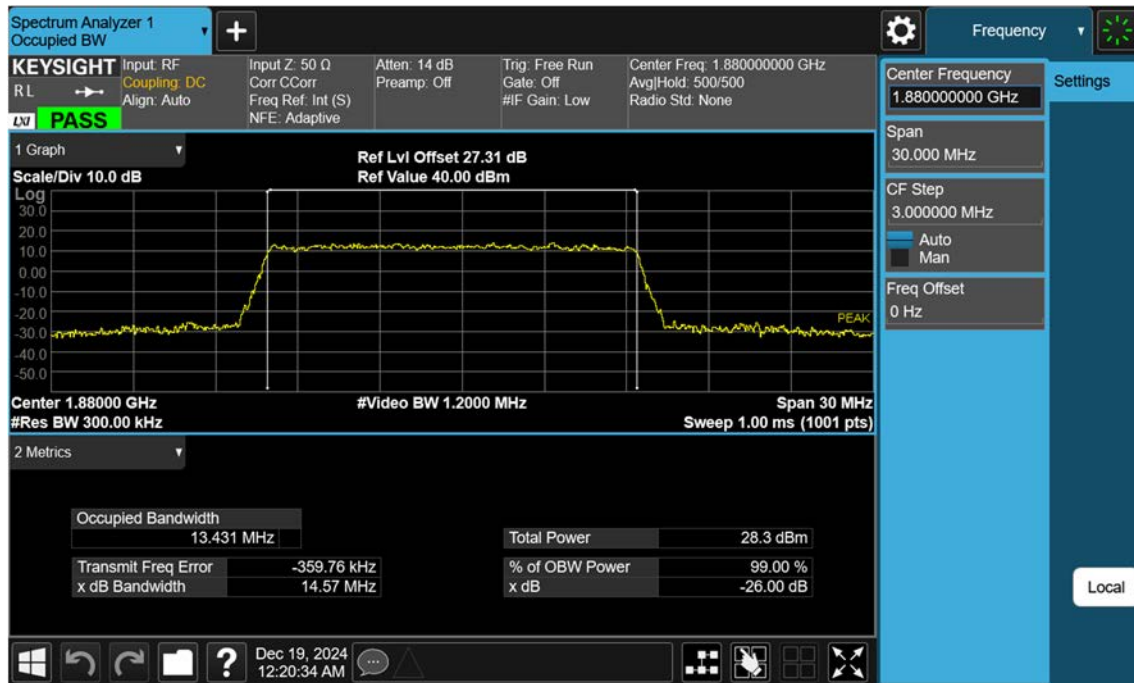
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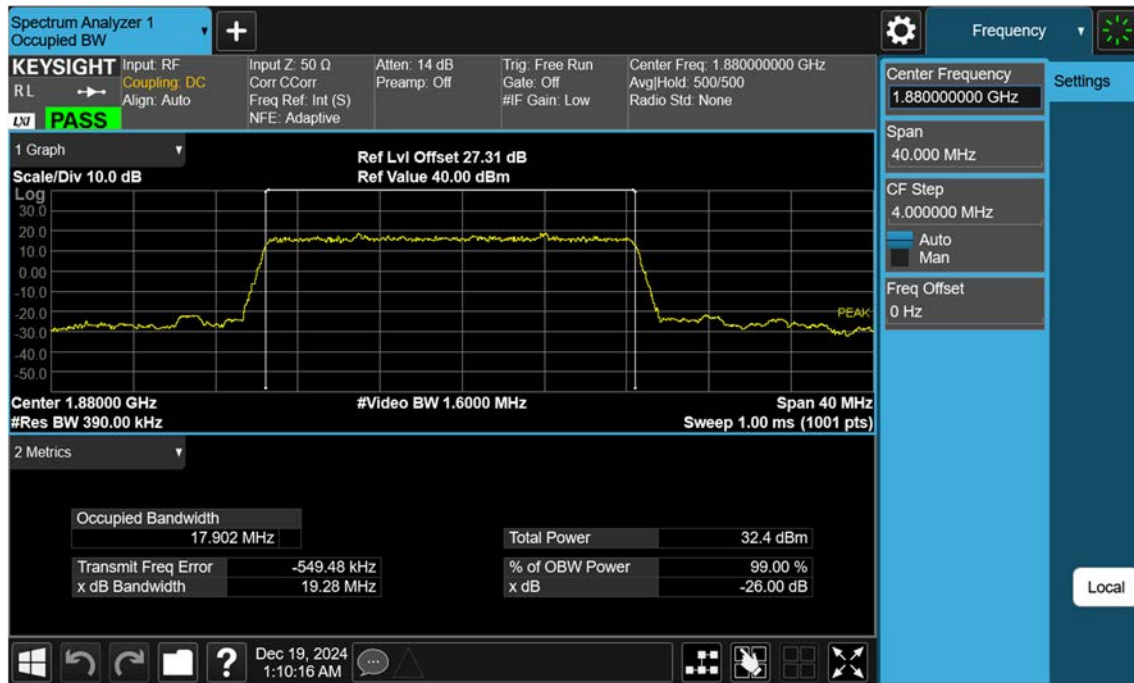
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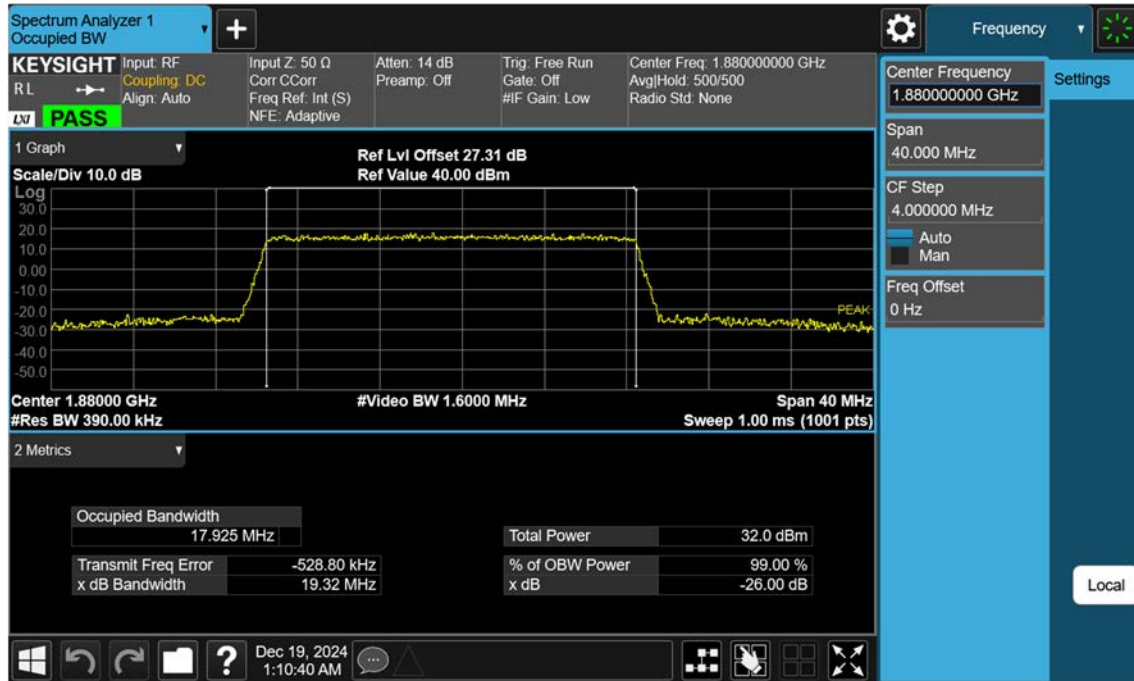
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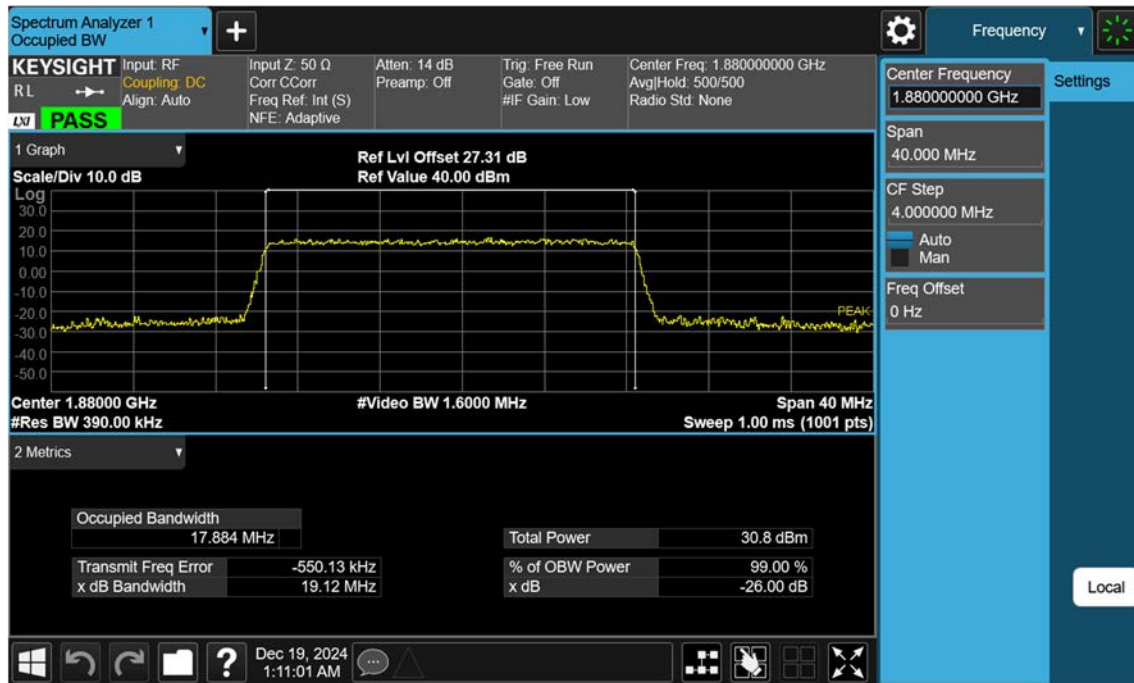
NR2_20 M_OBW_Mid_BPSK_FullIRB



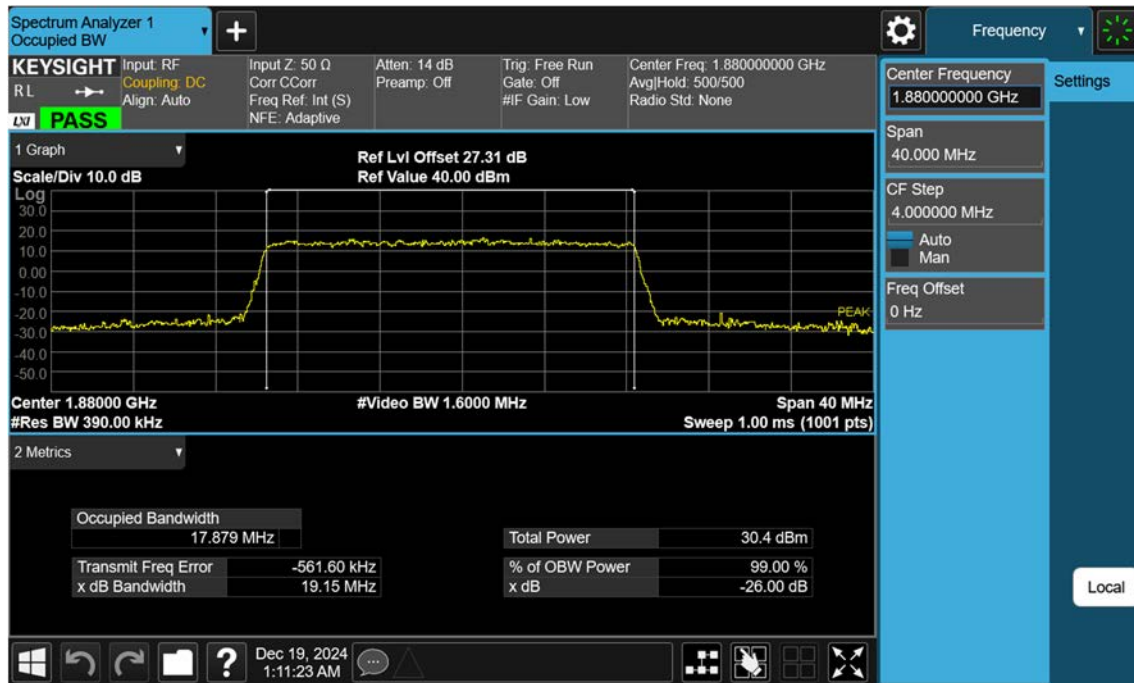
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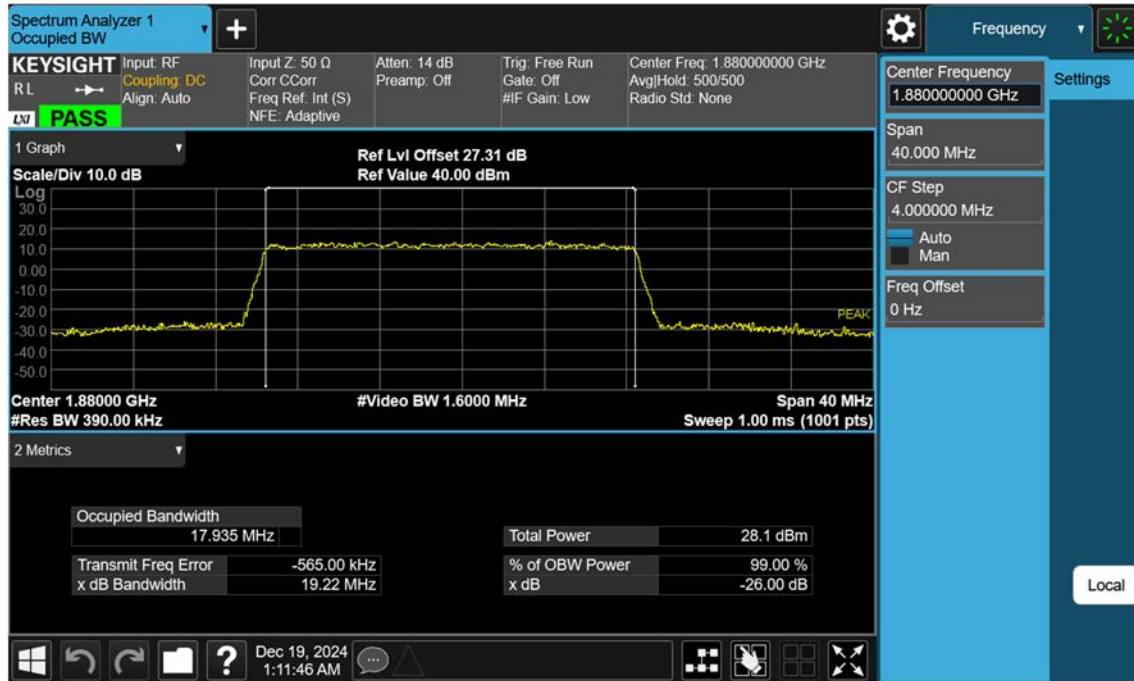
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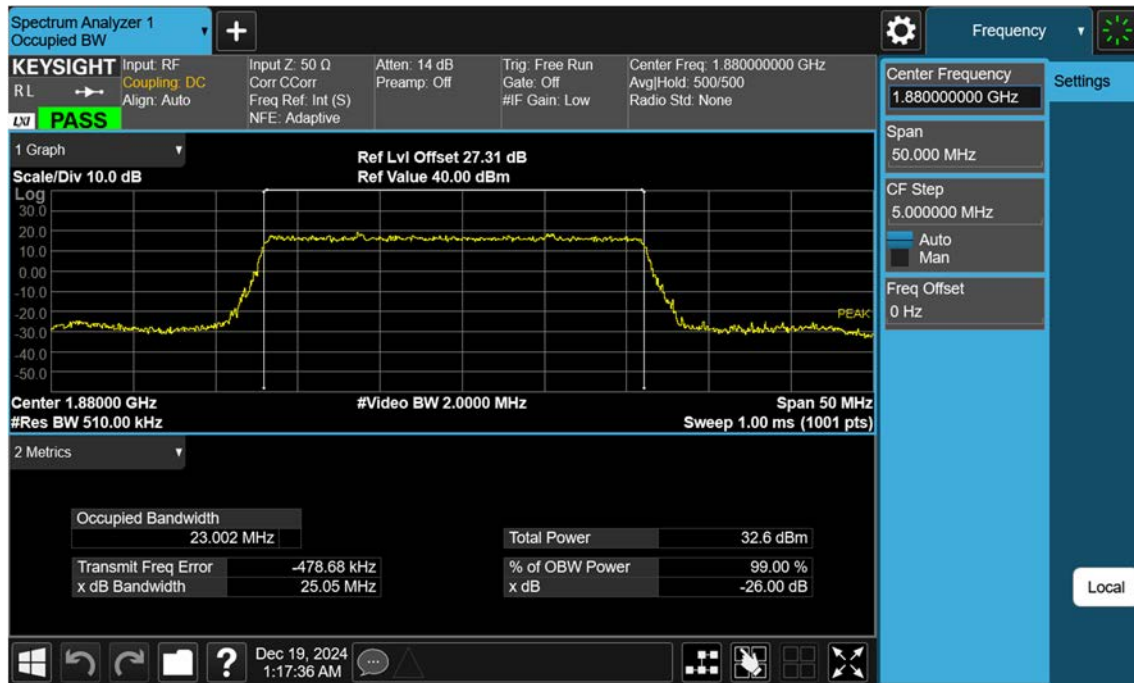
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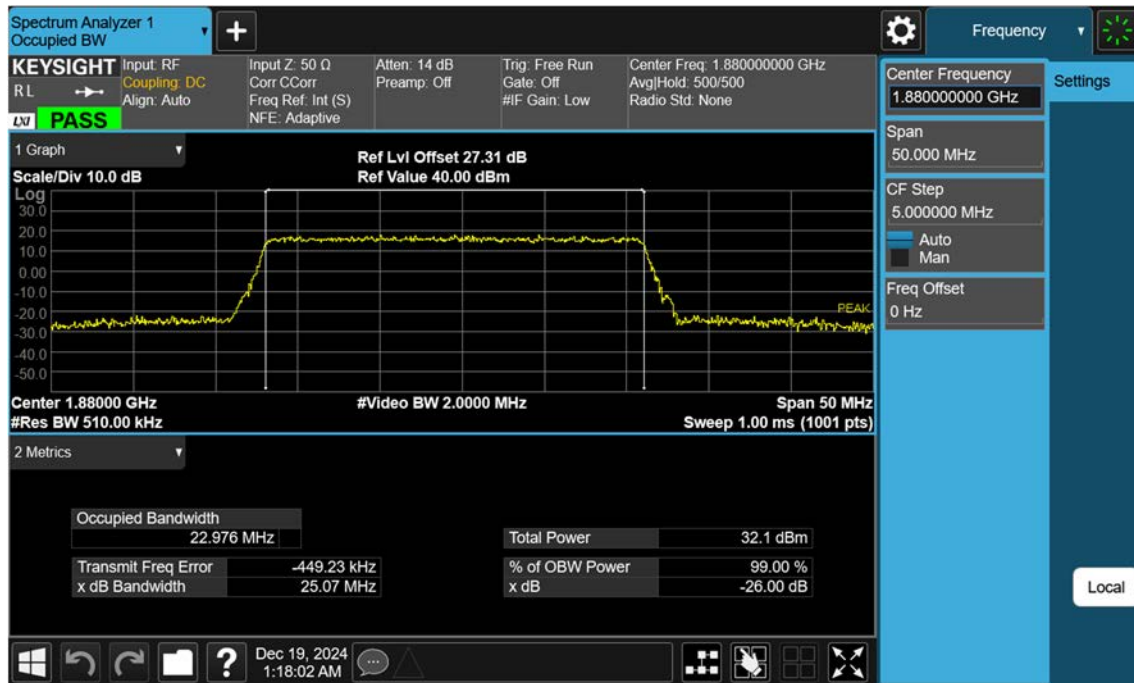
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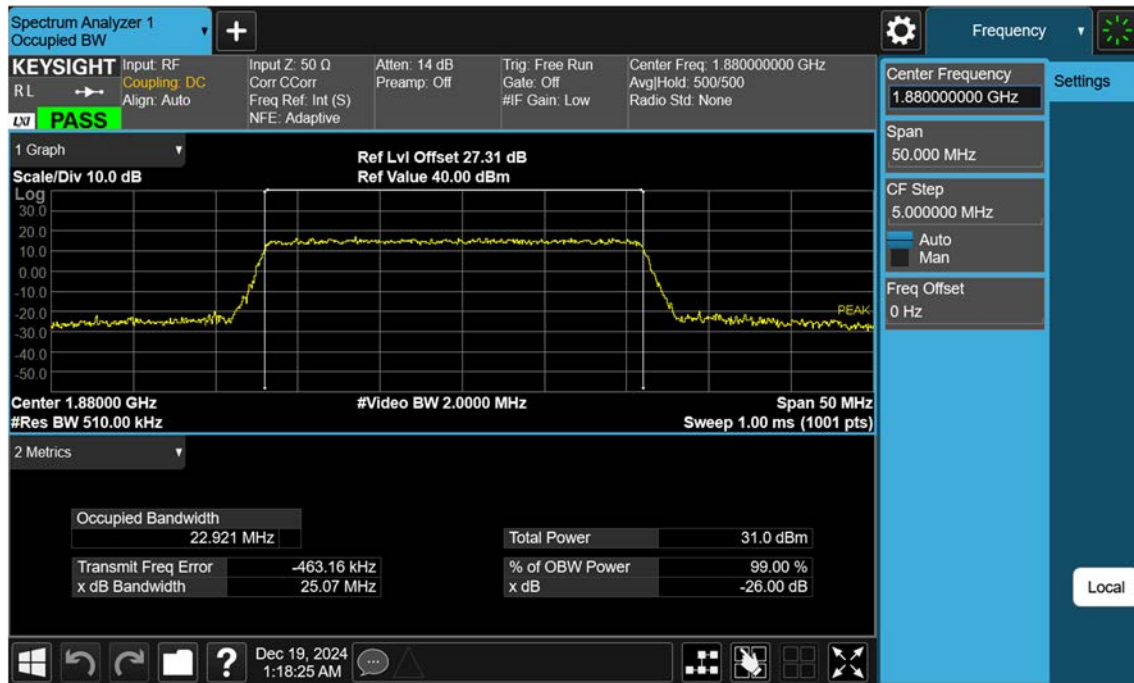
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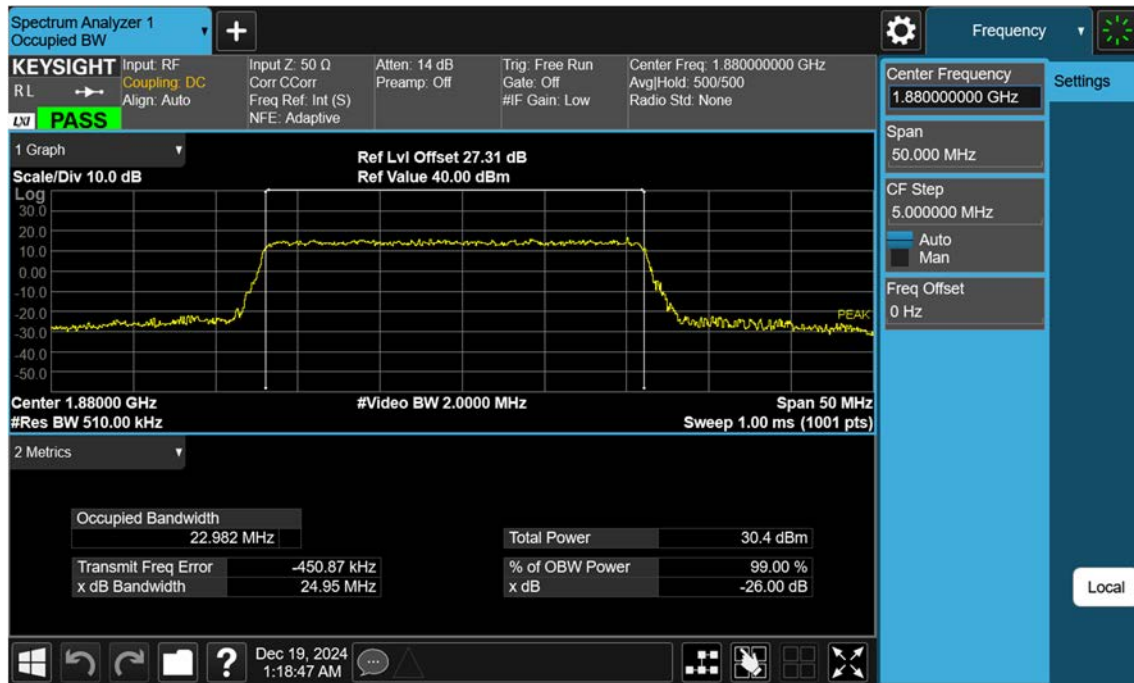
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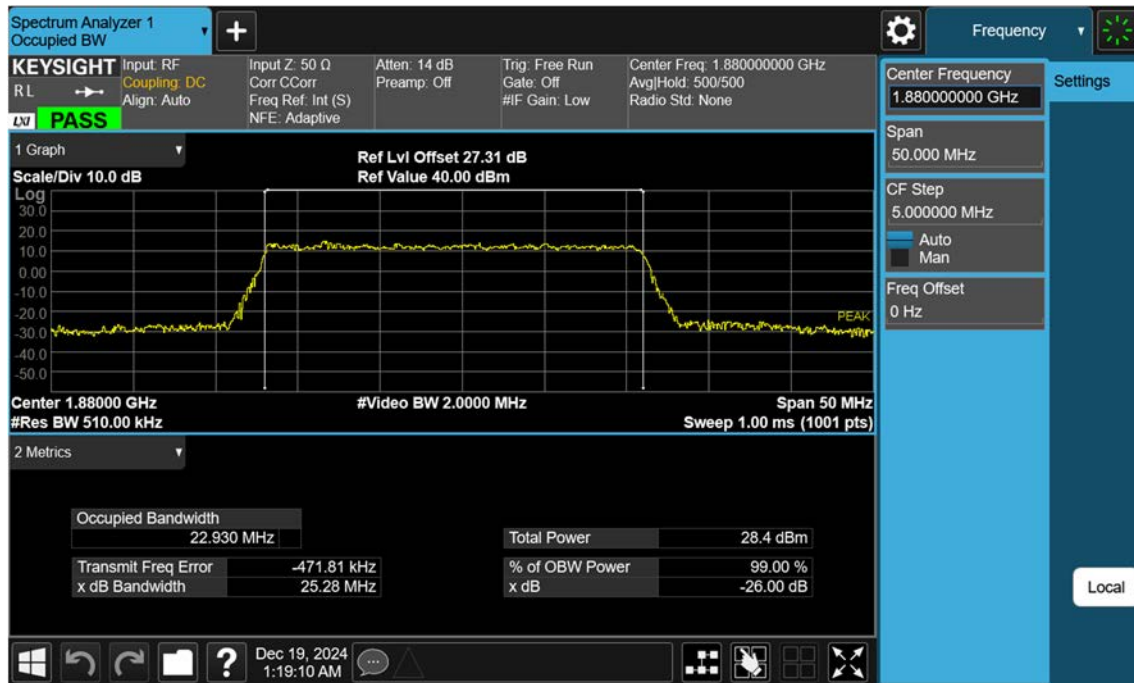
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NR2_25 M_OBW_Mid_64QAM_FullRB



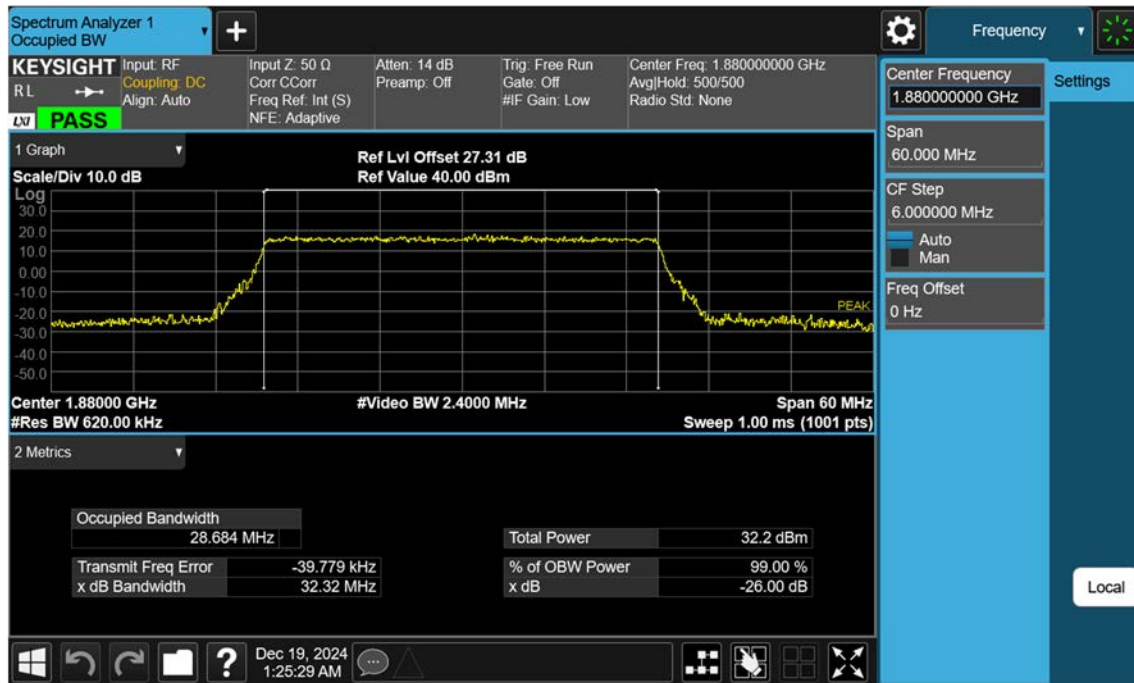
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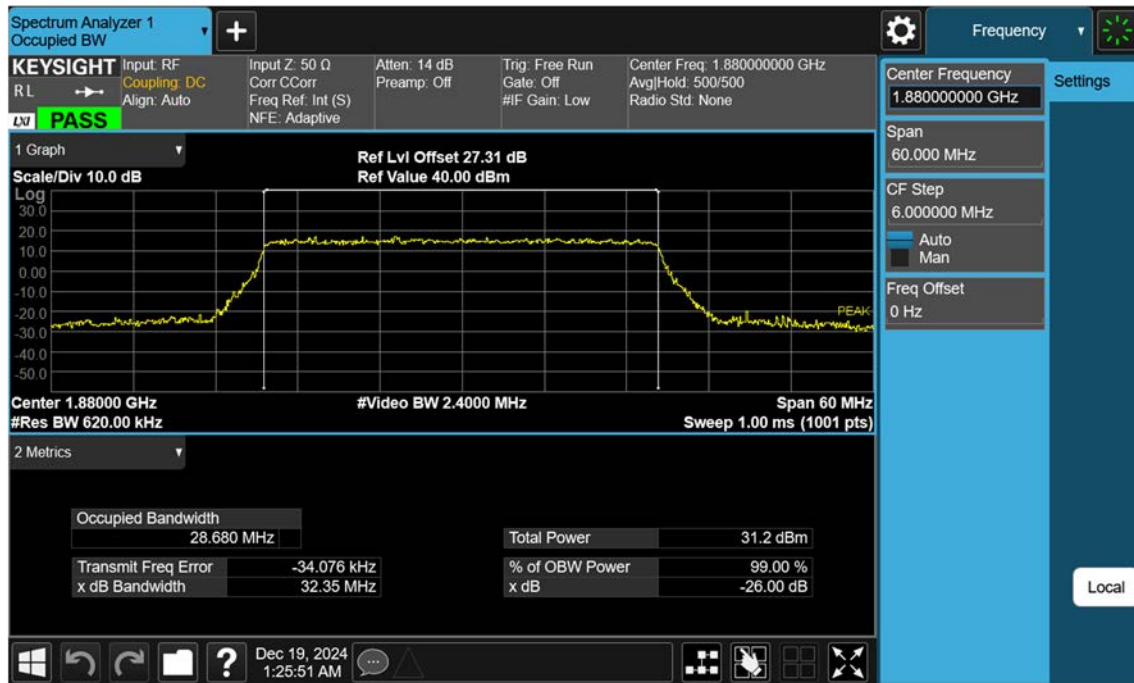
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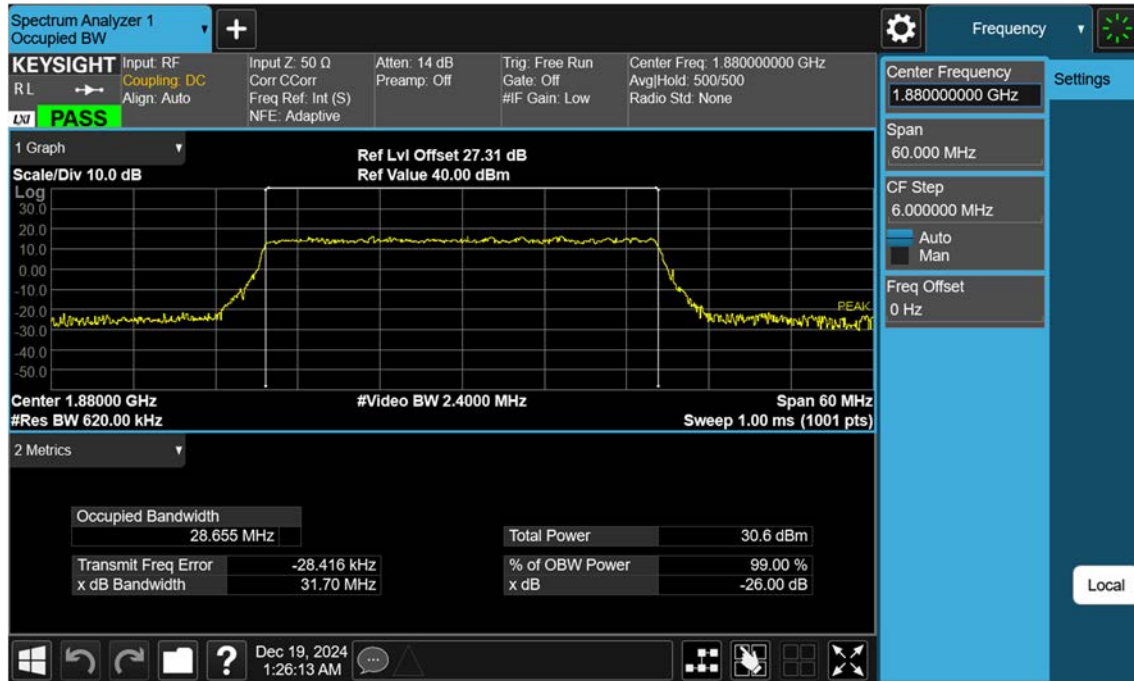
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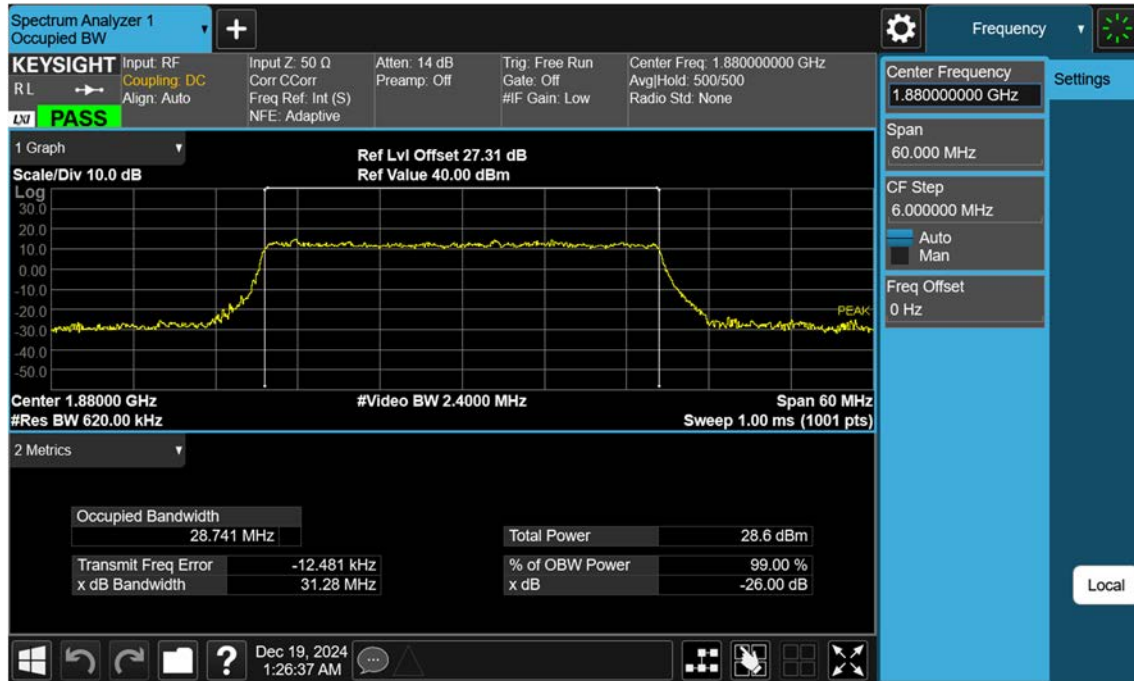
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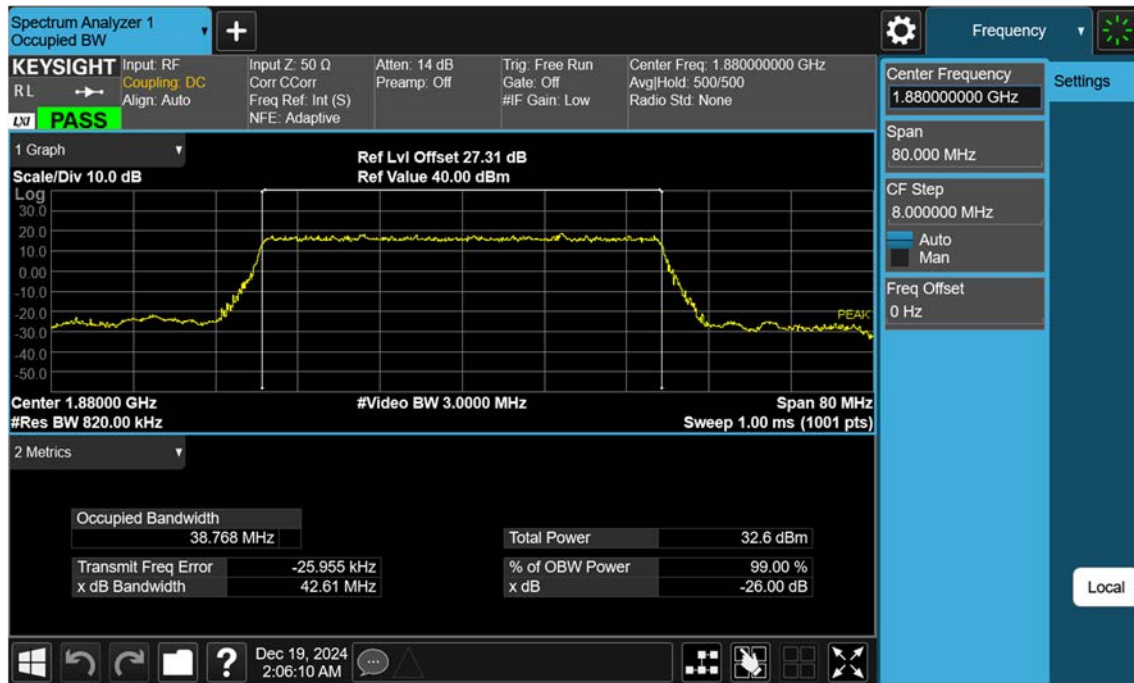
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NR2_30 M_OBW_Mid_256QAM_FullRB



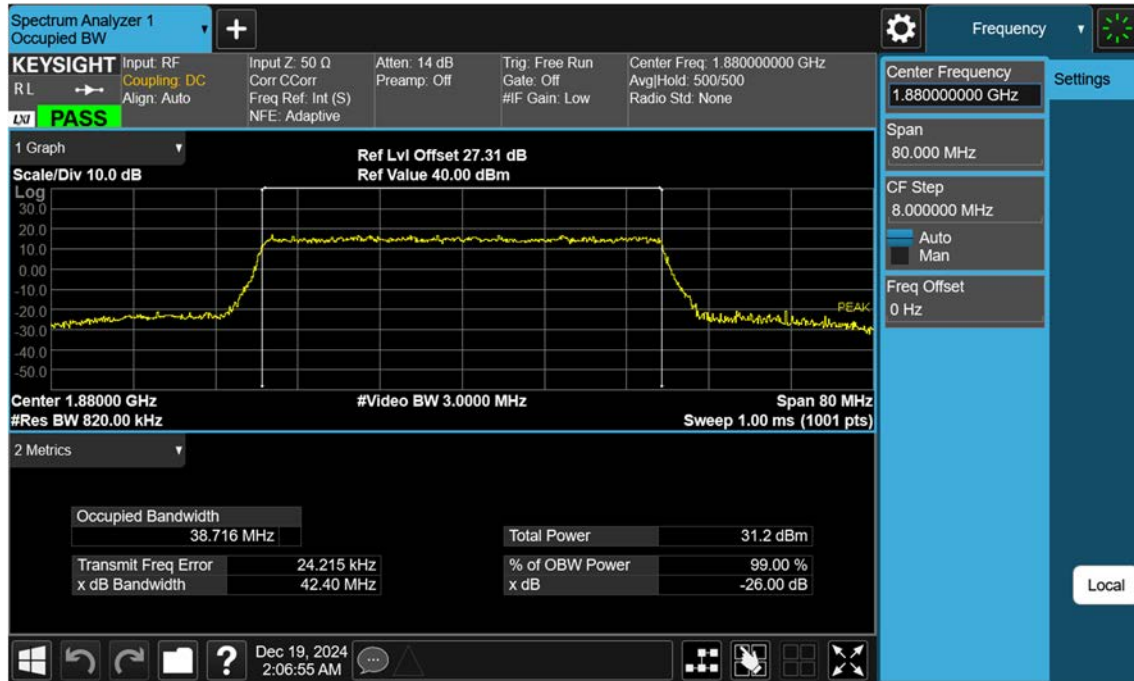
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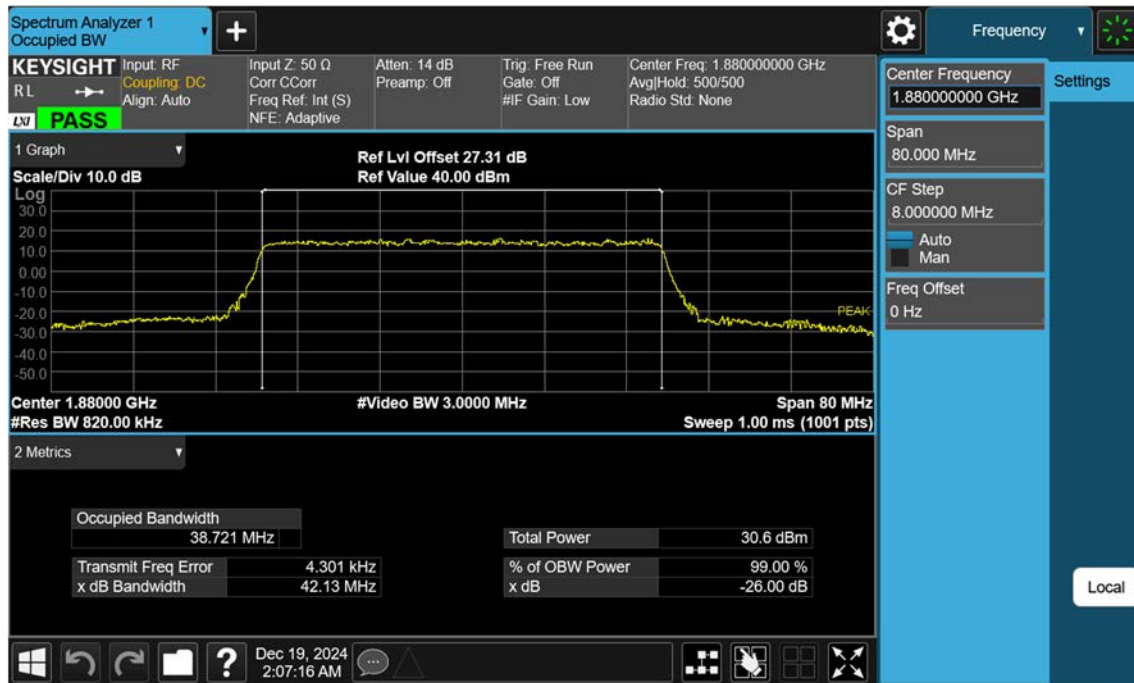
NR2_40 M_OBW_Mid_QPSK_FullRB



NR2_40 M_OBW_Mid_16QAM_FullRB



NR2_40 M_OBW_Mid_64QAM_FullRB



NR2_40 M_OBW_Mid_256QAM_FullRB

