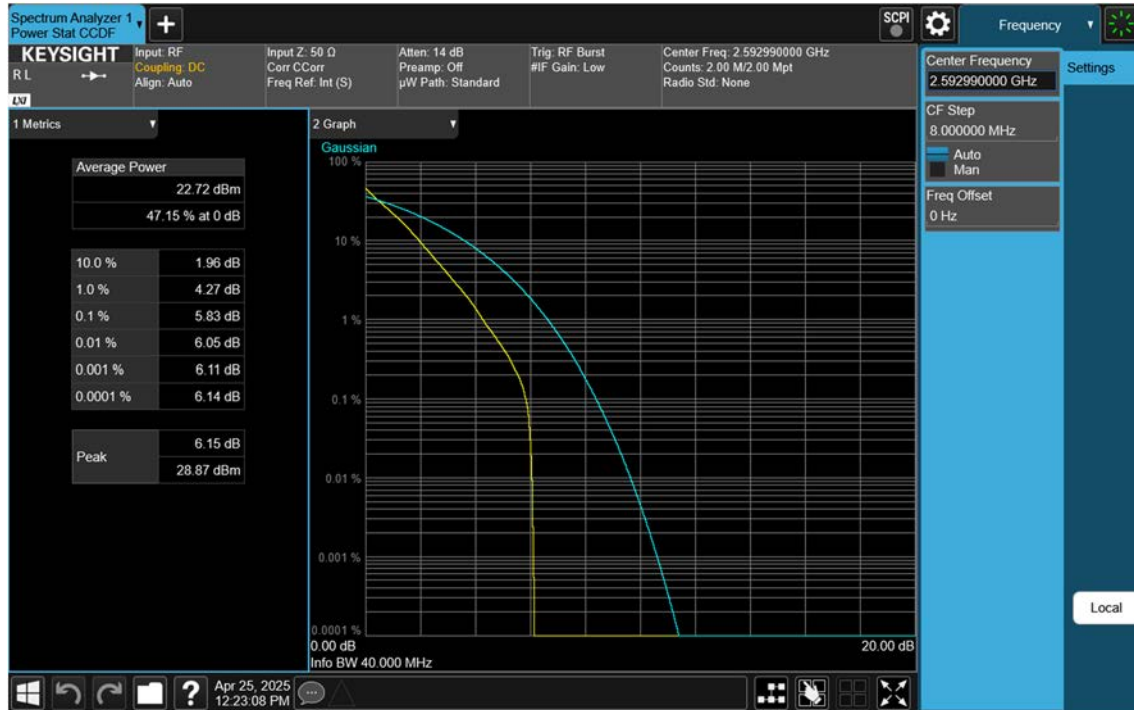
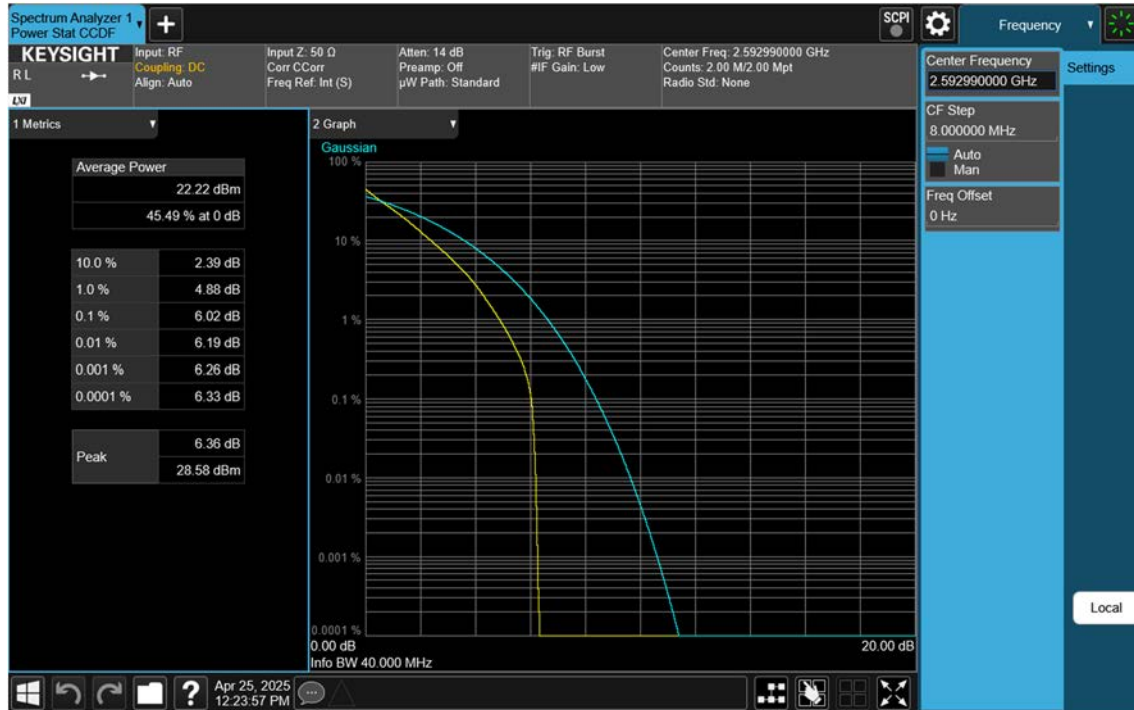


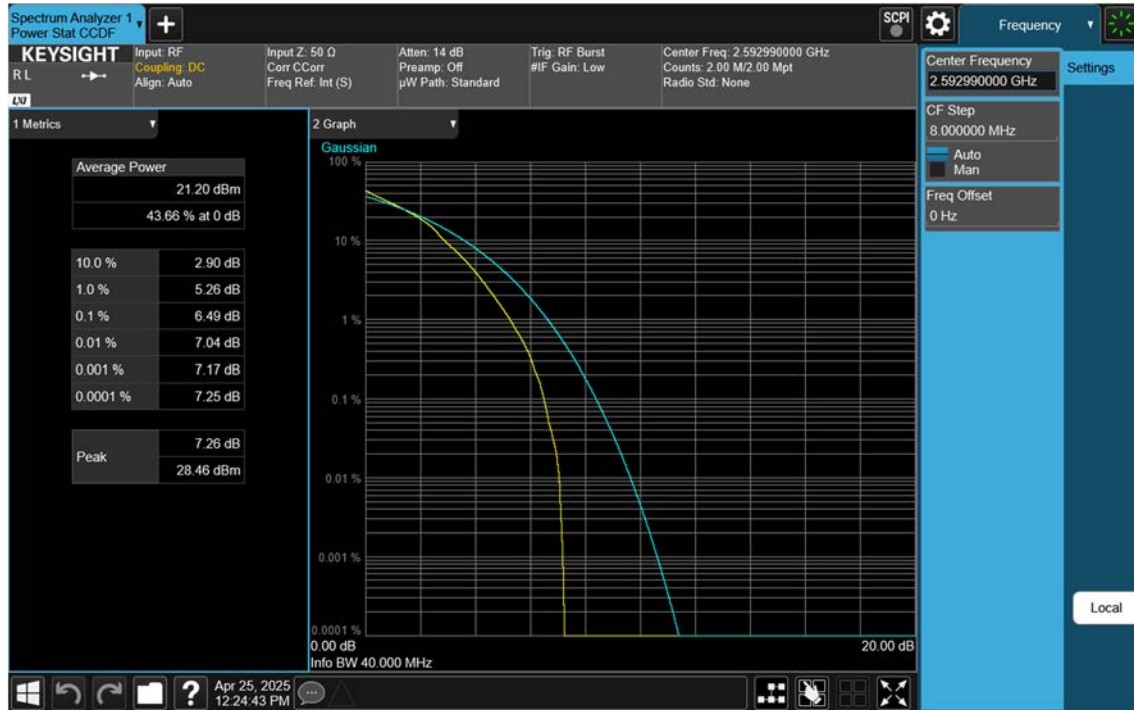
NR41_40 M_PAR_Mid_BPSK_FullRB



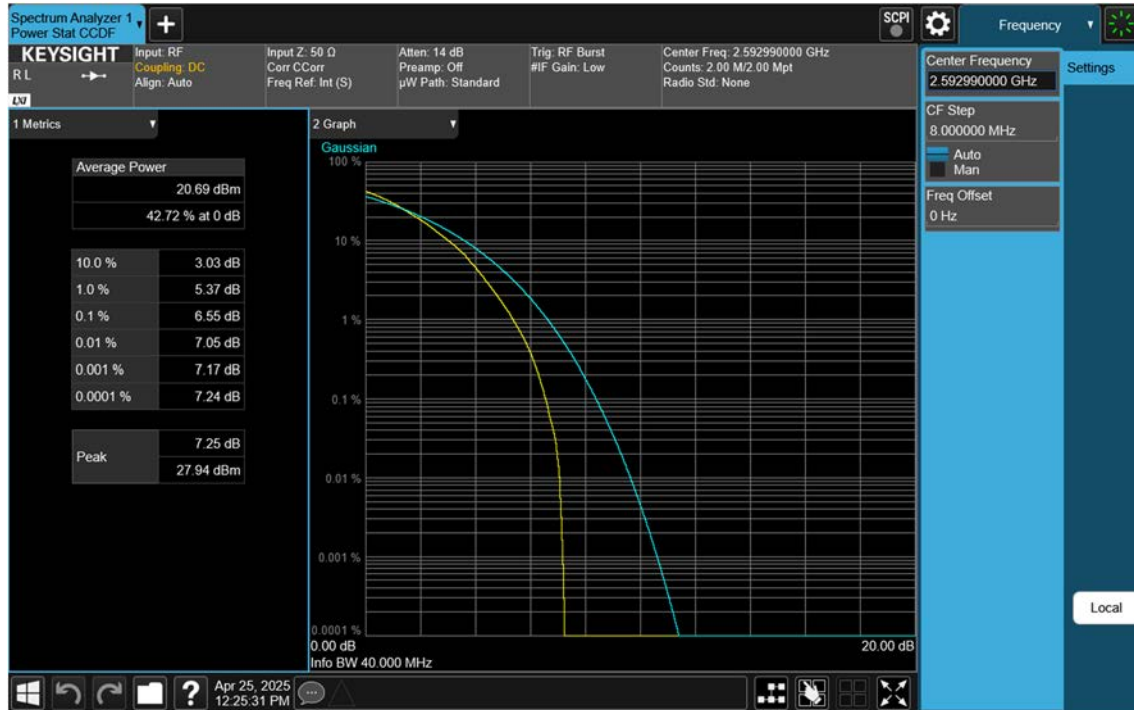
NR41_40 M_PAR_Mid_QPSK_FullRB



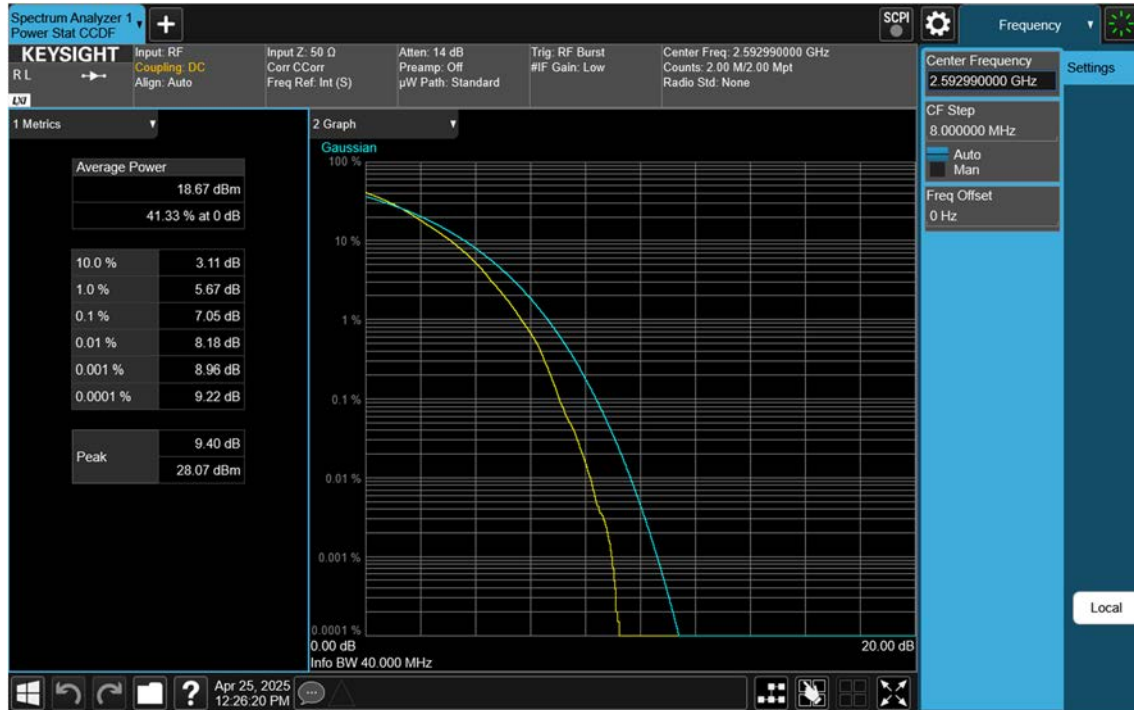
NR41_40 M_PAR_Mid_16QAM_FullRB



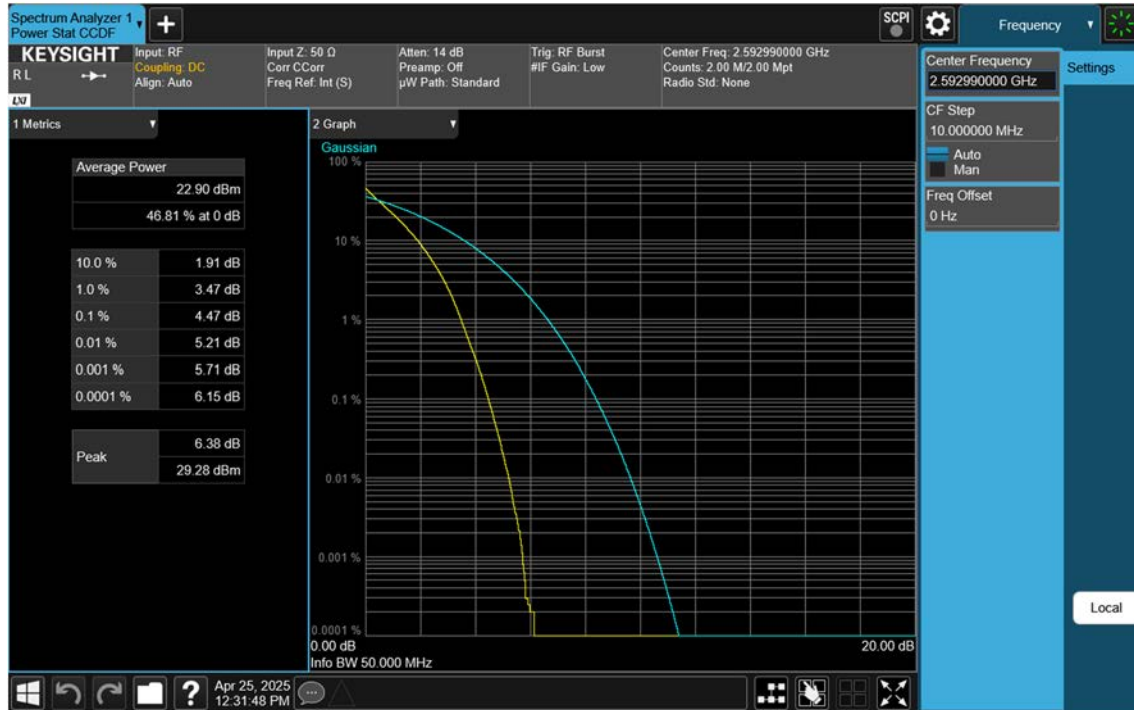
NR41_40 M_PAR_Mid_64QAM_FullRB



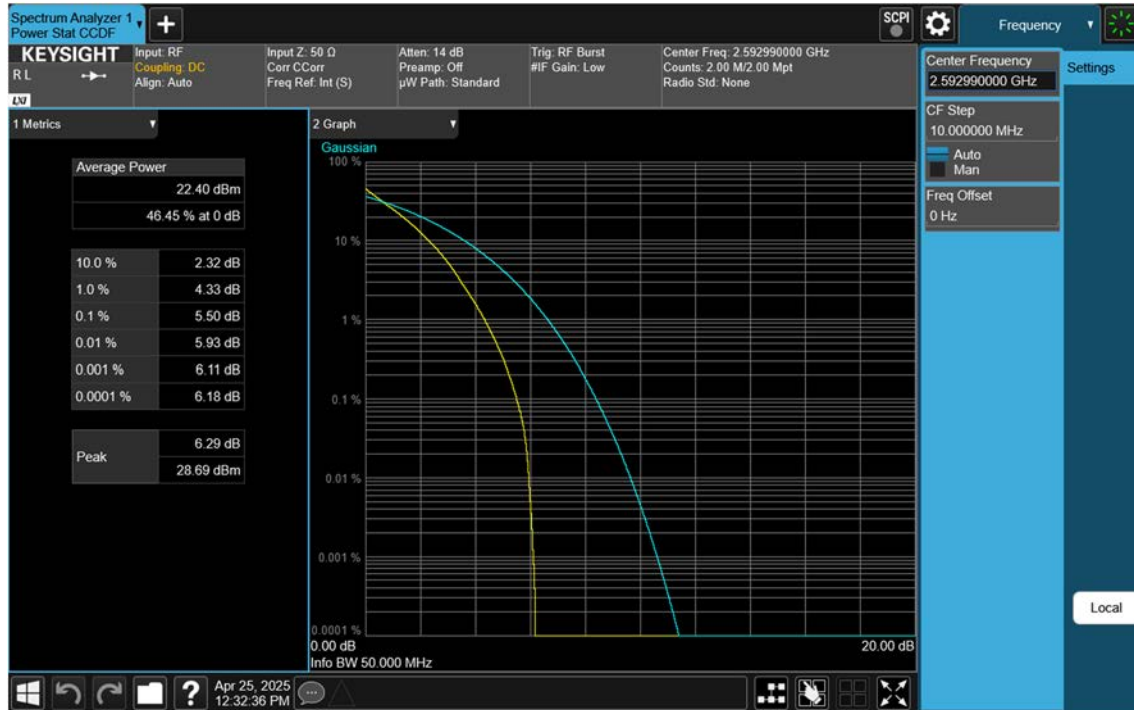
NR41_40 M_PAR_Mid_256QAM_FullRB



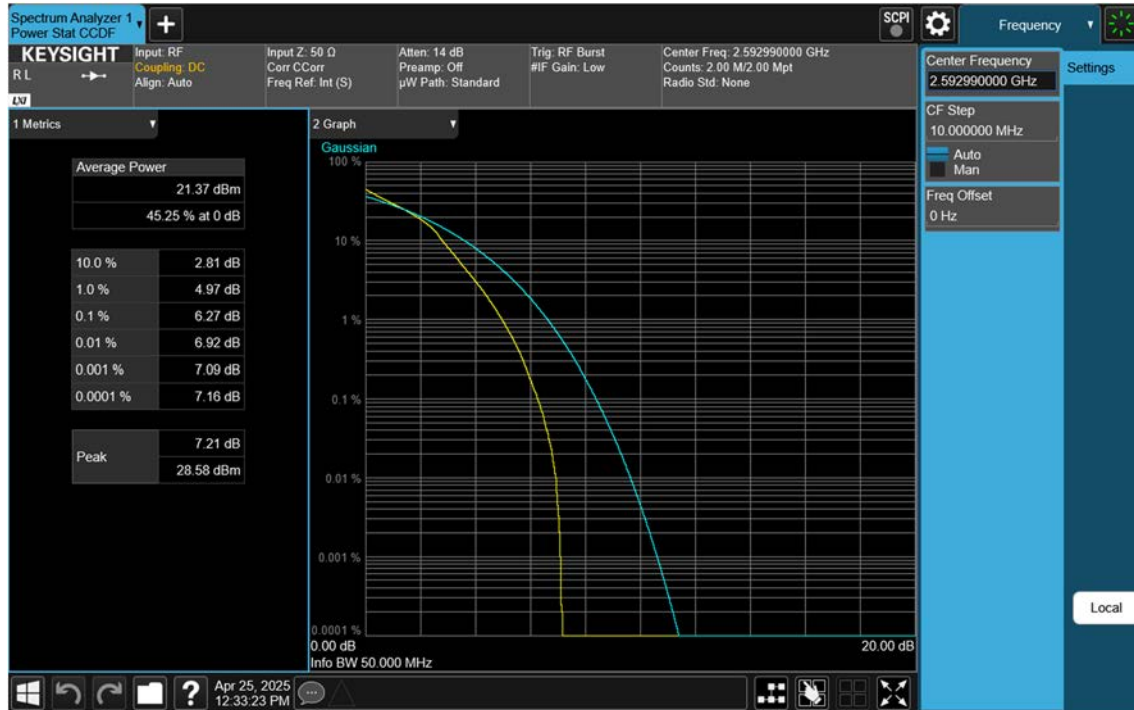
NR41_50 M_PAR_Mid_BPSK_FullRB



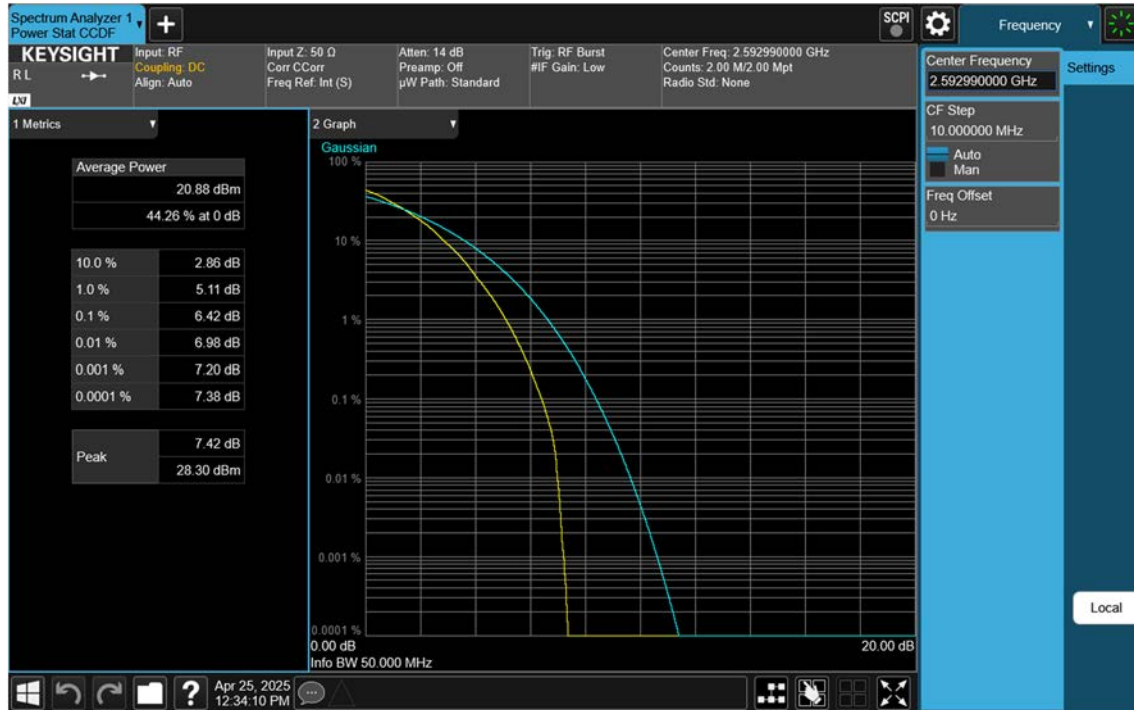
NR41_50 M_PAR_Mid_QPSK_FullRB



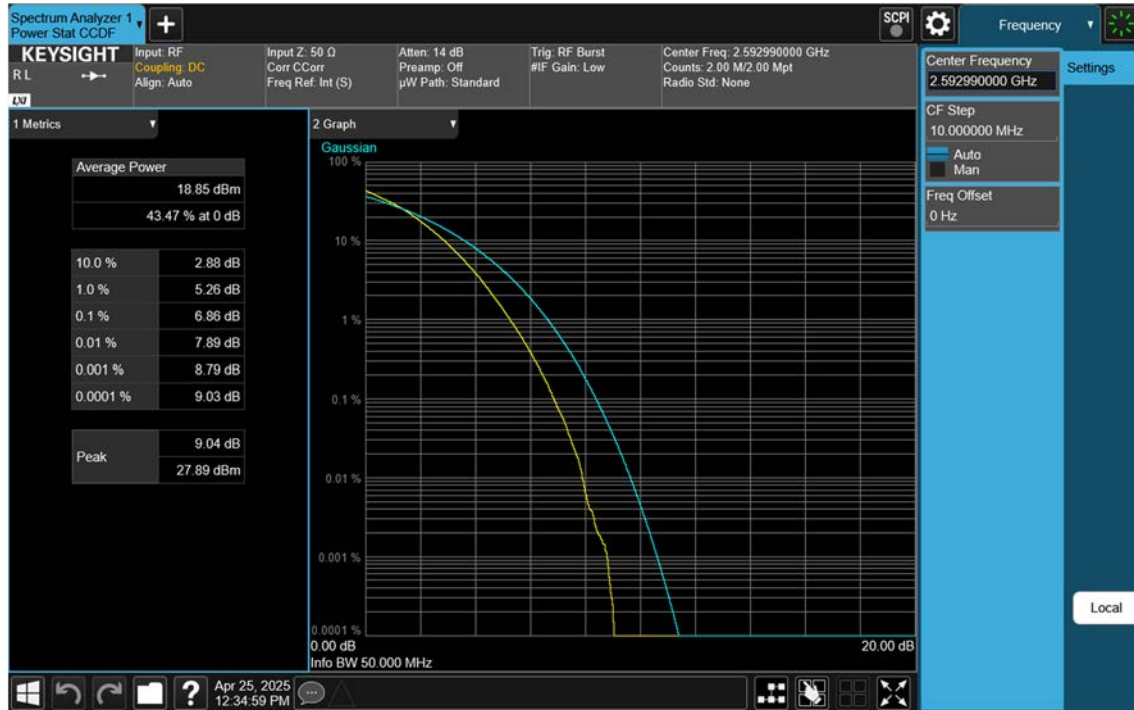
NR41_50 M_PAR_Mid_16QAM_FullRB



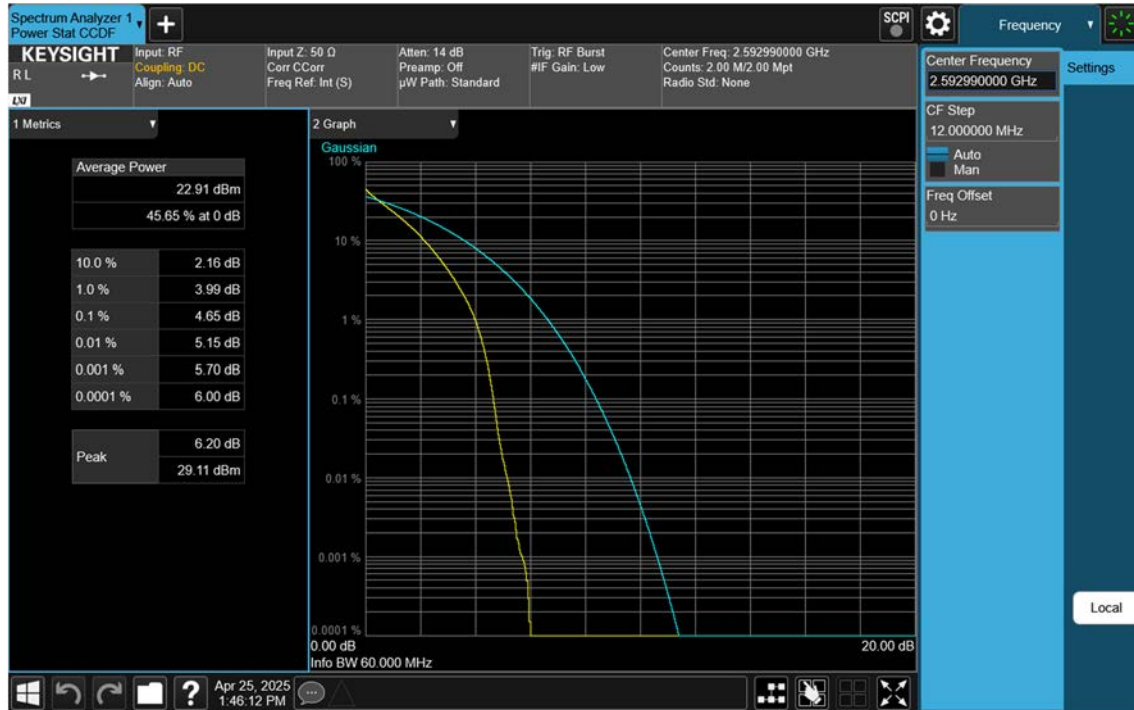
NR41_50 M_PAR_Mid_64QAM_FullRB



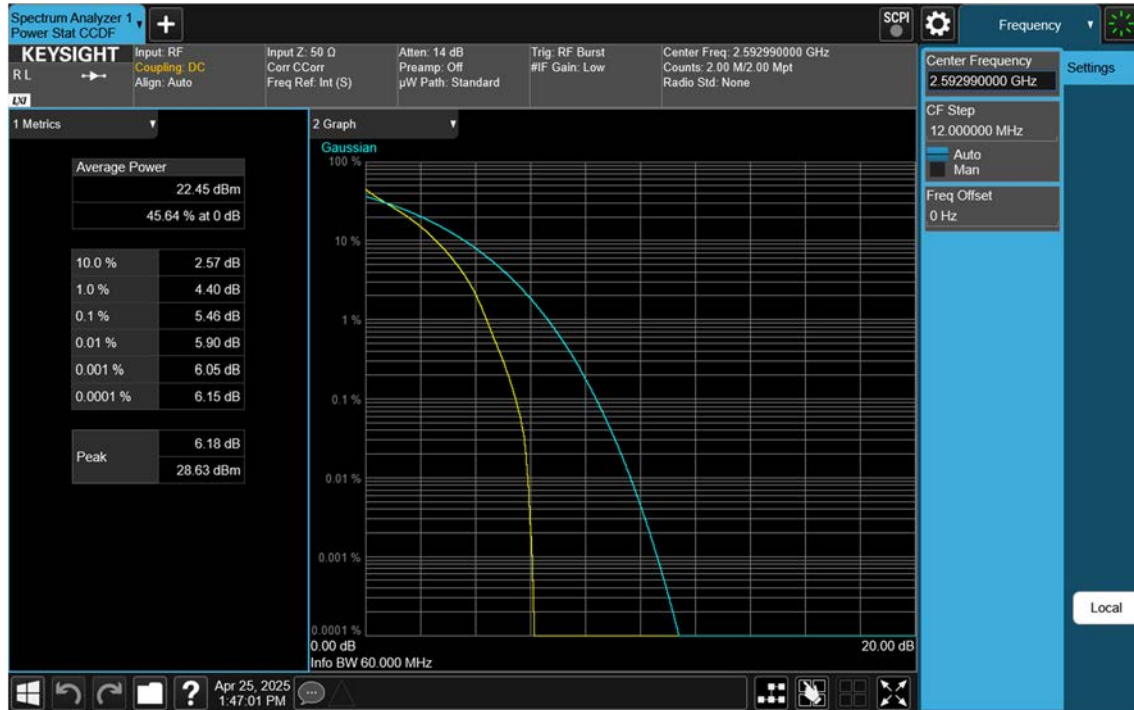
NR41_50 M_PAR_Mid_256QAM_FullRB



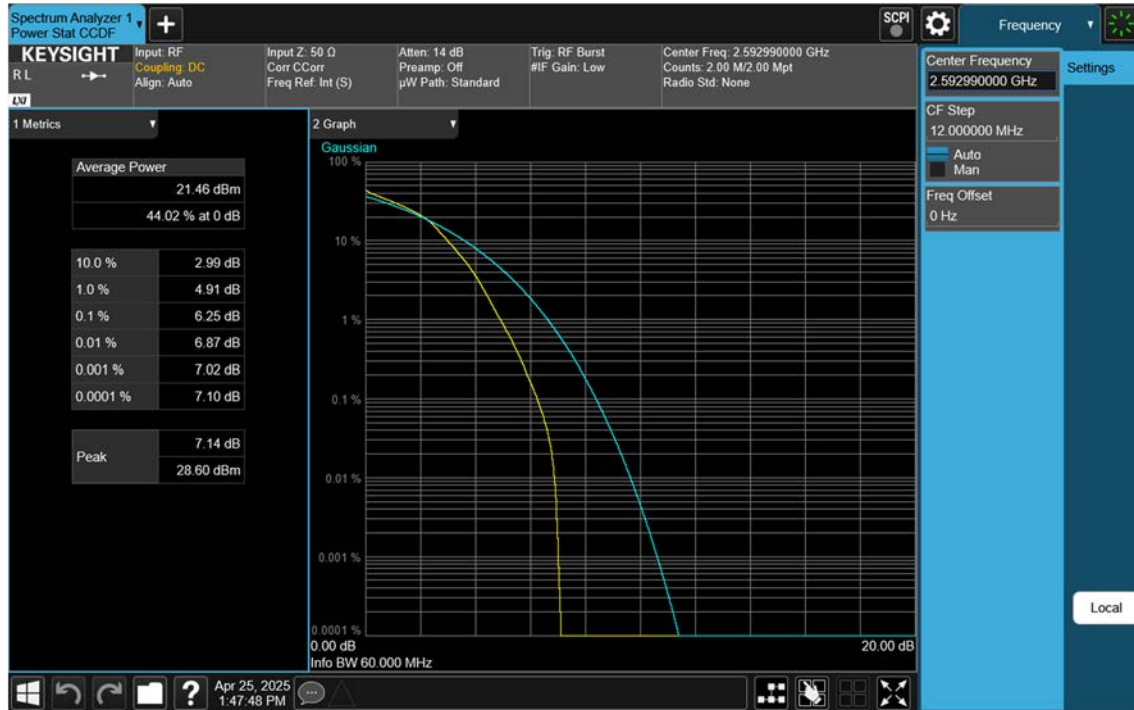
NR41_60 M_PAR_Mid_BPSK_FullRB



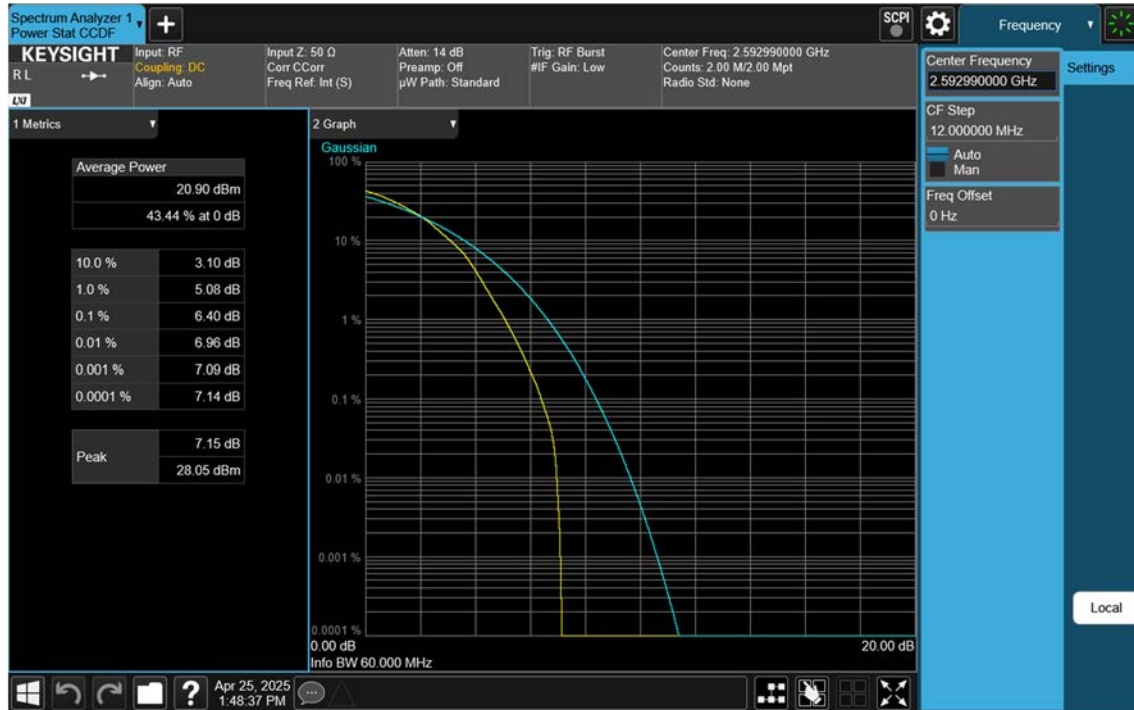
NR41_60 M_PAR_Mid_QPSK_FullRB



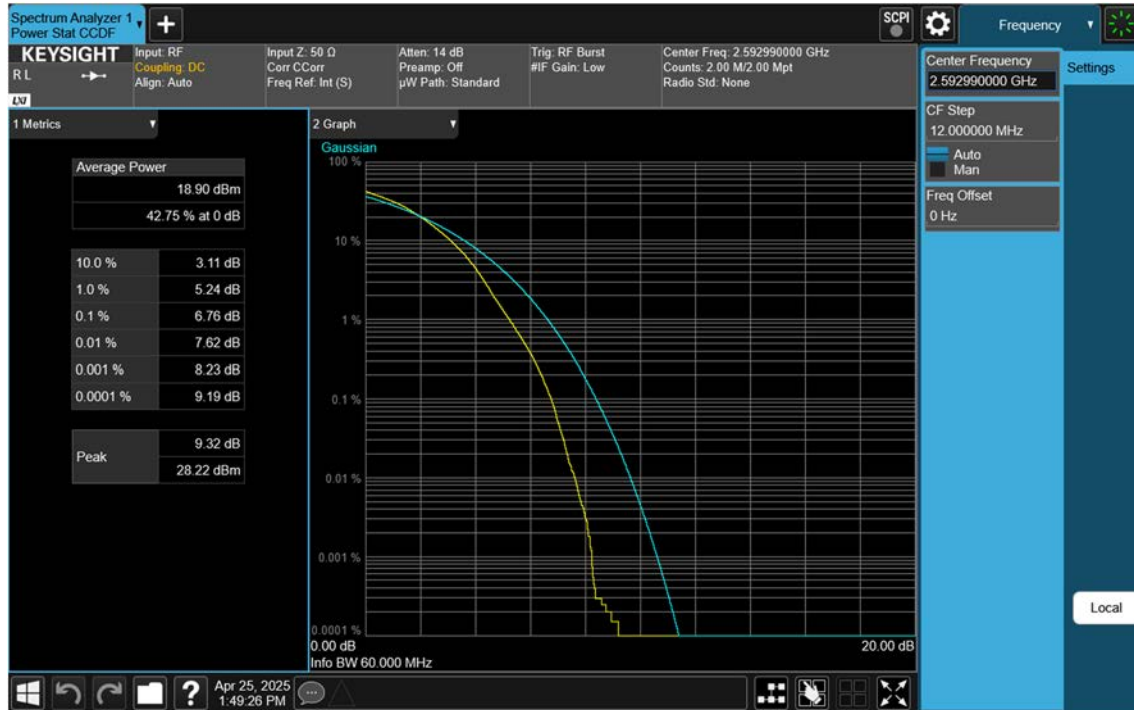
NR41_60 M_PAR_Mid_16QAM_FullRB



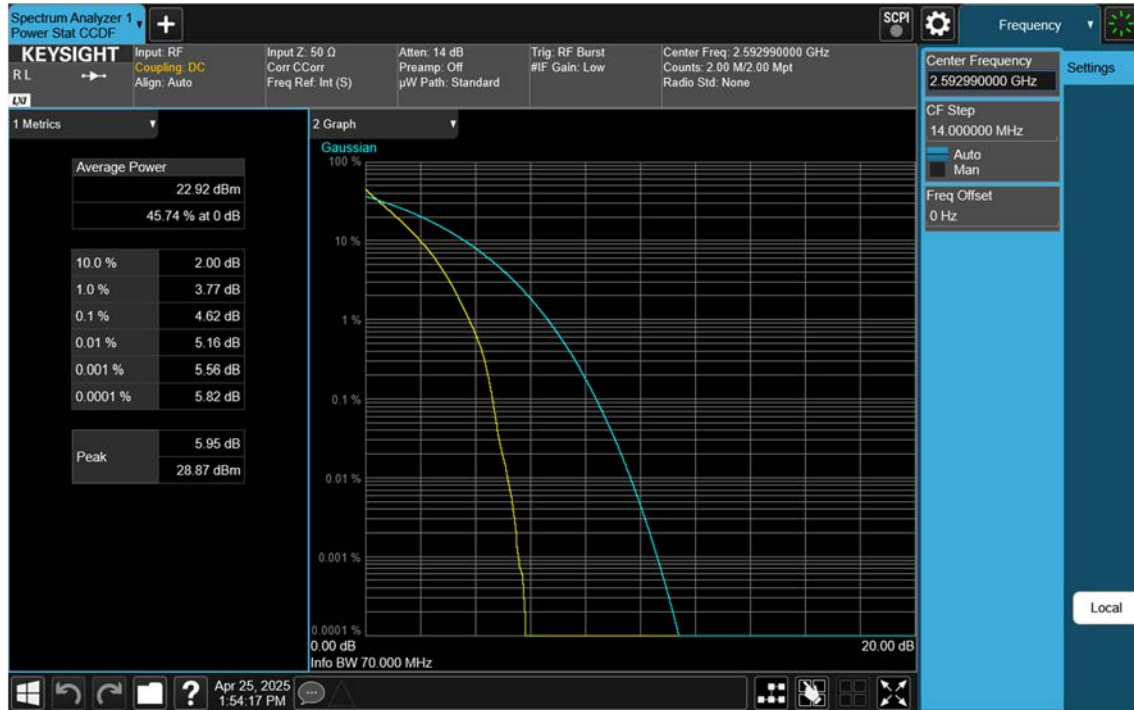
NR41_60 M_PAR_Mid_64QAM_FullRB



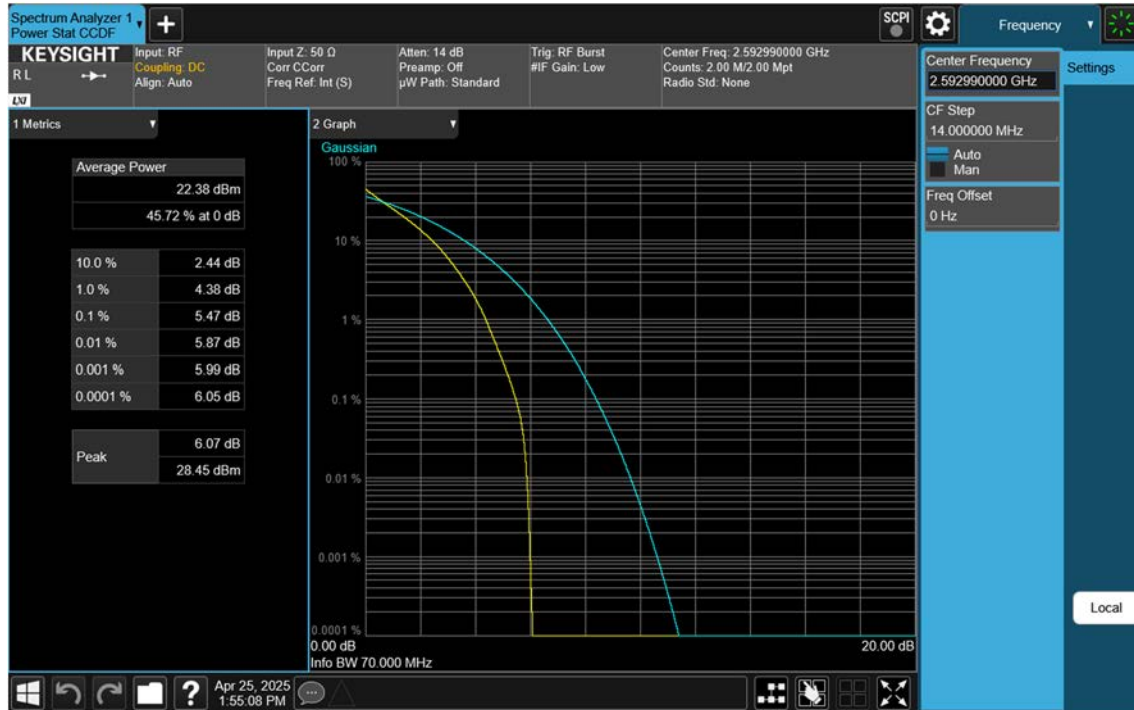
NR41_60 M_PAR_Mid_256QAM_FullRB



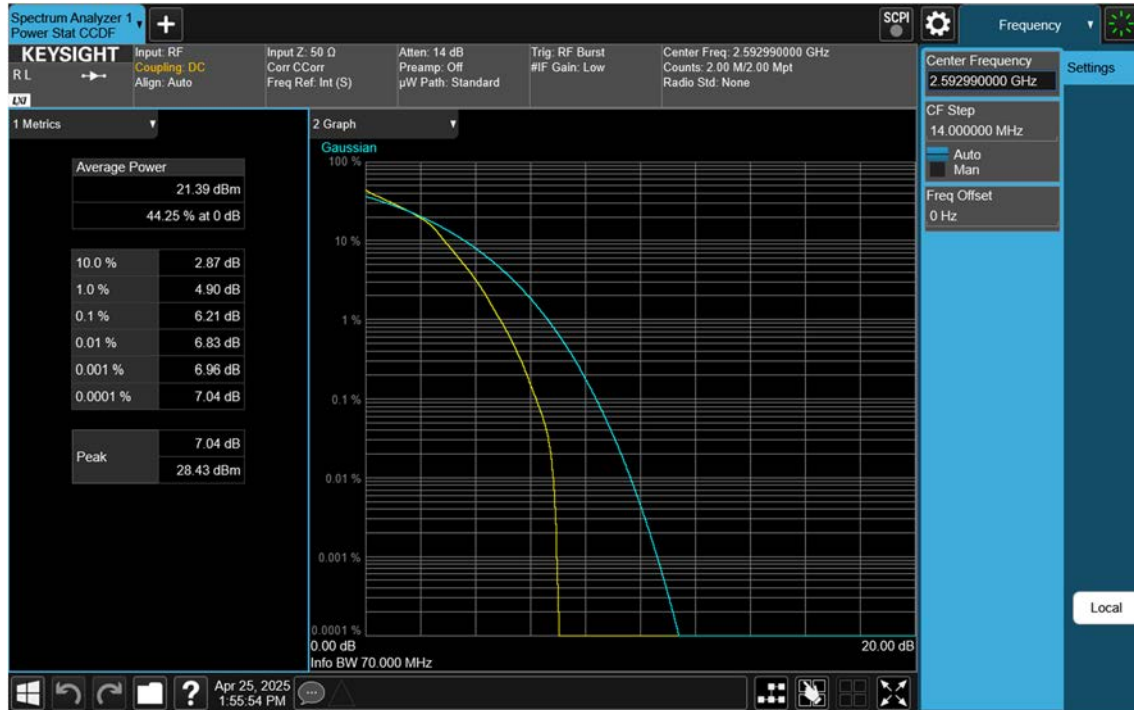
NR41_70 M_PAR_Mid_BPSK_FullRB



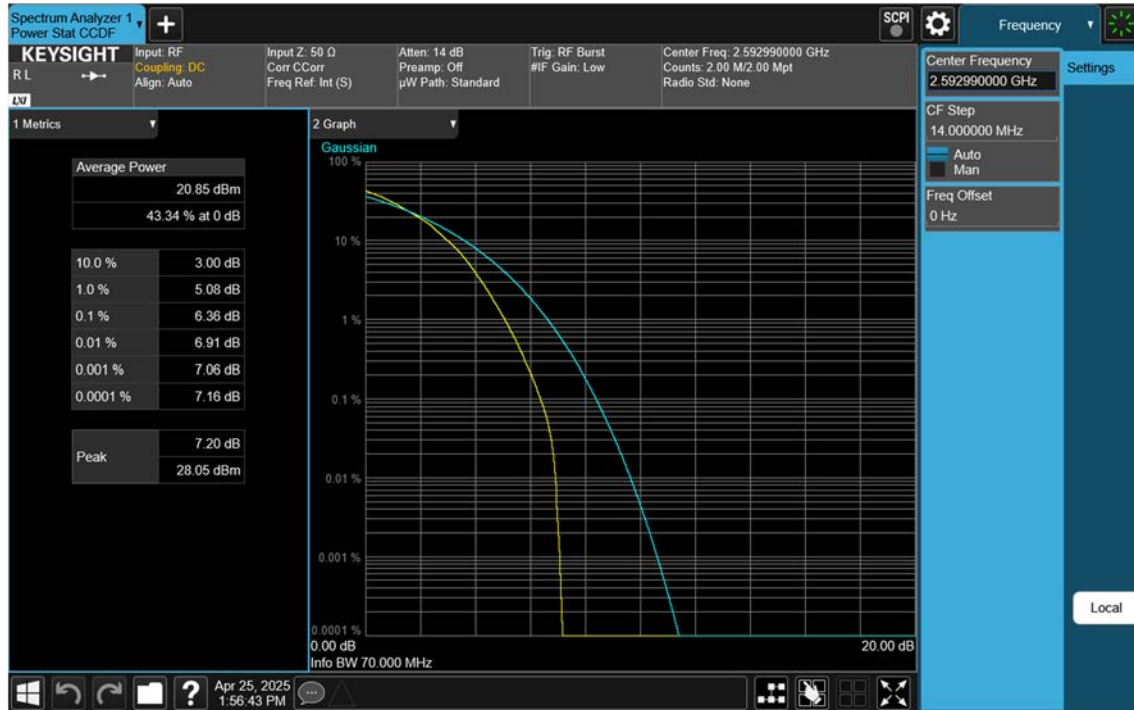
NR41_70 M_PAR_Mid_QPSK_FullRB



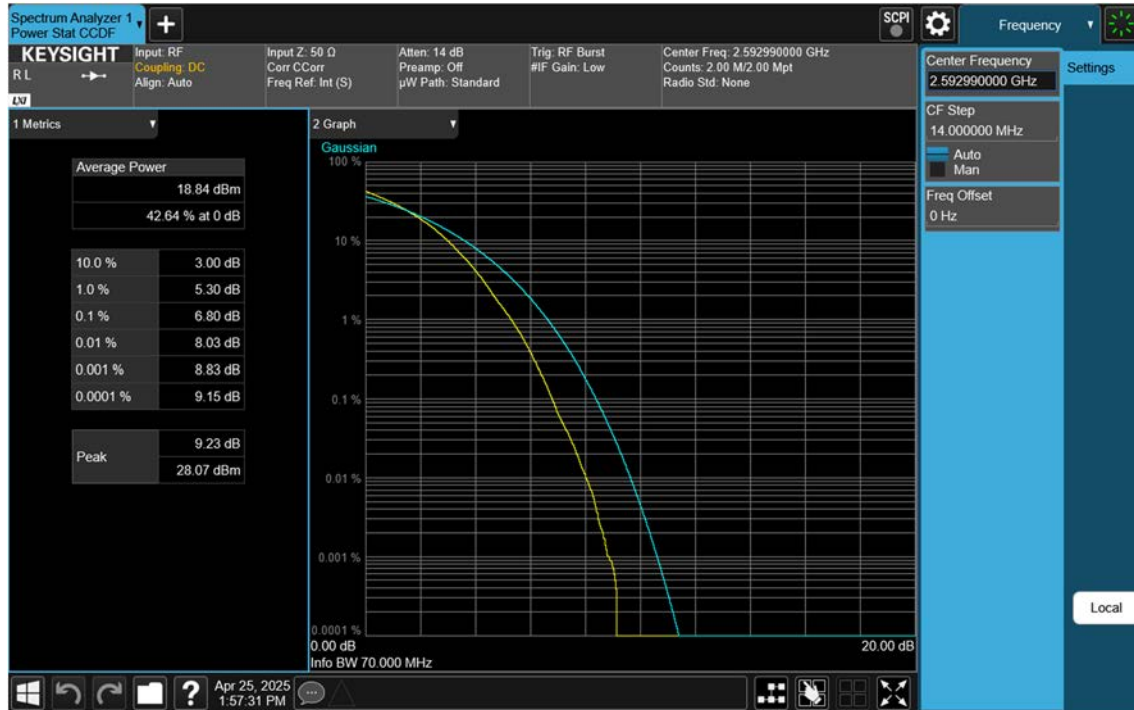
NR41_70 M_PAR_Mid_16QAM_FullRB



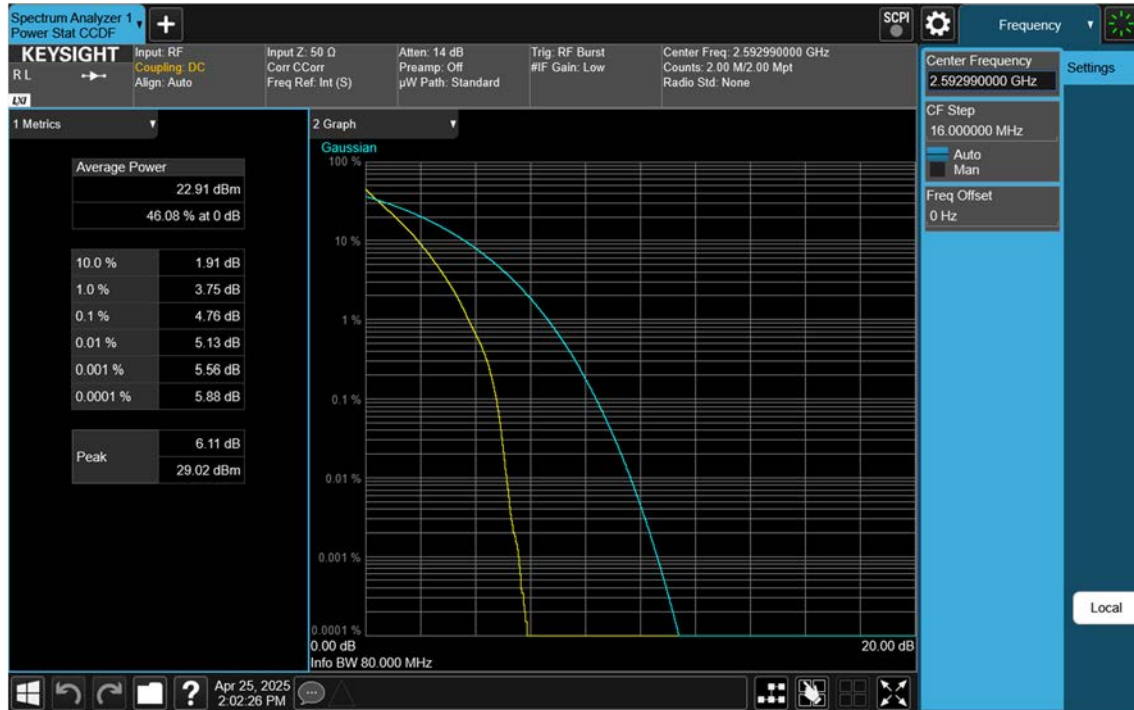
NR41_70 M_PAR_Mid_64QAM_FullRB



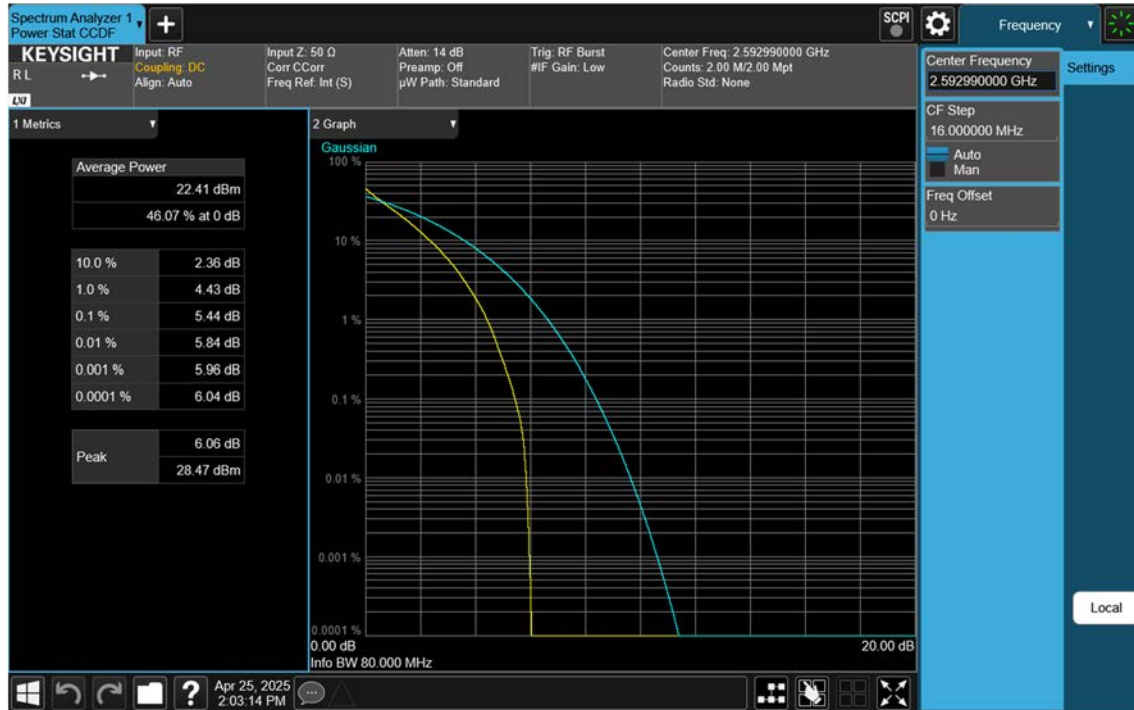
NR41_70 M_PAR_Mid_256QAM_FullRB



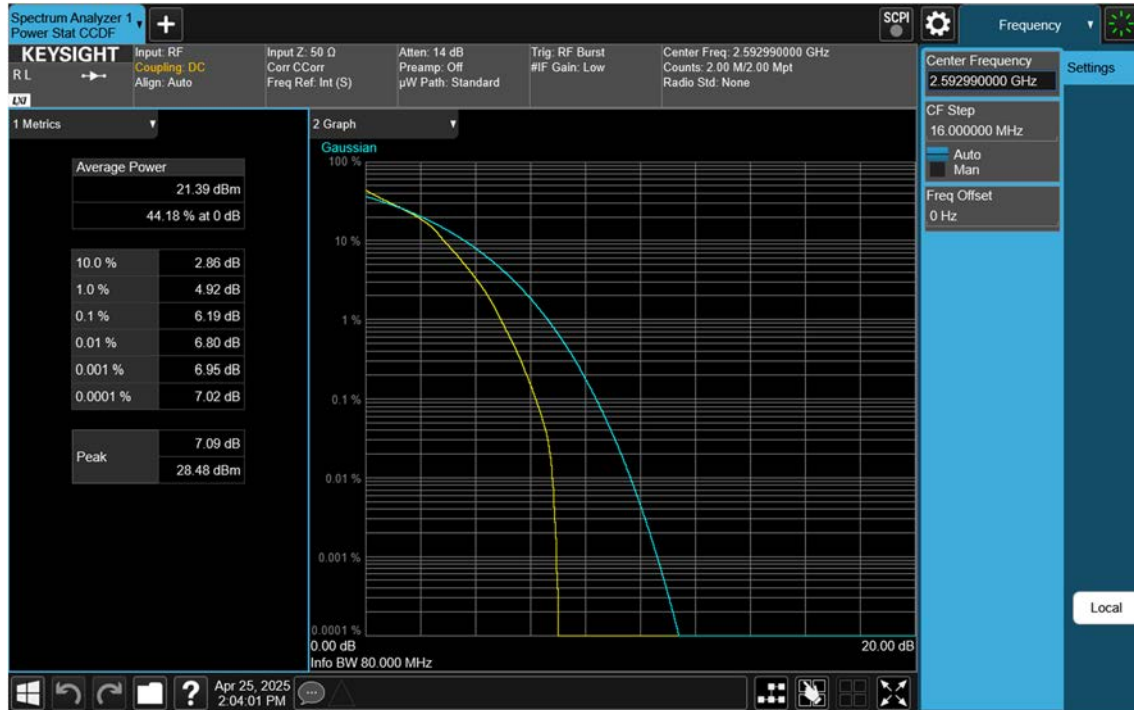
NR41_80 M_PAR_Mid_BPSK_FullRB



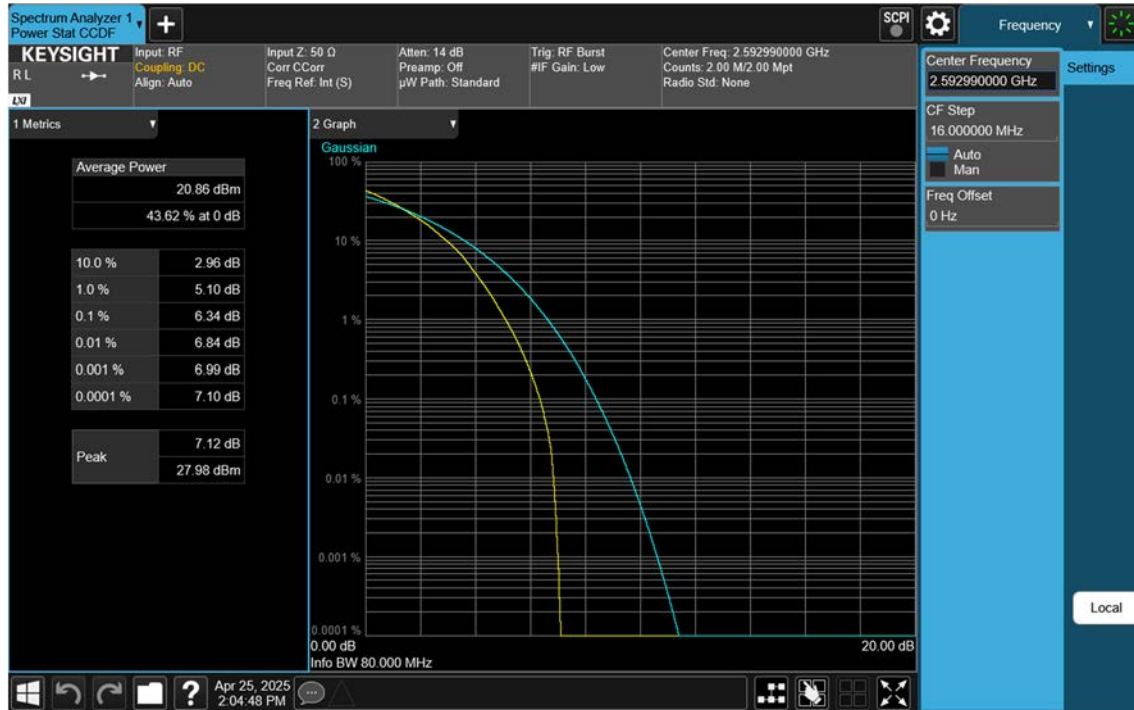
NR41_80 M_PAR_Mid_QPSK_FullRB



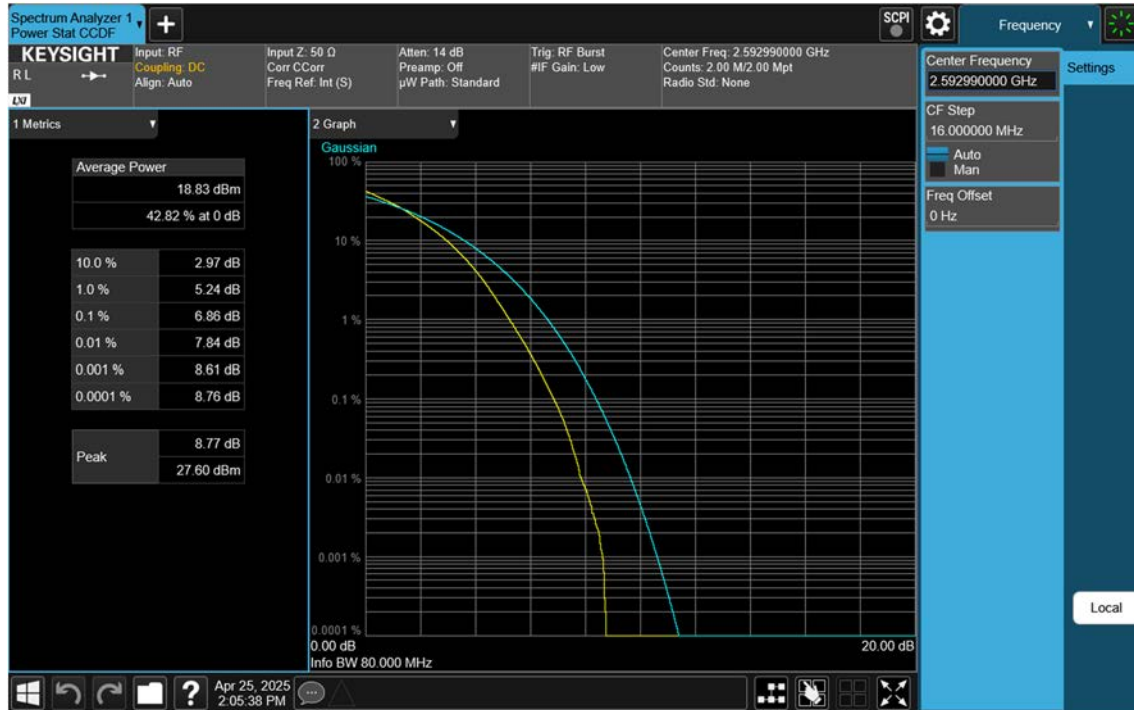
NR41_80 M_PAR_Mid_16QAM_FullRB



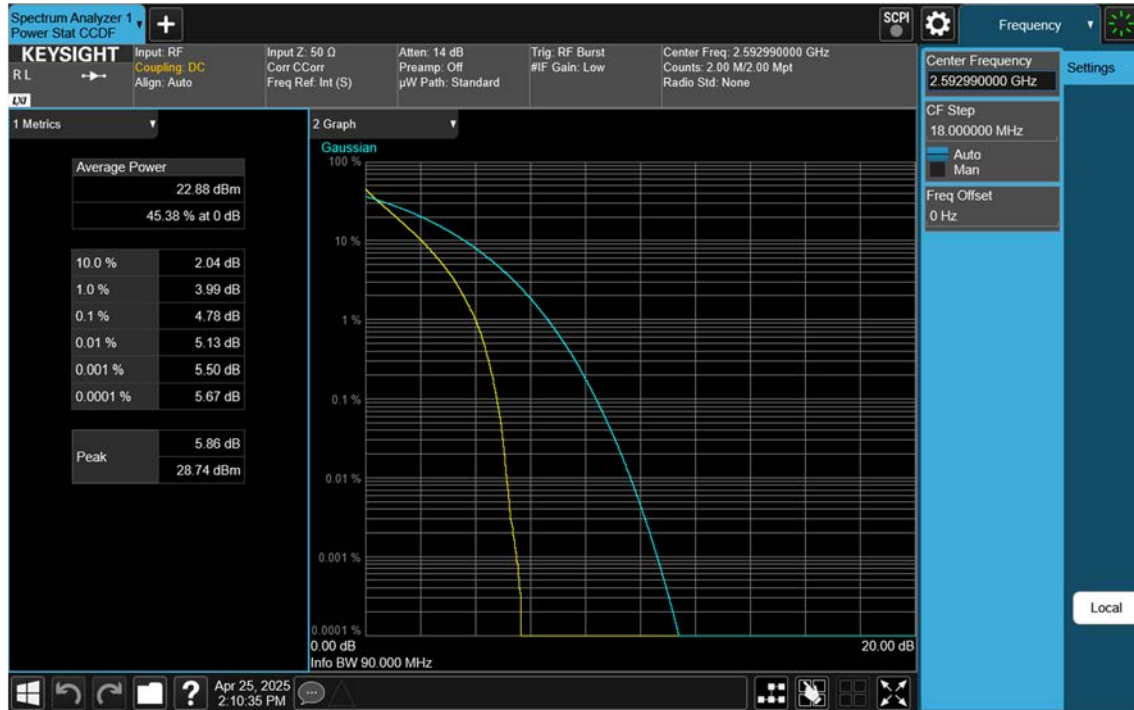
NR41_80 M_PAR_Mid_64QAM_FullRB



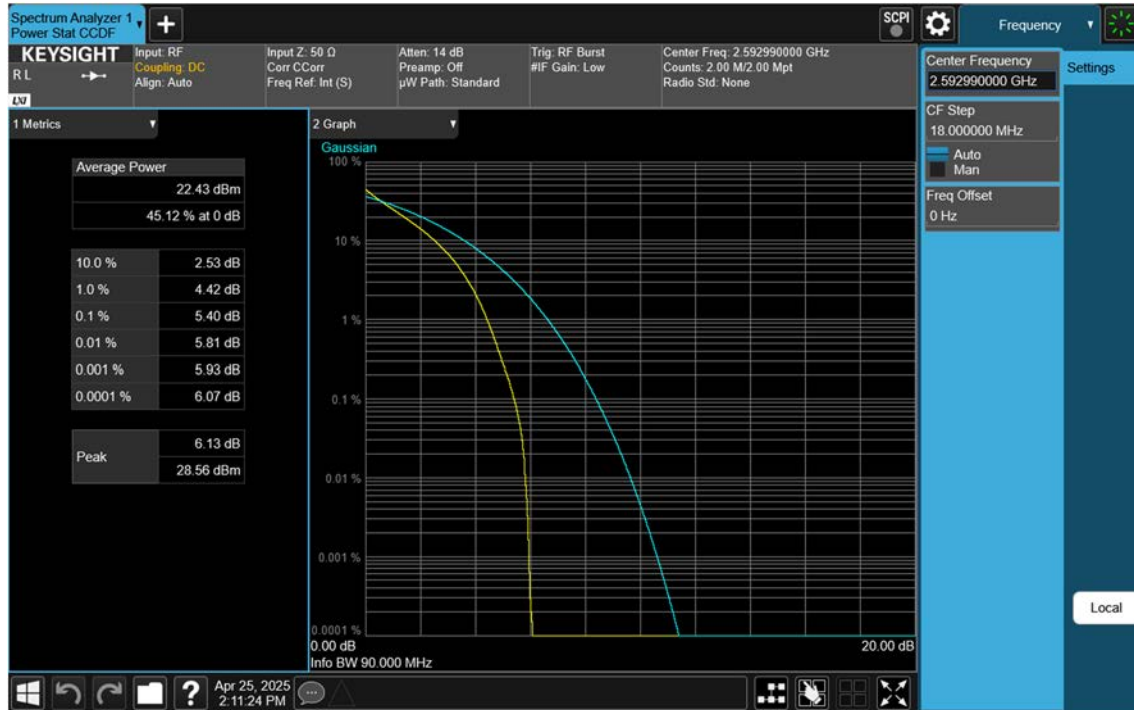
NR41_80 M_PAR_Mid_256QAM_FullRB



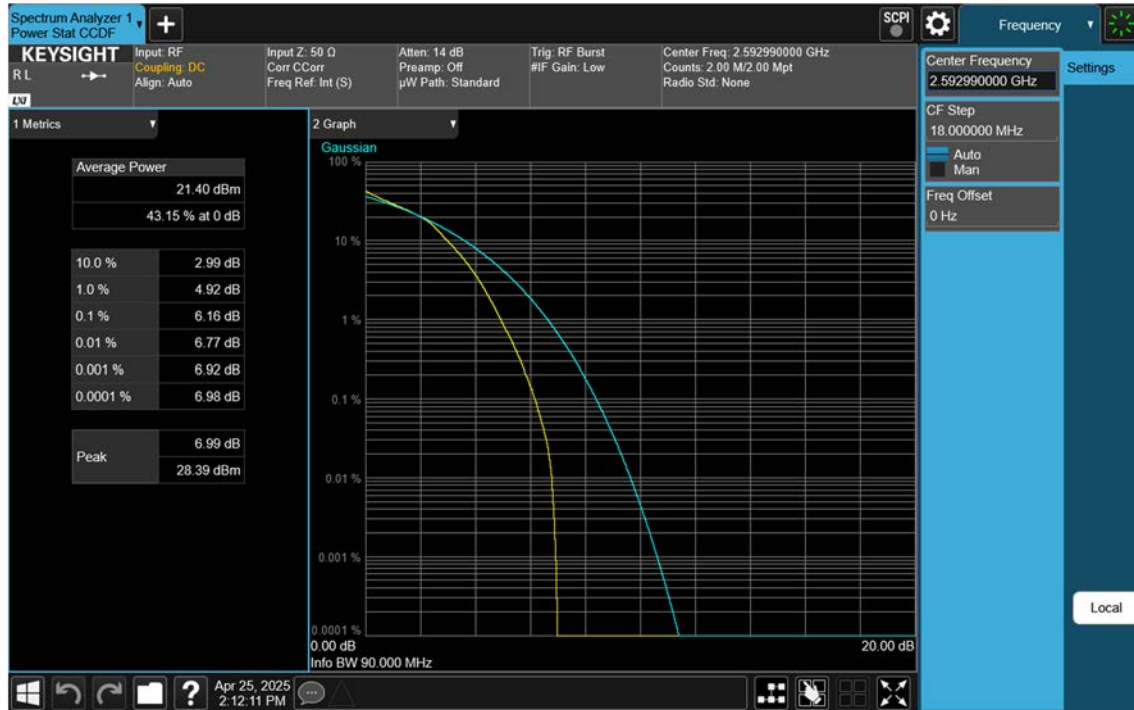
NR41_90 M_PAR_Mid_BPSK_FullRB



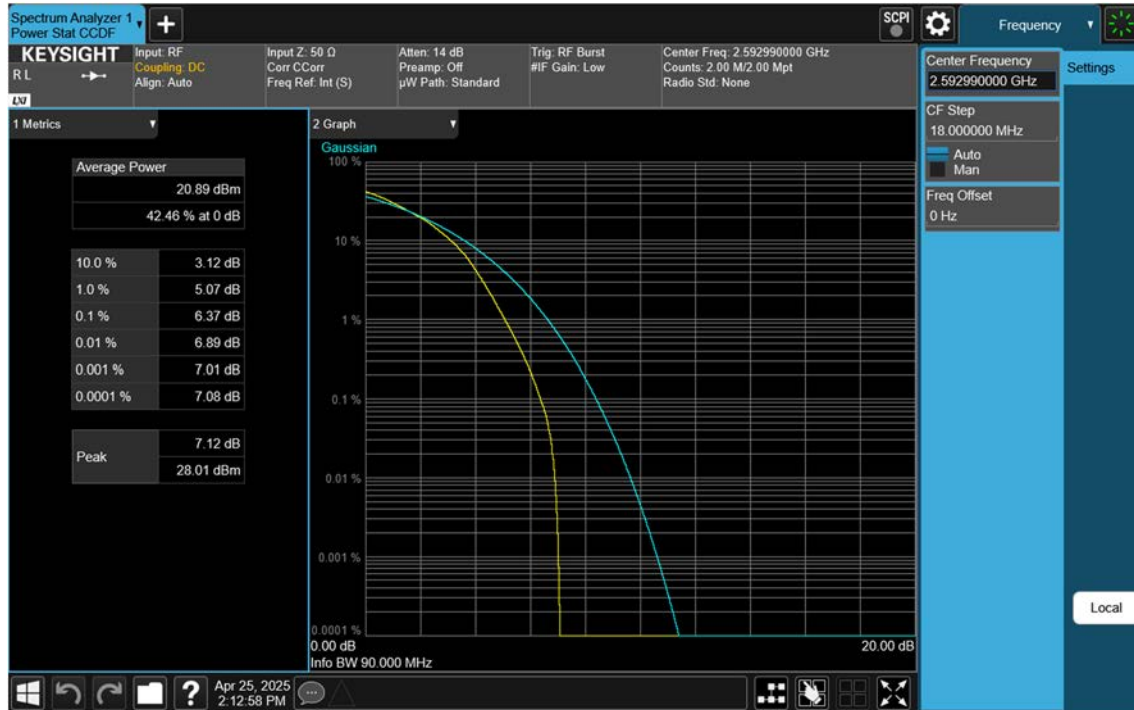
NR41_90 M_PAR_Mid_QPSK_FullRB



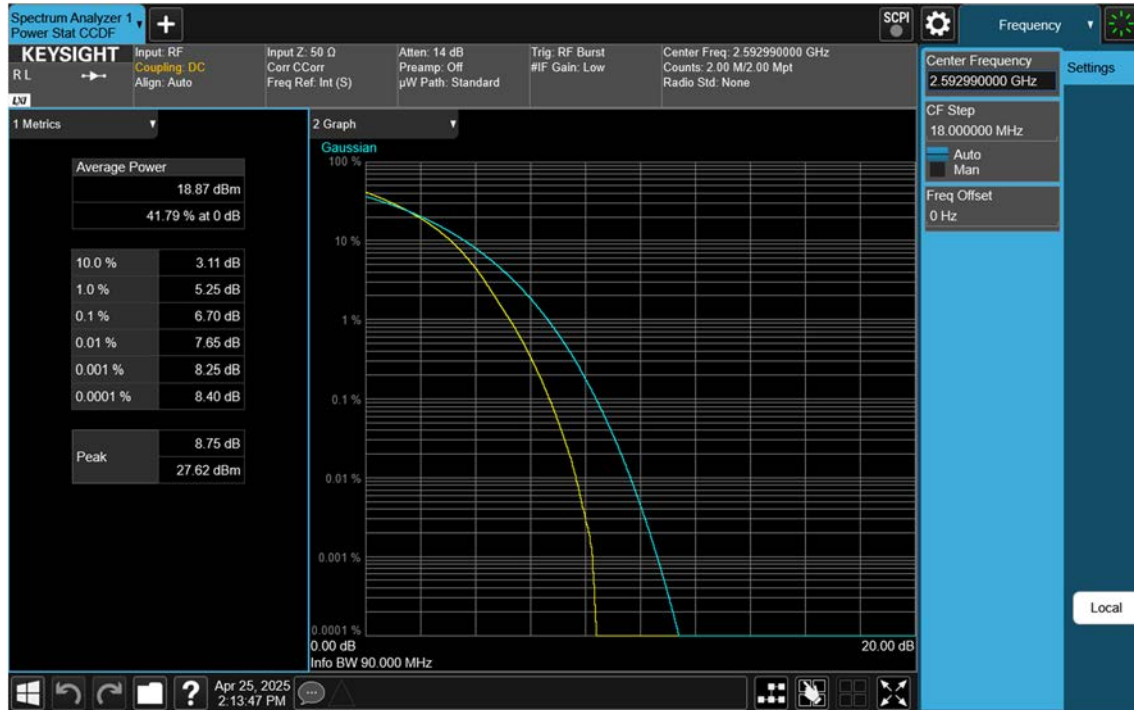
NR41_90 M_PAR_Mid_16QAM_FullRB



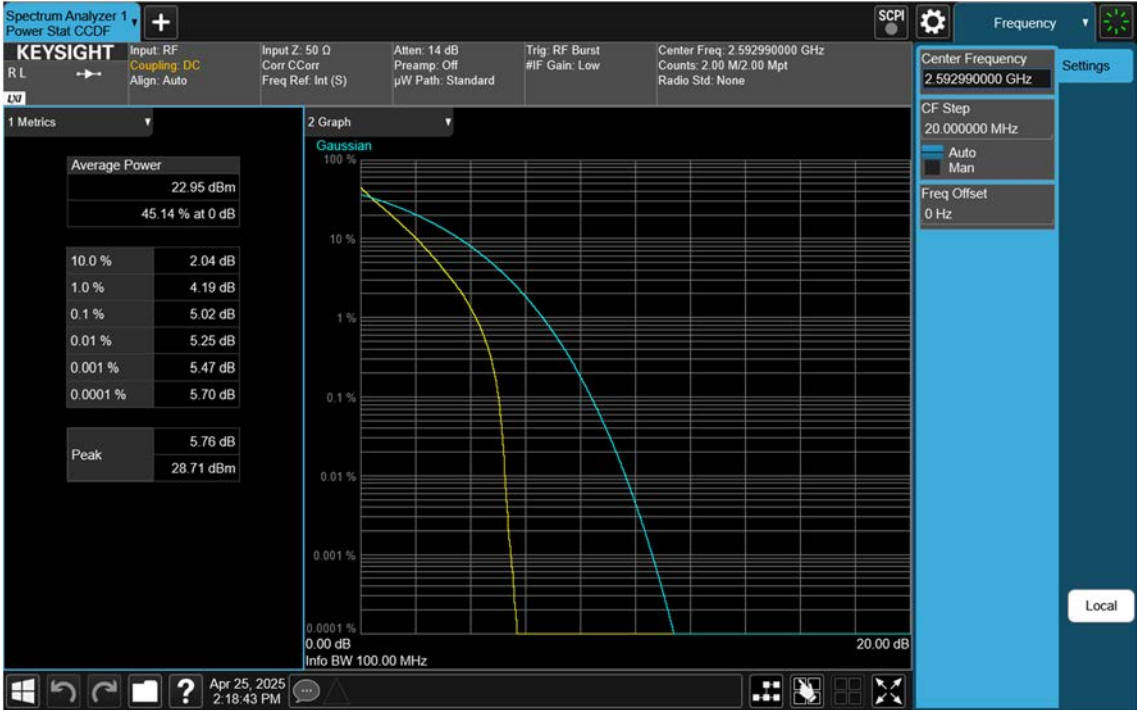
NR41_90 M_PAR_Mid_64QAM_FullRB



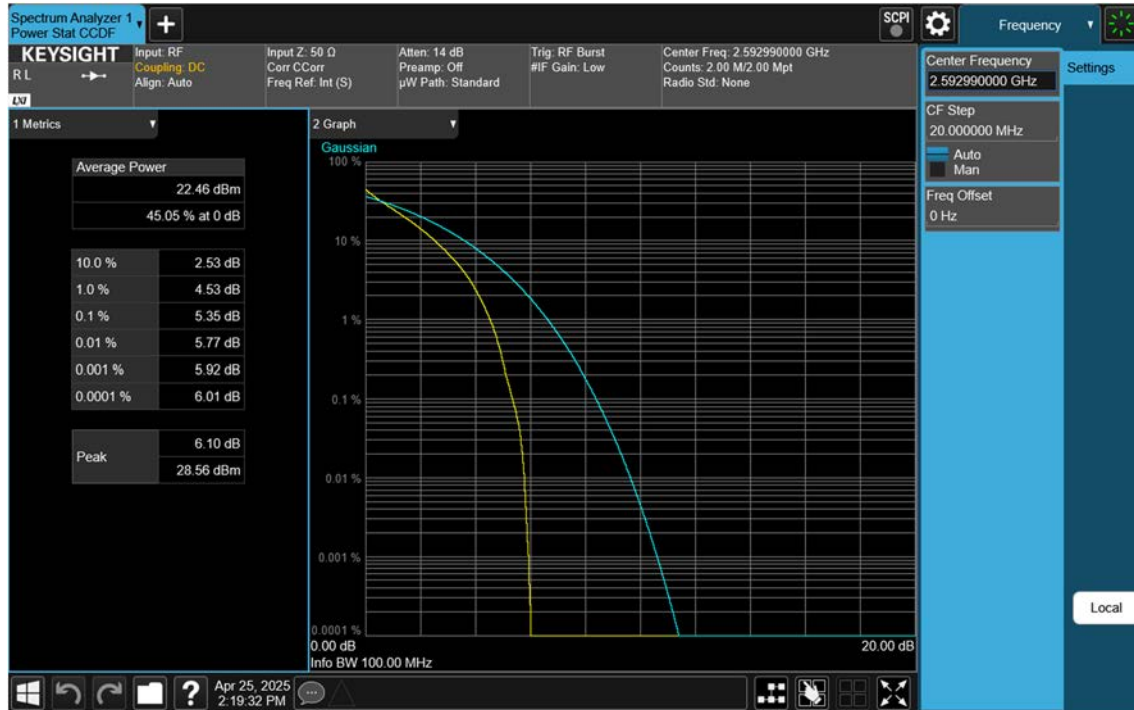
NR41_90 M_PAR_Mid_256QAM_FullRB



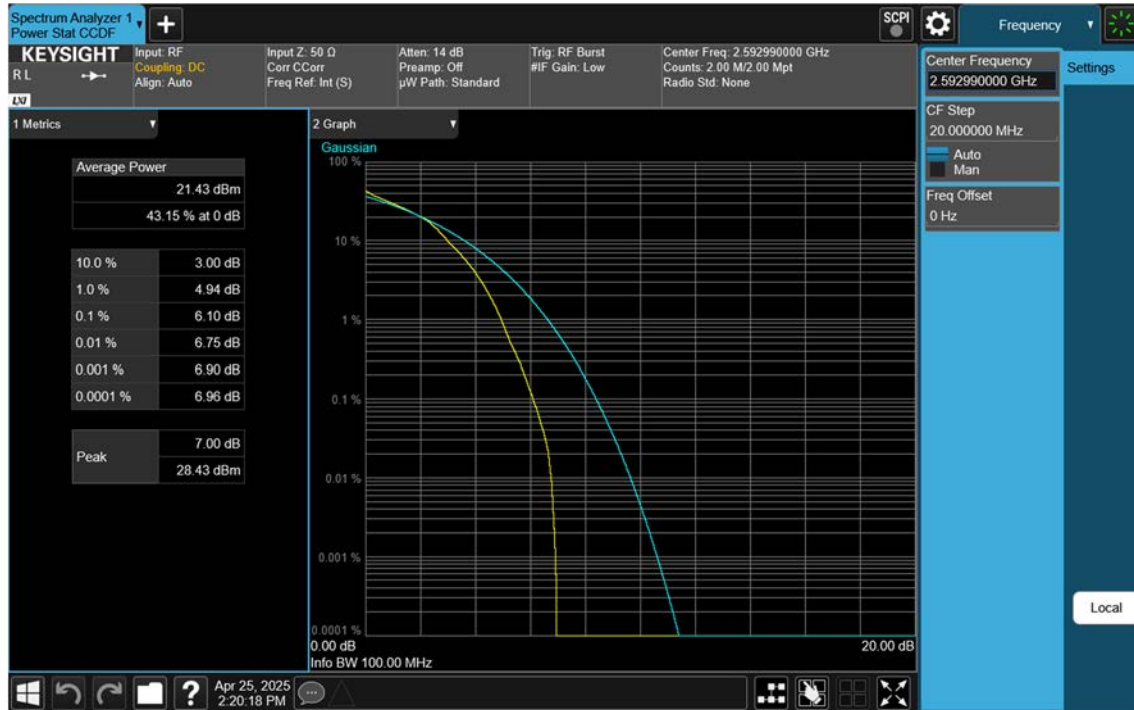
NR41_100 M_PAR_Mid_BPSK_FullRB



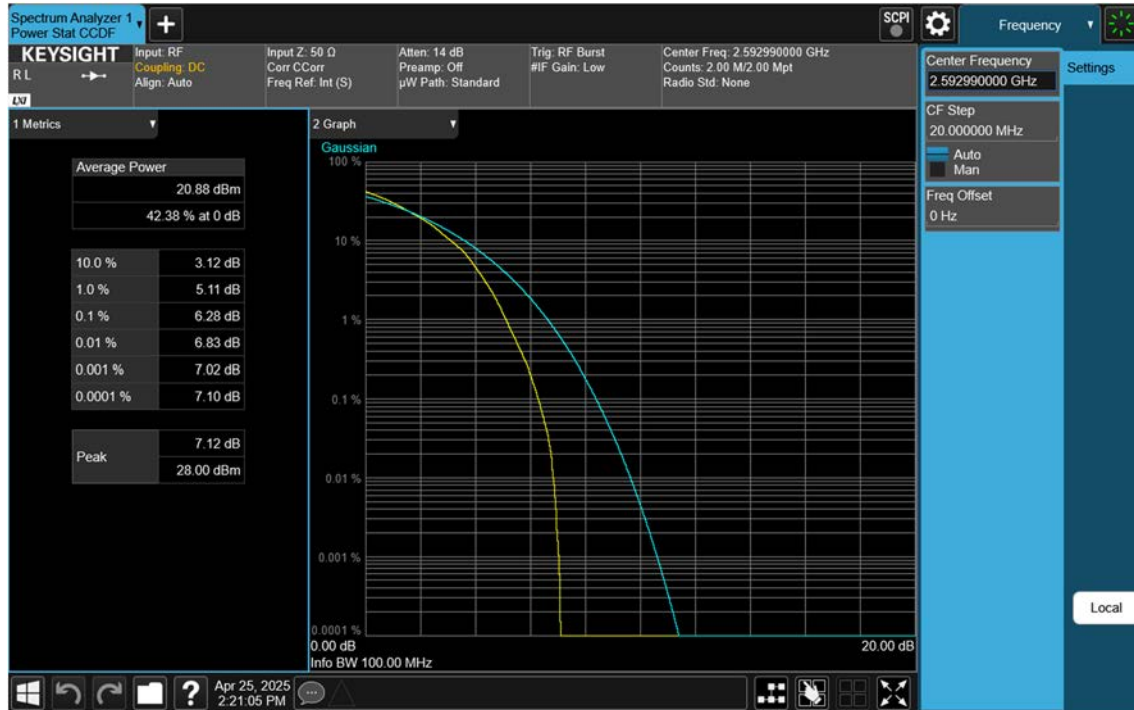
NR41_100 M_PAR_Mid_QPSK_FullRB



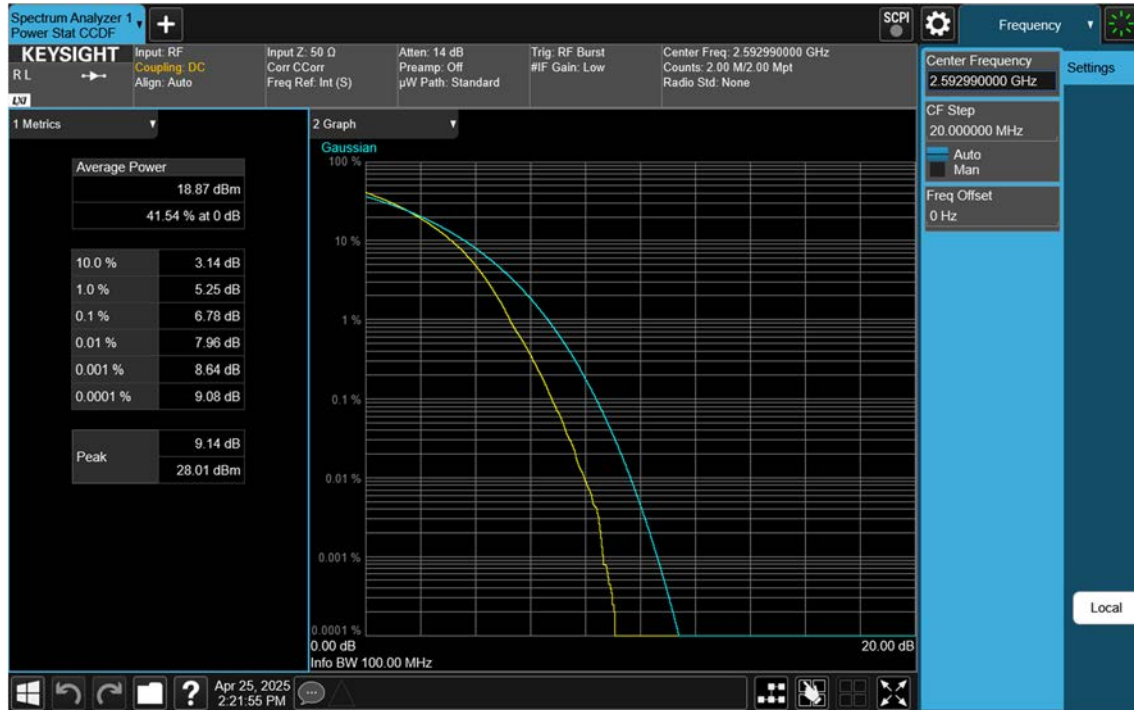
NR41_100 M_PAR_Mid_16QAM_FullRB



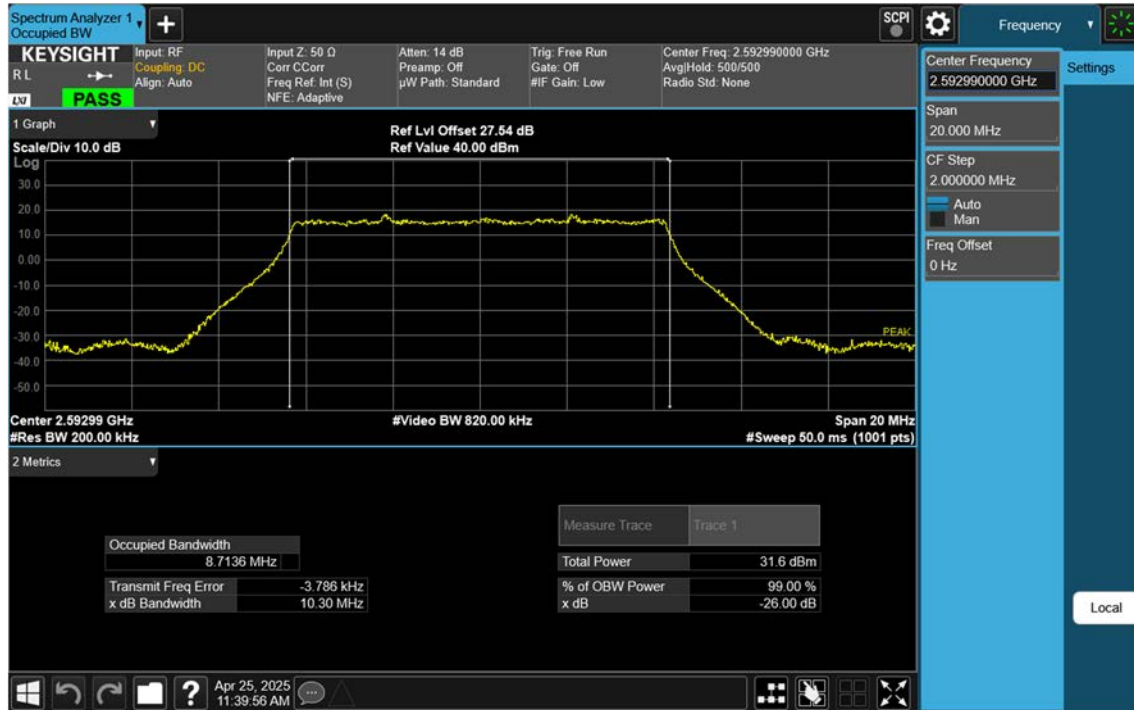
NR41_100 M_PAR_Mid_64QAM_FullRB



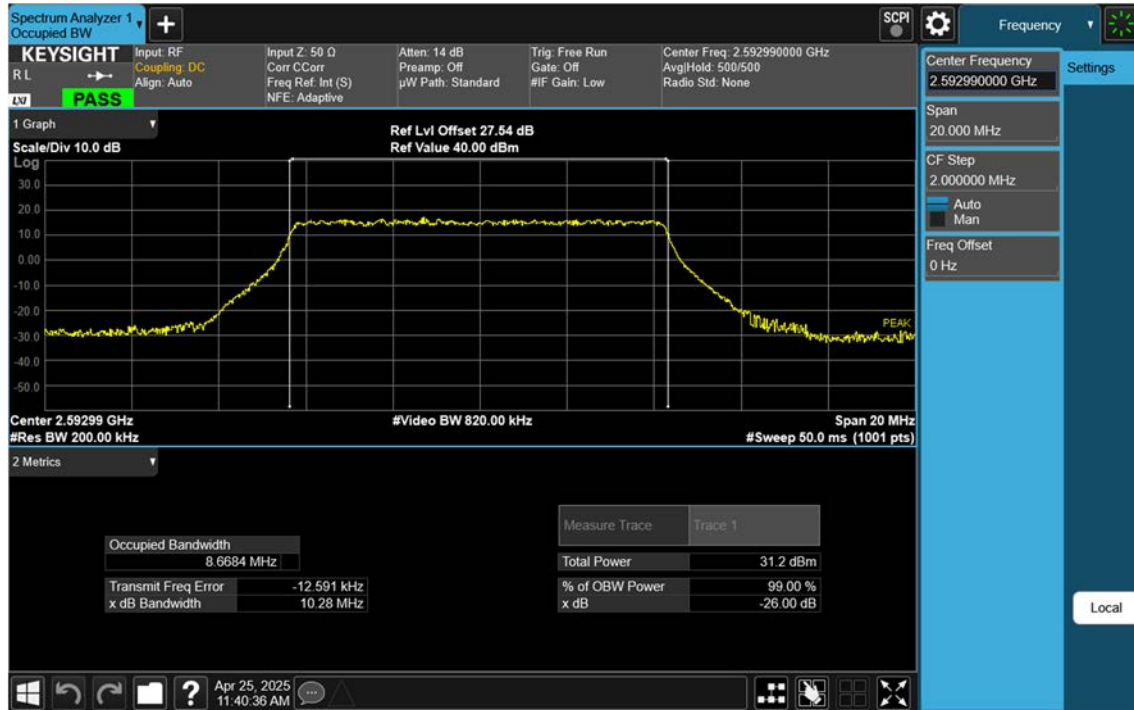
NR41_100 M_PAR_Mid_256QAM_FullRB



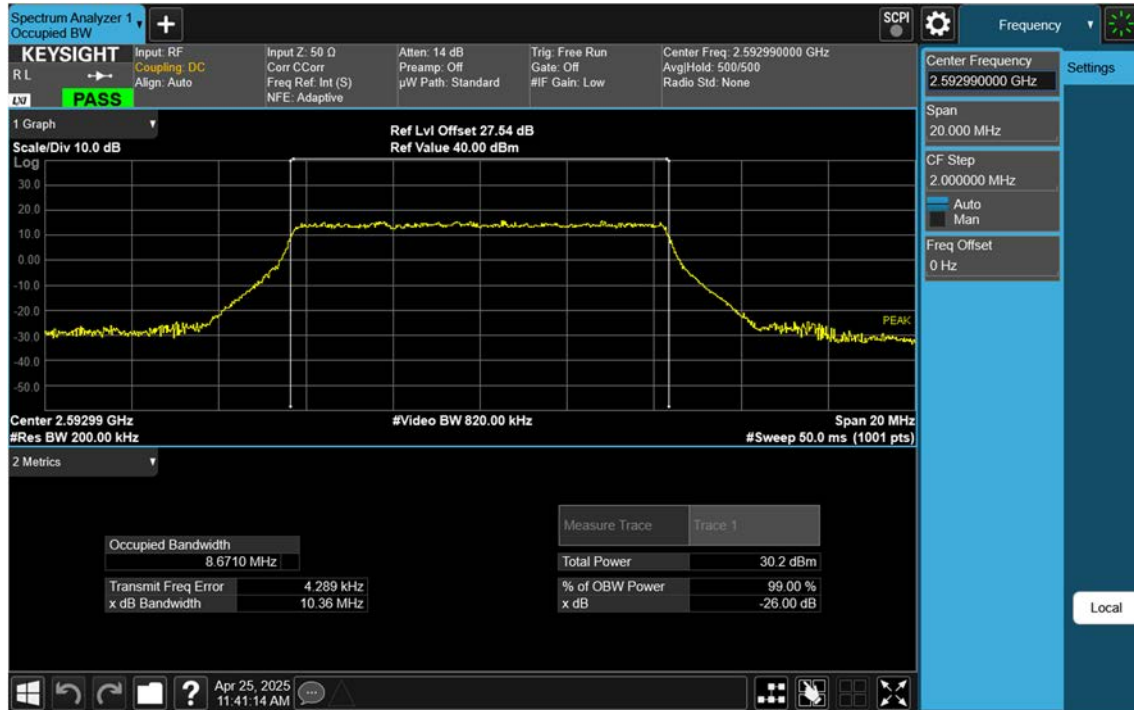
NR41_10 M_OBW_Mid_BPSK_FullRB



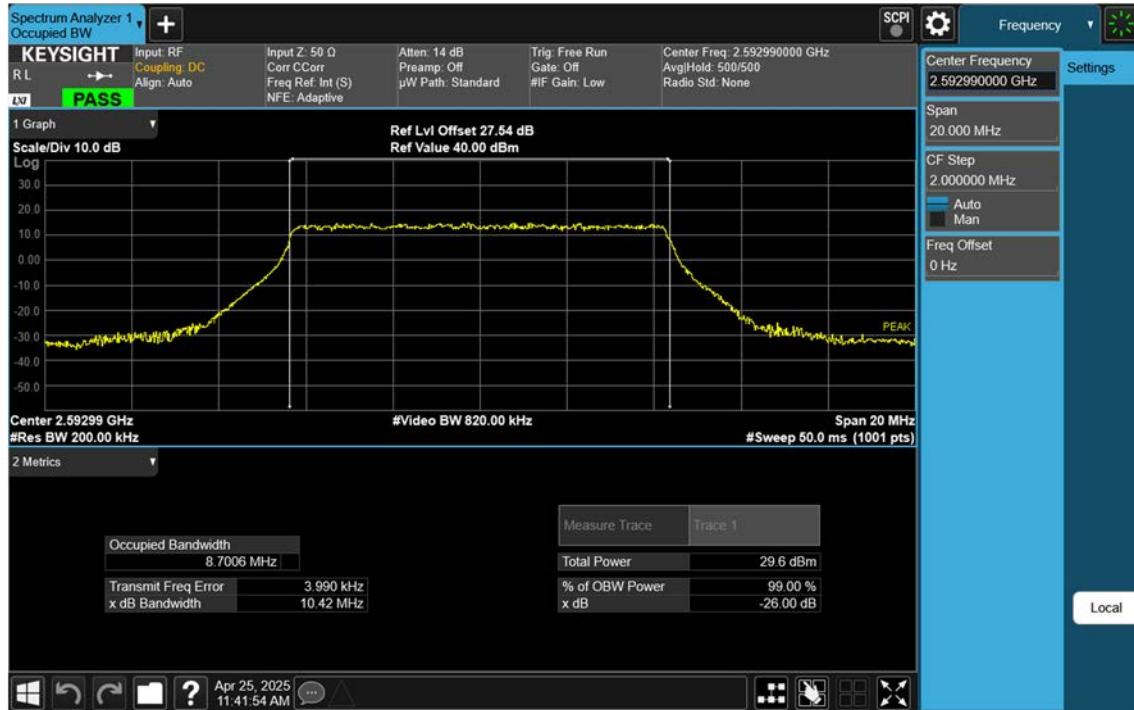
NR41_10 M_OBW_Mid_QPSK_FullRB



NR41_10 M_OBW_Mid_16QAM_FullRB



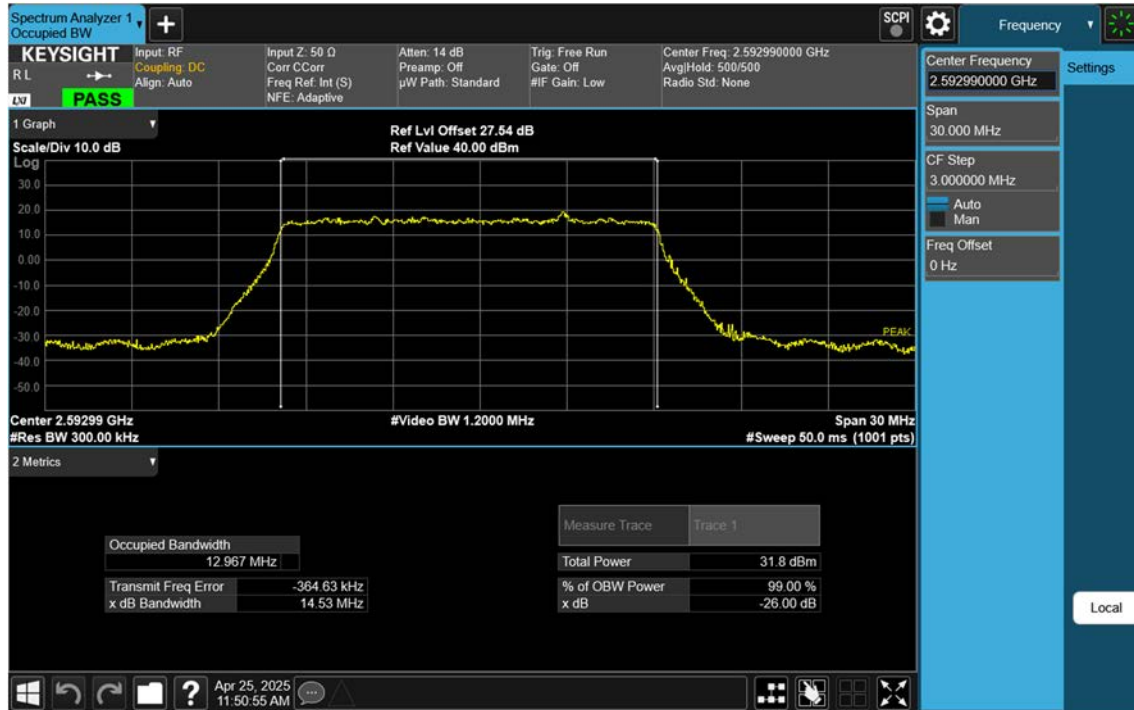
NR41_10 M_OBW_Mid_64QAM_FullRB



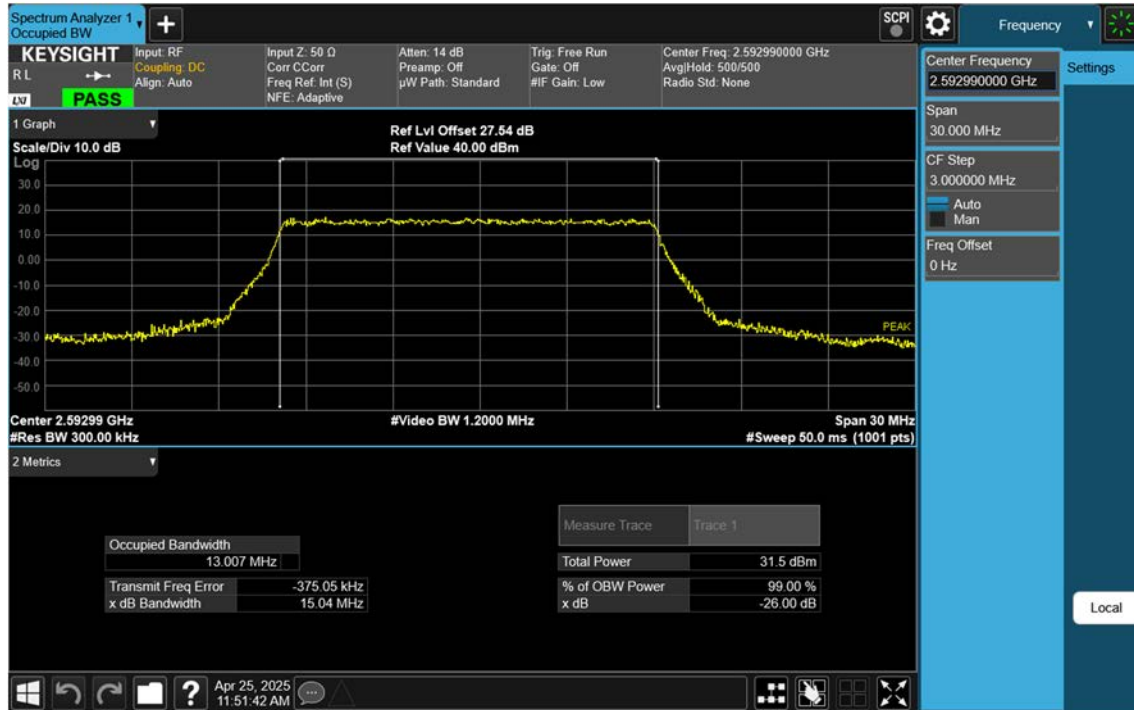
NR41_10 M_OBW_Mid_256QAM_FullRB



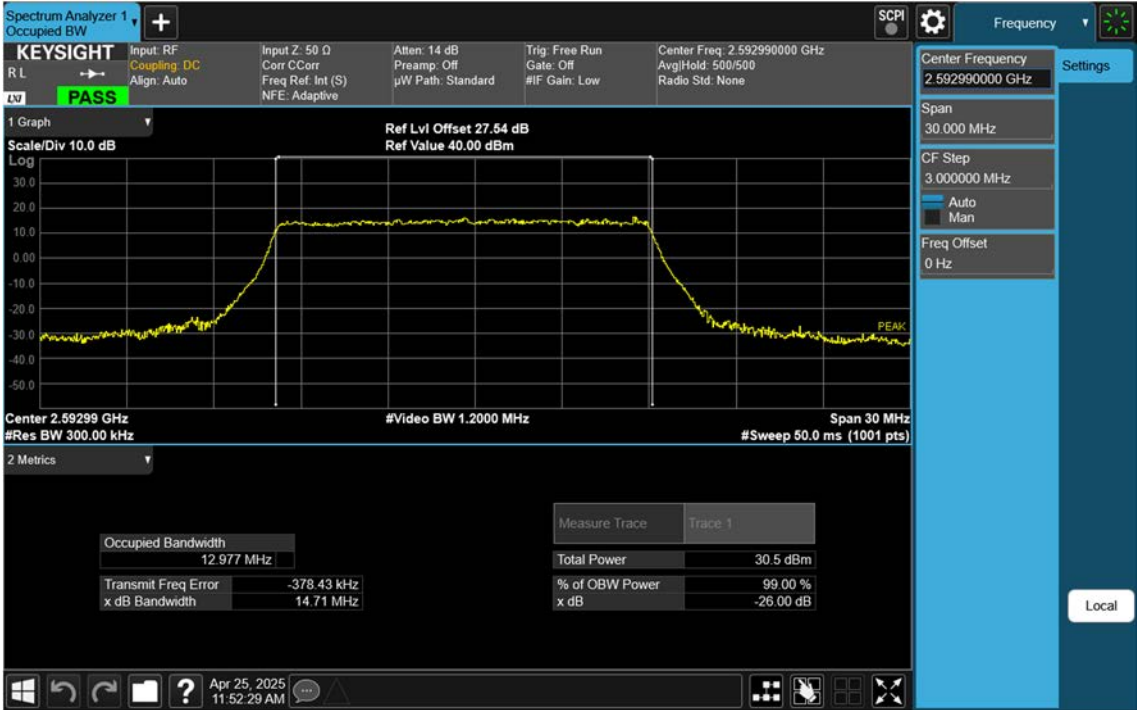
NR41_15 M_OBW_Mid_BPSK_FullRB



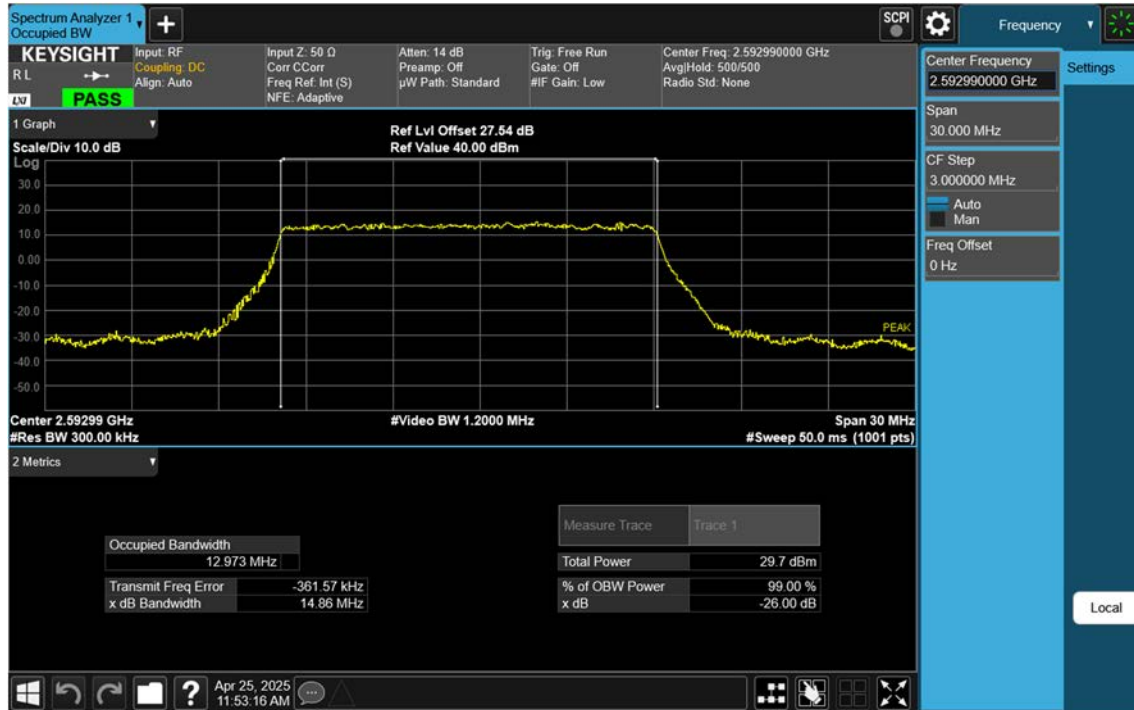
NR41_15 M_OBW_Mid_QPSK_FullRB



NR41_15 M_OBW_Mid_16QAM_FullRB



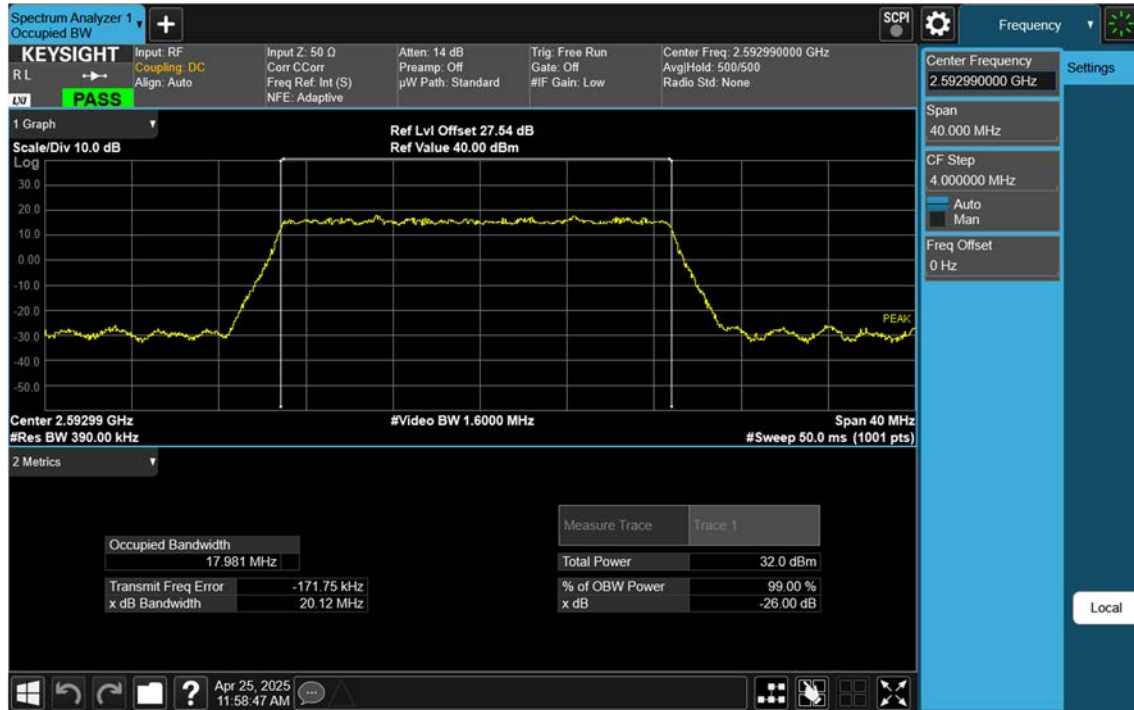
NR41_15 M_OBW_Mid_64QAM_FullRB



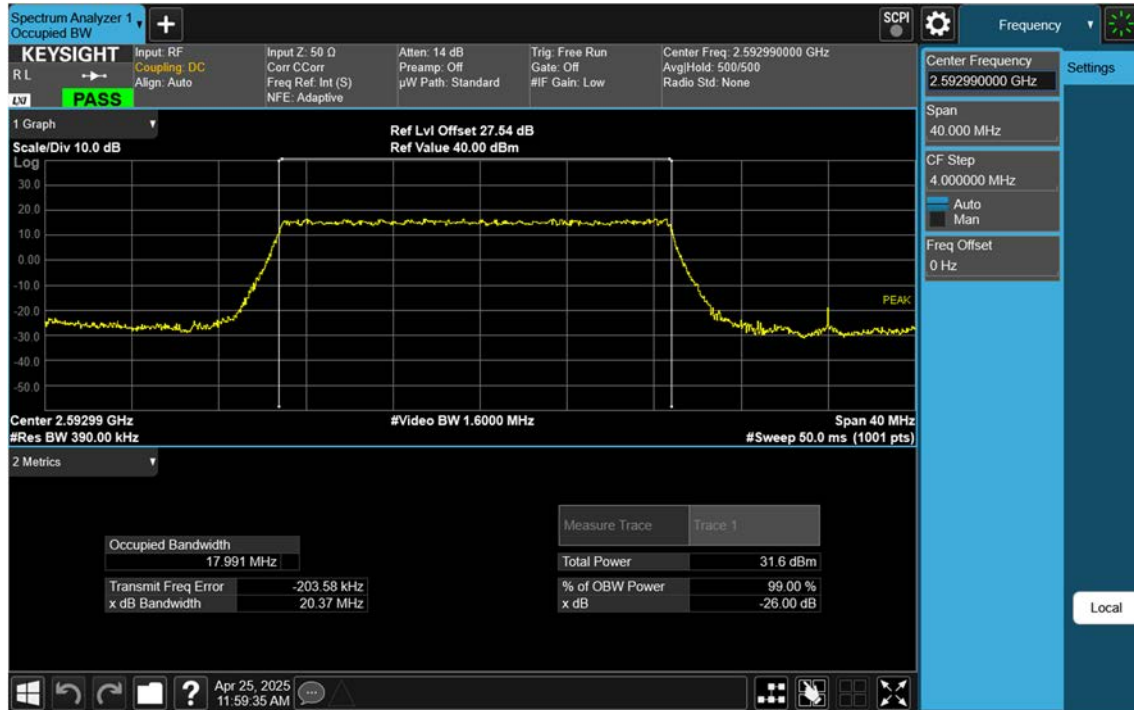
NR41_15 M_OBW_Mid_256QAM_FullRB



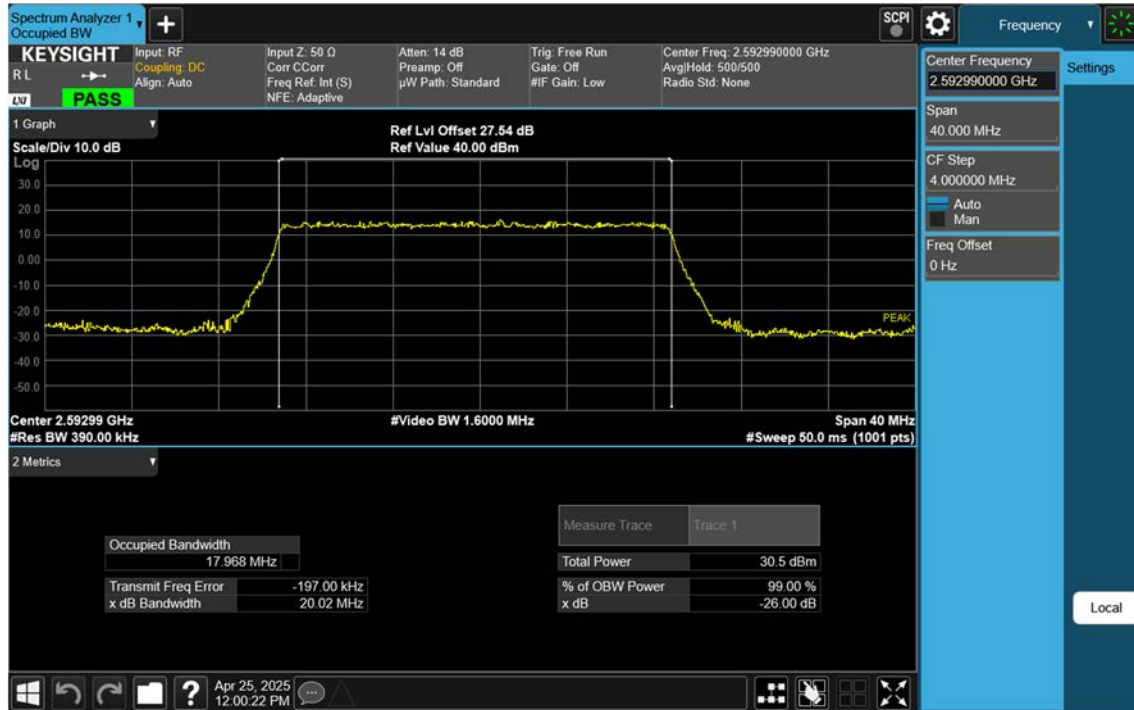
NR41_20 M_OBW_Mid_BPSK_FullRB



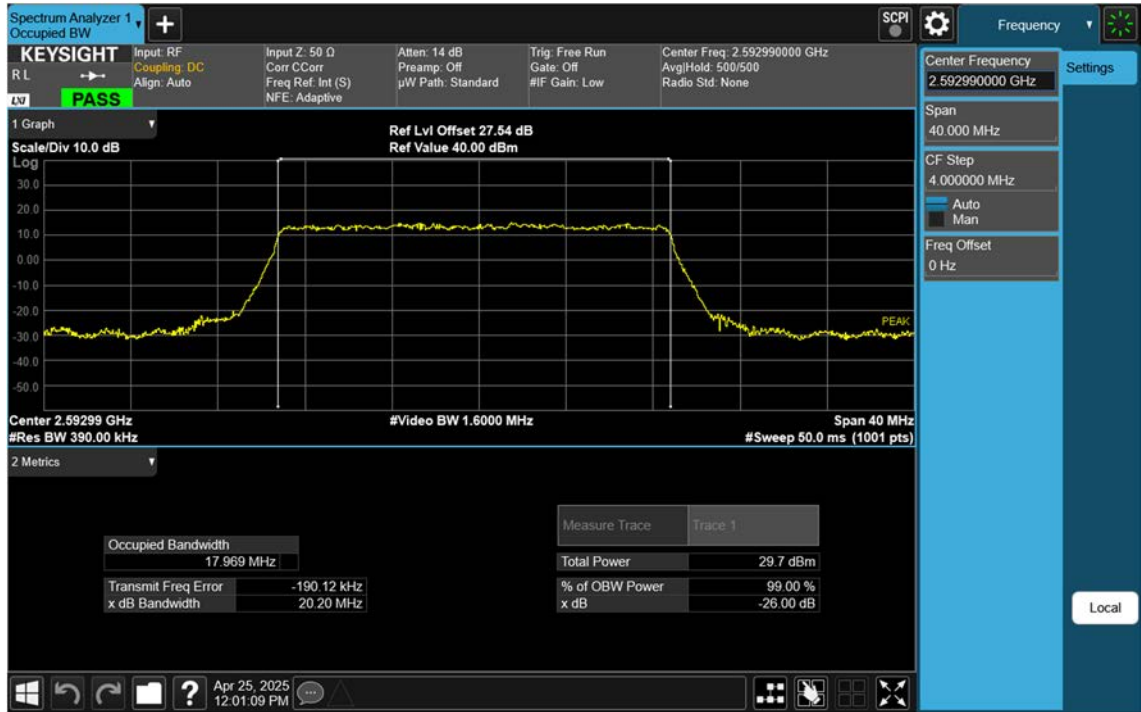
NR41_20 M_OBW_Mid_QPSK_FullRB



NR41_20 M_OBW_Mid_16QAM_FullRB



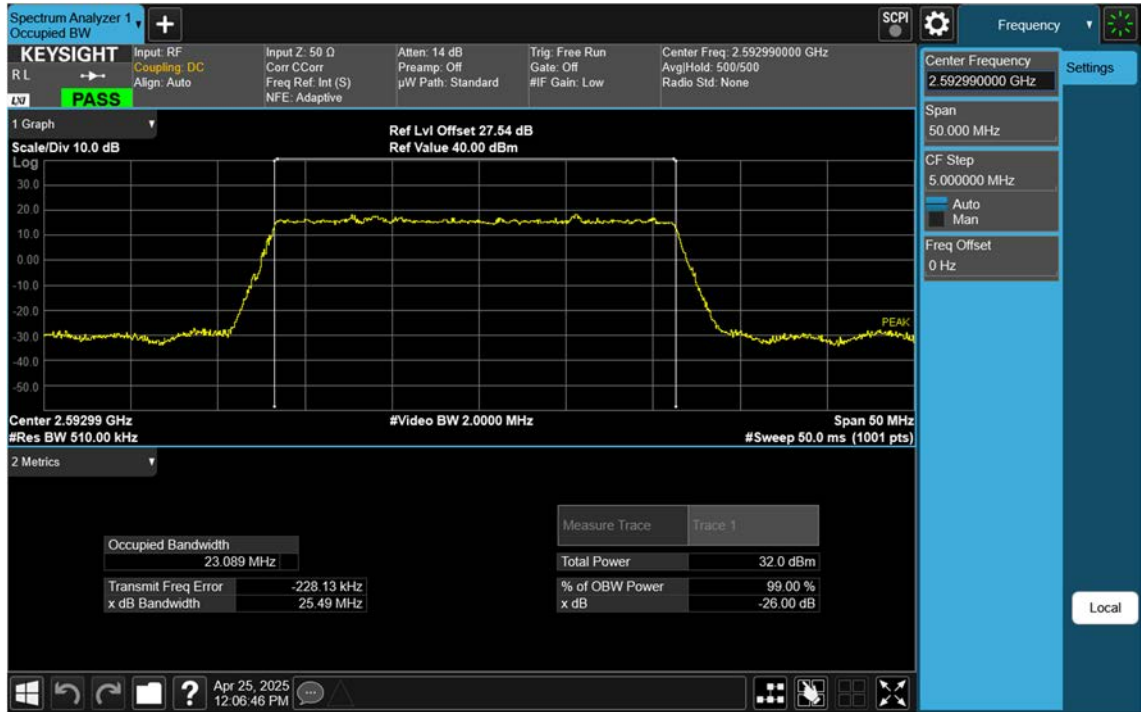
NR41_20 M_OBW_Mid_64QAM_FullRB



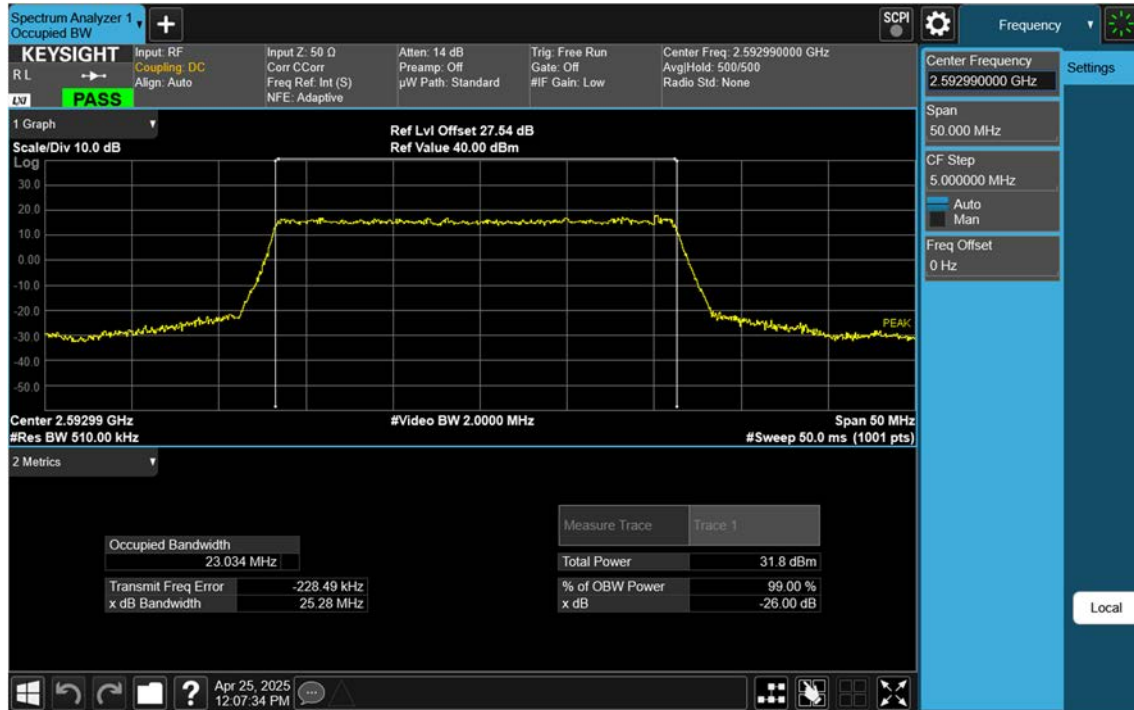
NR41_20 M_OBW_Mid_256QAM_FullRB



NR41_25 M_OBW_Mid_BPSK_FullRB



NR41_25 M_OBW_Mid_QPSK_FullRB



NR41_25 M_OBW_Mid_16QAM_FullRB



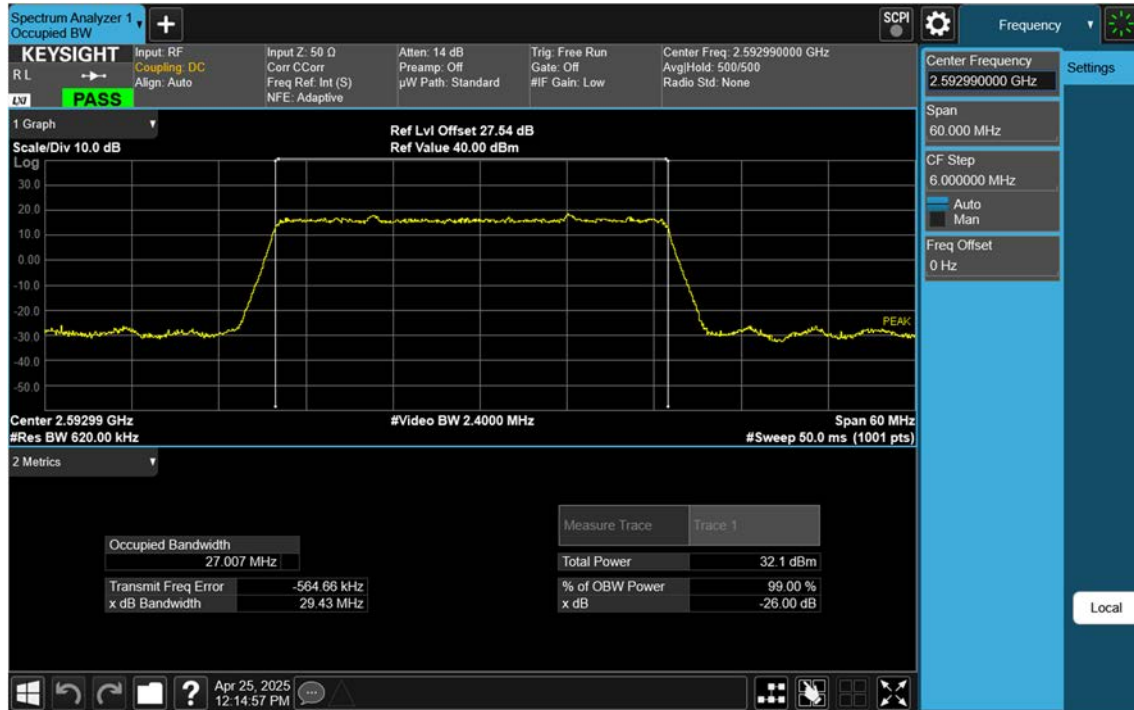
NR41_25 M_OBW_Mid_64QAM_FullRB



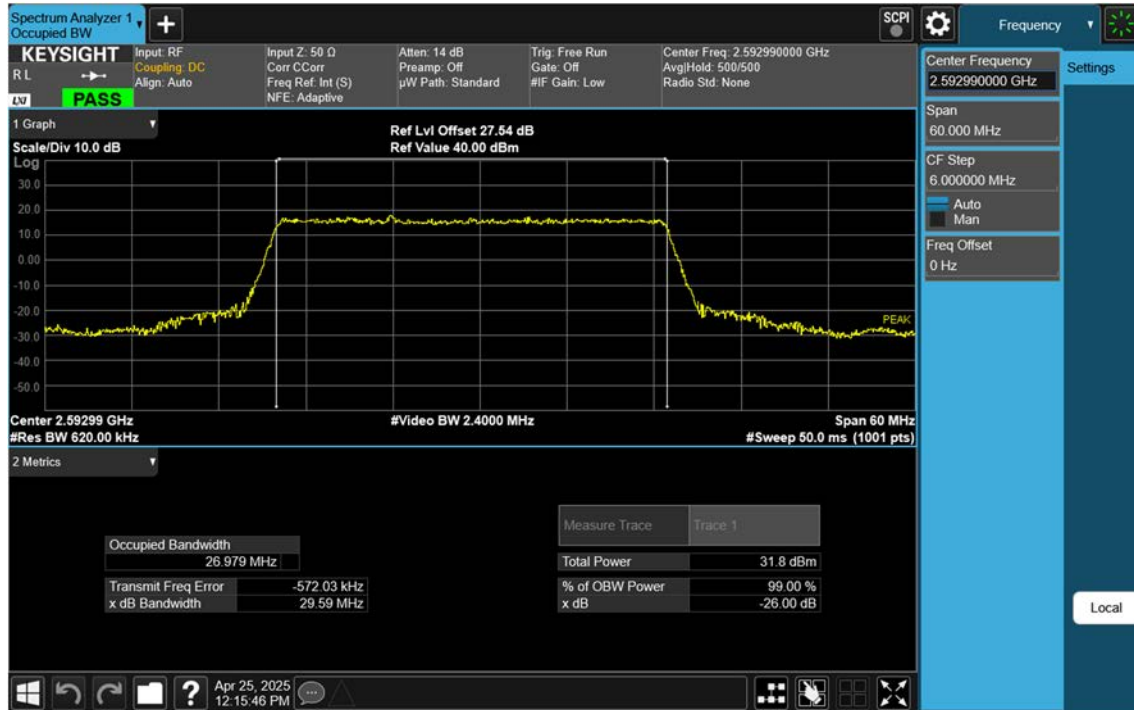
NR41_25 M_OBW_Mid_256QAM_FullRB



NR41_30 M_OBW_Mid_BPSK_FullRB



NR41_30 M_OBW_Mid_QPSK_FullRB



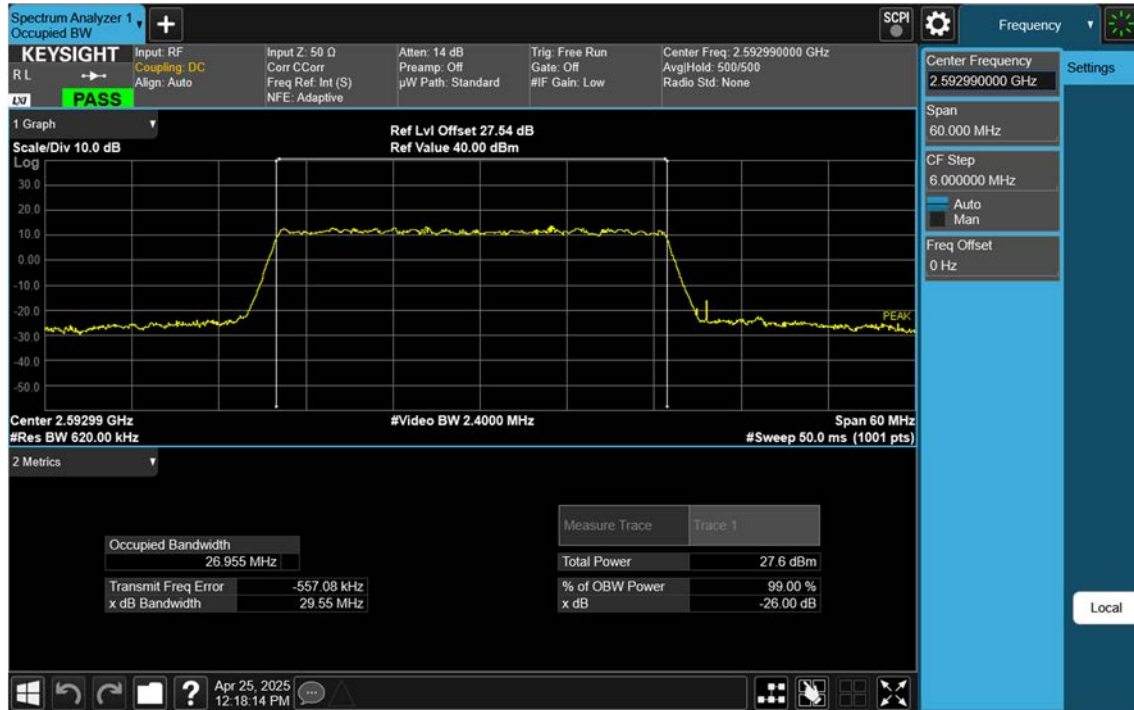
NR41_30 M_OBW_Mid_16QAM_FullRB



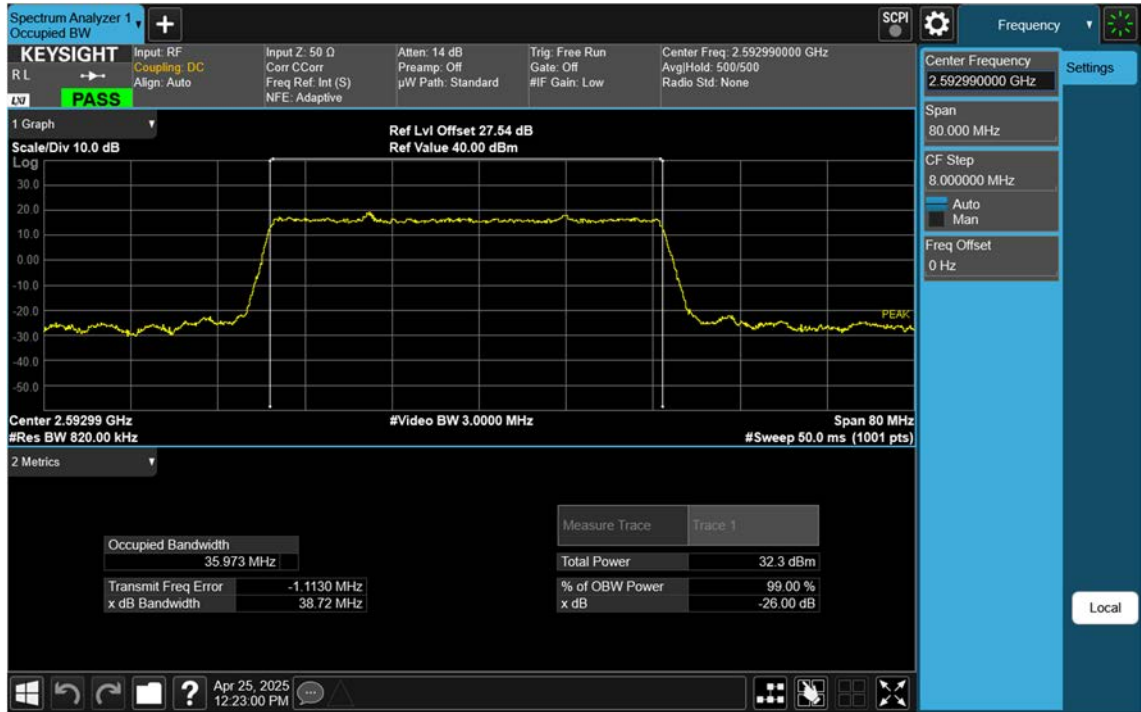
NR41_30 M_OBW_Mid_64QAM_FullRB



NR41_30 M_OBW_Mid_256QAM_FullRB



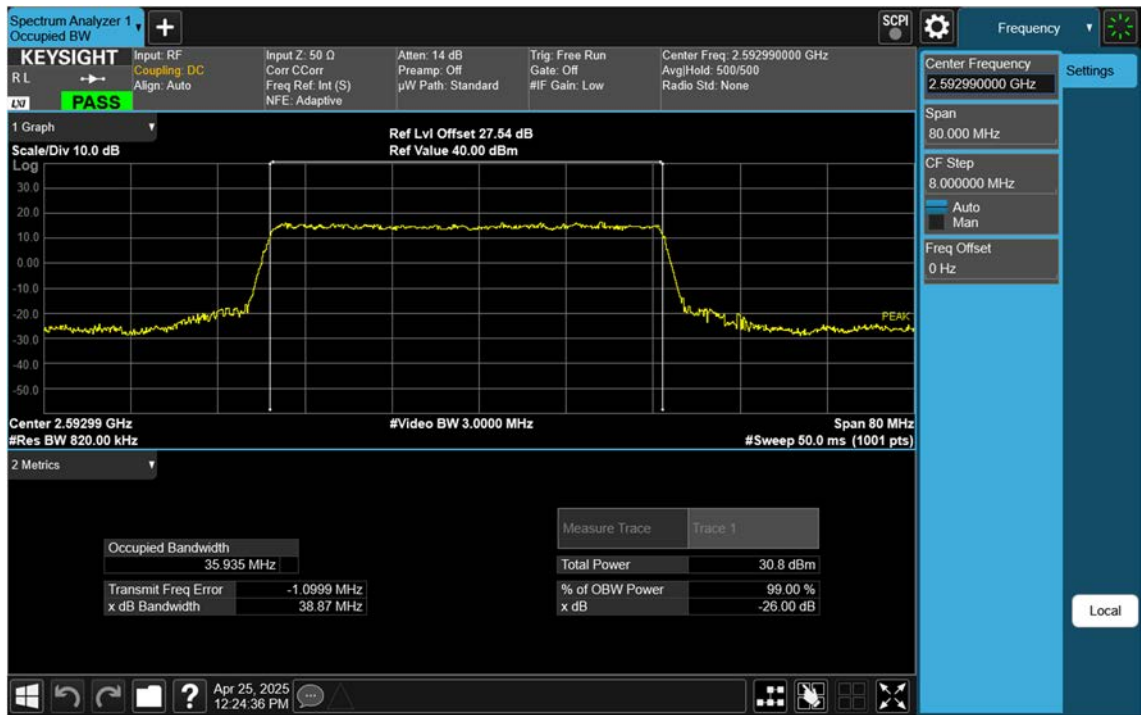
NR41_40 M_OBW_Mid_BPSK_FullRB



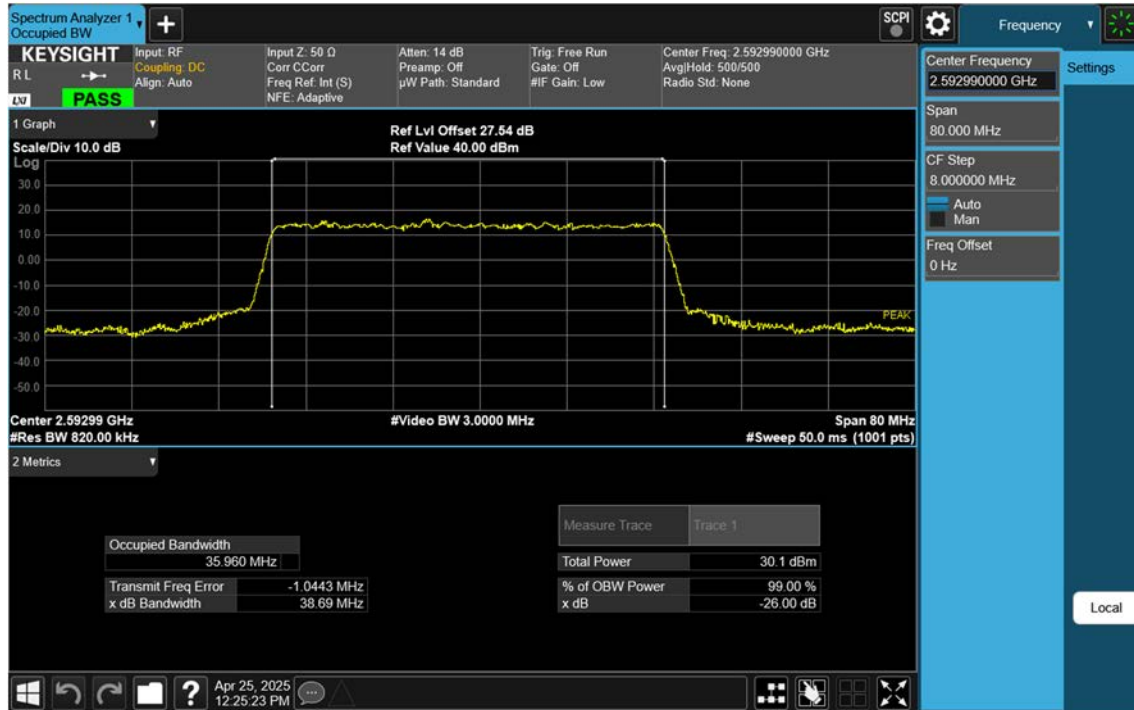
NR41_40 M_OBW_Mid_QPSK_FullRB



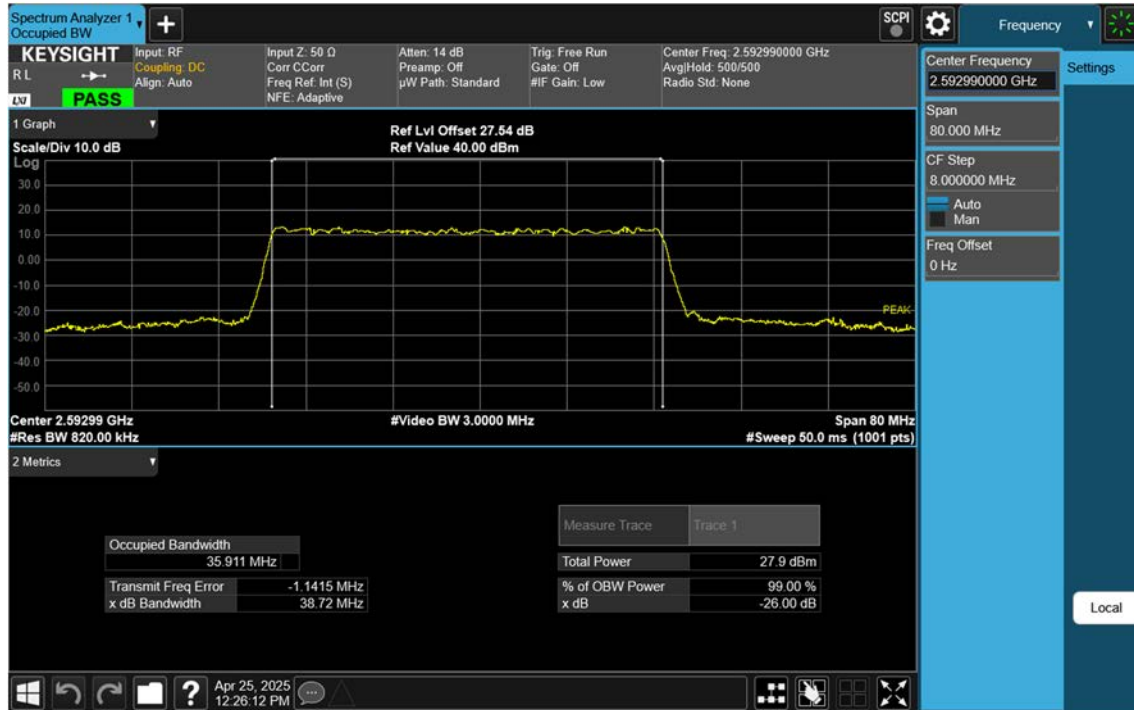
NR41_40 M_OBW_Mid_16QAM_FullRB



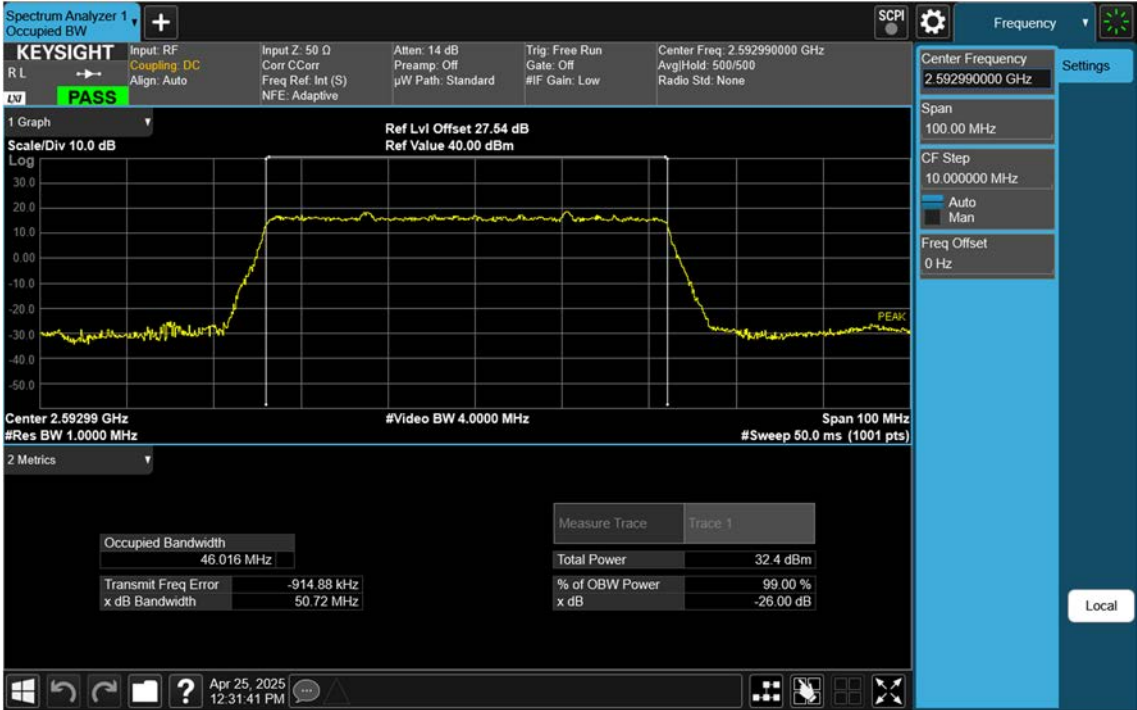
NR41_40 M_OBW_Mid_64QAM_FullRB



NR41_40 M_OBW_Mid_256QAM_FullRB



NR41_50 M_OBW_Mid_BPSK_FullRB



NR41_50 M_OBW_Mid_QPSK_FullRB



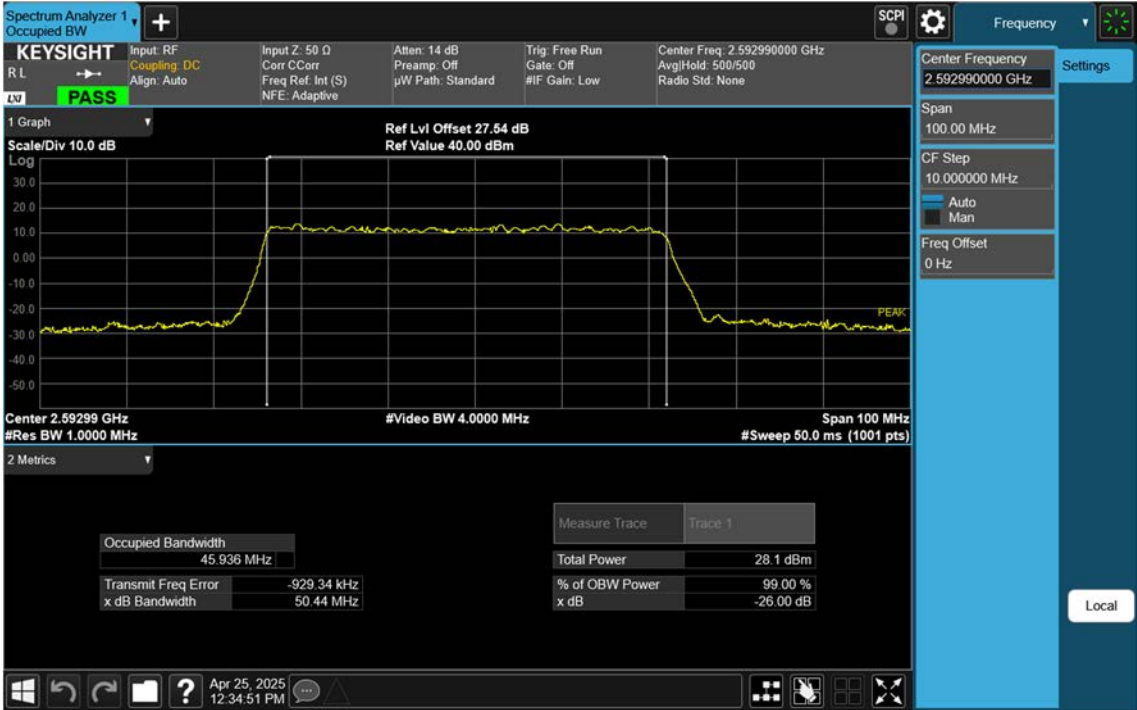
NR41_50 M_OBW_Mid_16QAM_FullRB



NR41_50 M_OBW_Mid_64QAM_FullRB



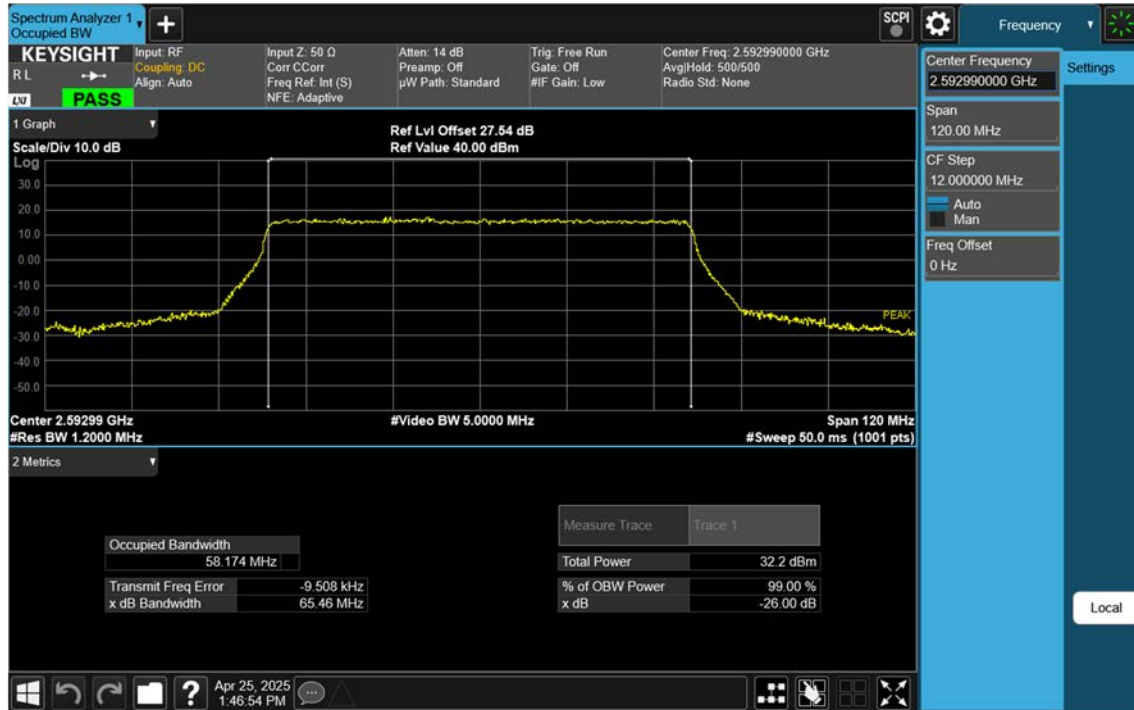
NR41_50 M_OBW_Mid_256QAM_FullRB



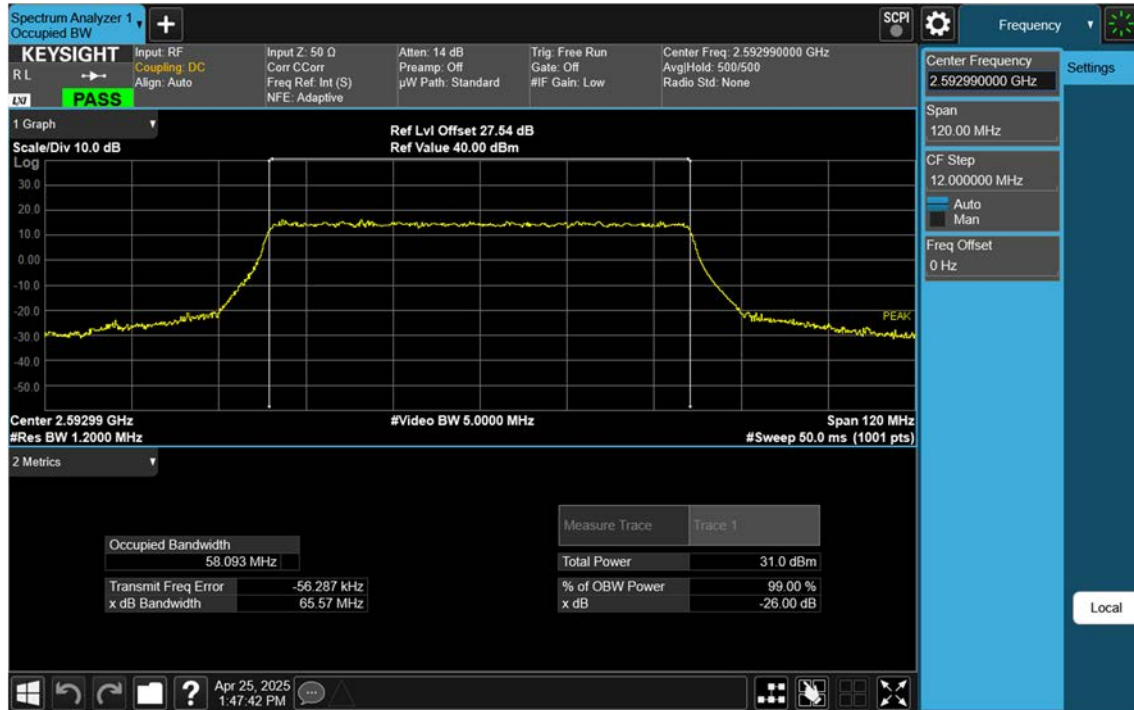
NR41_60 M_OBW_Mid_BPSK_FullRB



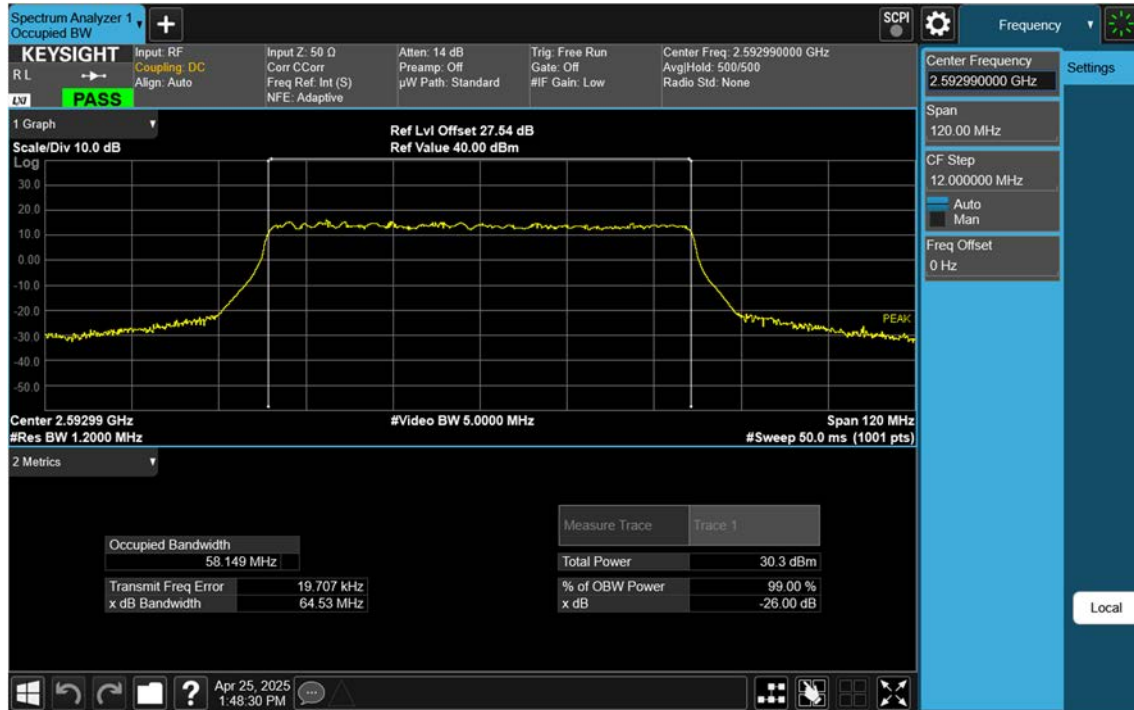
NR41_60 M_OBW_Mid_QPSK_FullRB



NR41_60 M_OBW_Mid_16QAM_FullRB



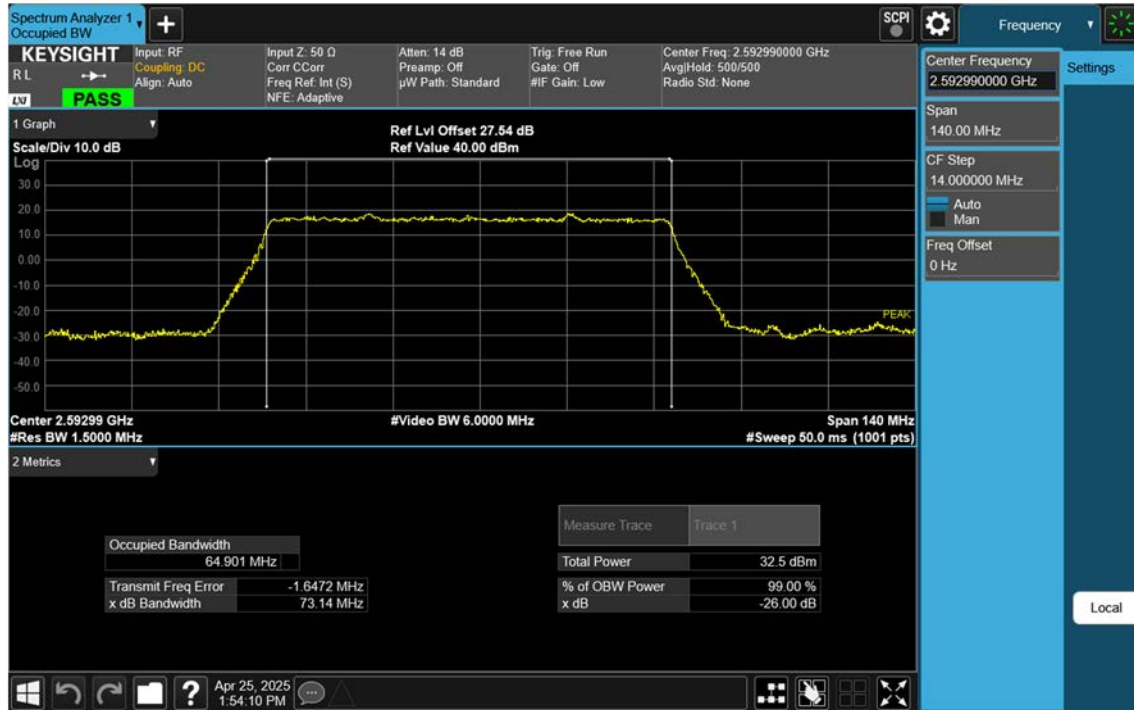
NR41_60 M_OBW_Mid_64QAM_FullRB



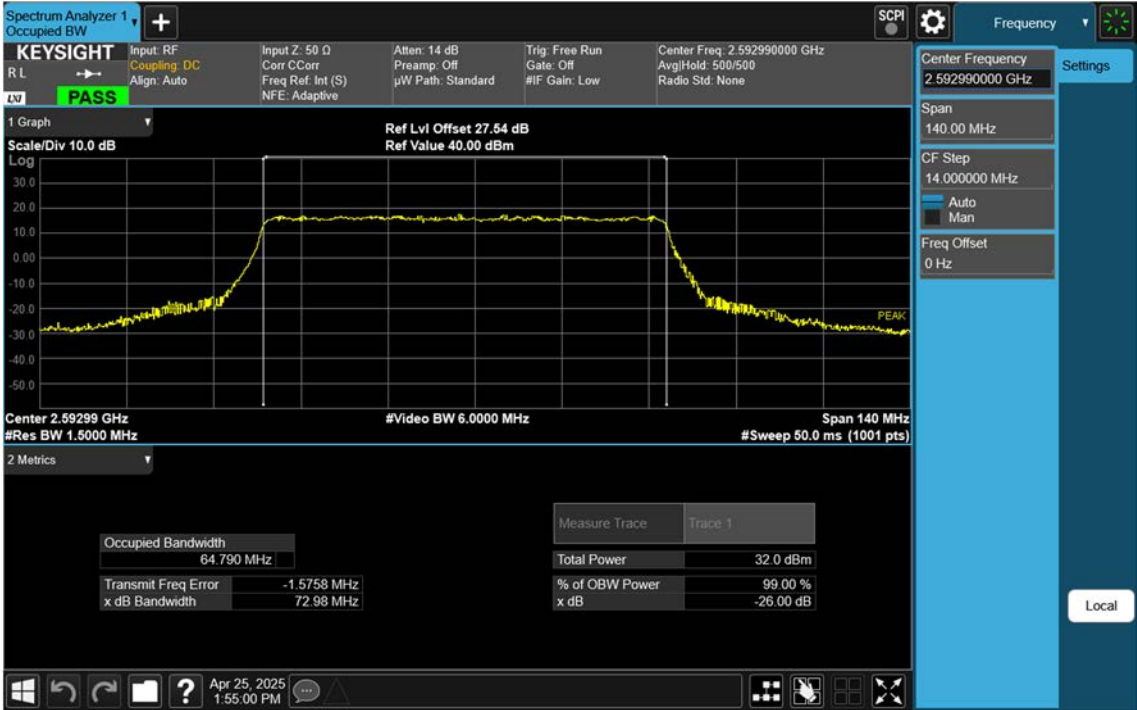
NR41_60 M_OBW_Mid_256QAM_FullRB



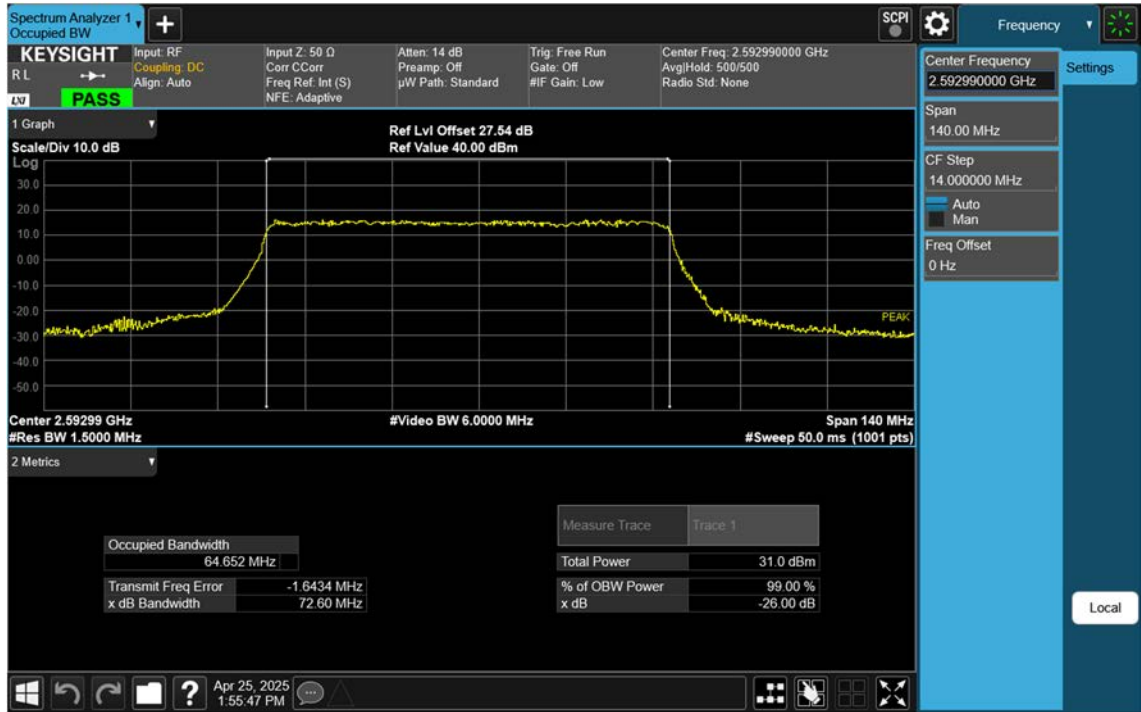
NR41_70 M_OBW_Mid_BPSK_FullRB



NR41_70 M_OBW_Mid_QPSK_FullRB



NR41_70 M_OBW_Mid_16QAM_FullRB



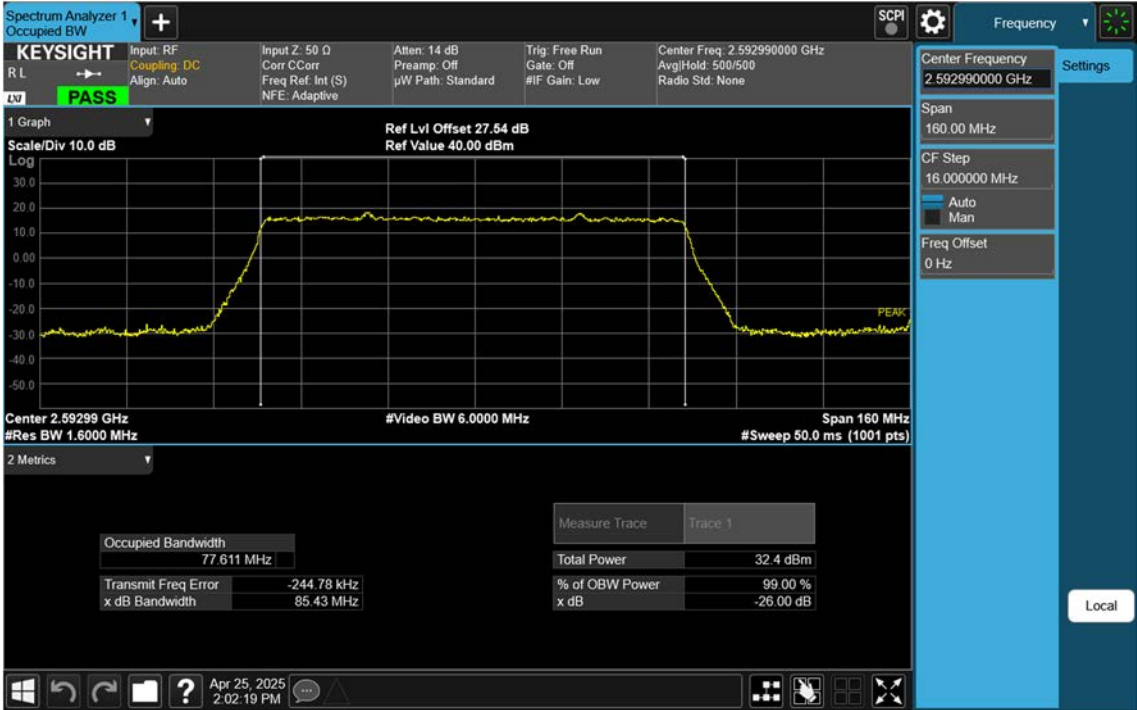
NR41_70 M_OBW_Mid_64QAM_FullRB



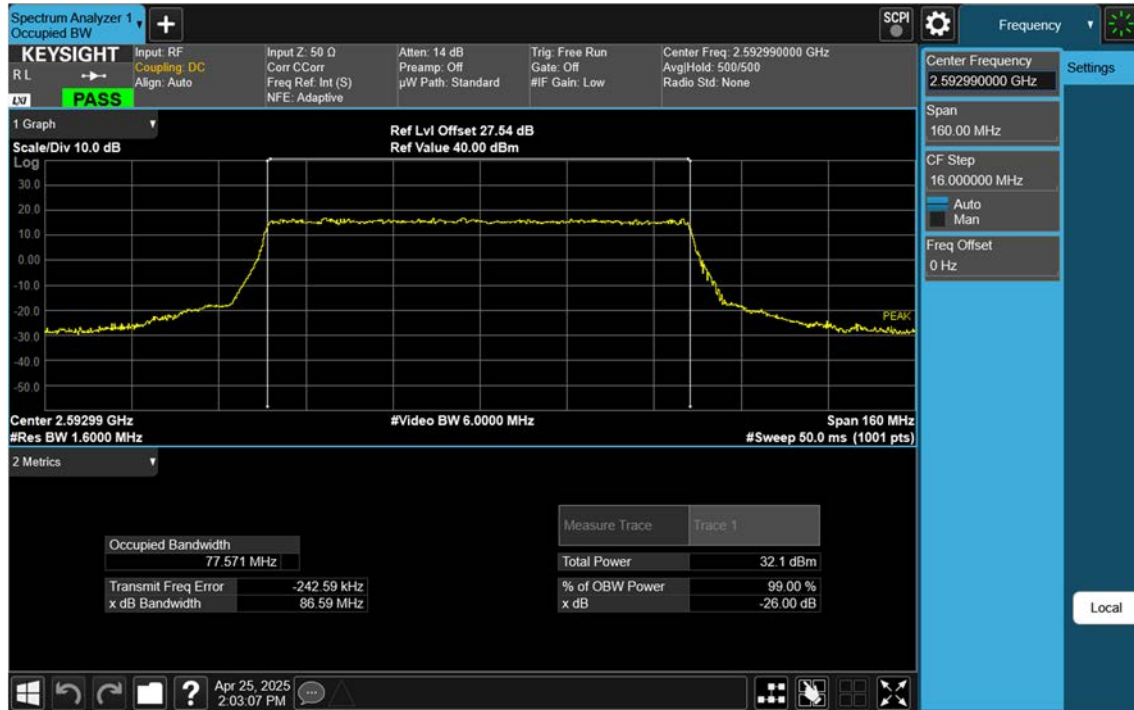
NR41_70 M_OBW_Mid_256QAM_FullRB



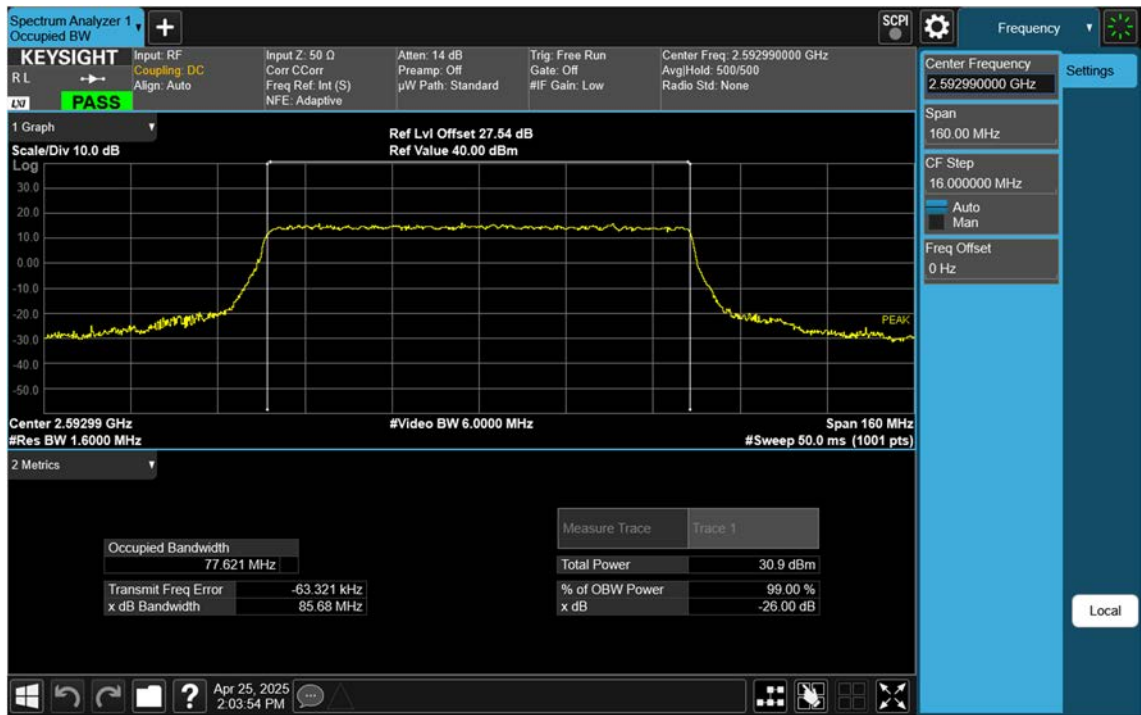
NR41_80 M_OBW_Mid_BPSK_FullRB



NR41_80 M_OBW_Mid_QPSK_FullRB



NR41_80 M_OBW_Mid_16QAM_FullRB



NR41_80 M_OBW_Mid_64QAM_FullRB

