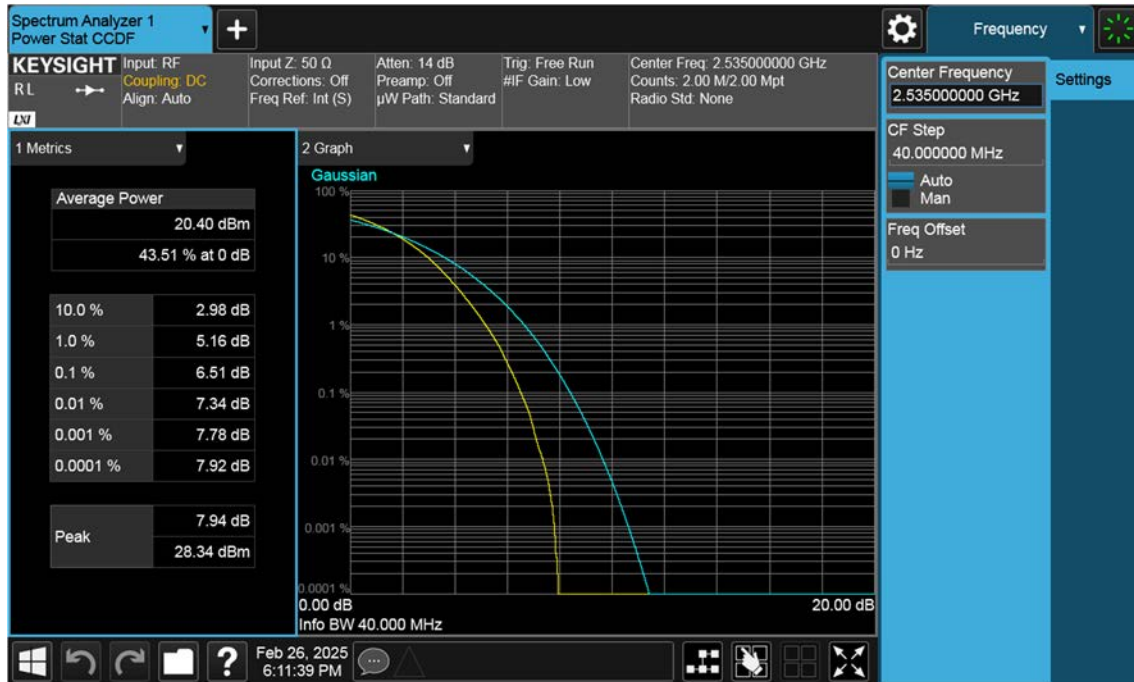


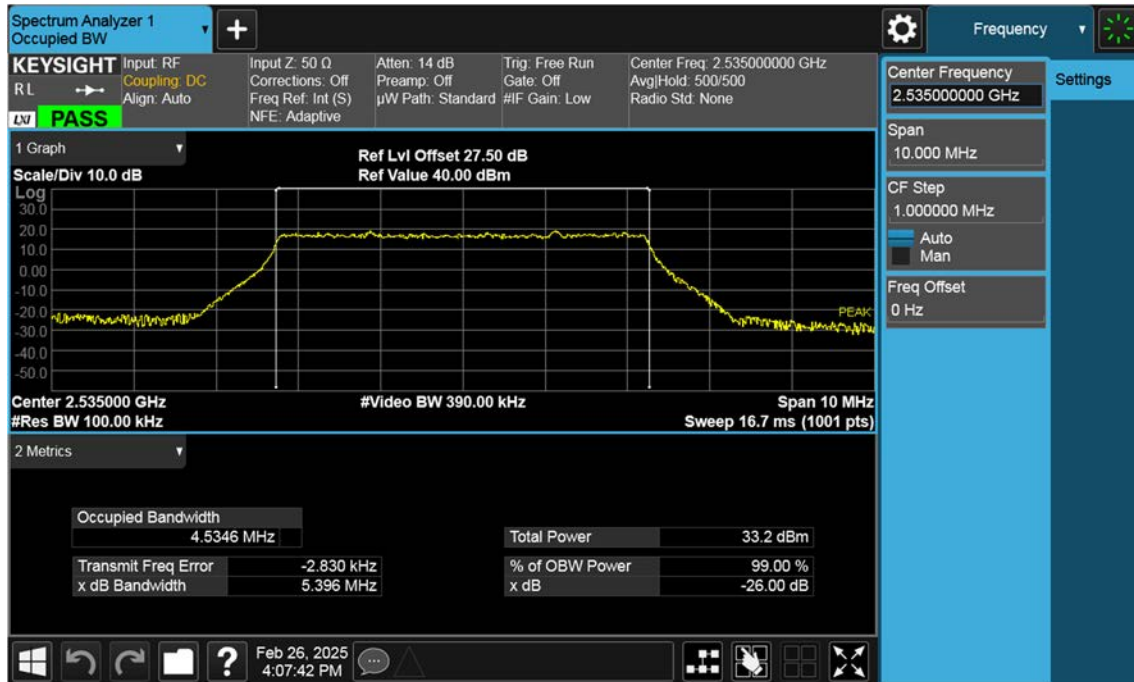
NR7_40 M_PAR_Mid_64QAM_FullRB



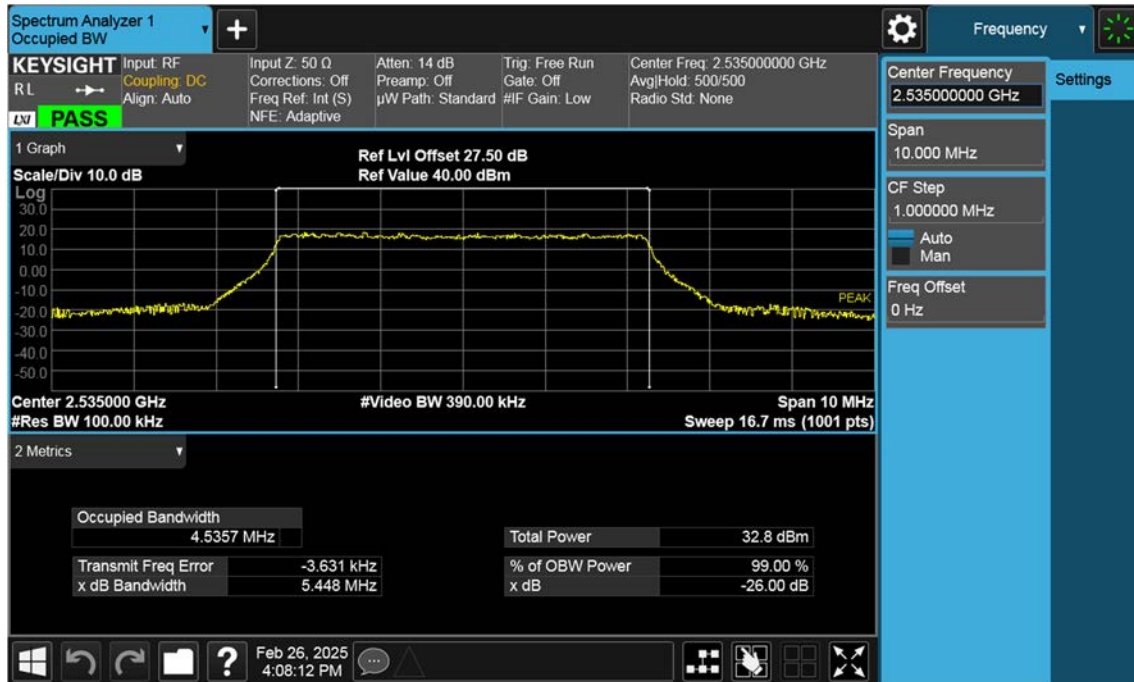
NR7_40 M_PAR_Mid_256QAM_FullRB



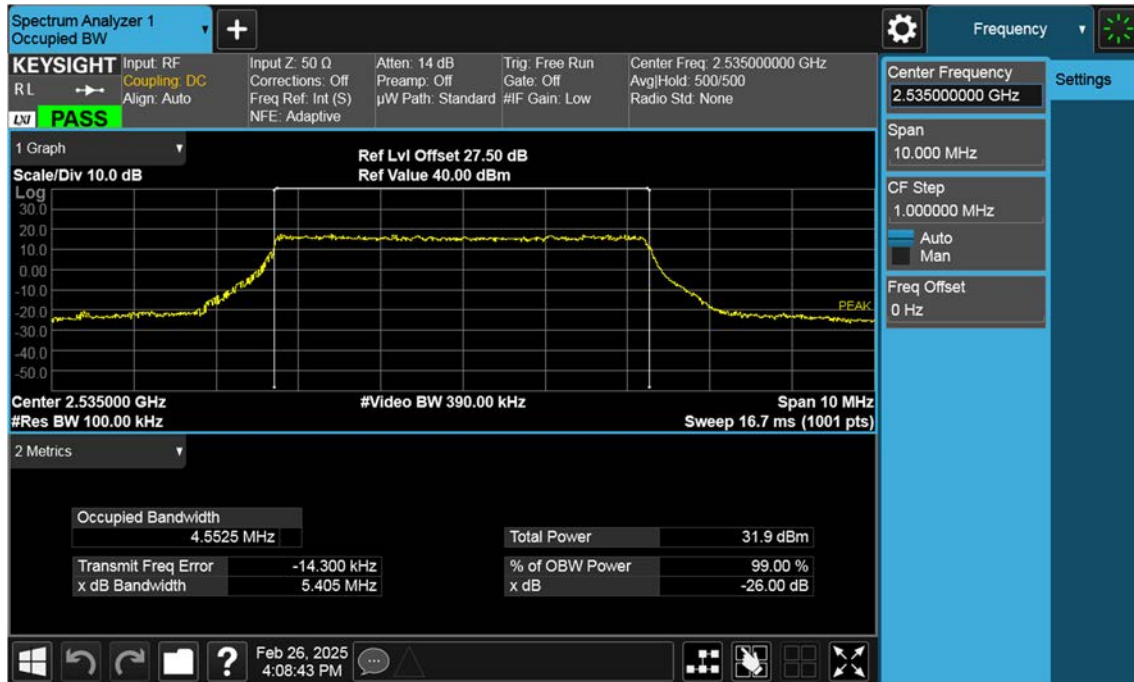
NR7_5 M_OBW_Mid_BPSK_FullRB



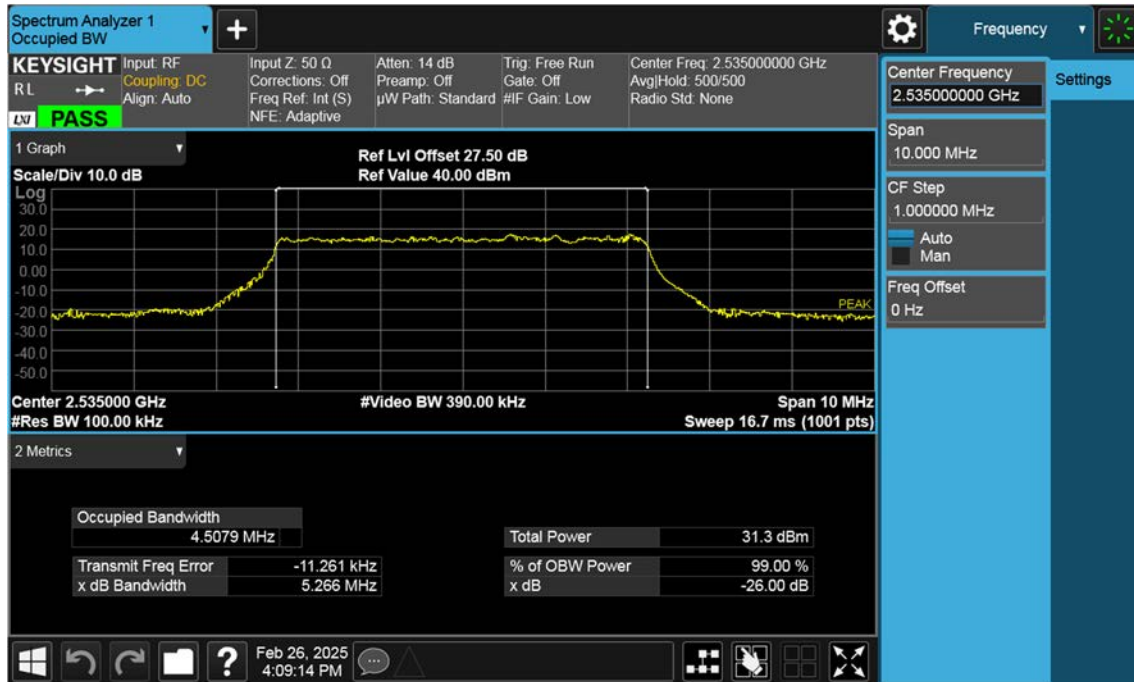
NR7_5 M_OBW_Mid_QPSK_FullRB



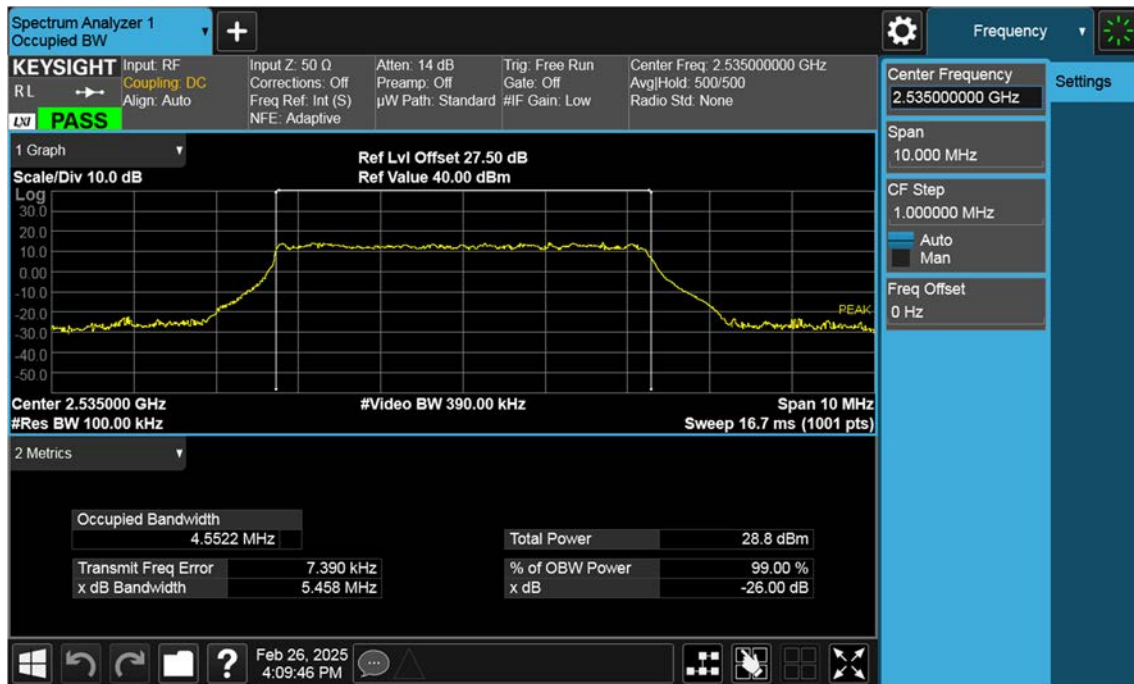
NR7_5 M_OBW_Mid_16QAM_FullRB



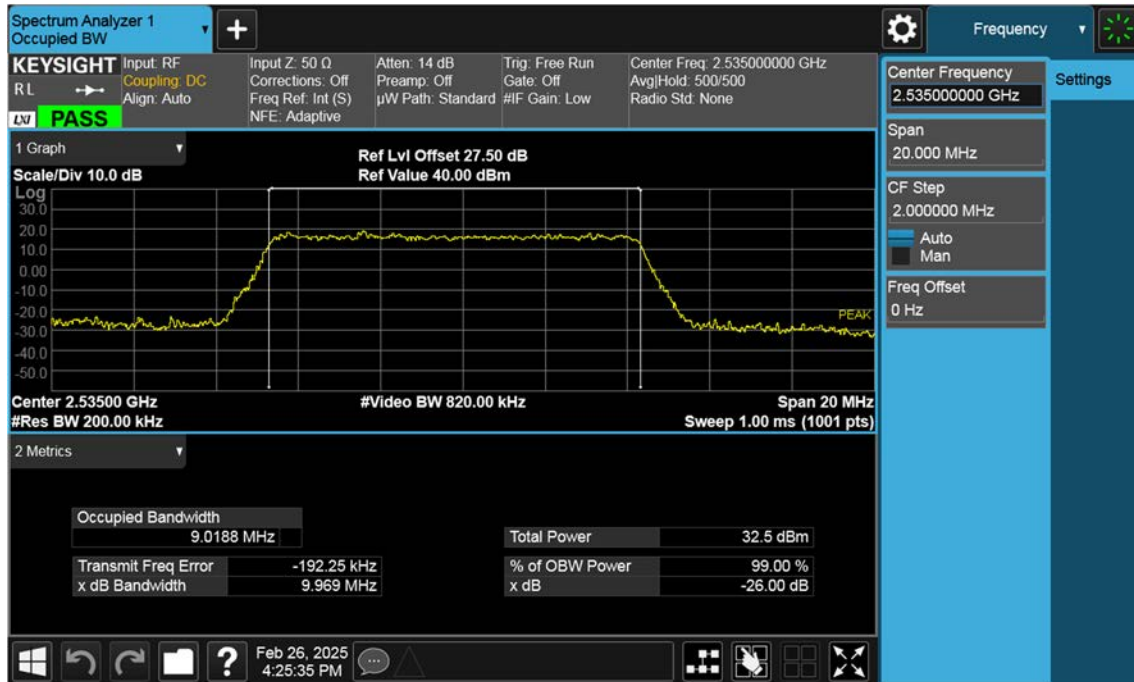
NR7_5 M_OBW_Mid_64QAM_FullRB



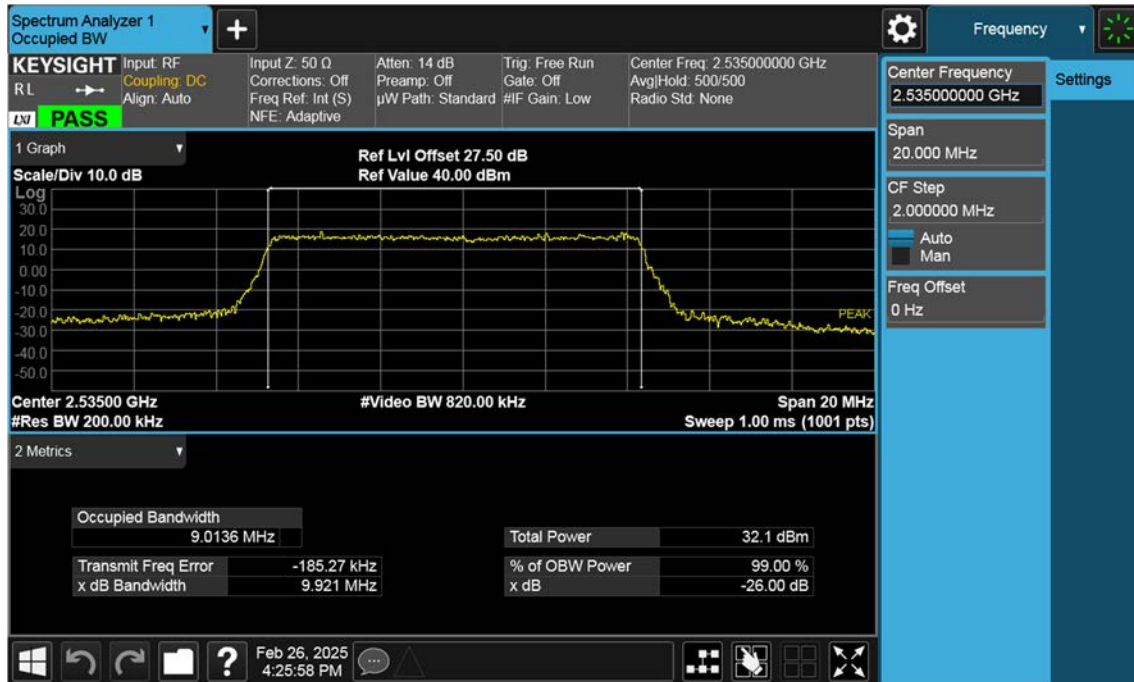
NR7_5 M_OBW_Mid_256QAM_FullRB



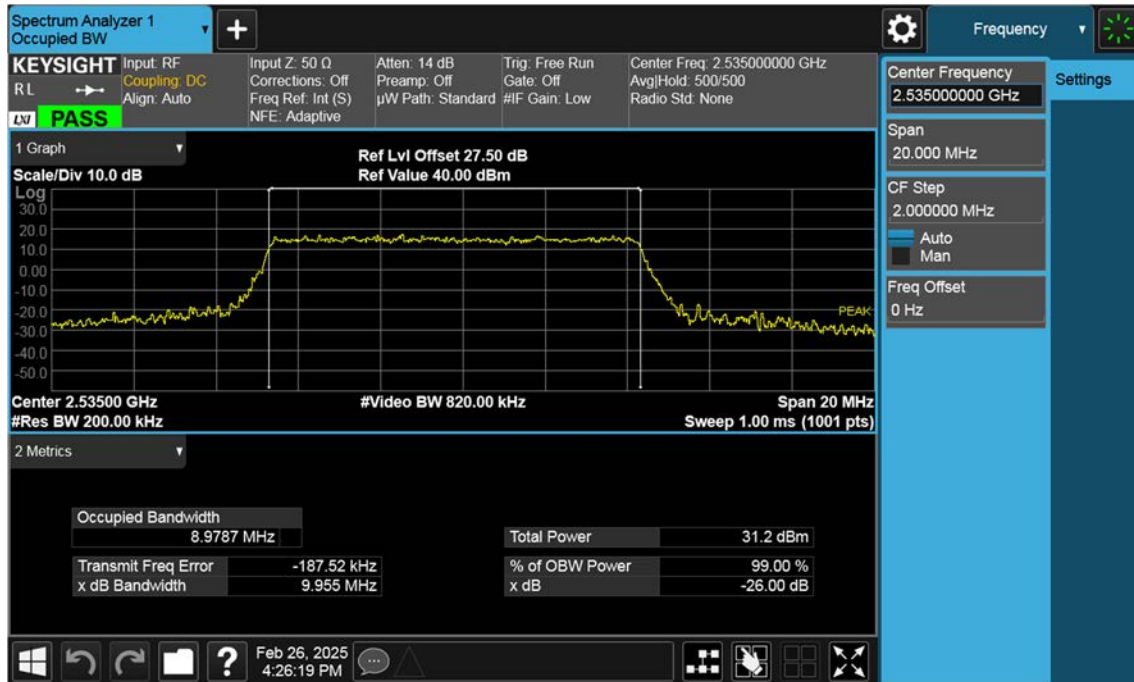
NR7_10 M_OBW_Mid_BPSK_FullRB



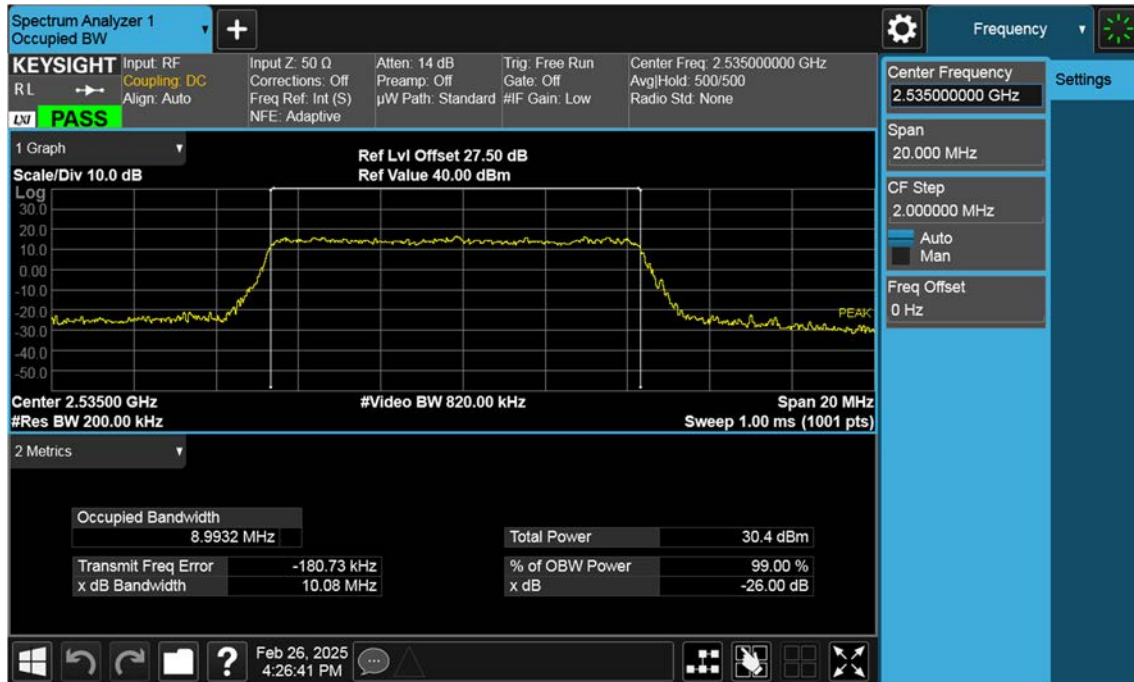
NR7_10 M_OBW_Mid_QPSK_FullRB



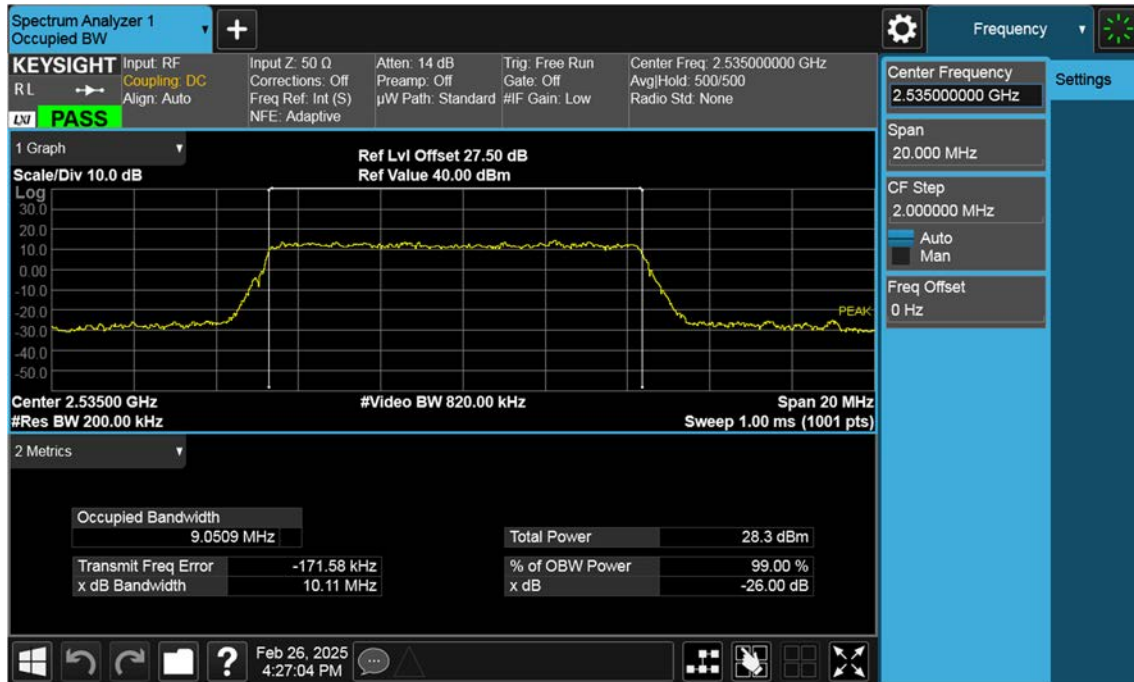
NR7_10 M_OBW_Mid_16QAM_FullRB



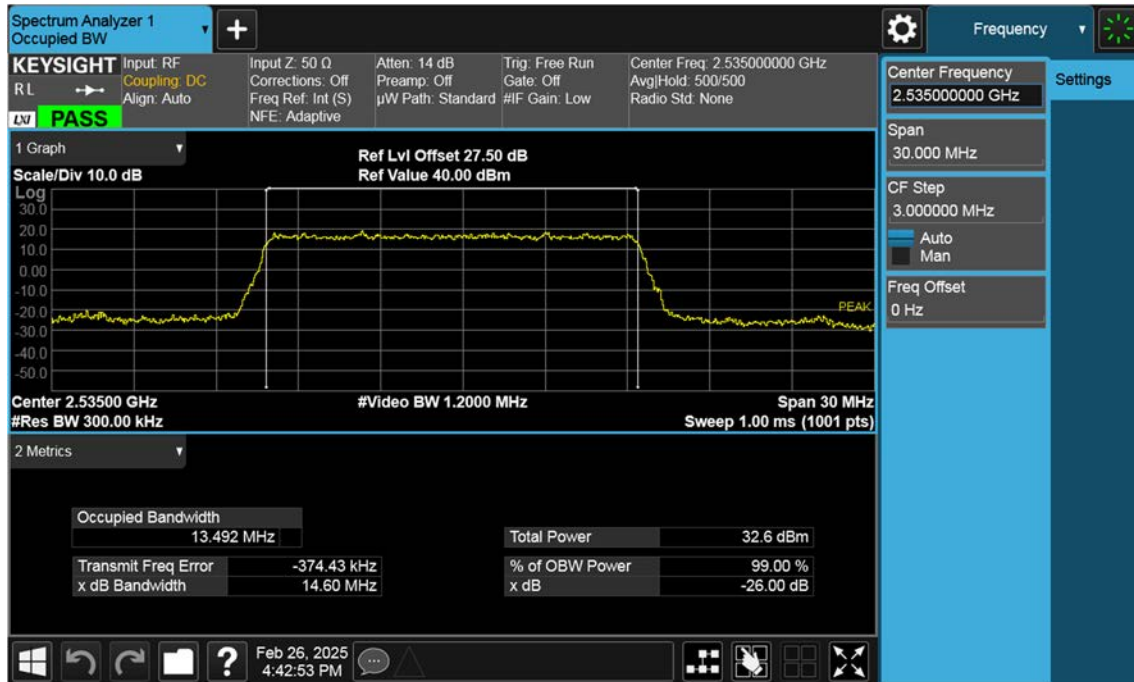
NR7_10 M_OBW_Mid_64QAM_FullRB



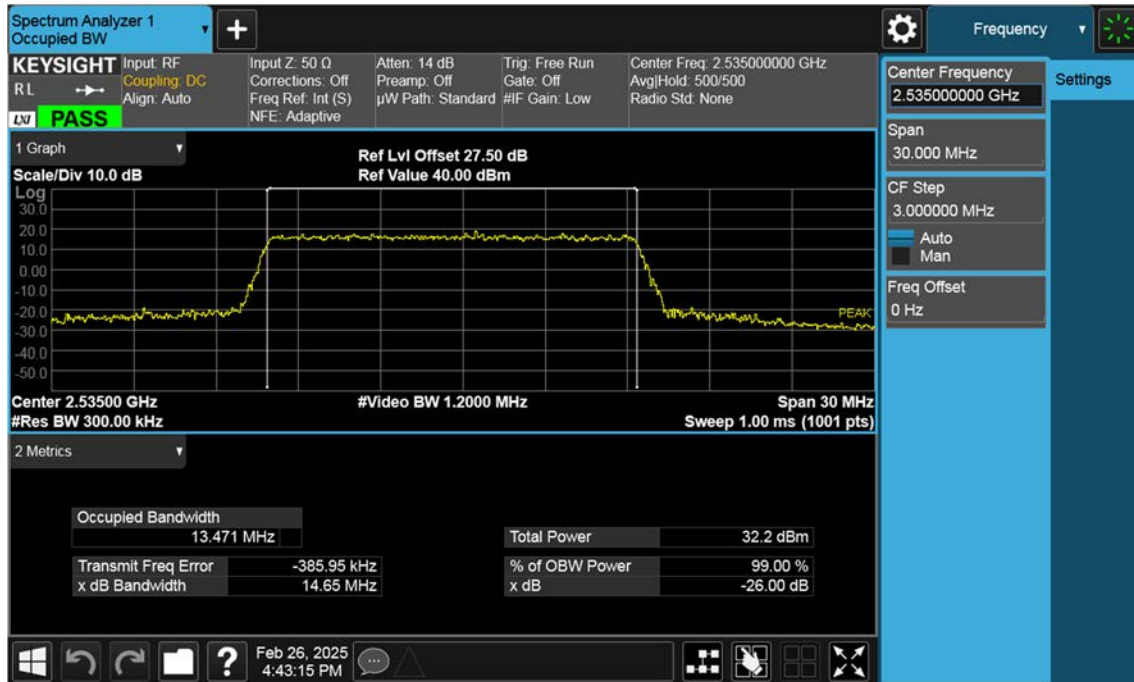
NR7_10 M_OBW_Mid_256QAM_FullRB



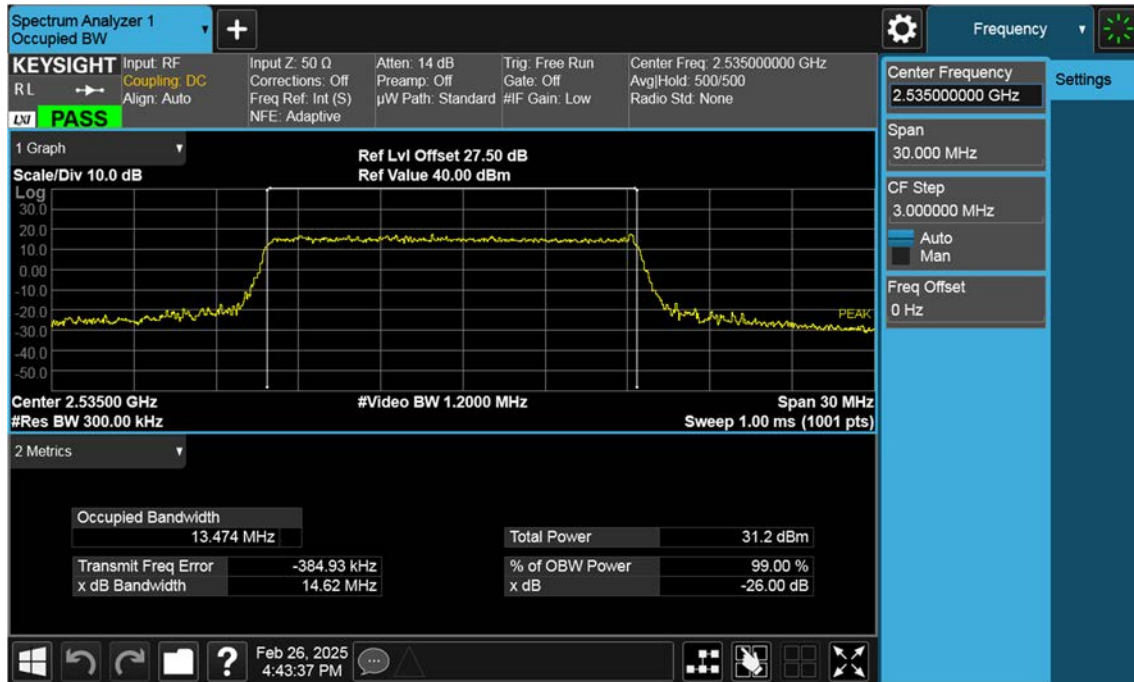
NR7_15 M_OBW_Mid_BPSK_FullRB



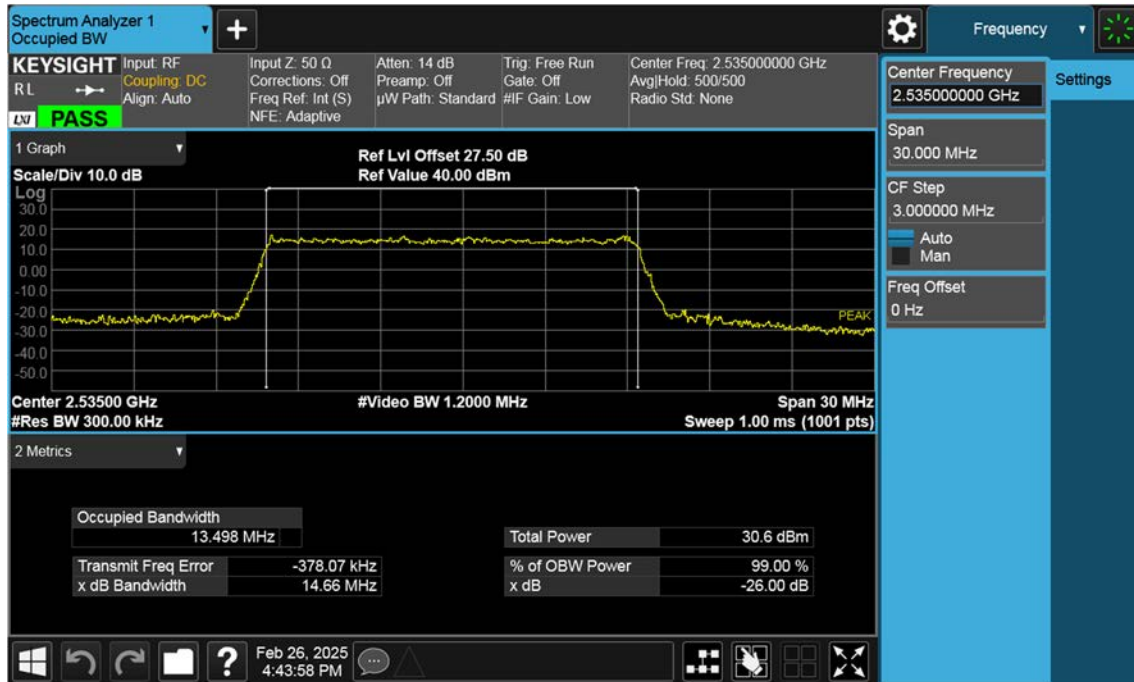
NR7_15 M_OBW_Mid_QPSK_FullRB



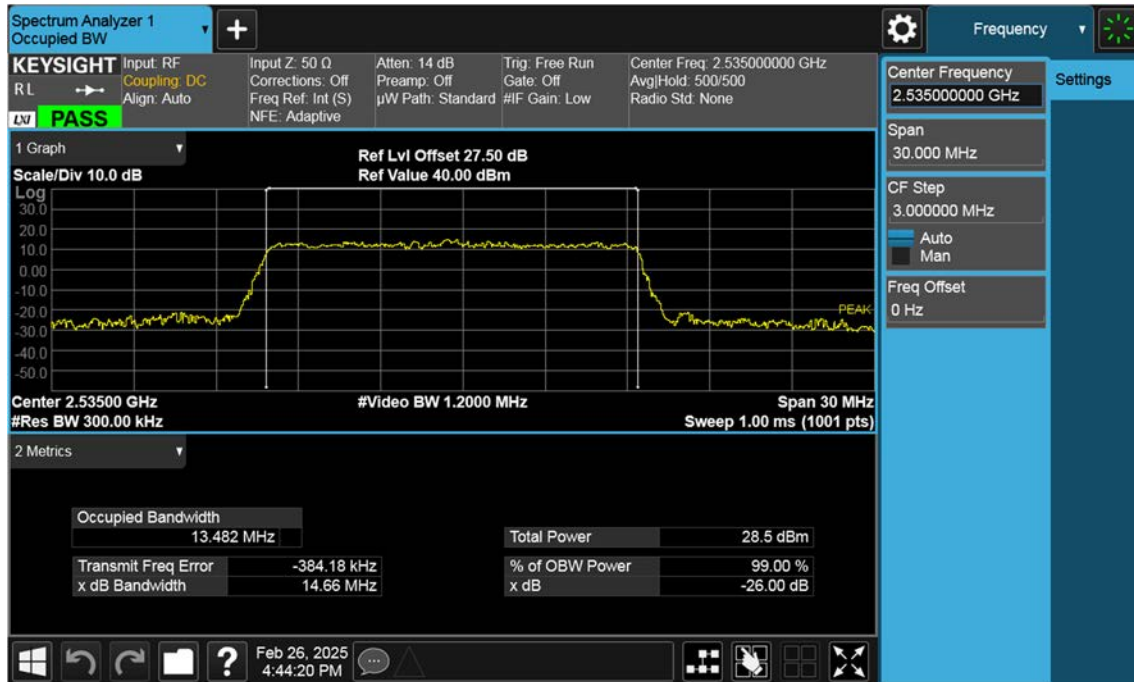
NR7_15 M_OBW_Mid_16QAM_FullRB



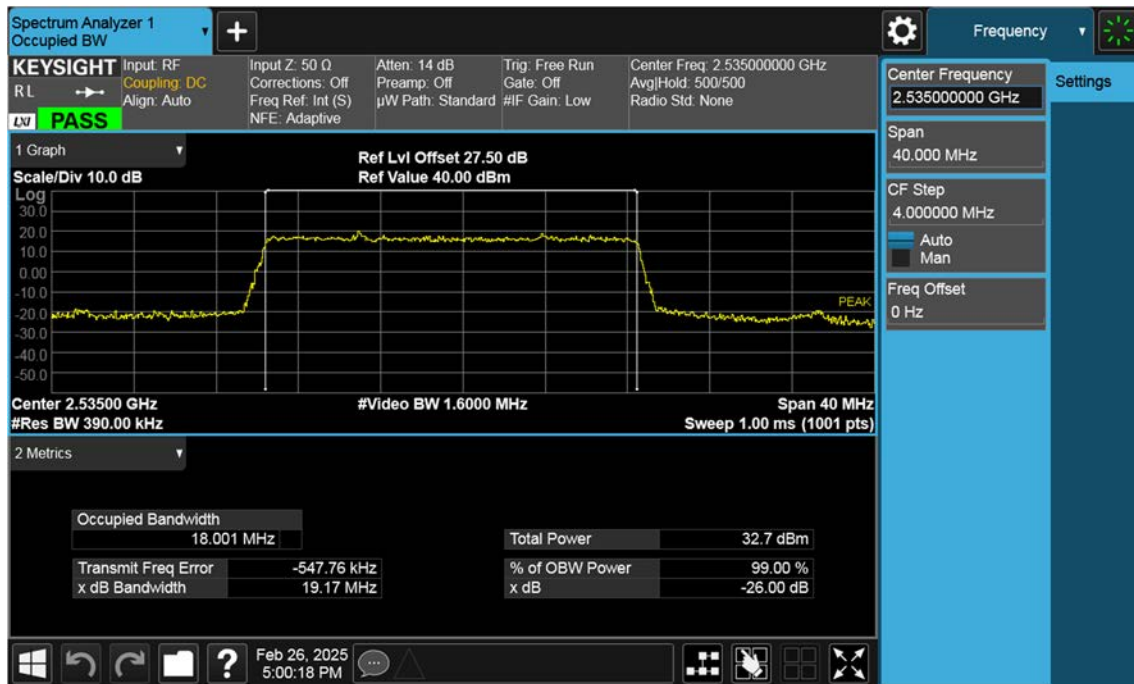
NR7_15 M_OBW_Mid_64QAM_FullRB



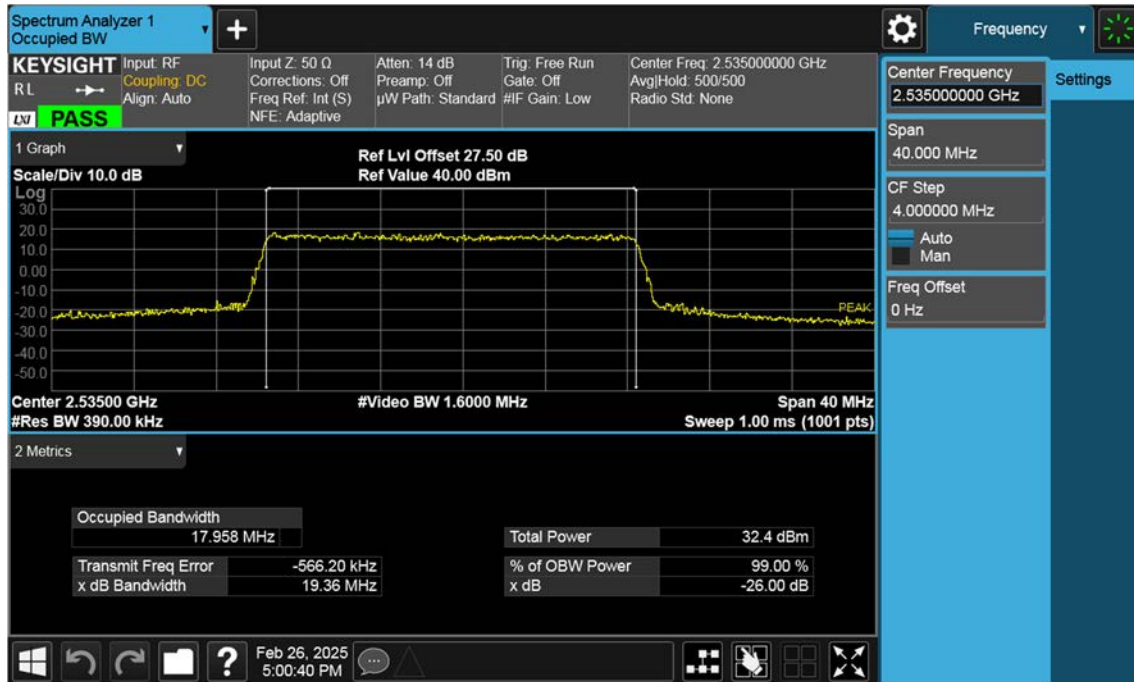
NR7_15 M_OBW_Mid_256QAM_FullRB



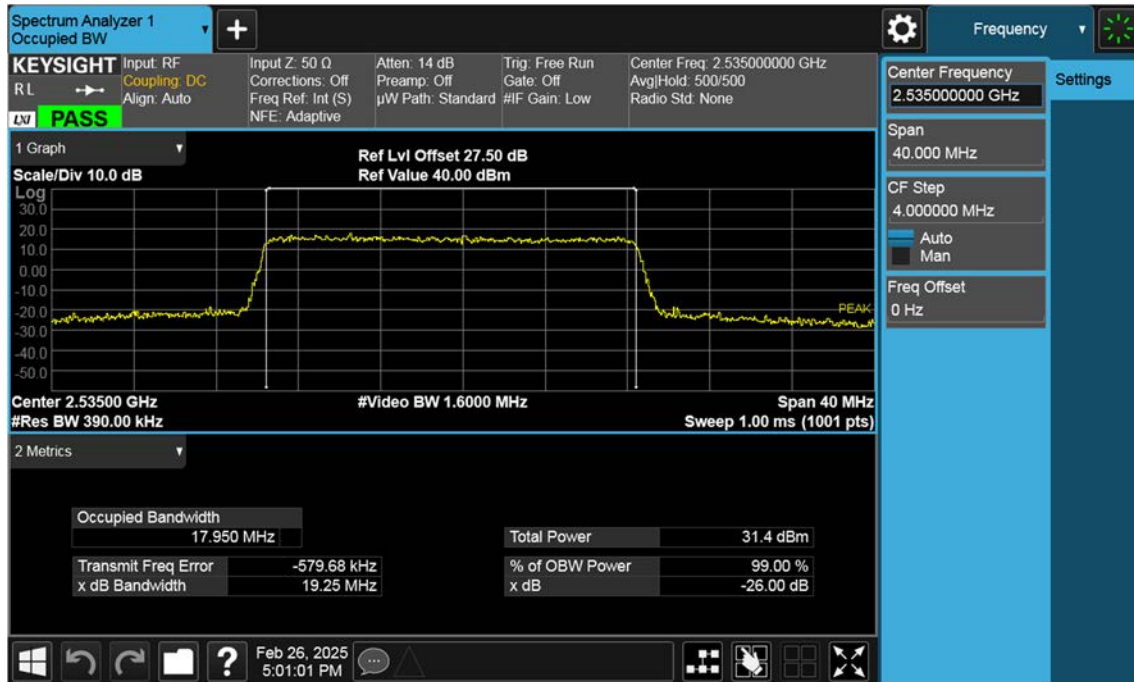
NR7_20 M_OBW_Mid_BPSK_FullRB



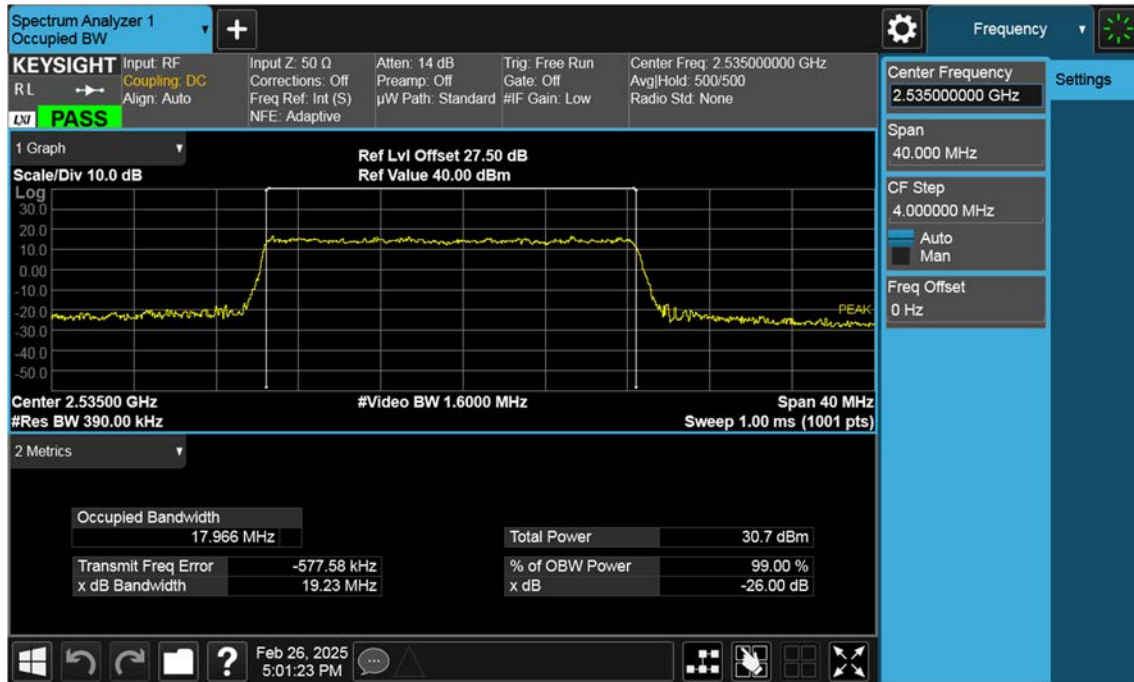
NR7_20 M_OBW_Mid_QPSK_FullRB



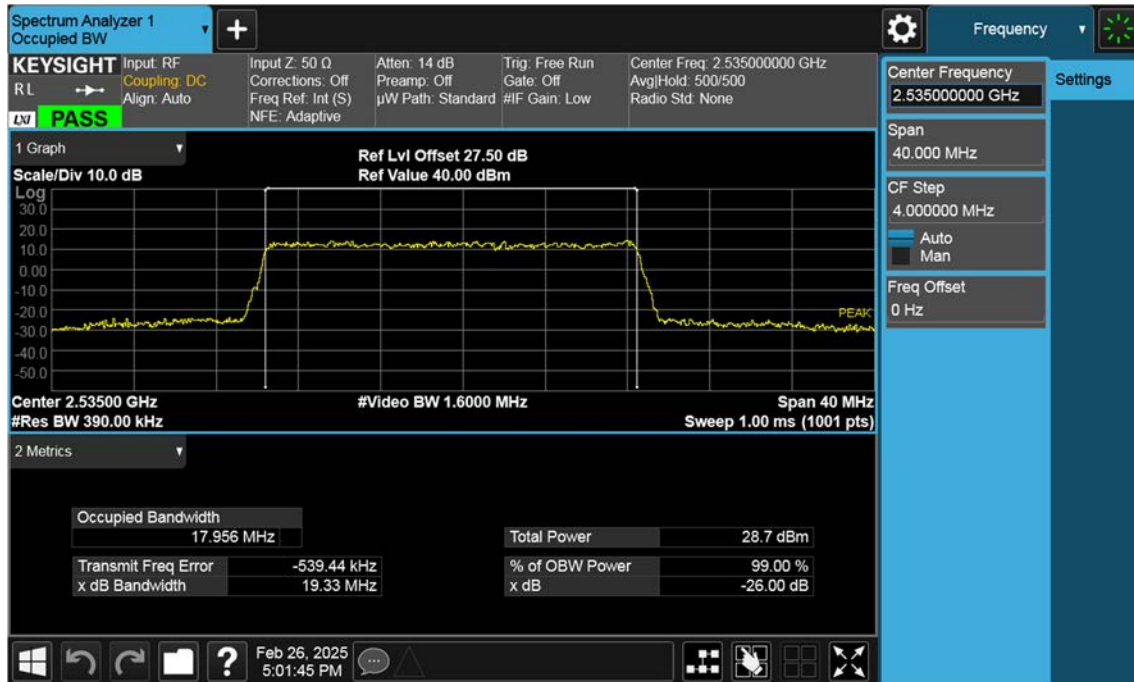
NR7_20 M_OBW_Mid_16QAM_FullRB



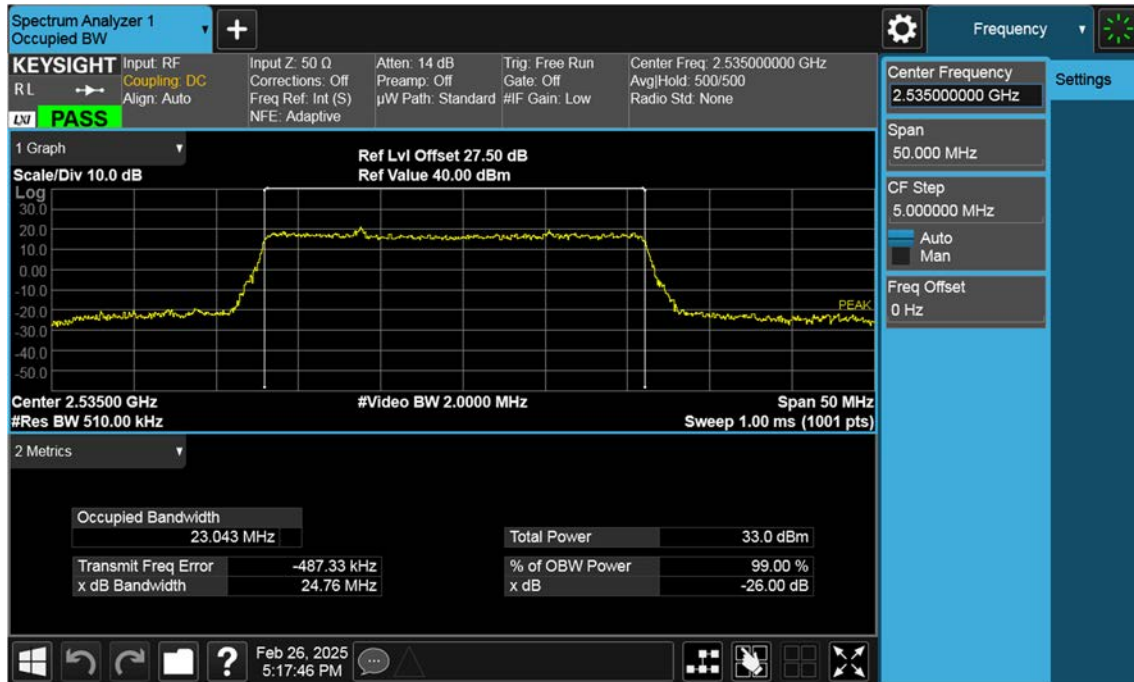
NR7_20 M_OBW_Mid_64QAM_FullRB



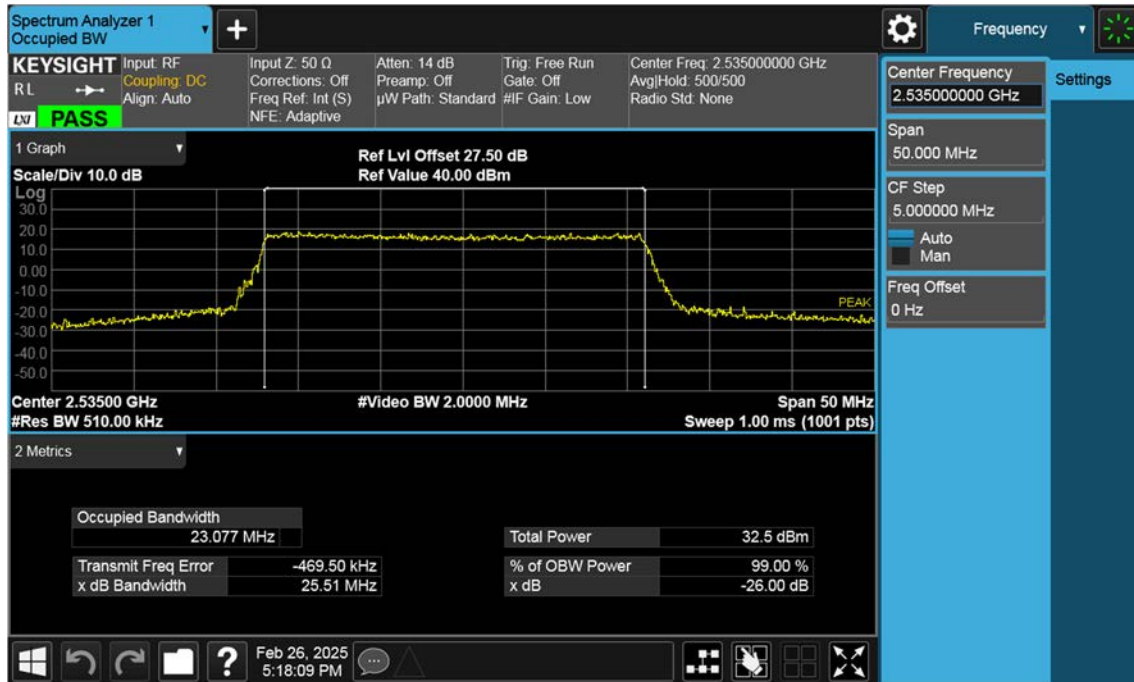
NR7_20 M_OBW_Mid_256QAM_FullRB



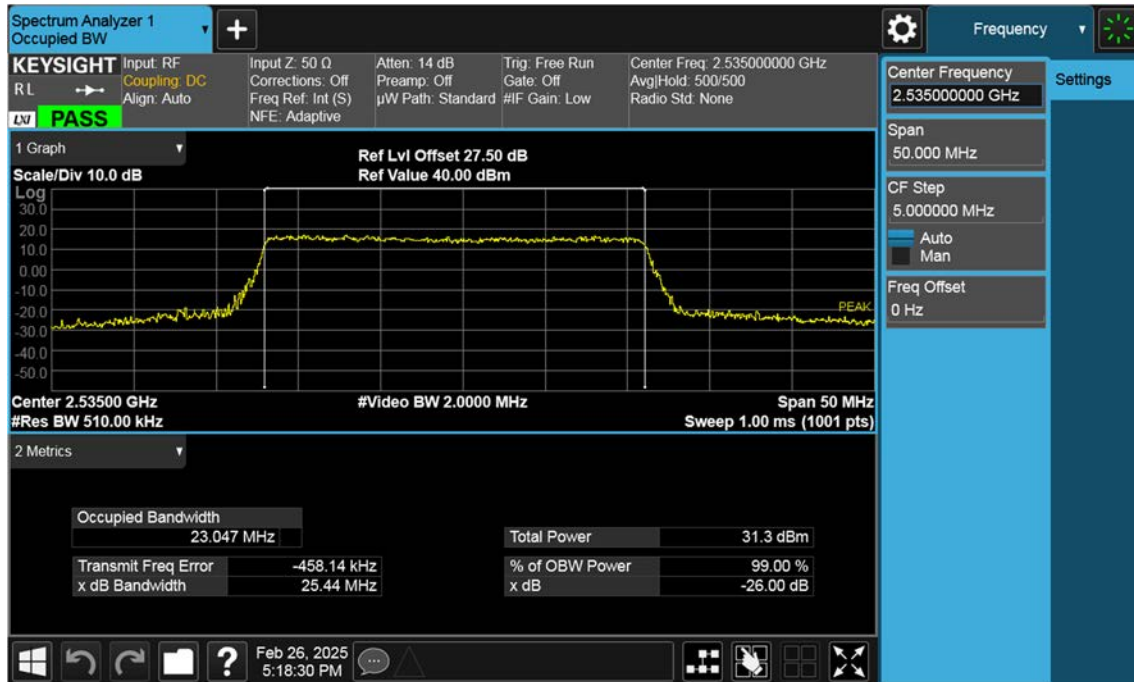
NR7_25 M_OBW_Mid_BPSK_FullRB



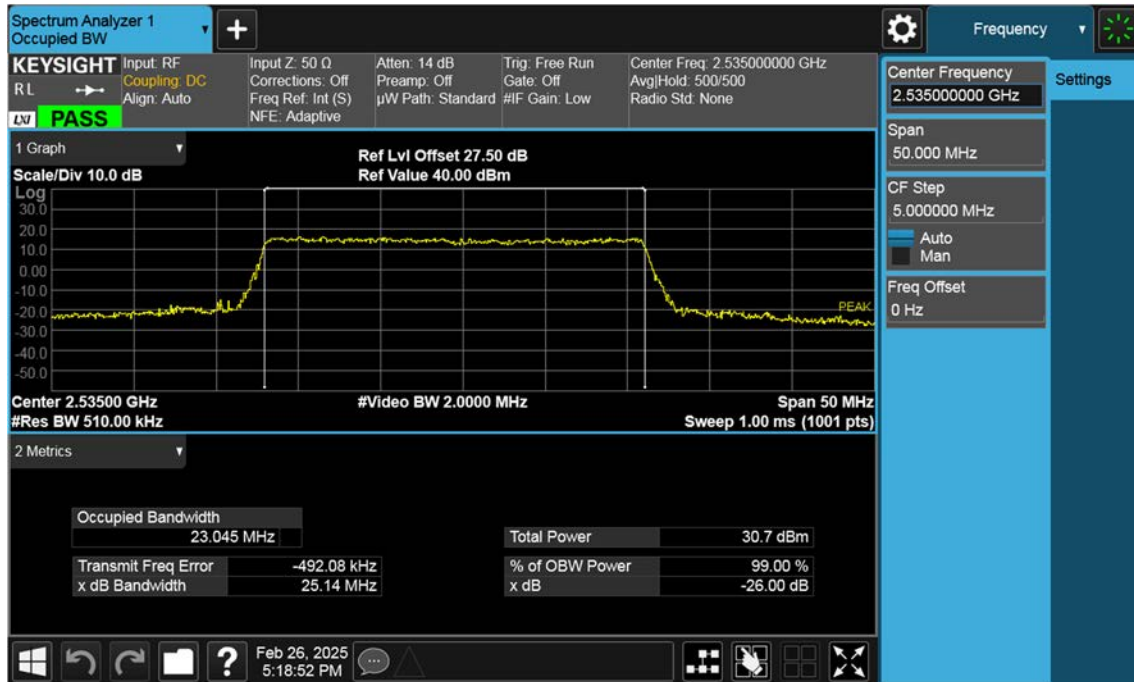
NR7_25 M_OBW_Mid_QPSK_FullRB



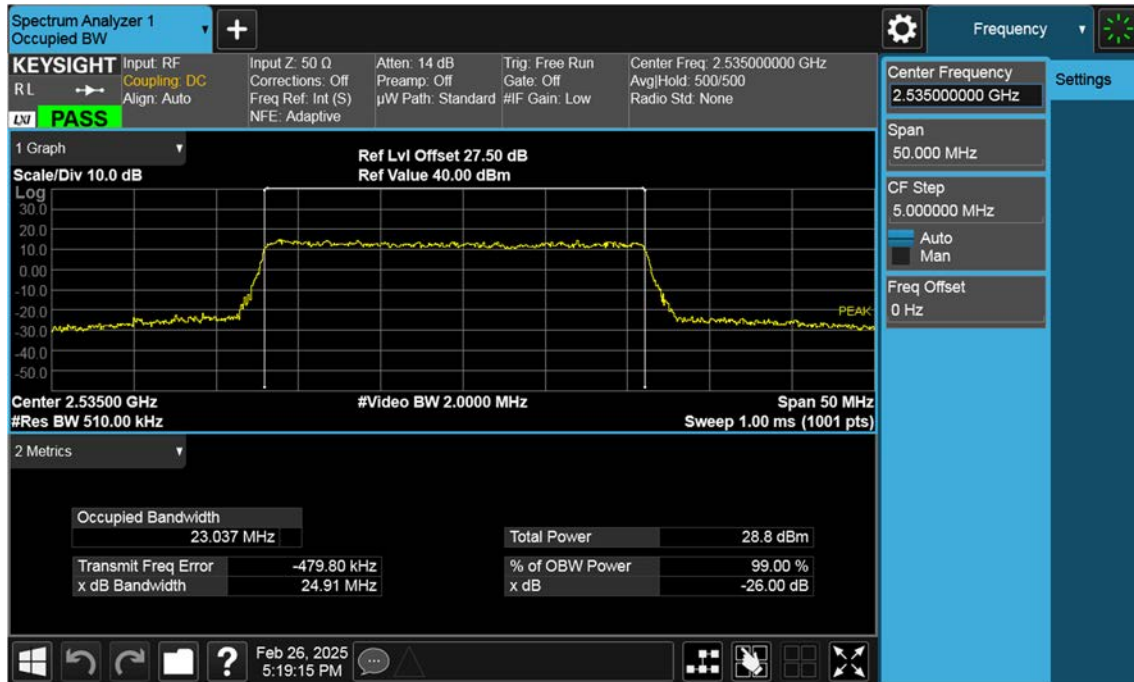
NR7_25 M_OBW_Mid_16QAM_FullRB



NR7_25 M_OBW_Mid_64QAM_FullRB



NR7_25 M_OBW_Mid_256QAM_FullRB



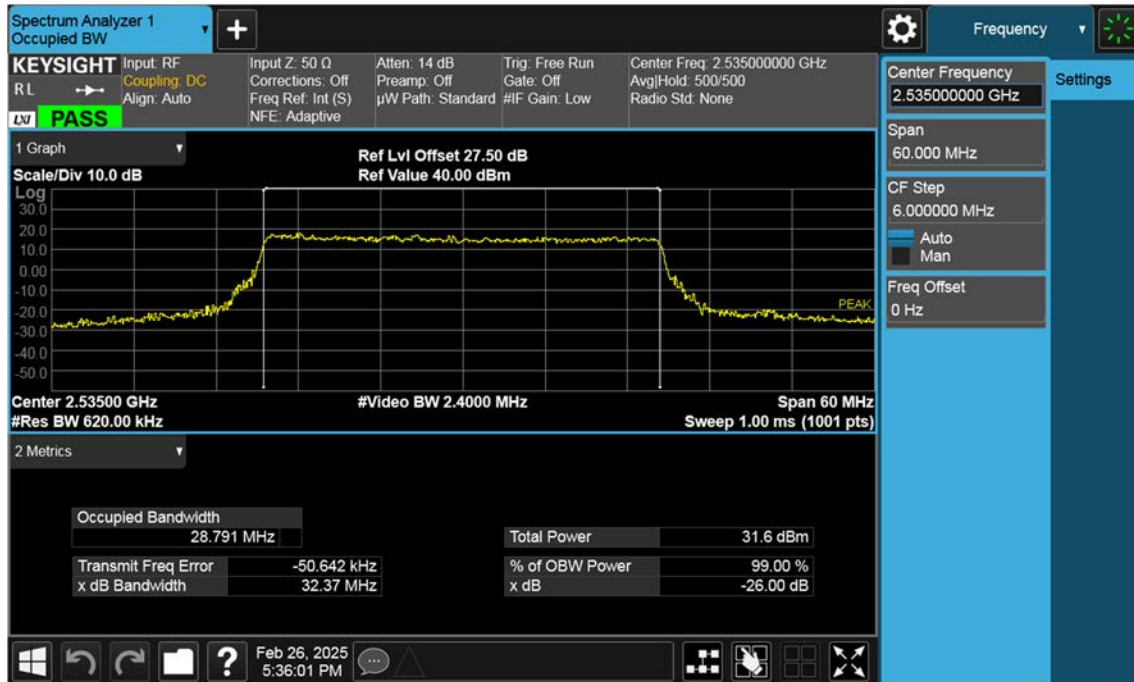
NR7_30 M_OBW_Mid_BPSK_FullRB



NR7_30 M_OBW_Mid_QPSK_FullRB



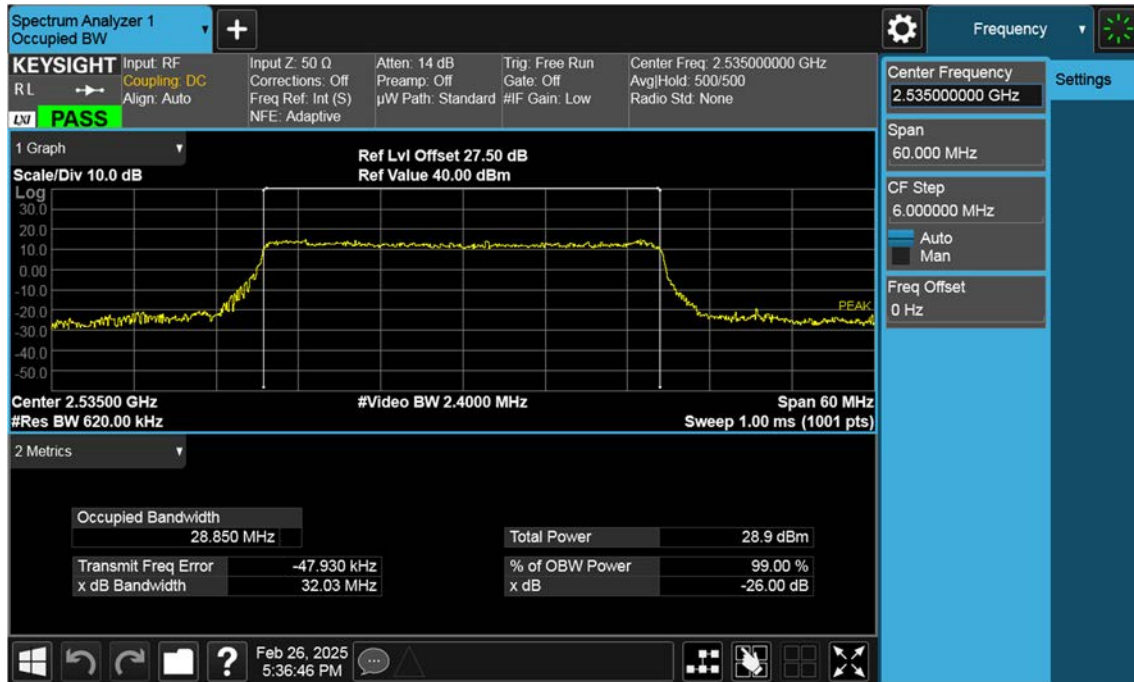
NR7_30 M_OBW_Mid_16QAM_FullRB



NR7_30 M_OBW_Mid_64QAM_FullRB



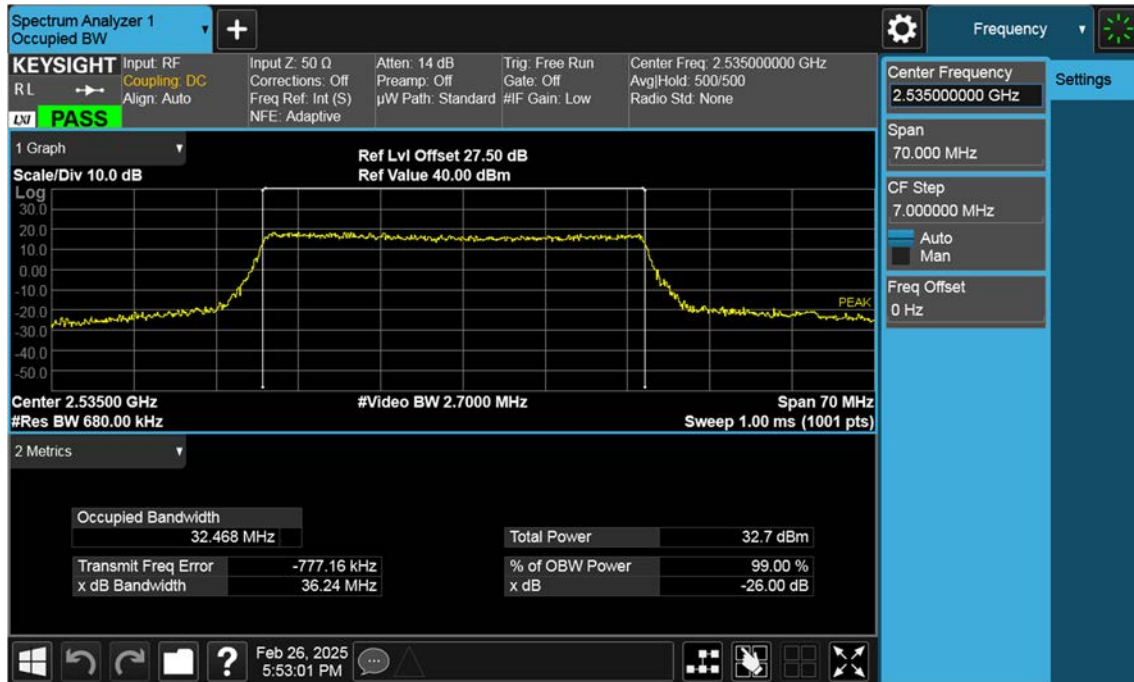
NR7_30 M_OBW_Mid_256QAM_FullRB



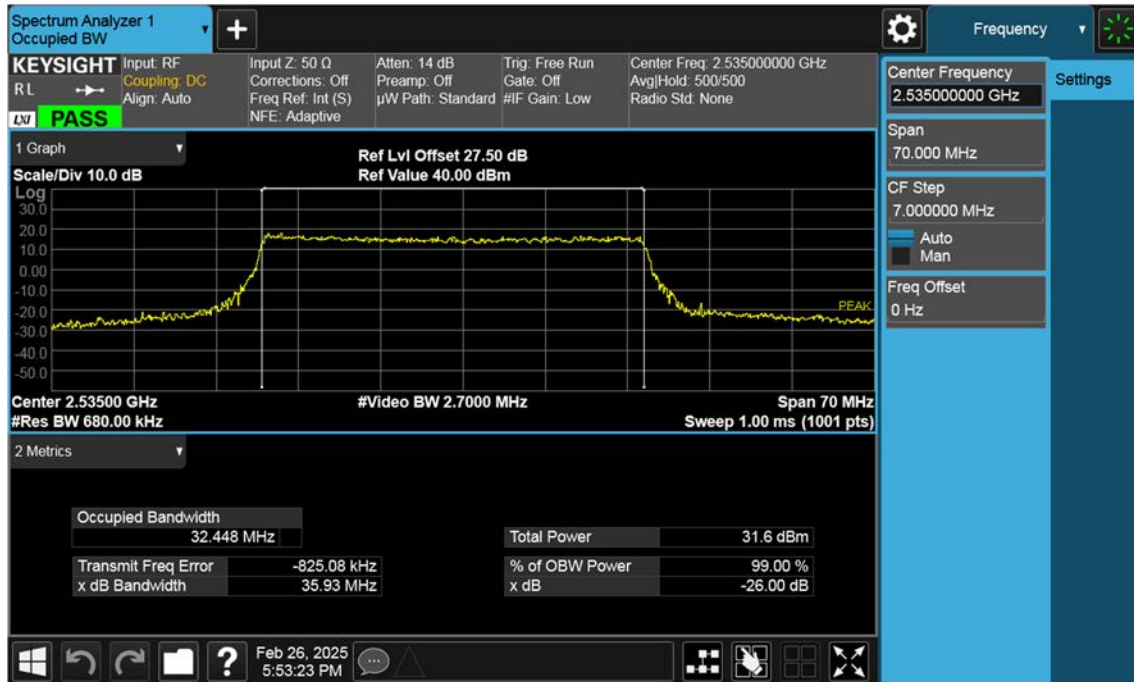
NR7_35 M_OBW_Mid_BPSK_FullRB



NR7_35 M_OBW_Mid_QPSK_FullRB



NR7_35 M_OBW_Mid_16QAM_FullRB



NR7_35 M_OBW_Mid_64QAM_FullRB



NR7_35 M_OBW_Mid_256QAM_FullRB



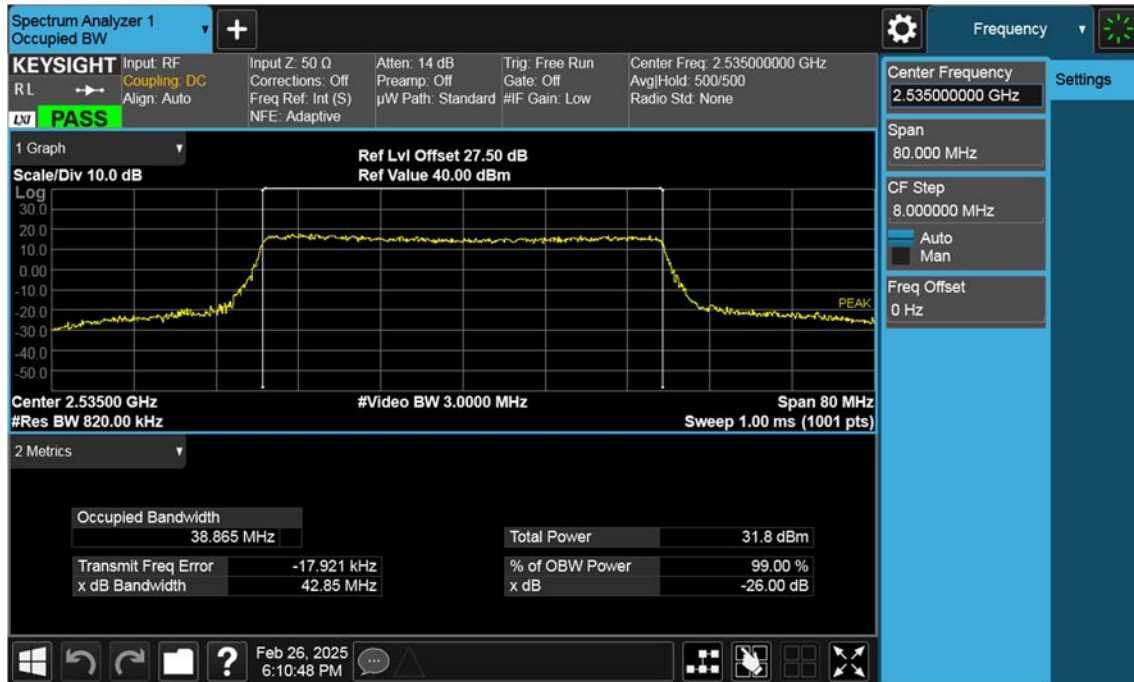
NR7_40 M_OBW_Mid_BPSK_FullRB



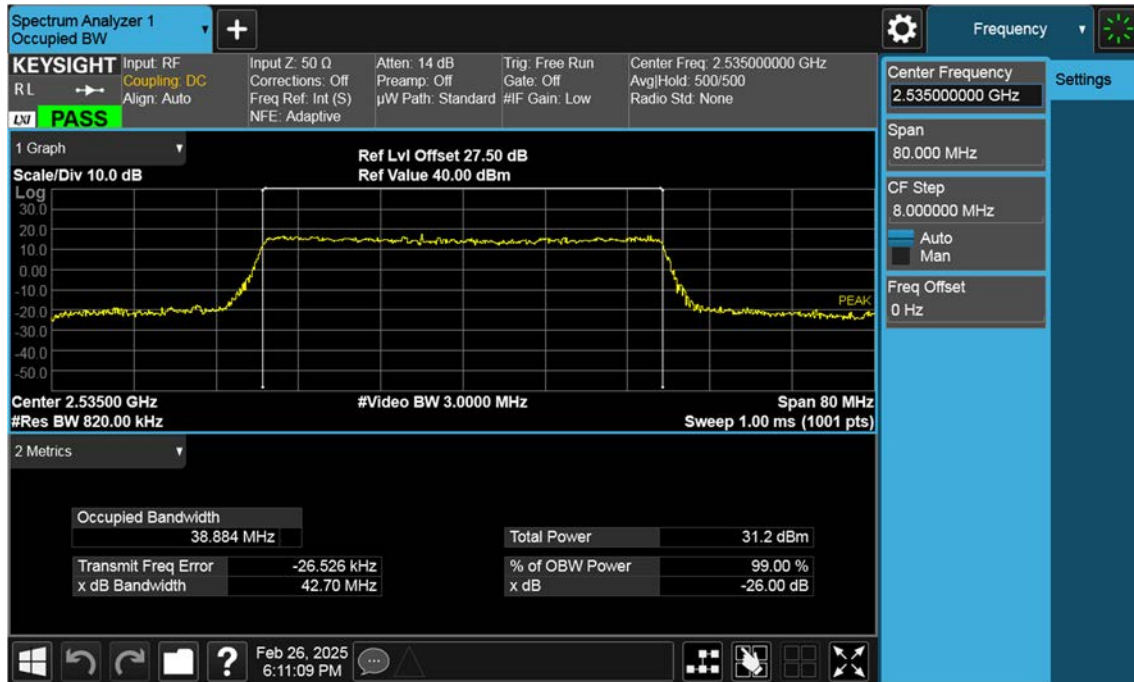
NR7_40 M_OBW_Mid_QPSK_FullRB



NR7_40 M_OBW_Mid_16QAM_FullRB



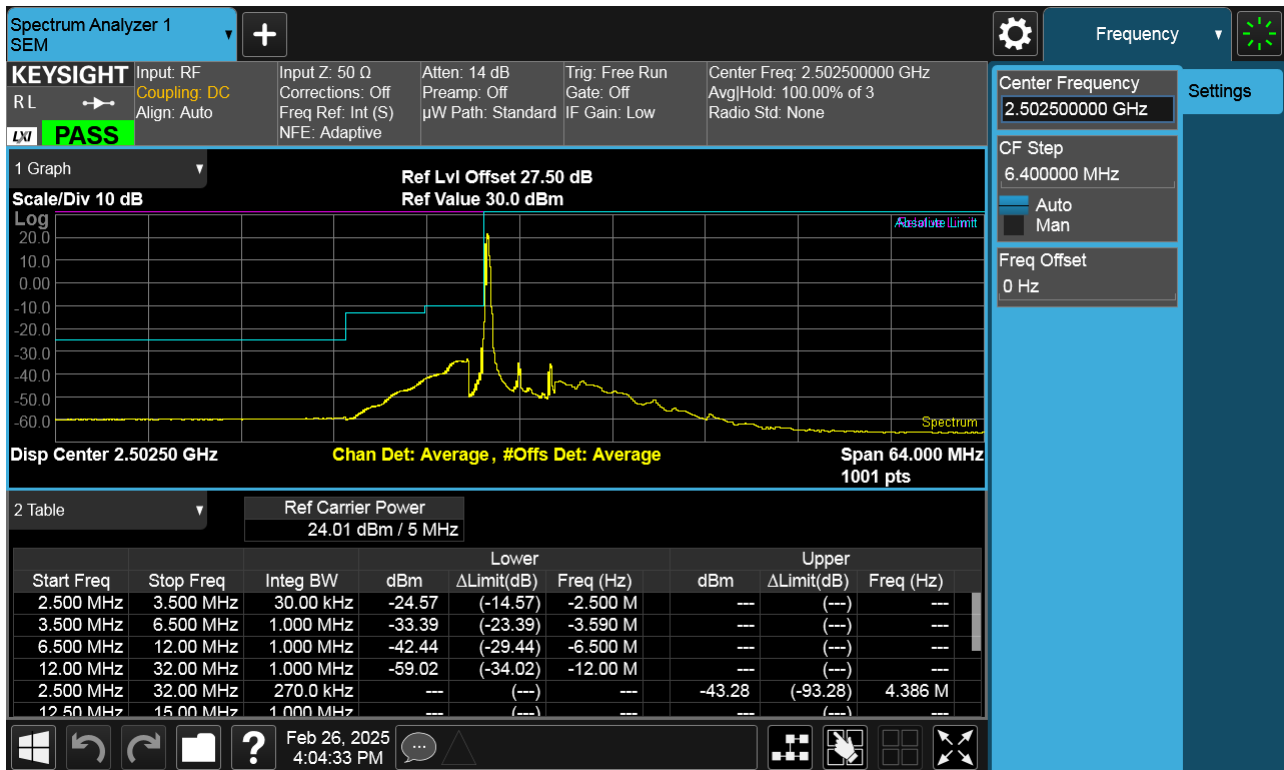
NR7_40 M_OBW_Mid_64QAM_FullRB



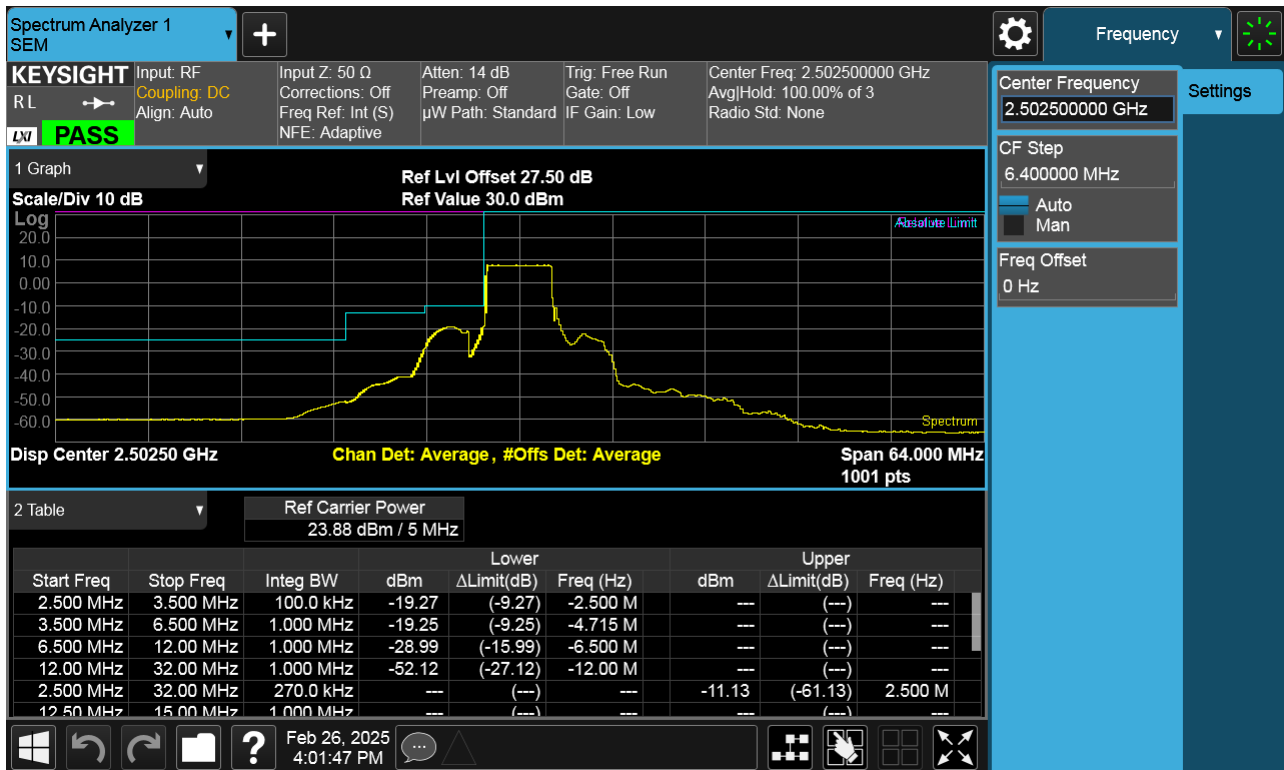
NR7_40 M_OBW_Mid_256QAM_FullRB



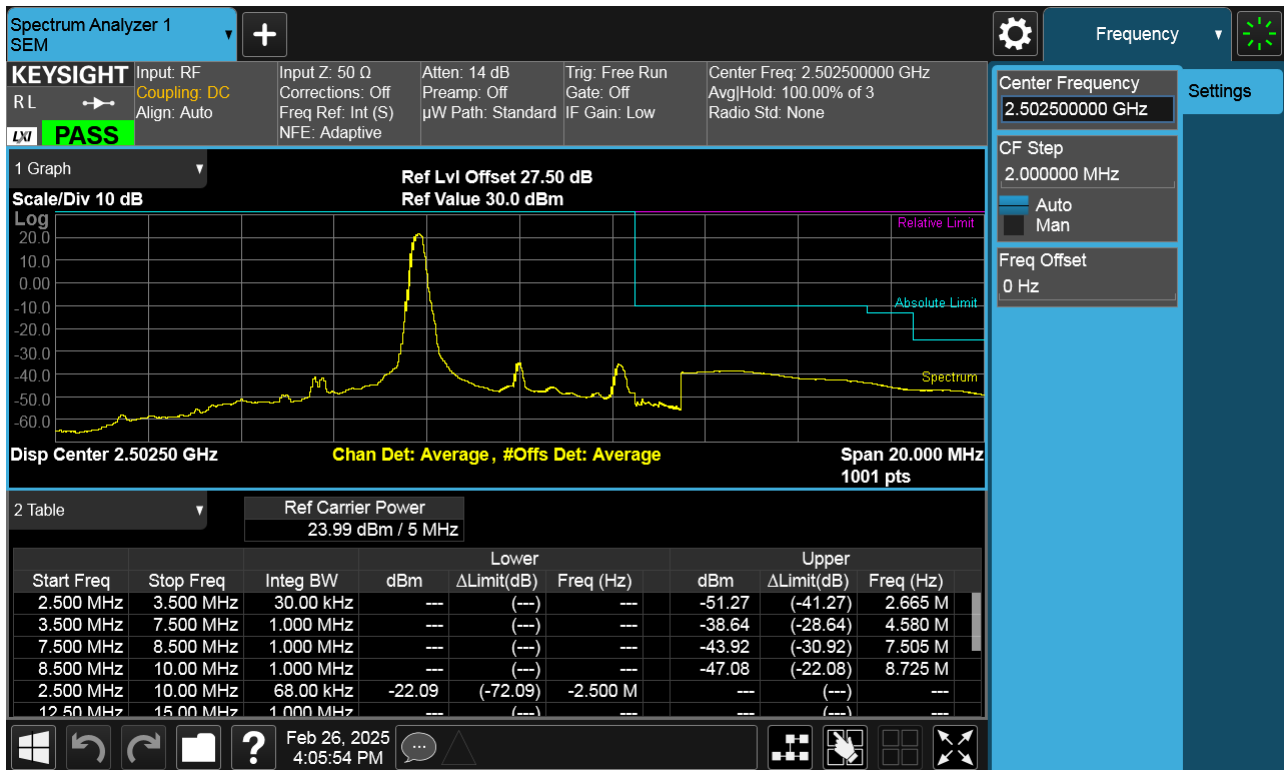
Low Channel Edge Plot (5 MHz BPSK RB 1)-1



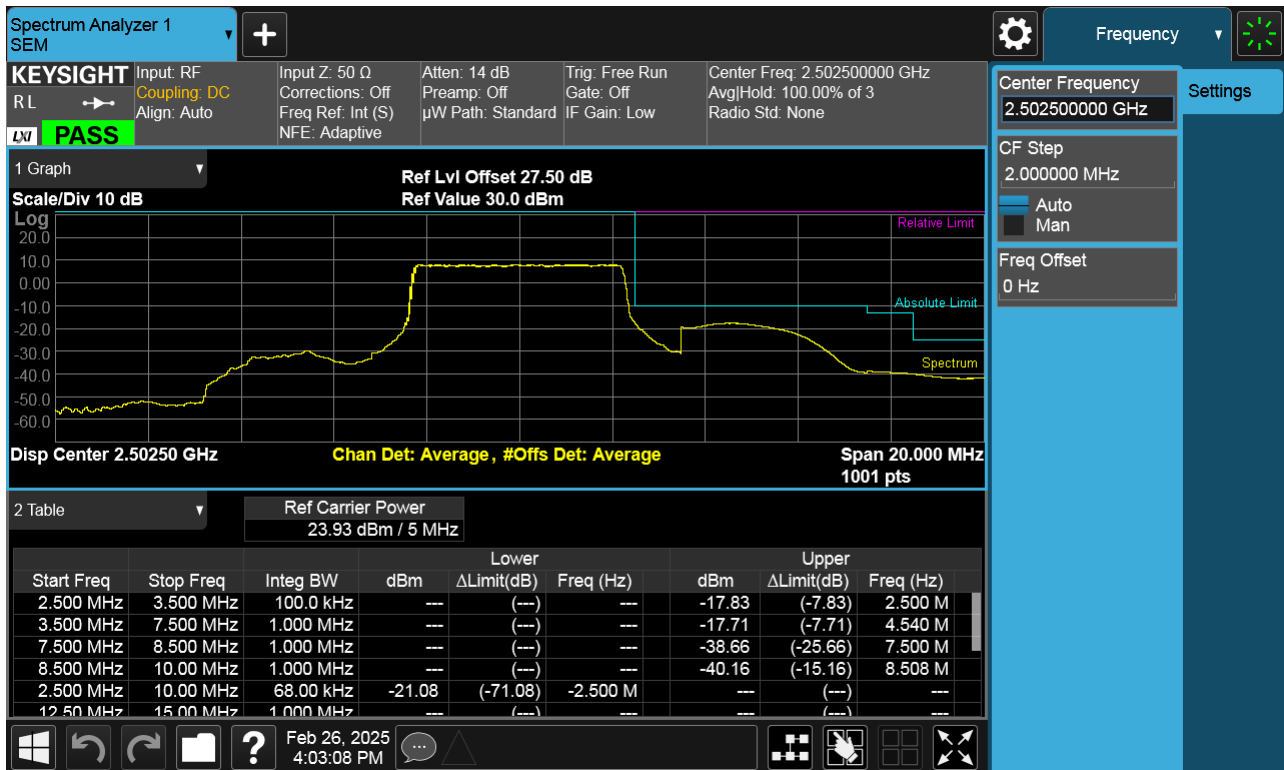
Low Channel Edge Plot (5 MHz BPSK)-1



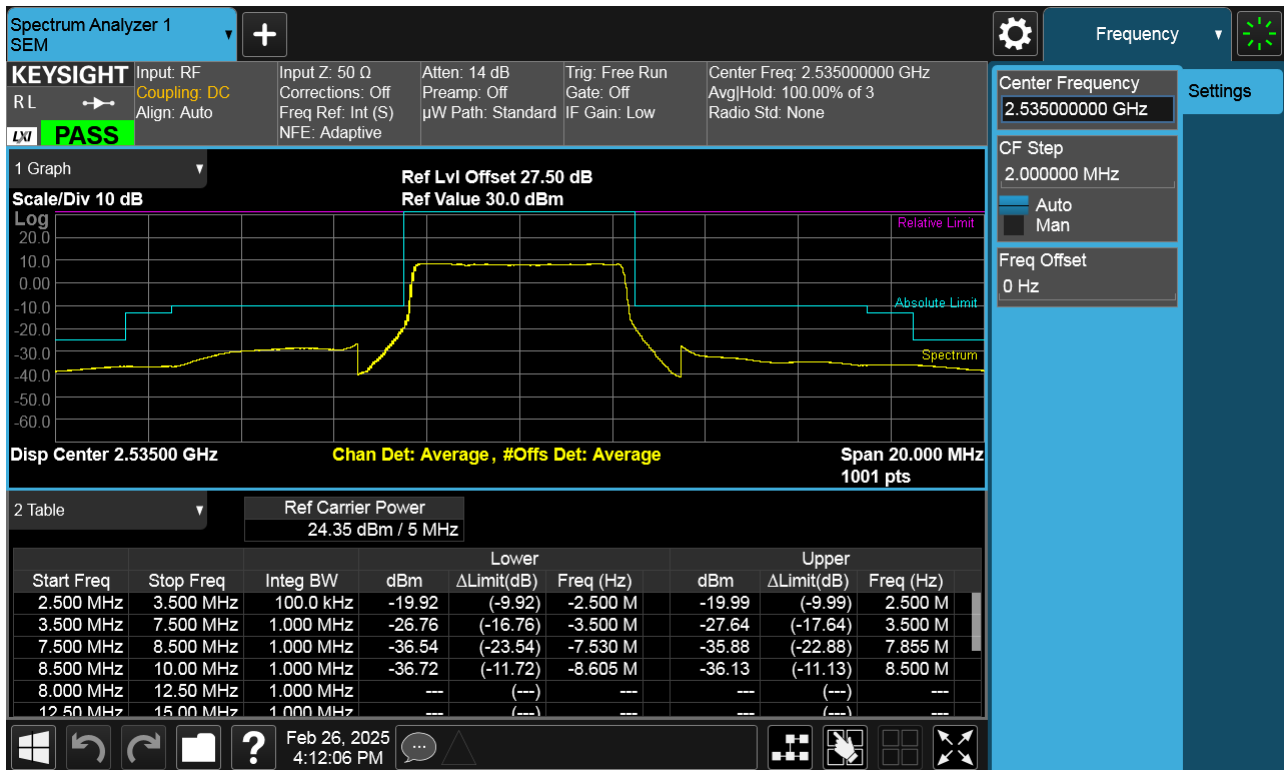
Low Channel Edge Plot (5 MHz BPSK_RB 1)-2



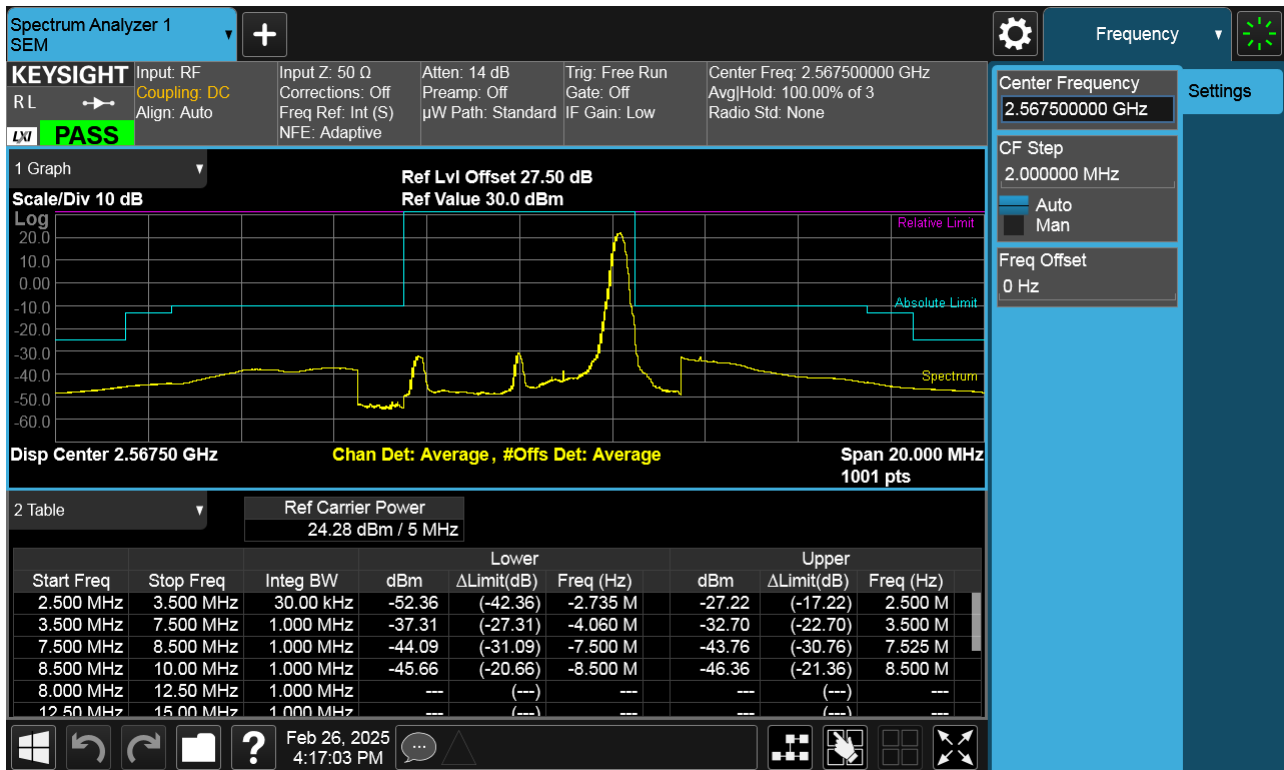
Low Channel Edge Plot (5 MHz BPSK)-2



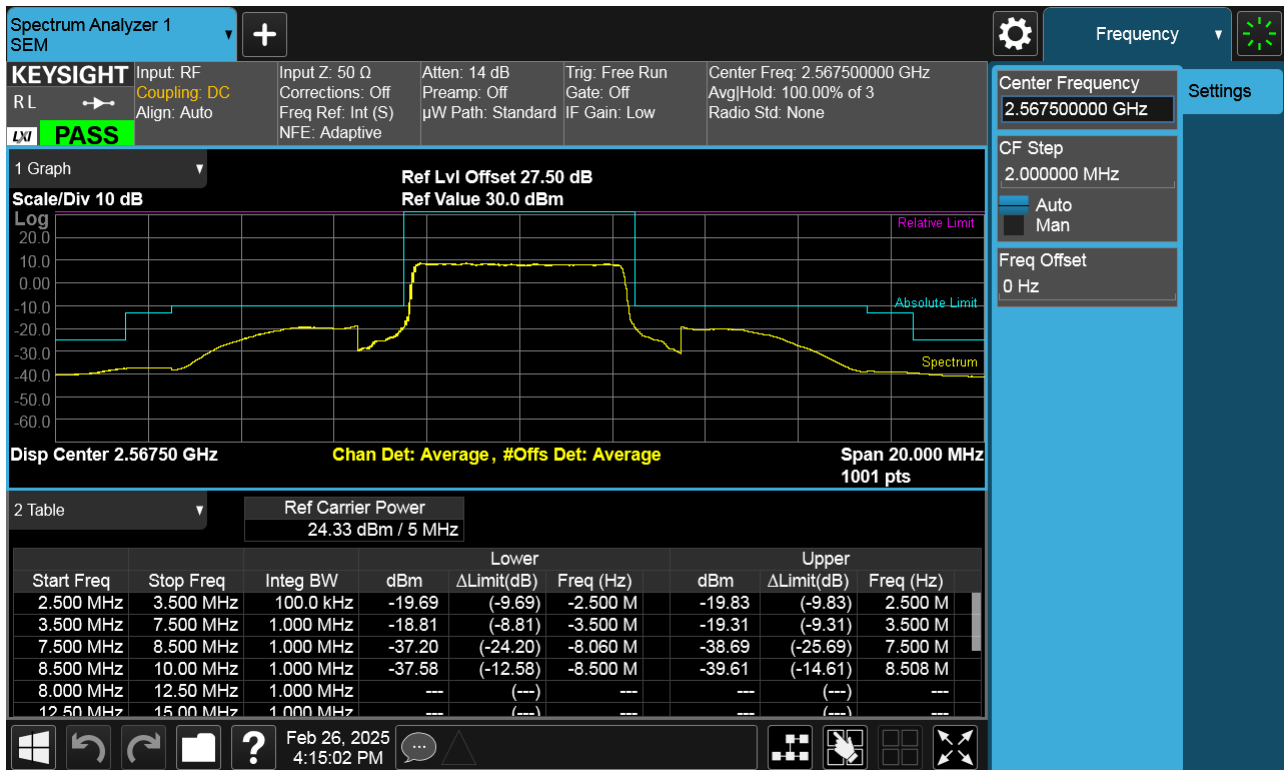
Mid Channel Edge Plot (5 MHz BPSK)



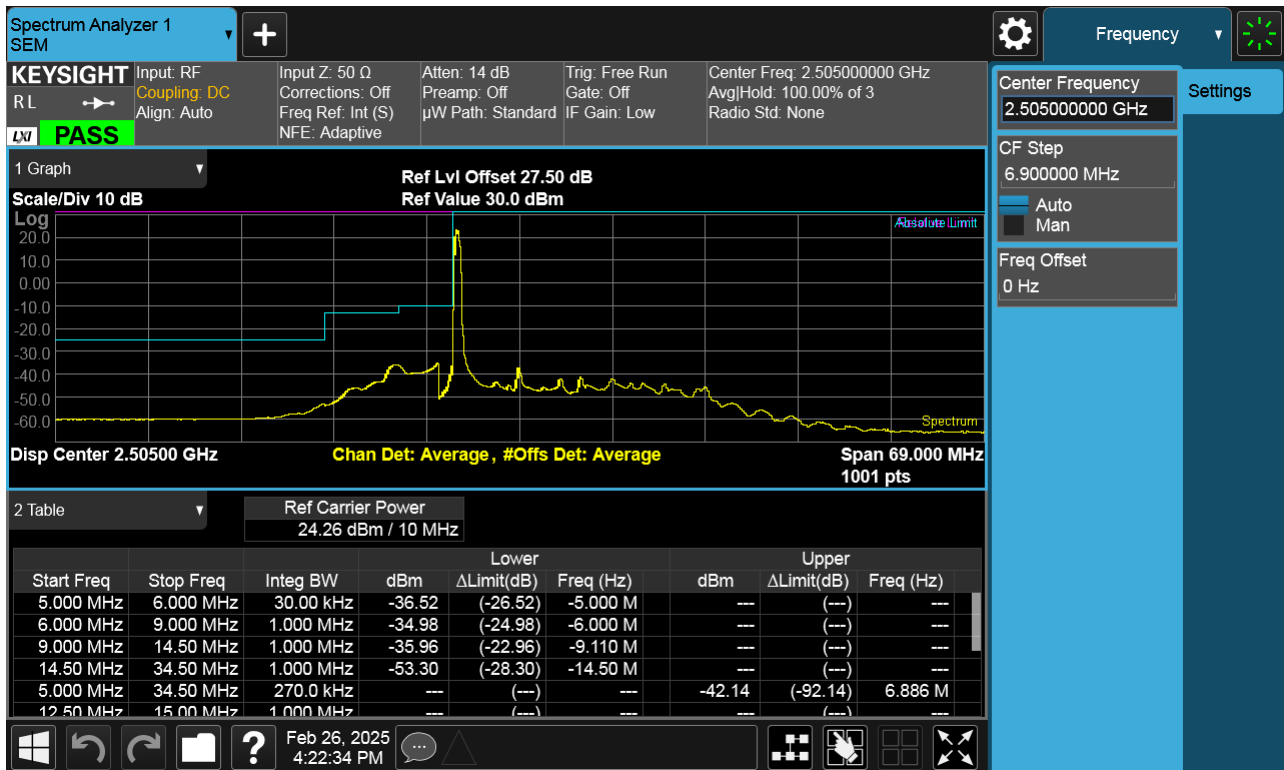
High Channel Edge Plot (5 MHz BPSK RB 1)



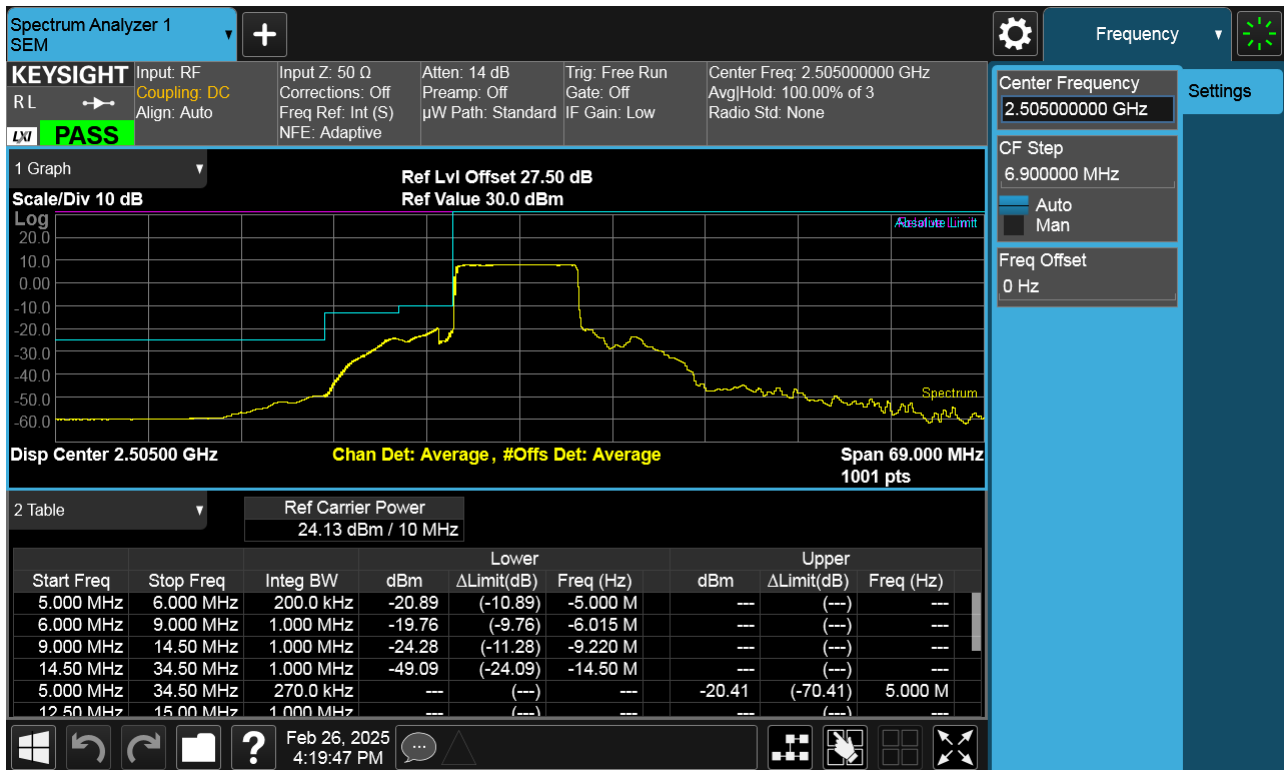
High Channel Edge Plot (5 MHz BPSK)



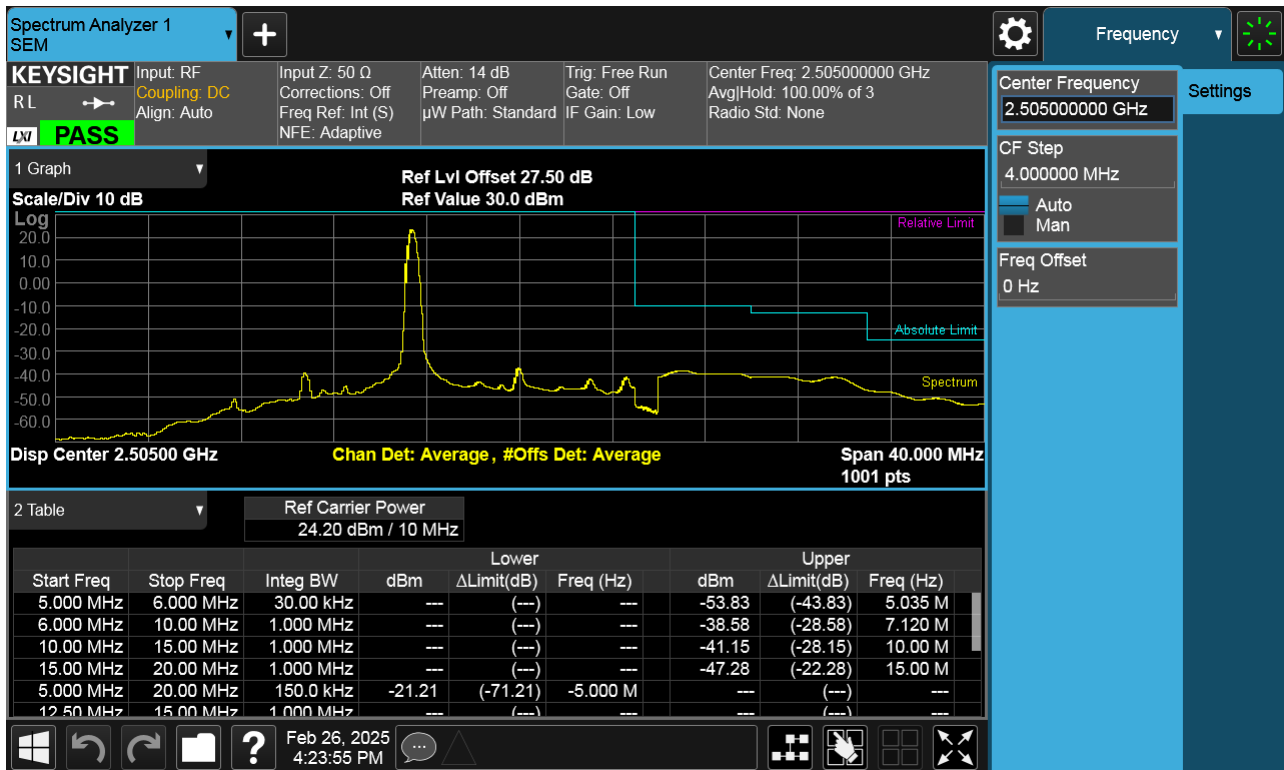
Low Channel Edge Plot (10 MHz BPSK RB 1)-1



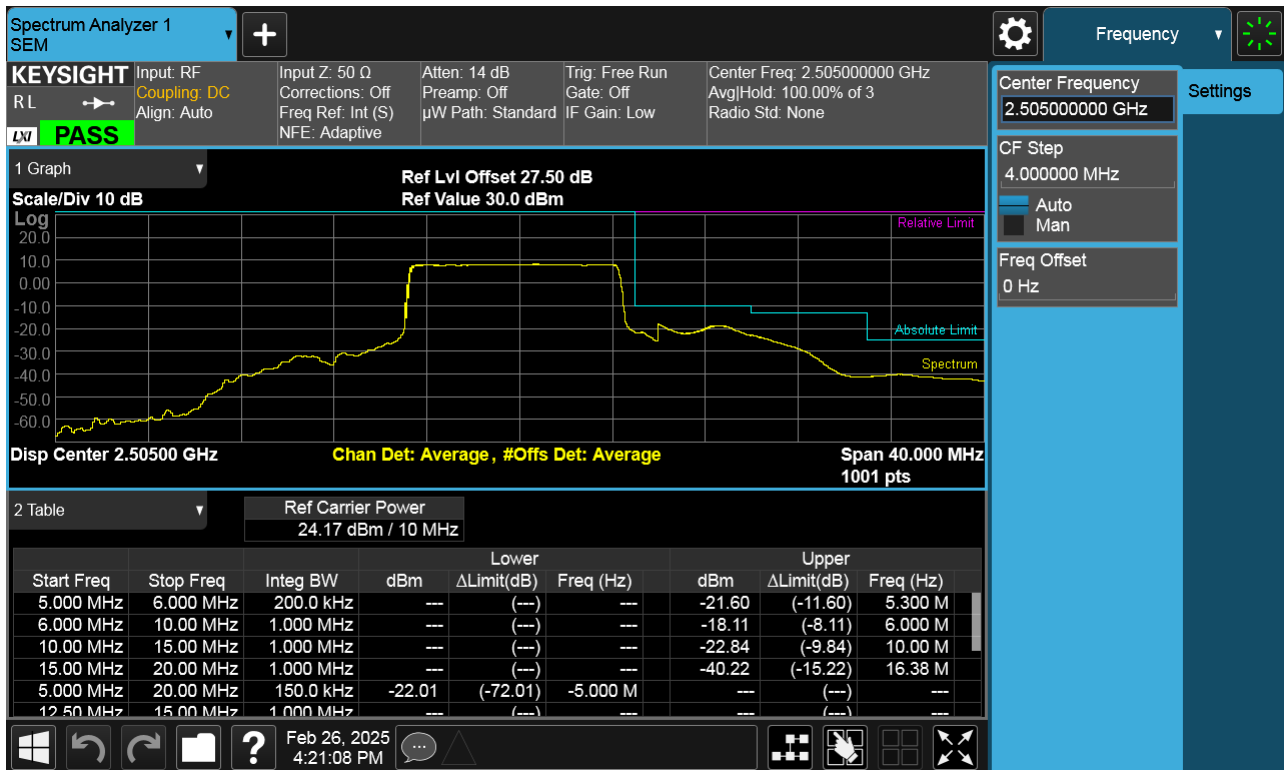
Low Channel Edge Plot (10 MHz BPSK)-1



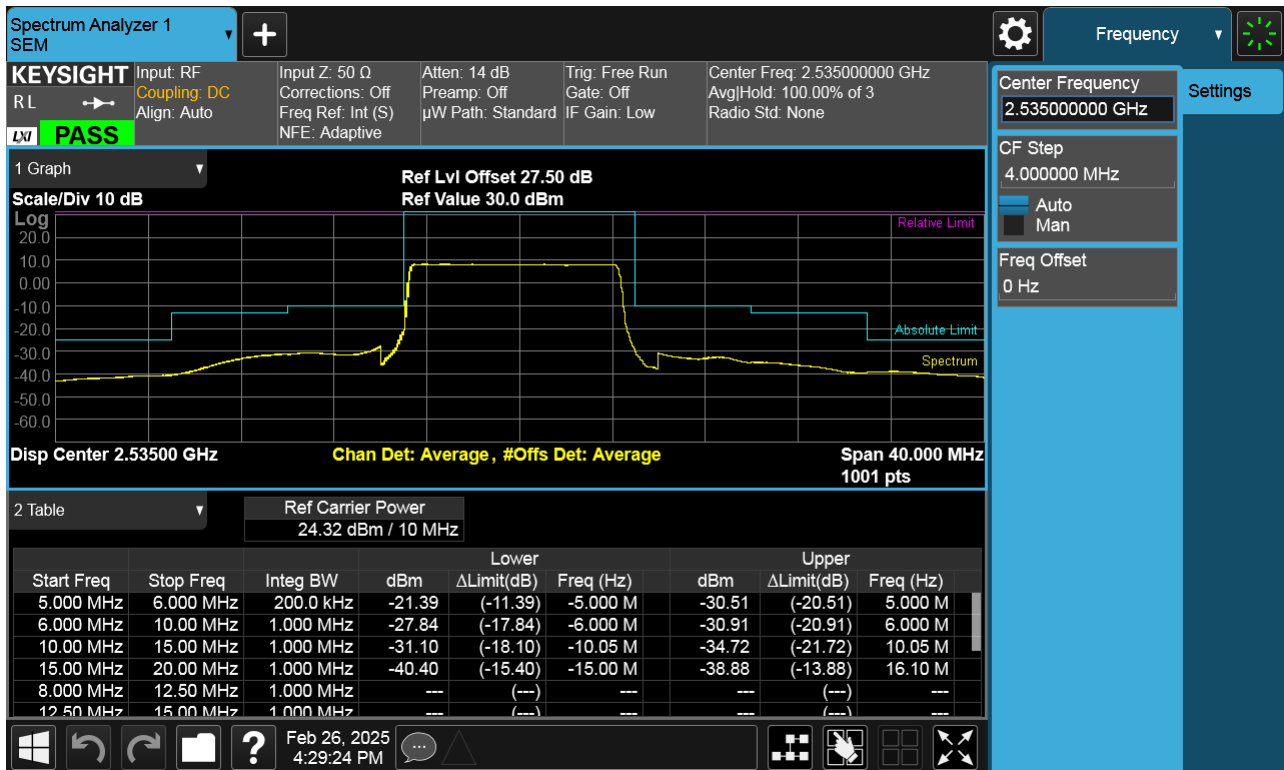
Low Channel Edge Plot (10 MHz BPSK RB 1)-2



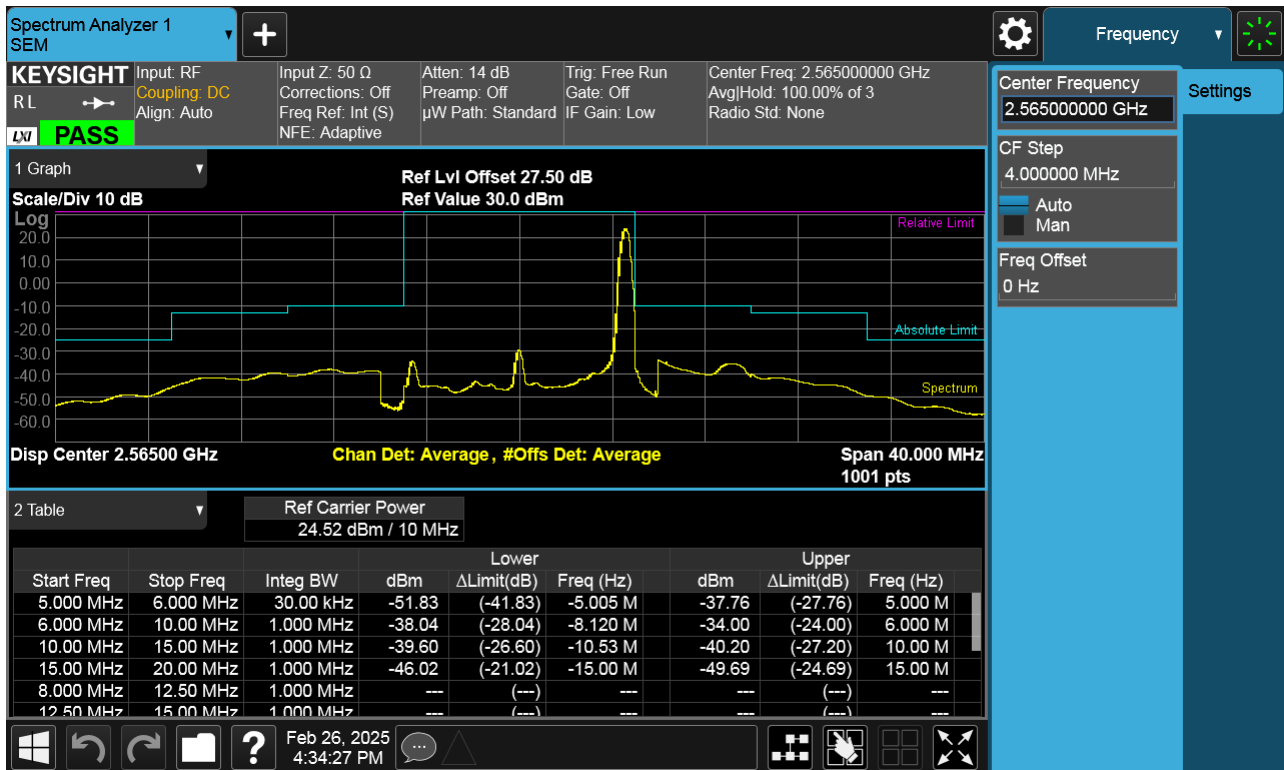
Low Channel Edge Plot (10 MHz BPSK)-2



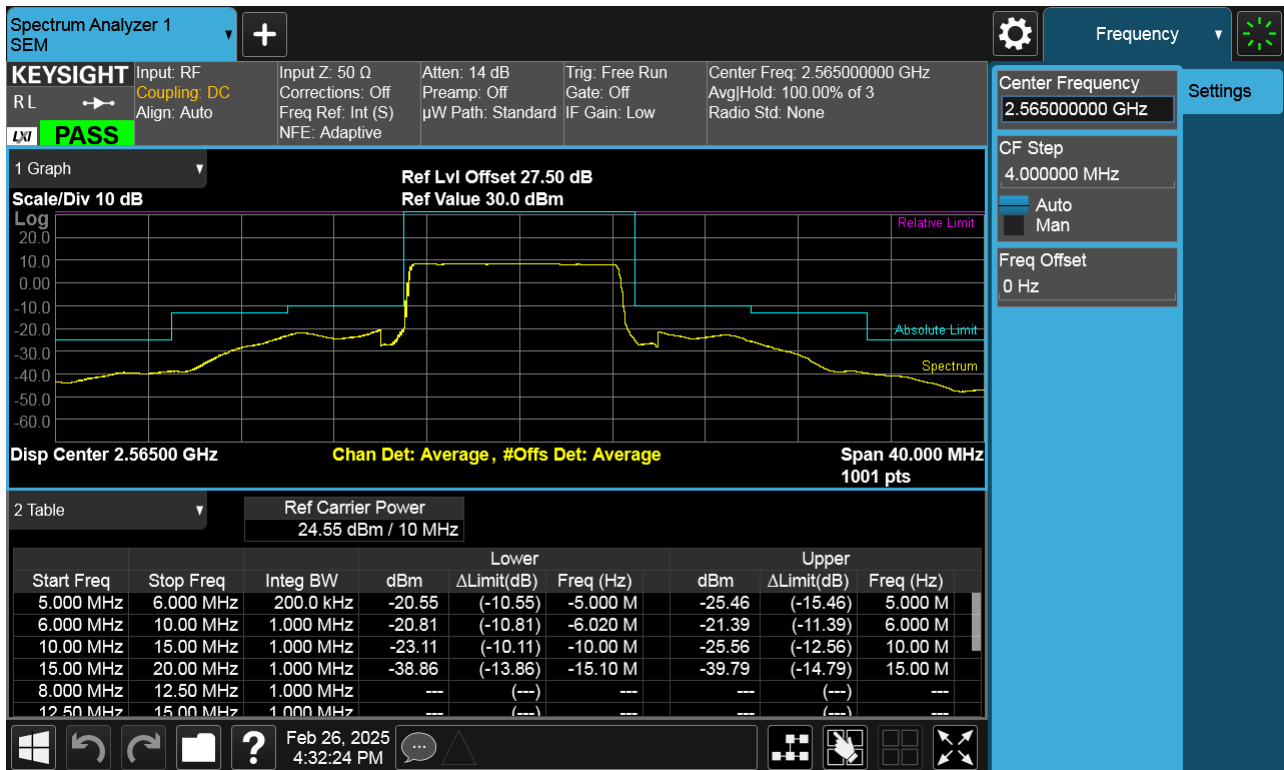
Mid Channel Edge Plot (10 MHz BPSK)



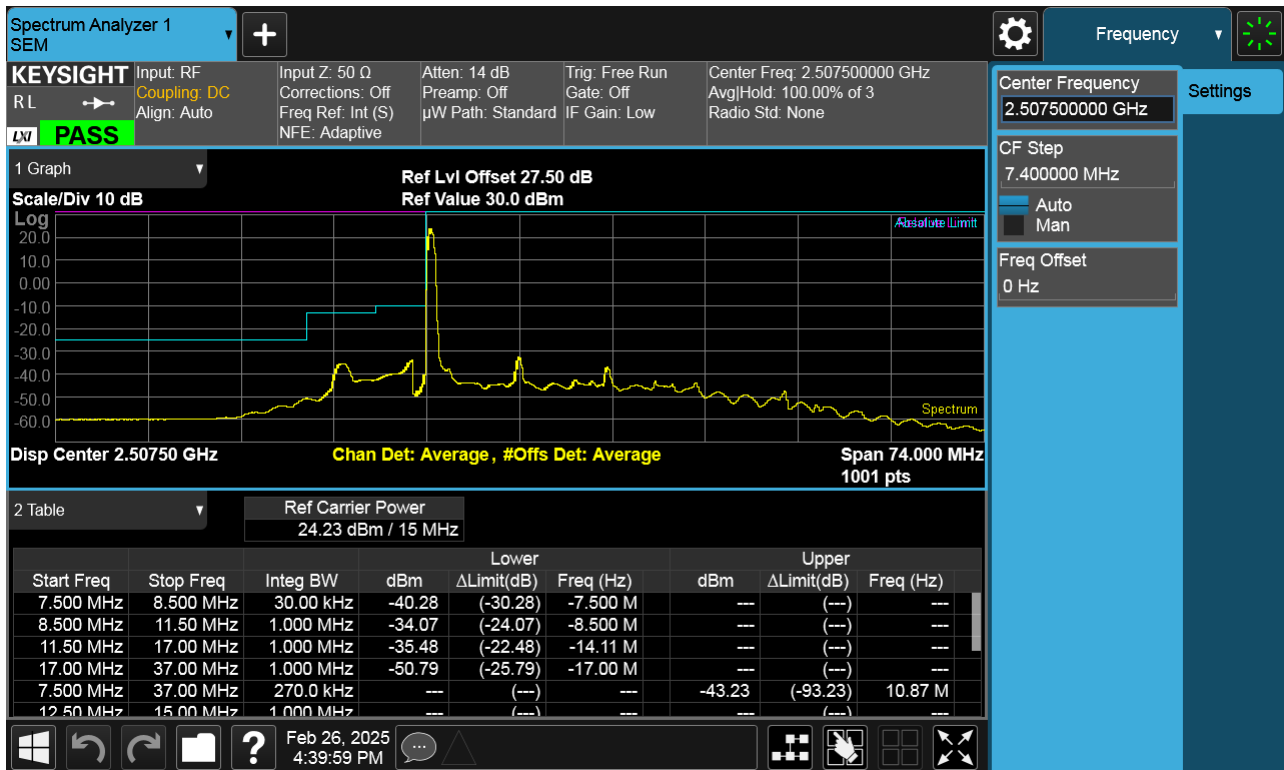
High Channel Edge Plot (10 MHz BPSK RB 1)



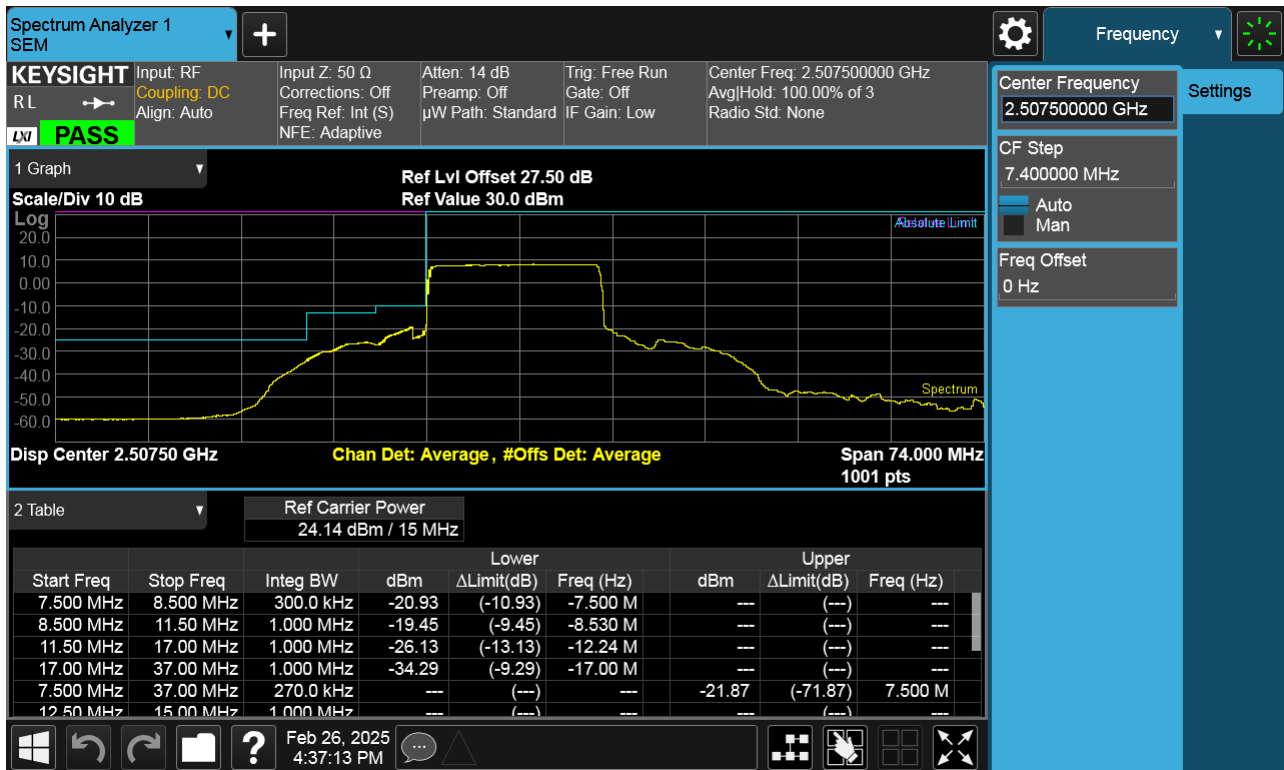
High Channel Edge Plot (10 MHz BPSK)



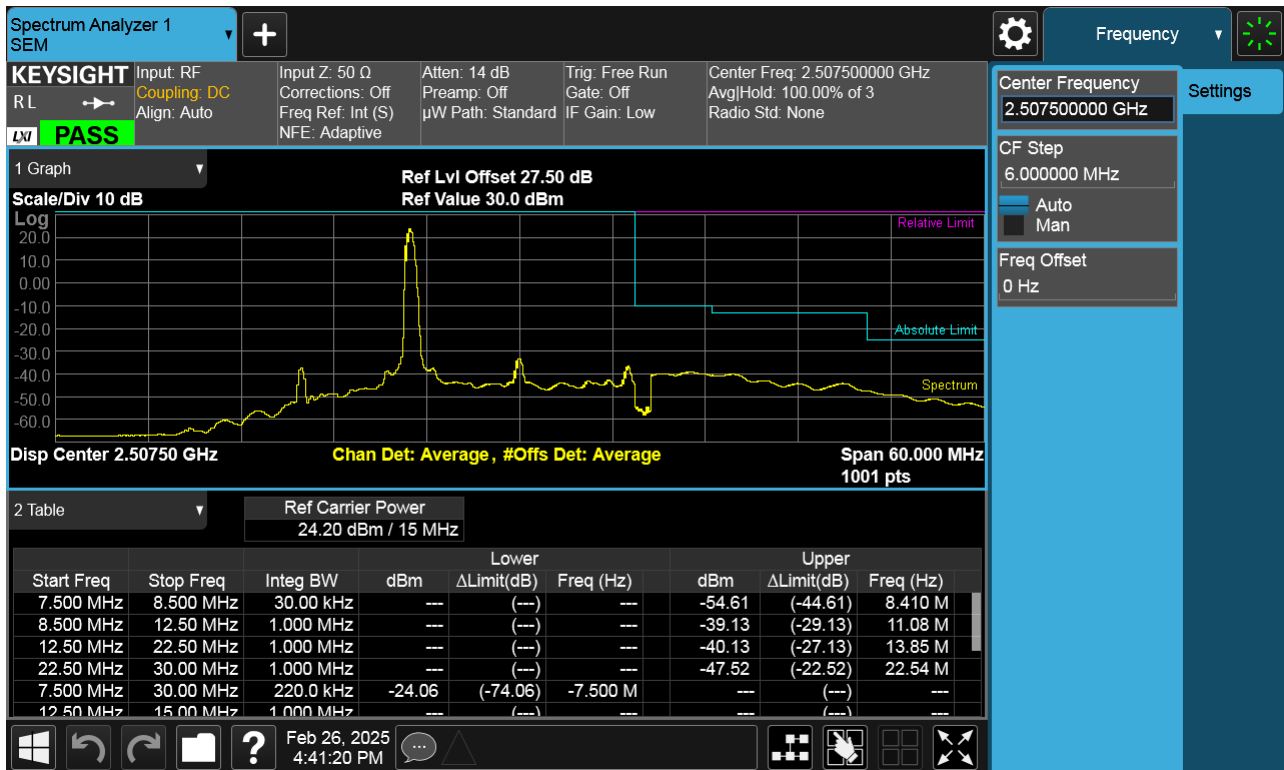
Low Channel Edge Plot (15 MHz BPSK RB 1)-1



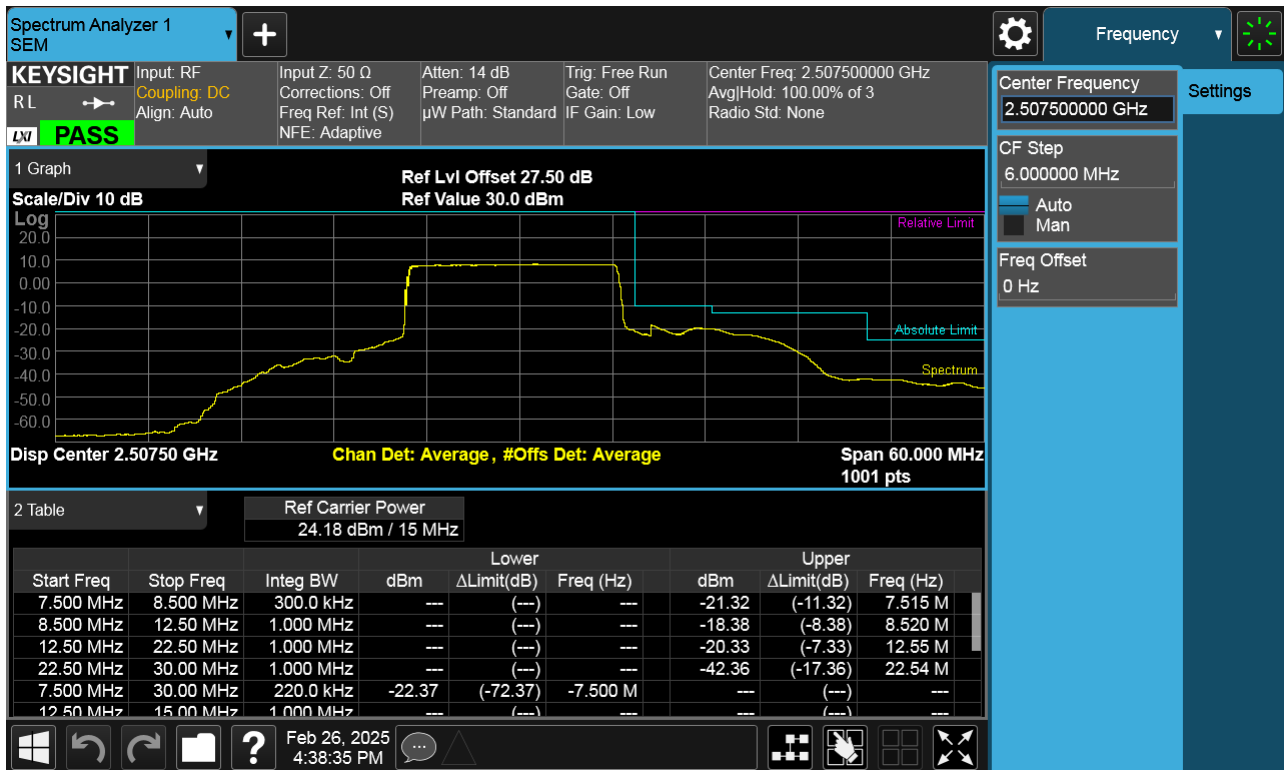
Low Channel Edge Plot (15 MHz BPSK)-1



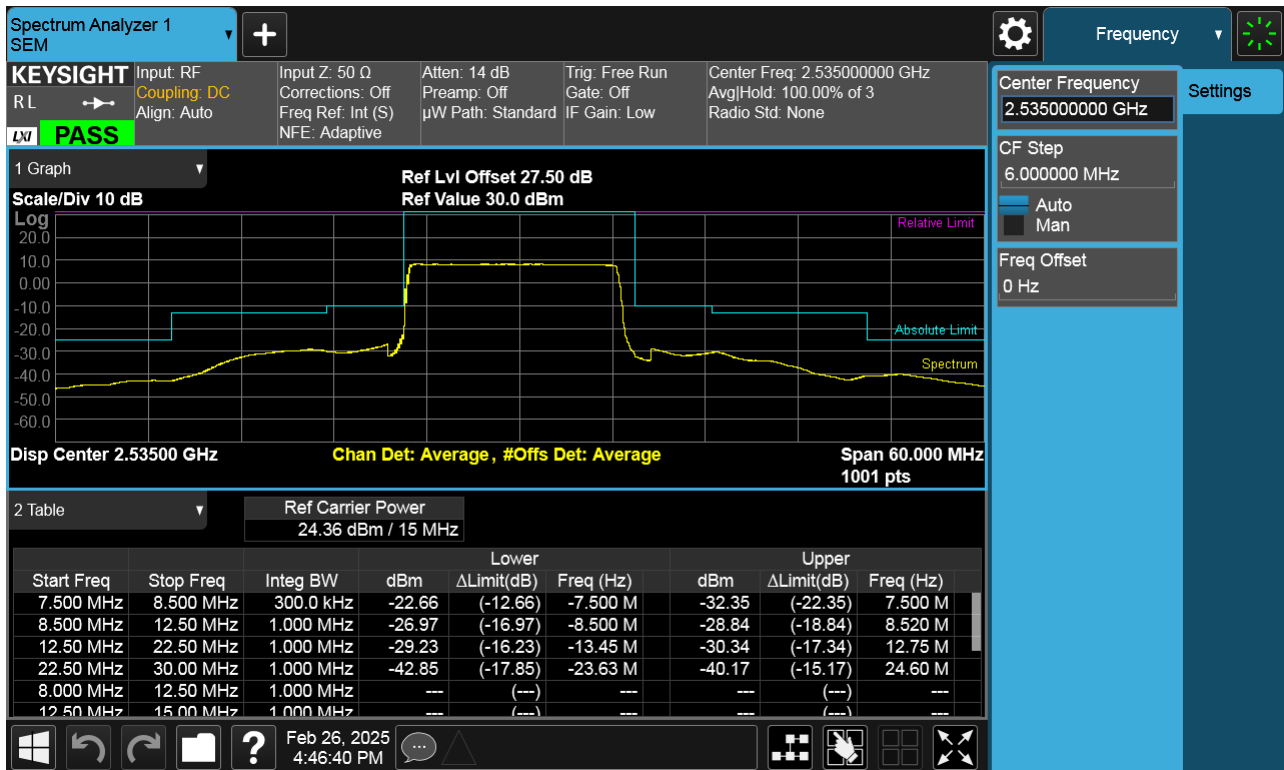
Low Channel Edge Plot (15 MHz BPSK_RB1)-2



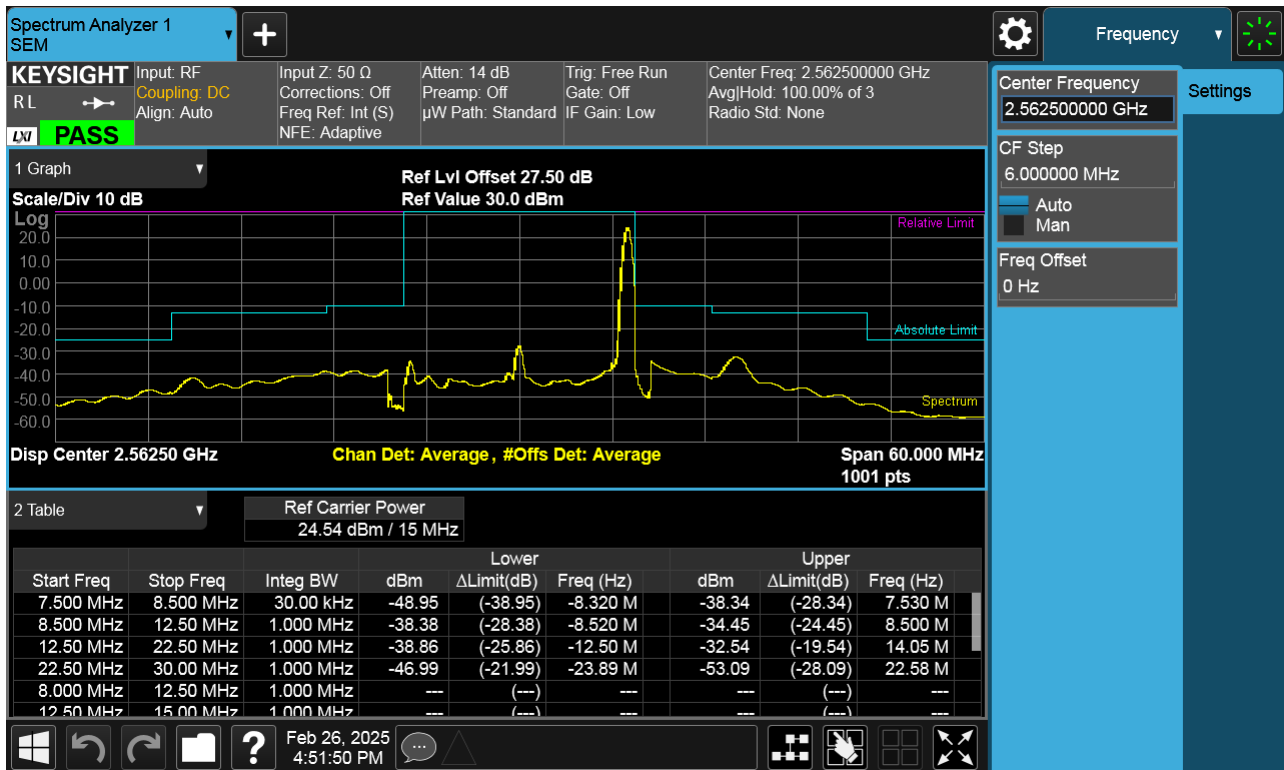
Low Channel Edge Plot (15 MHz BPSK)-2



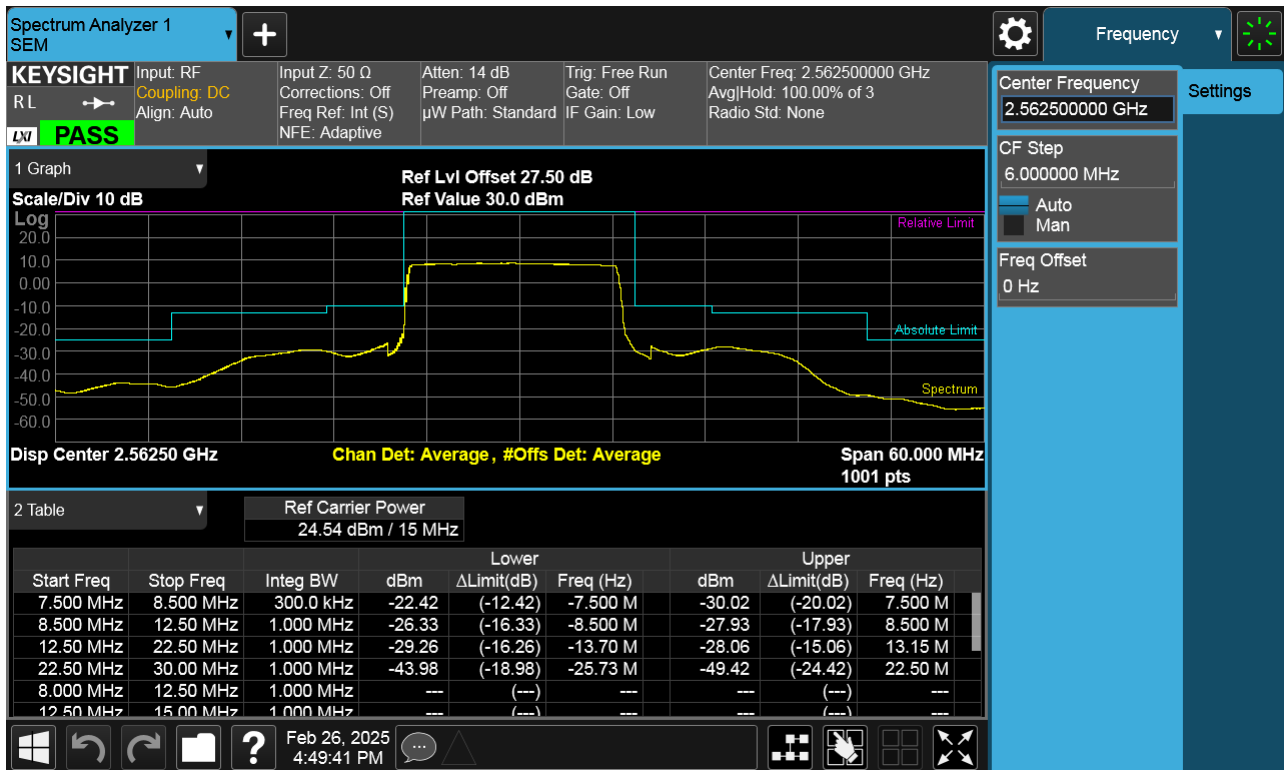
Mid Channel Edge Plot (15 MHz BPSK)



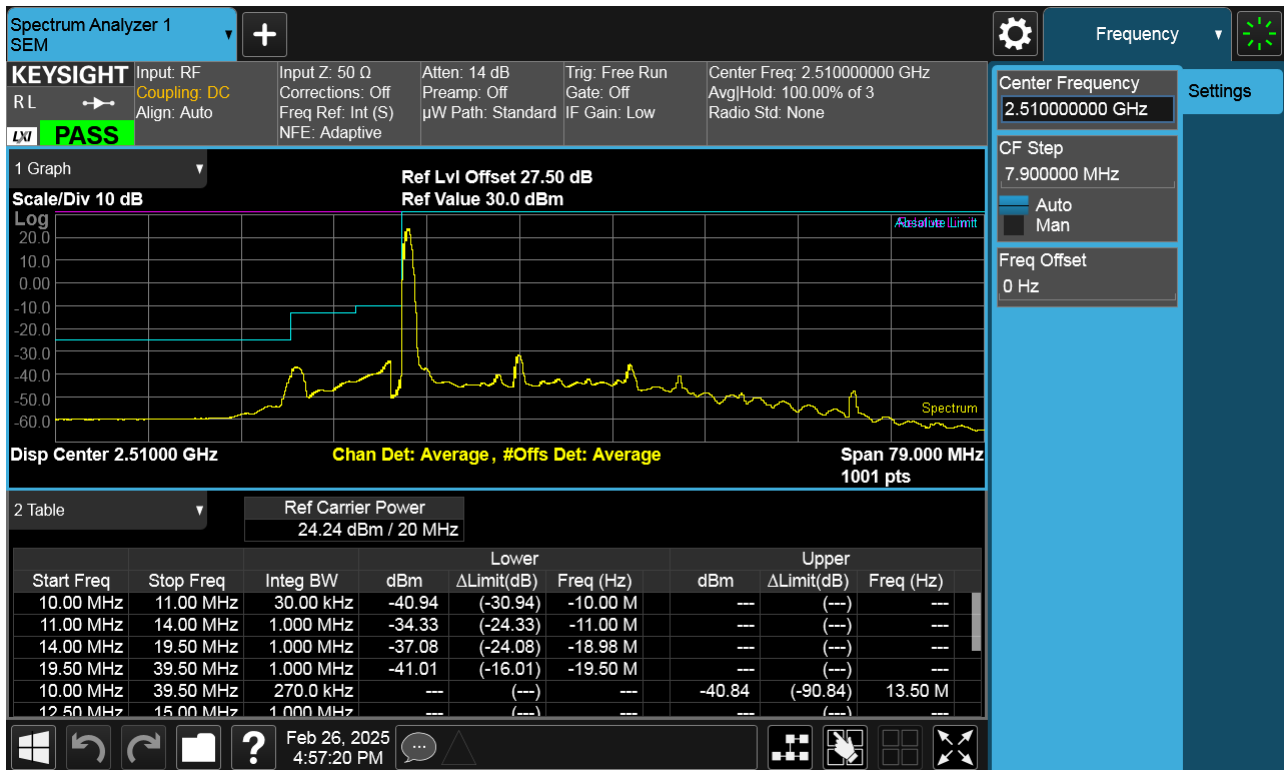
High Channel Edge Plot (15 MHz BPSK RB 1)



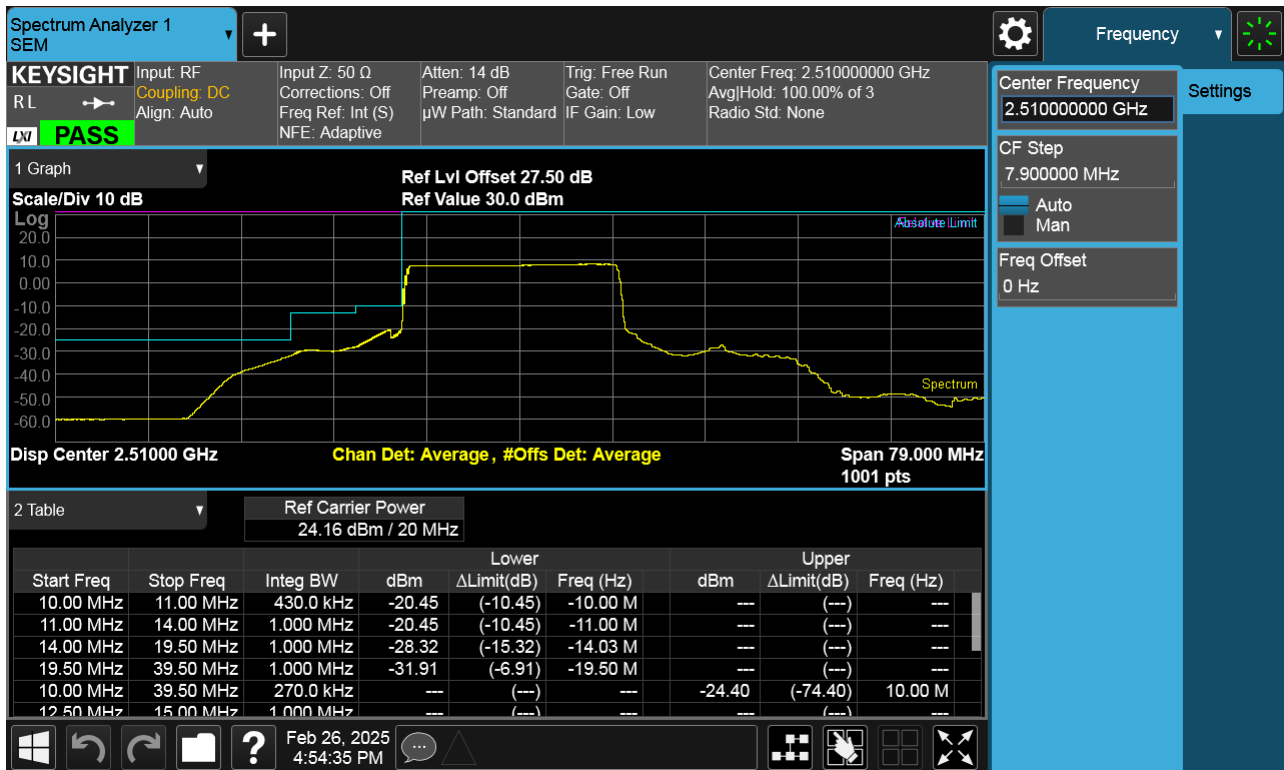
High Channel Edge Plot (15 MHz BPSK)



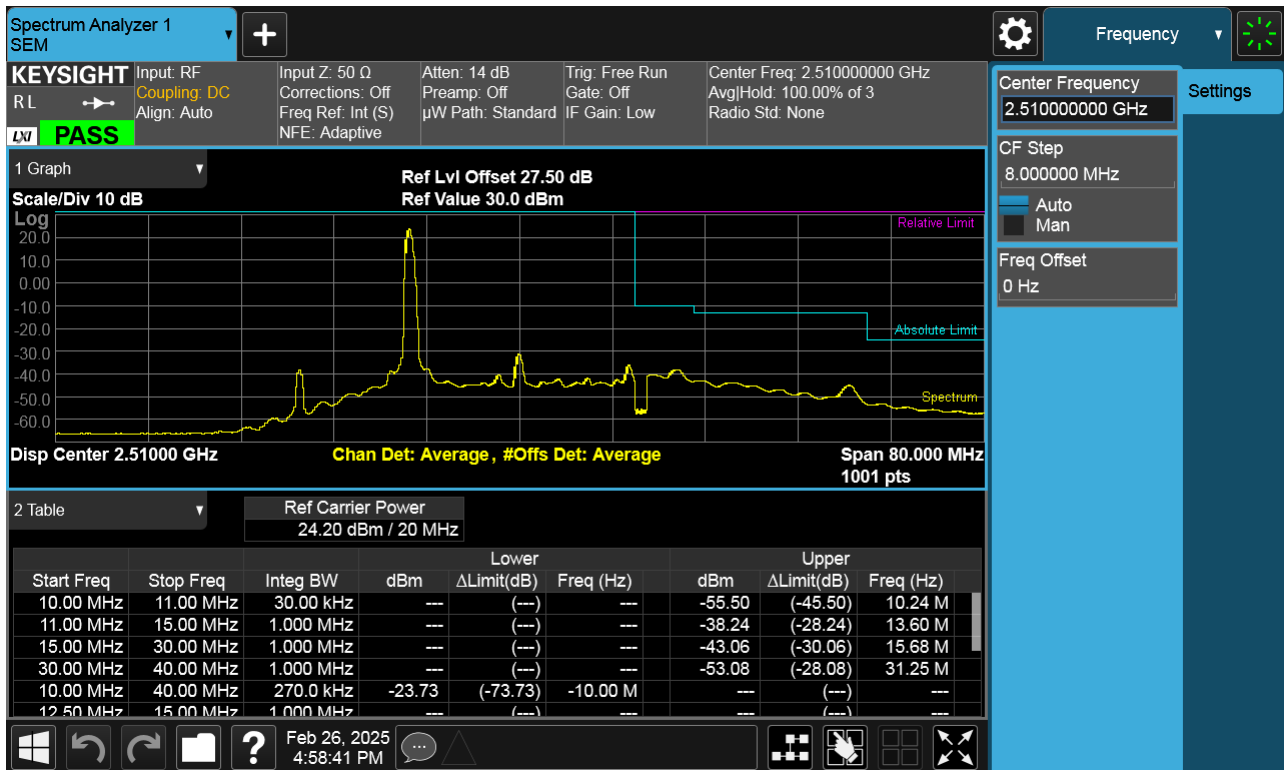
Low Channel Edge Plot (20 MHz BPSK RB 1)-1



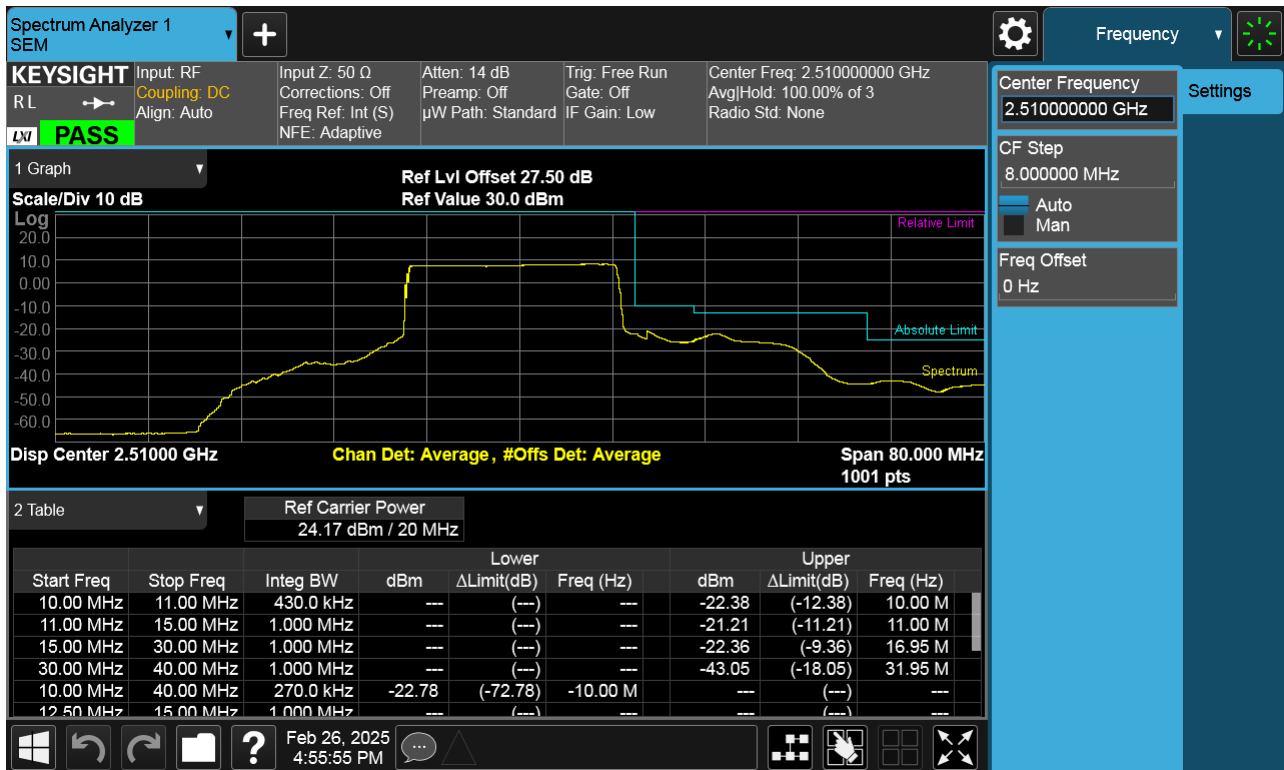
Low Channel Edge Plot (20 MHz BPSK)-1



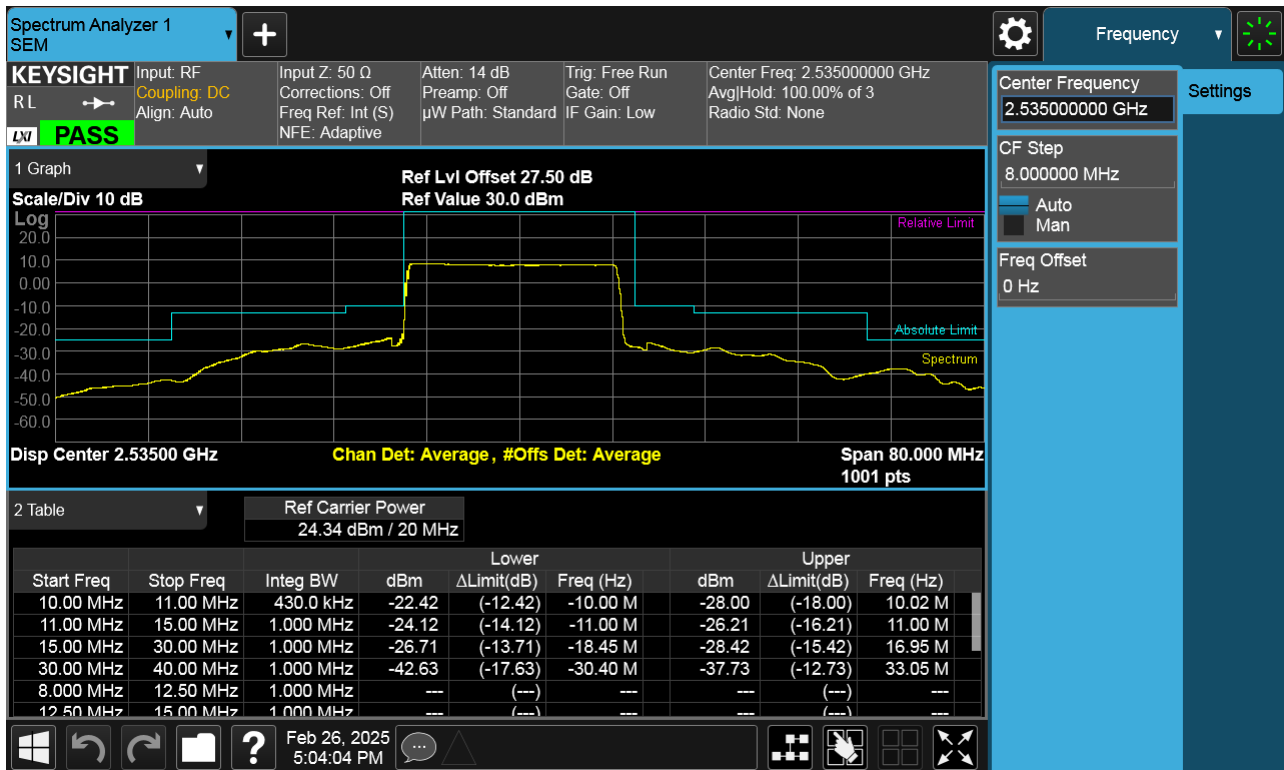
Low Channel Edge Plot (20 MHz BPSK_RB1)-2



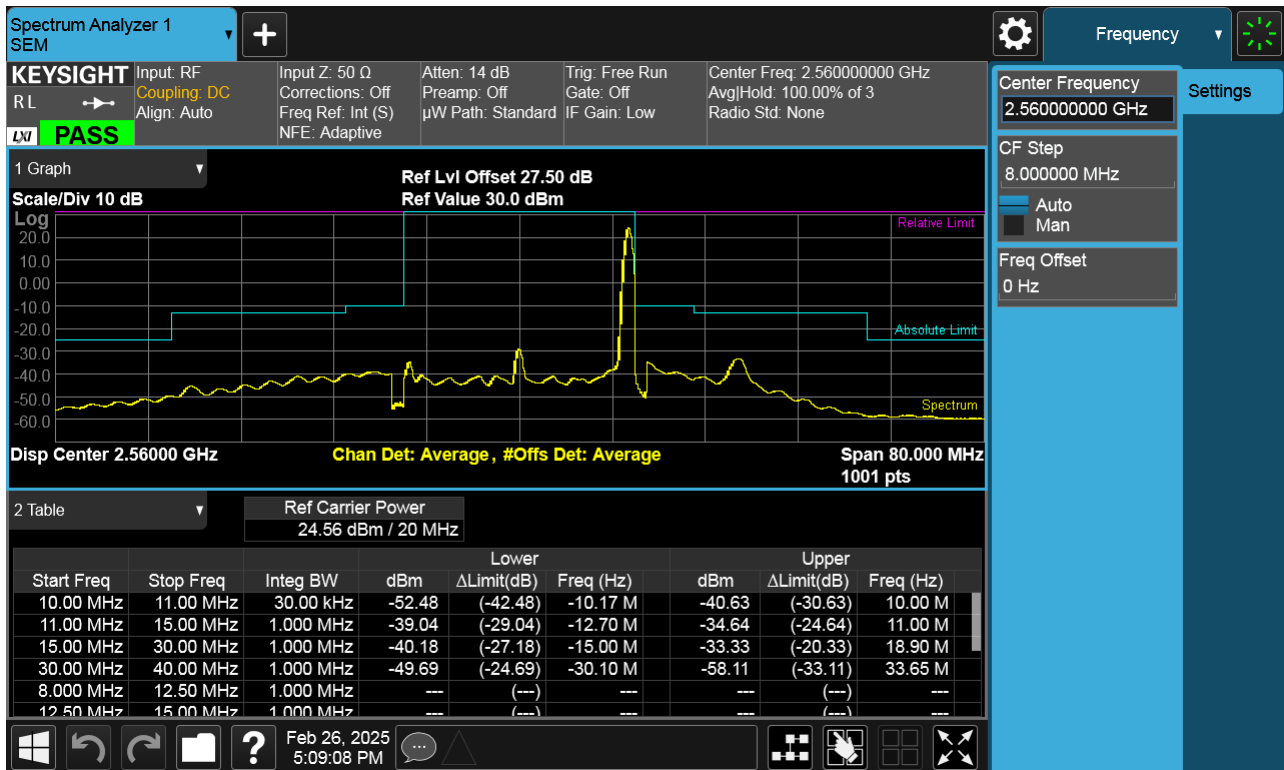
Low Channel Edge Plot (20 MHz BPSK)-2



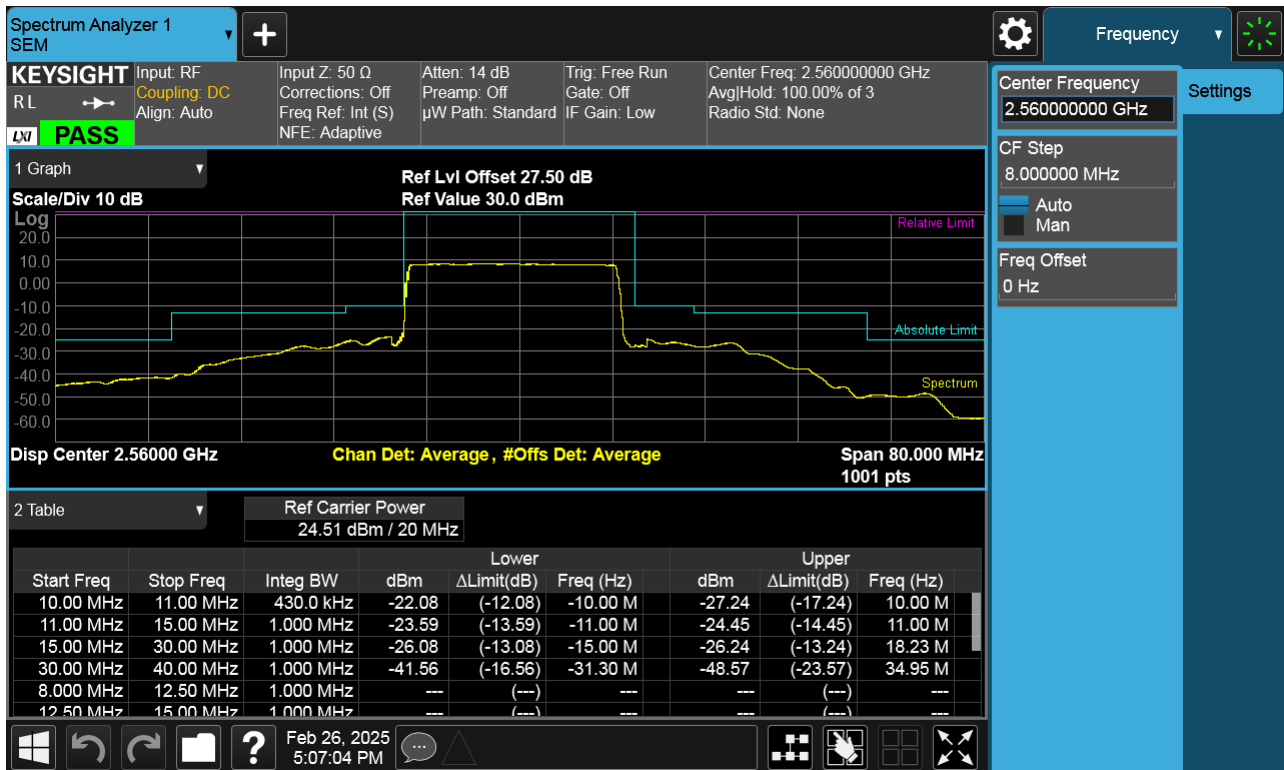
Mid Channel Edge Plot (20 MHz BPSK)



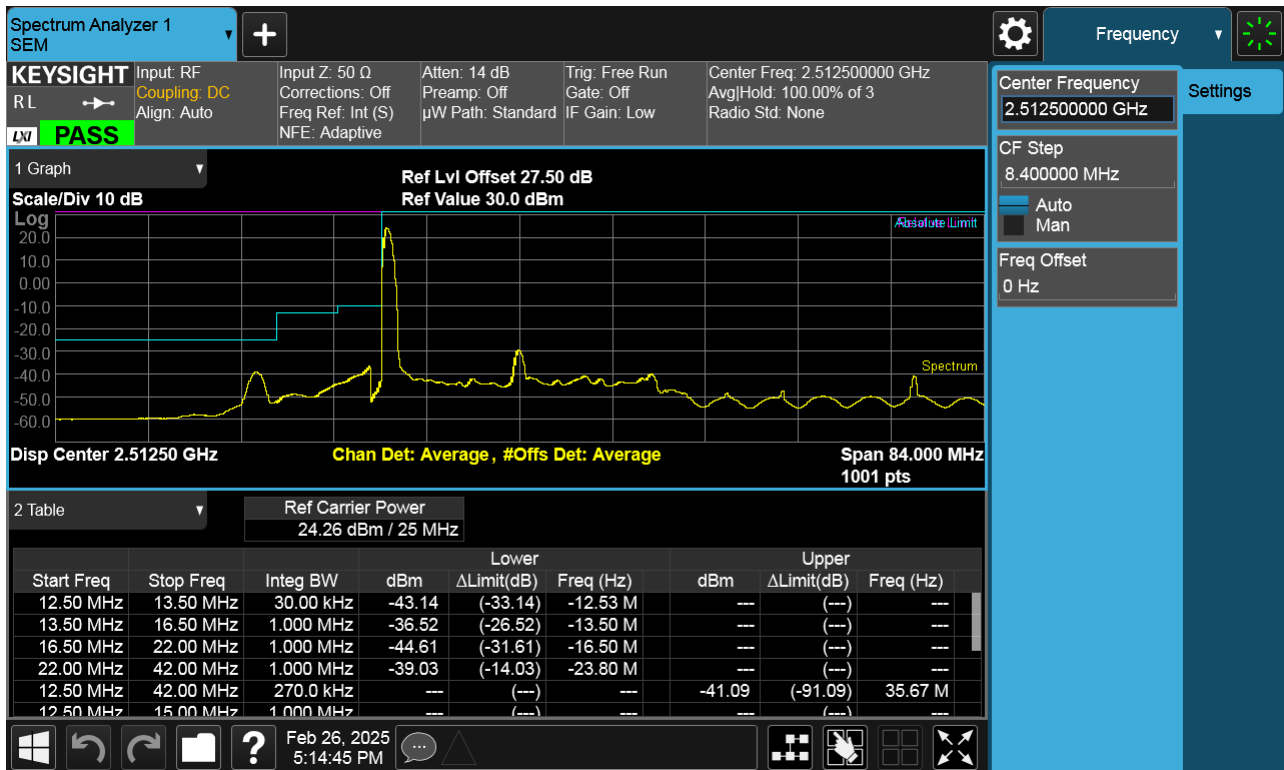
High Channel Edge Plot (20 MHz BPSK RB 1)



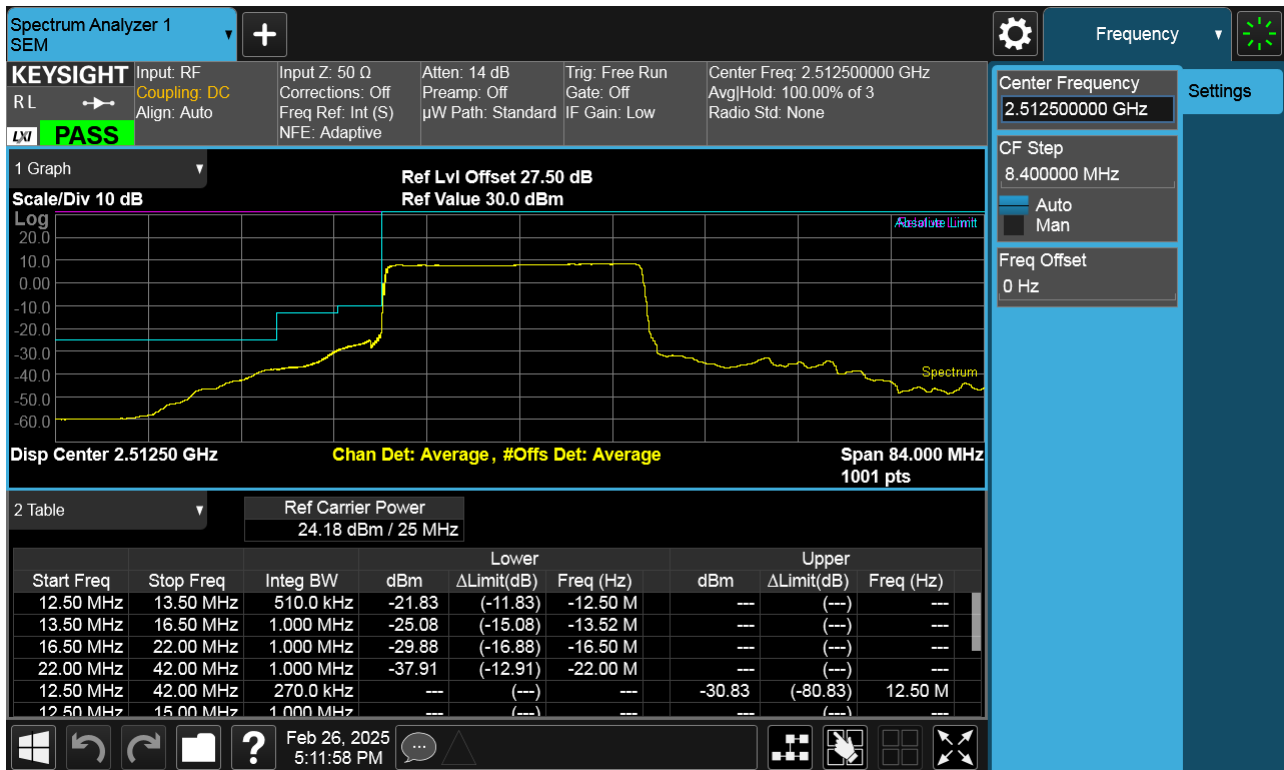
High Channel Edge Plot (20 MHz BPSK)



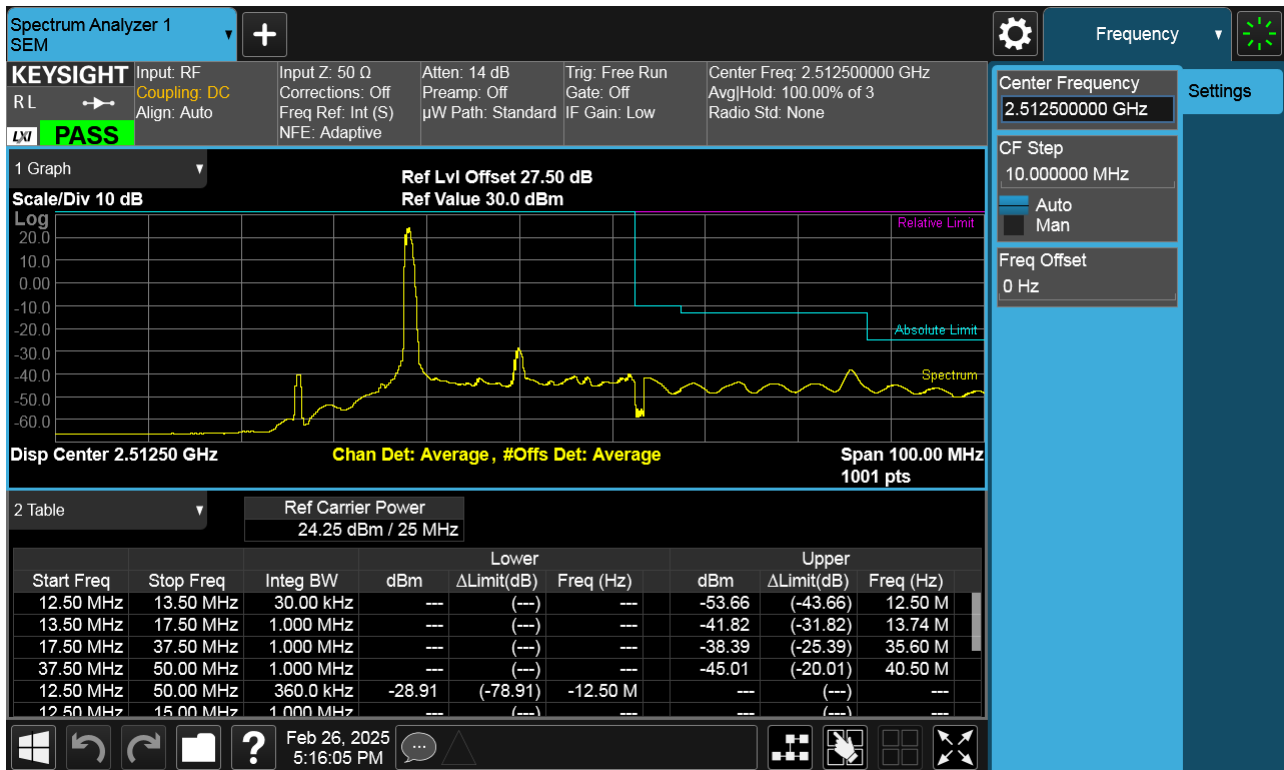
Low Channel Edge Plot (25 MHz BPSK RB 1)-1



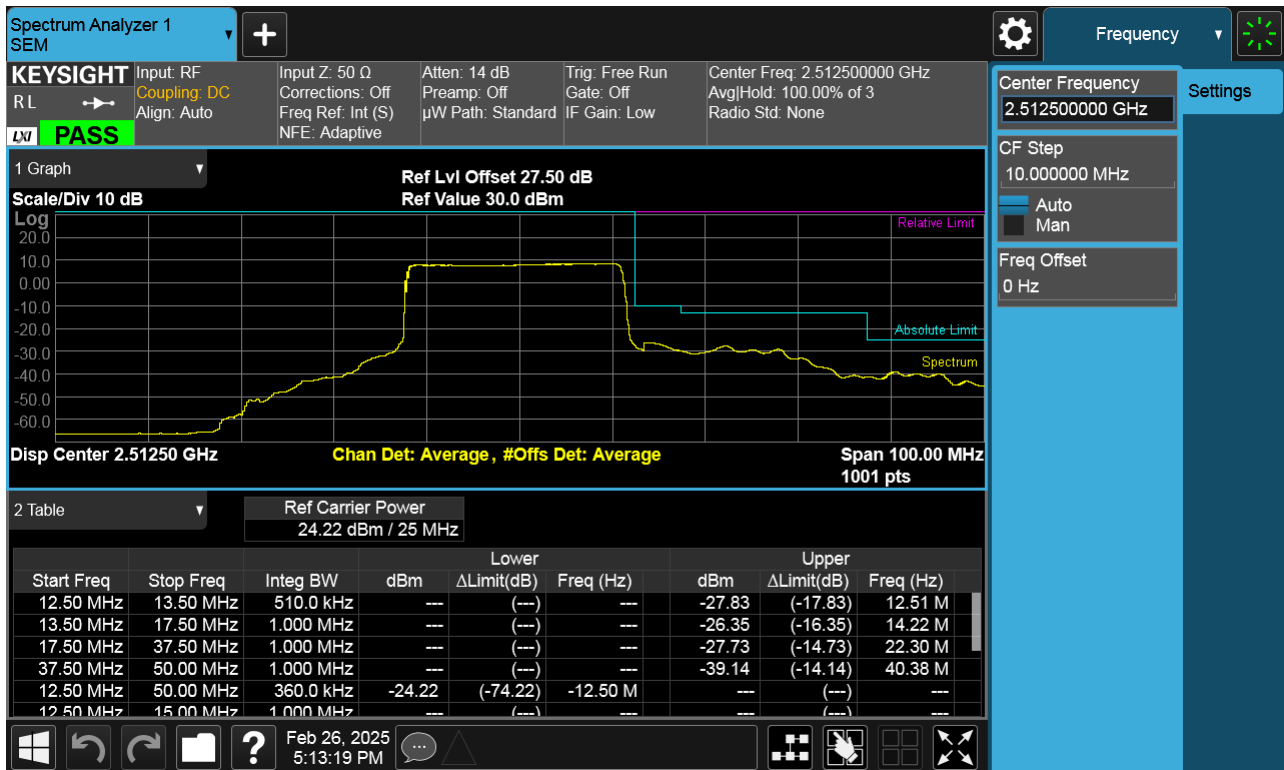
Low Channel Edge Plot (25 MHz BPSK)-1



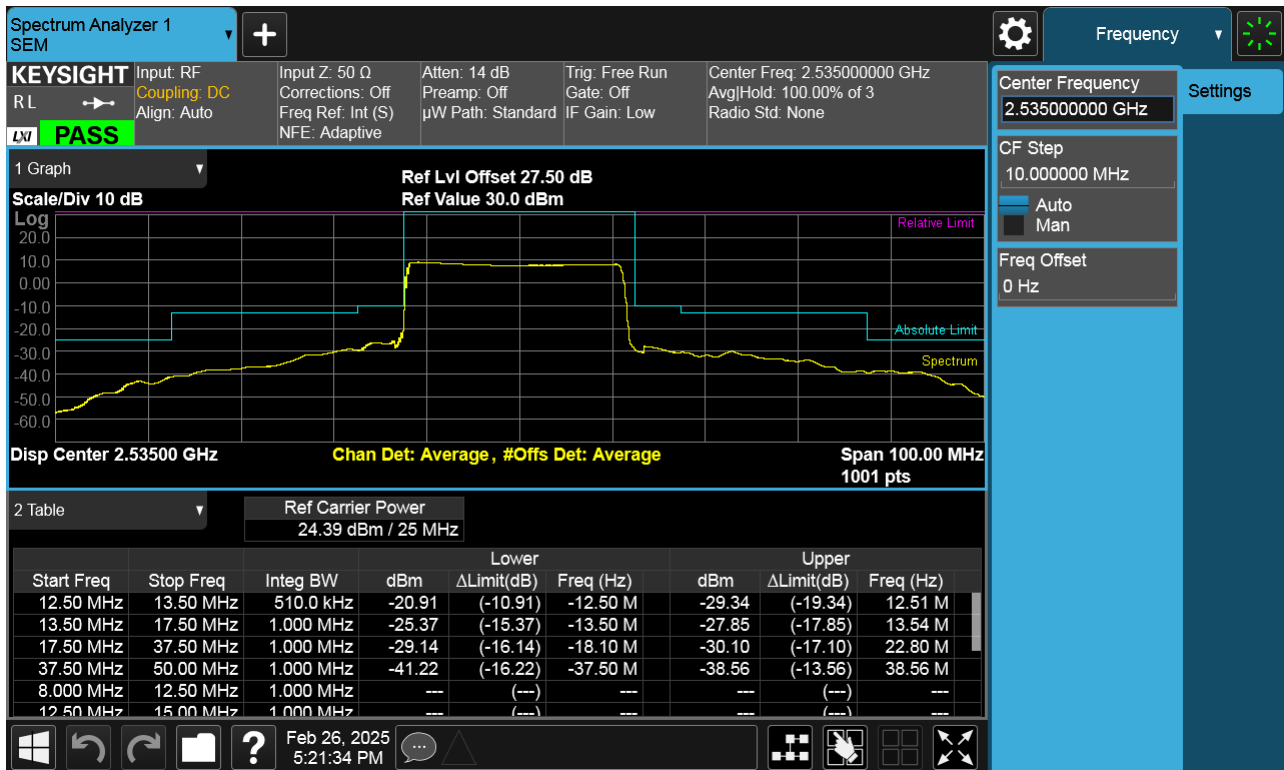
Low Channel Edge Plot (25 MHz BPSK_RB1)-2



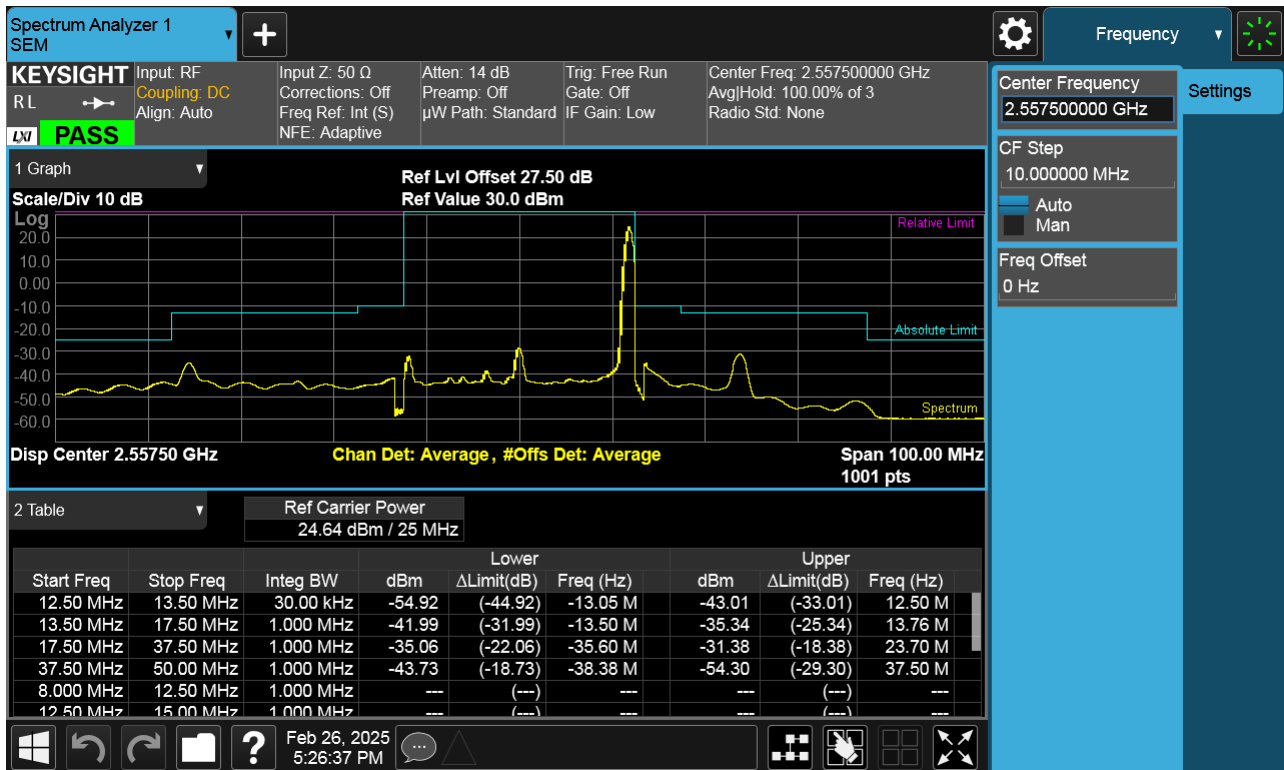
Low Channel Edge Plot (25 MHz BPSK)-2



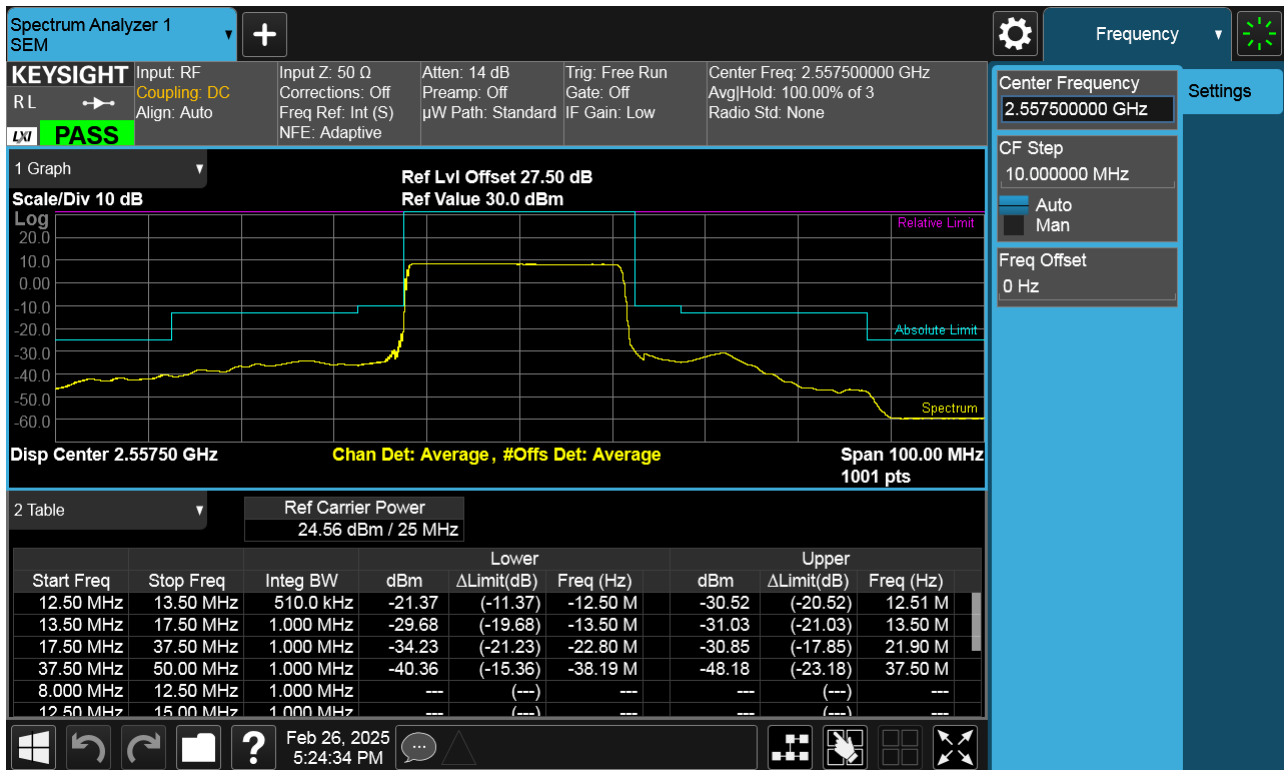
Mid Channel Edge Plot (25 MHz BPSK)



High Channel Edge Plot (25 MHz BPSK RB 1)



High Channel Edge Plot (25 MHz BPSK)



Low Channel Edge Plot (30 MHz BPSK RB 1)-1

