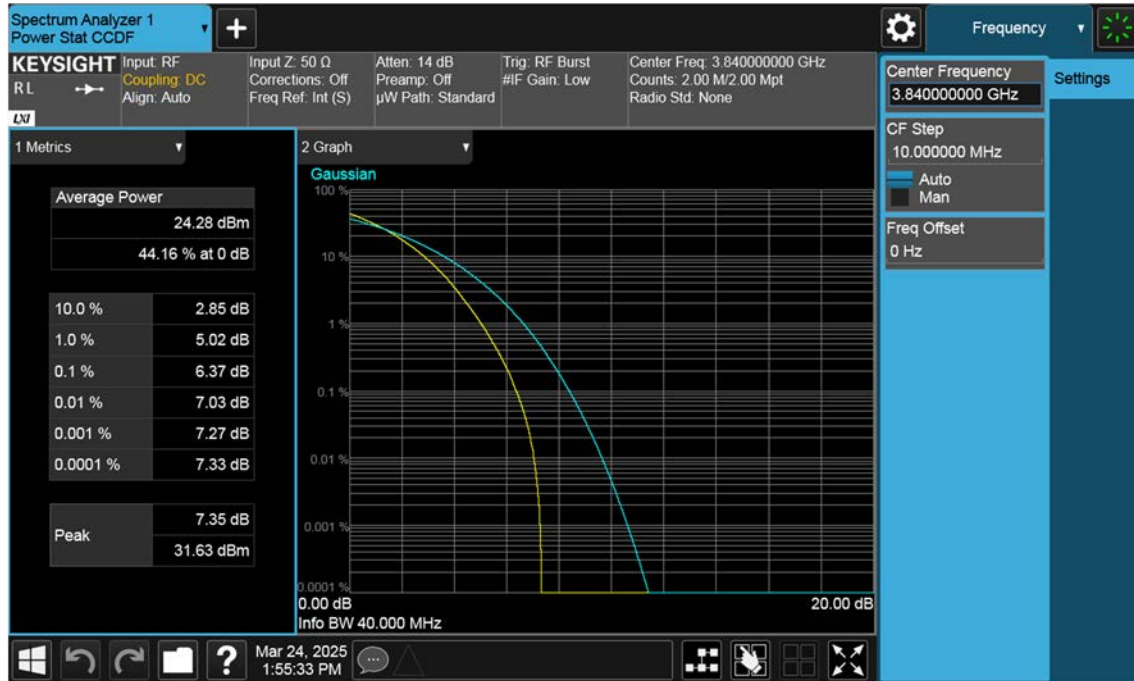
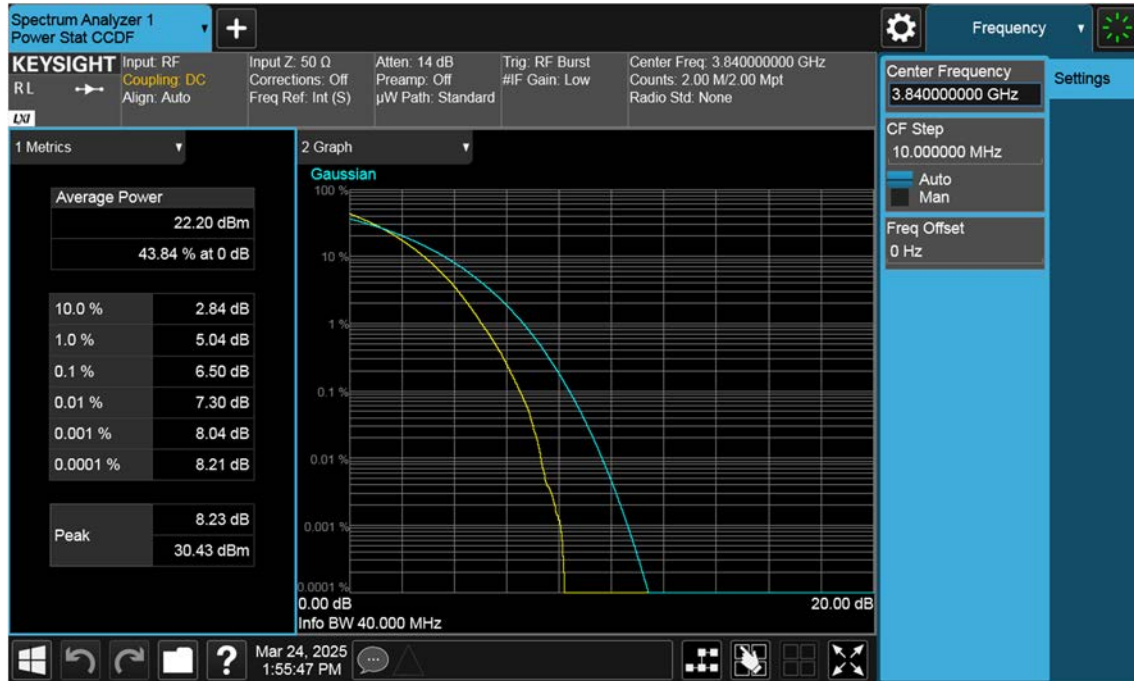


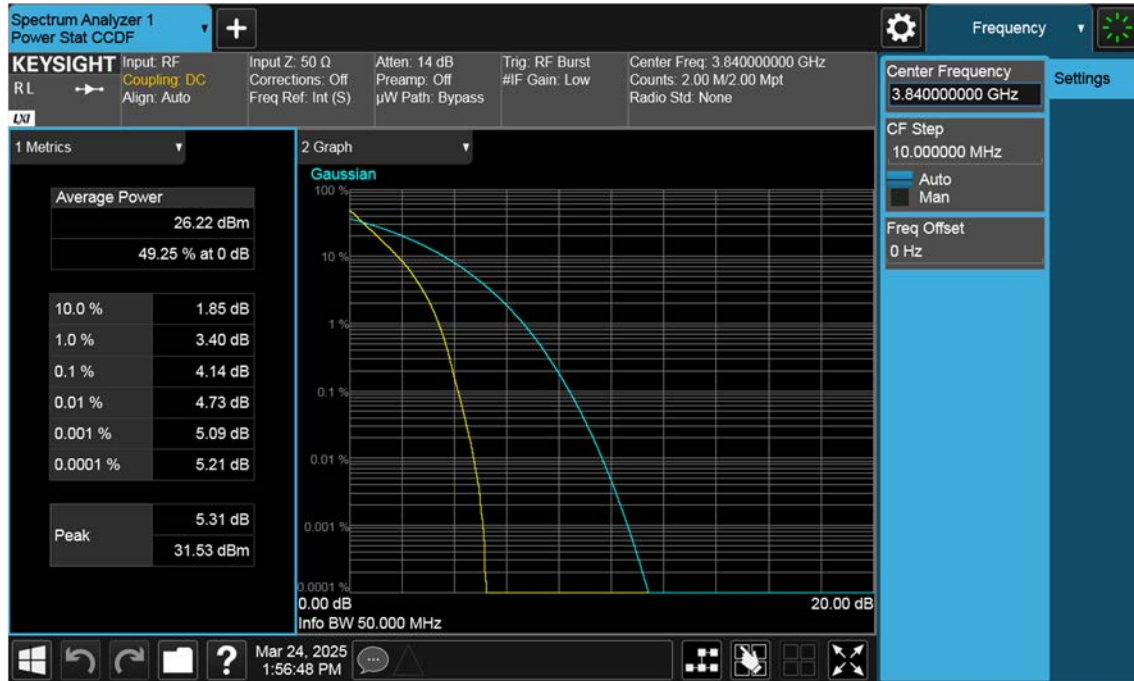
n77(3700~3980 MHz)_40 M_PAR_Mid_64QAM_FullRB



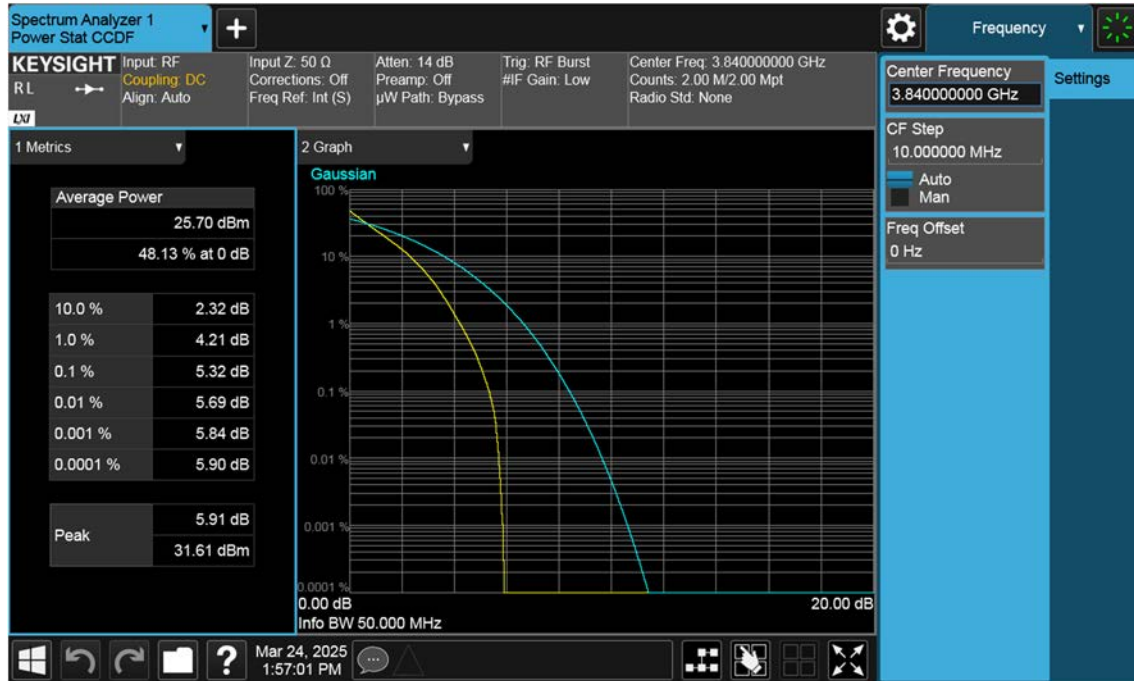
n77(3700~3980 MHz)_40 M_PAR_Mid_256QAM_FullRB



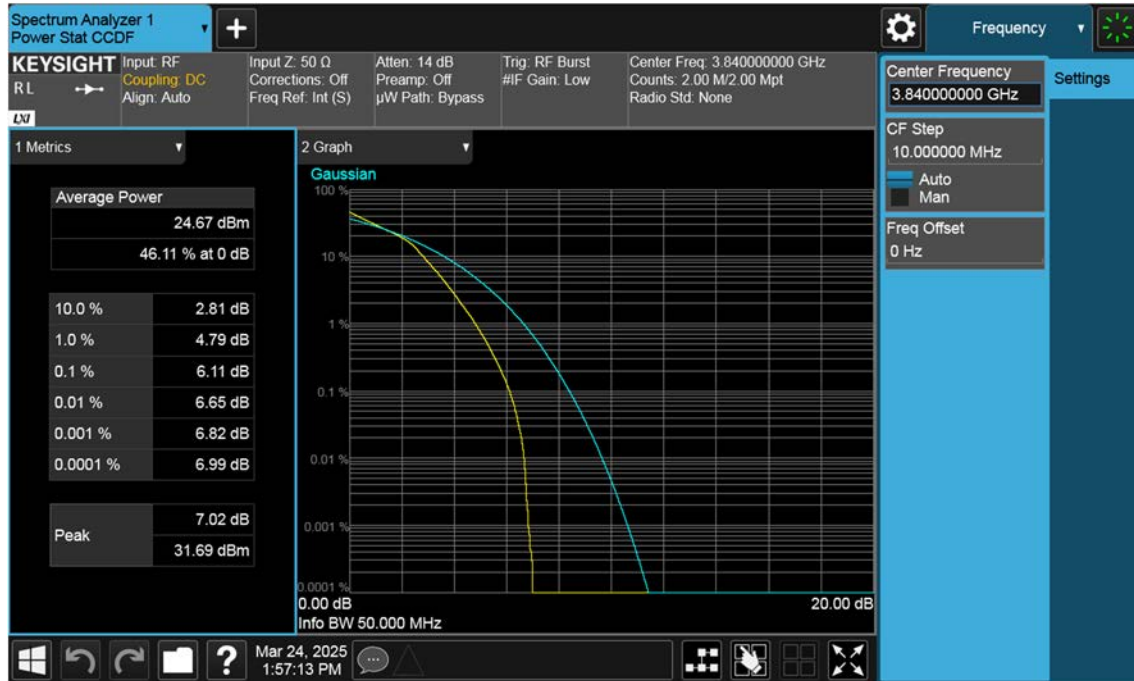
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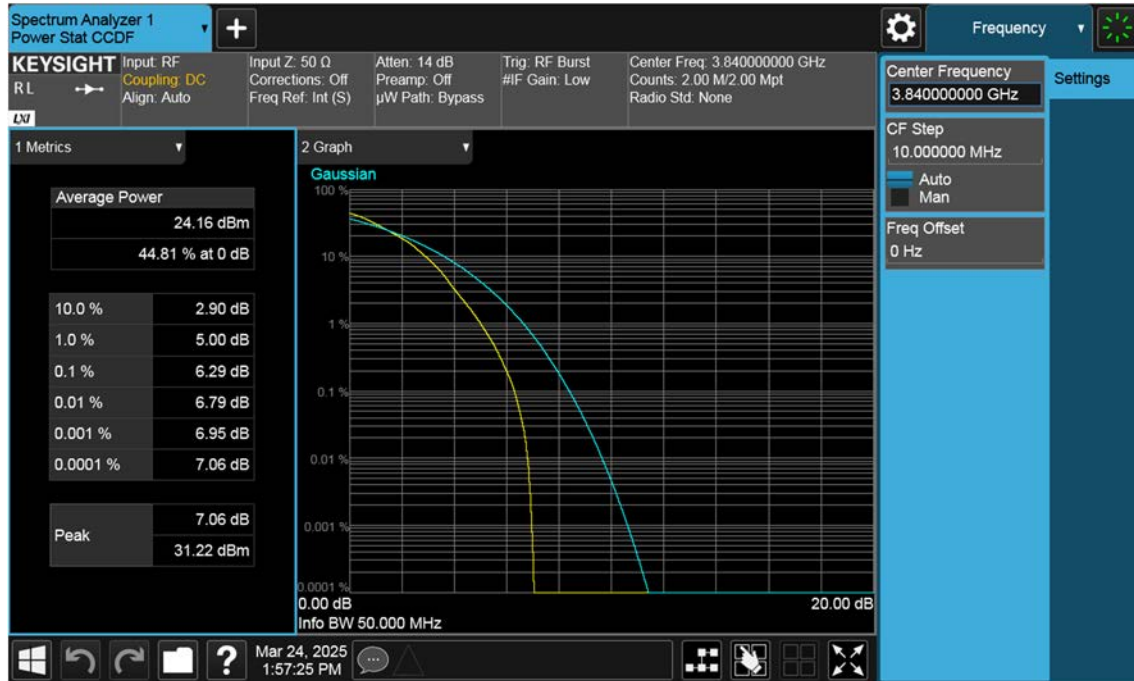
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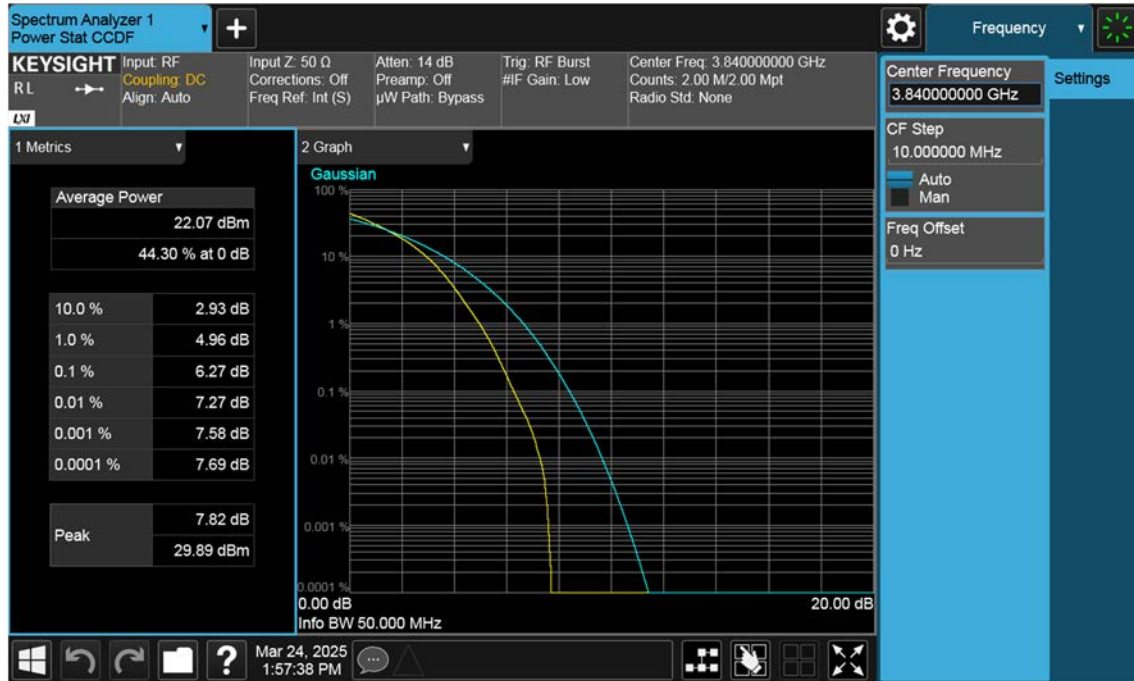
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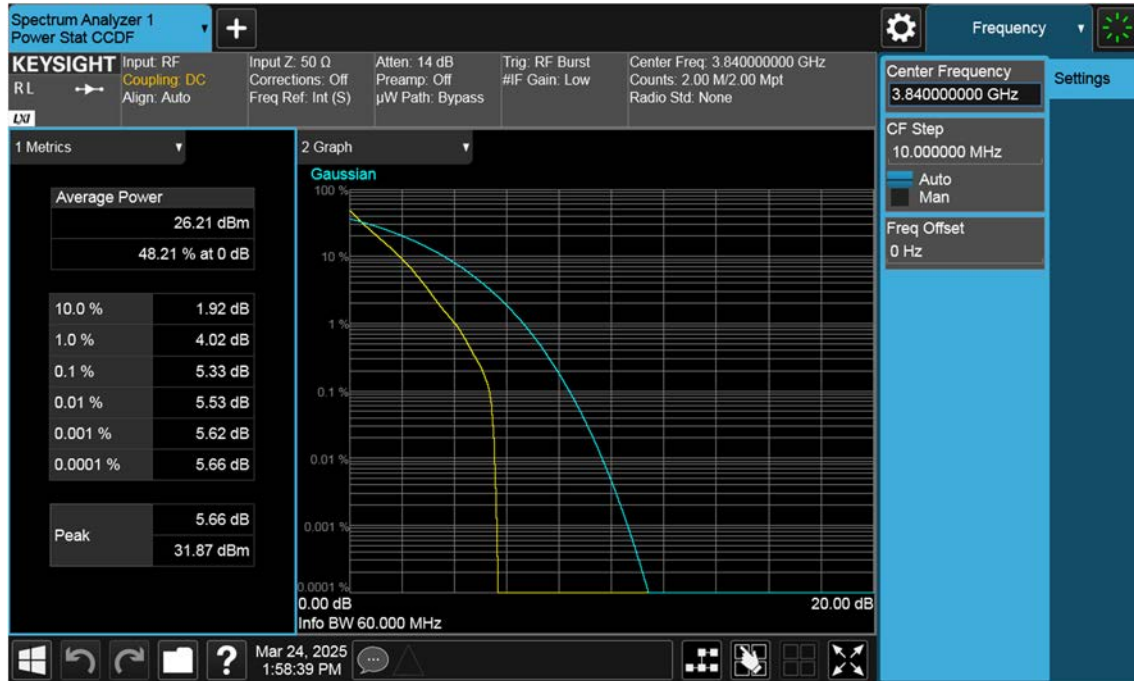
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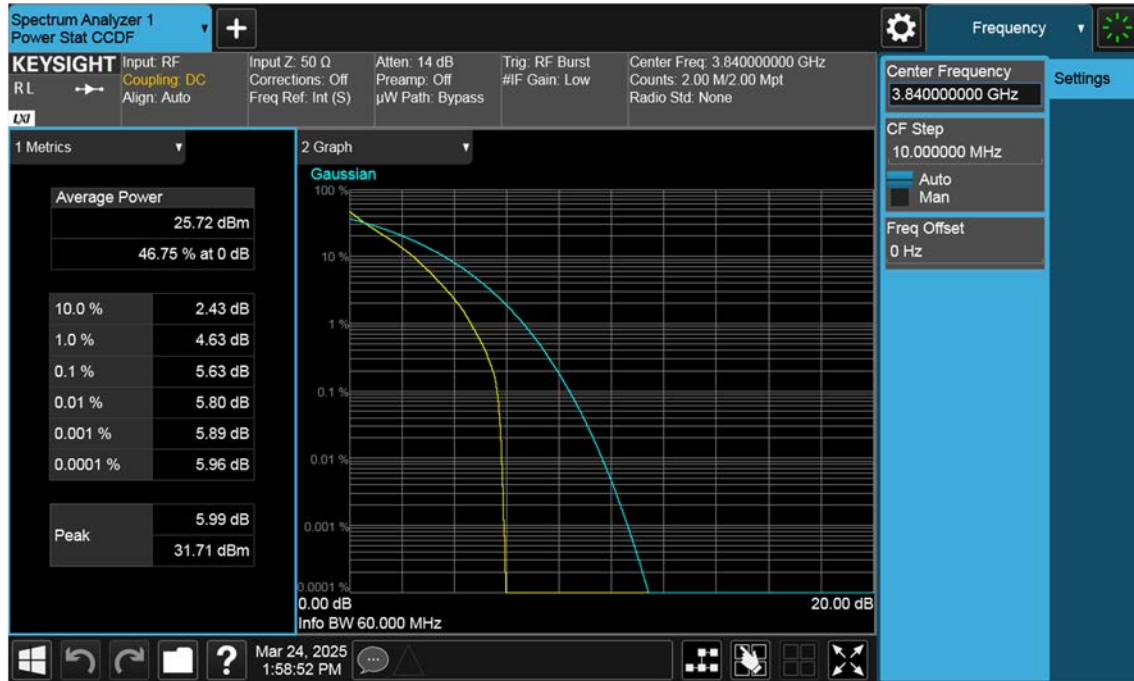
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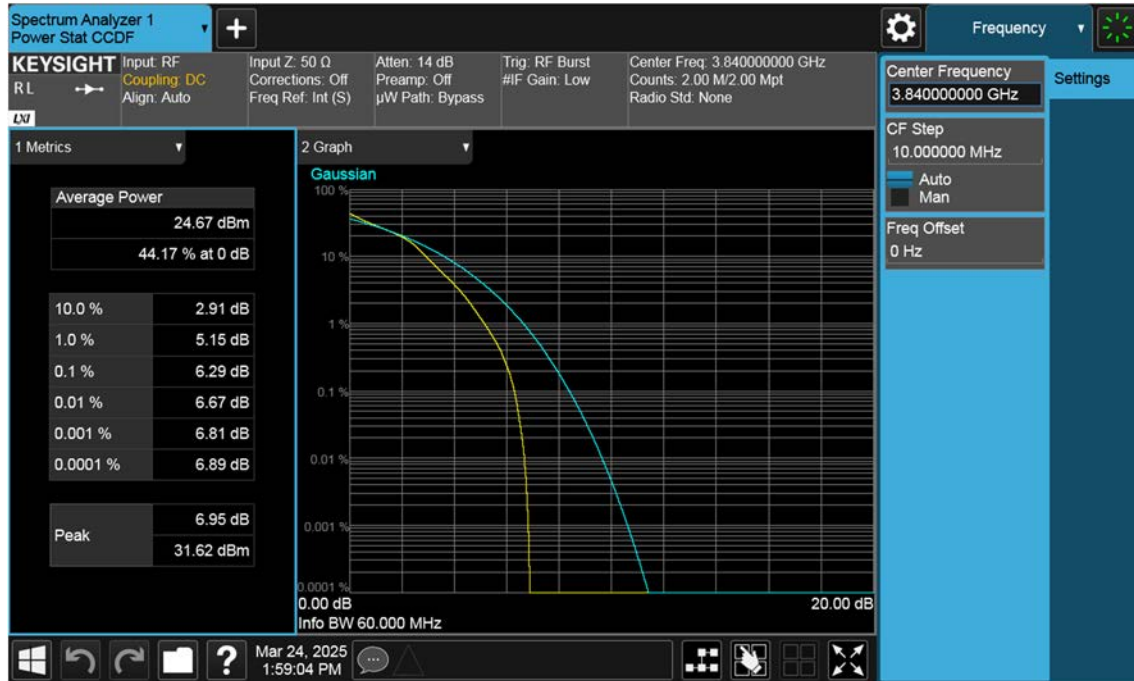
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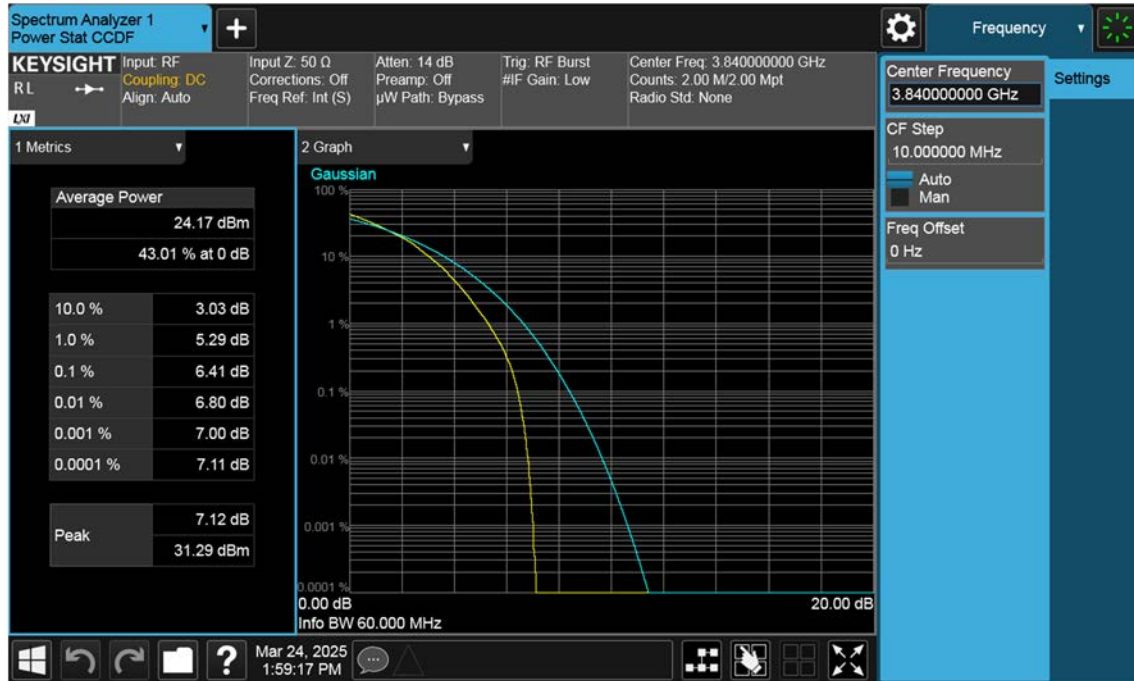
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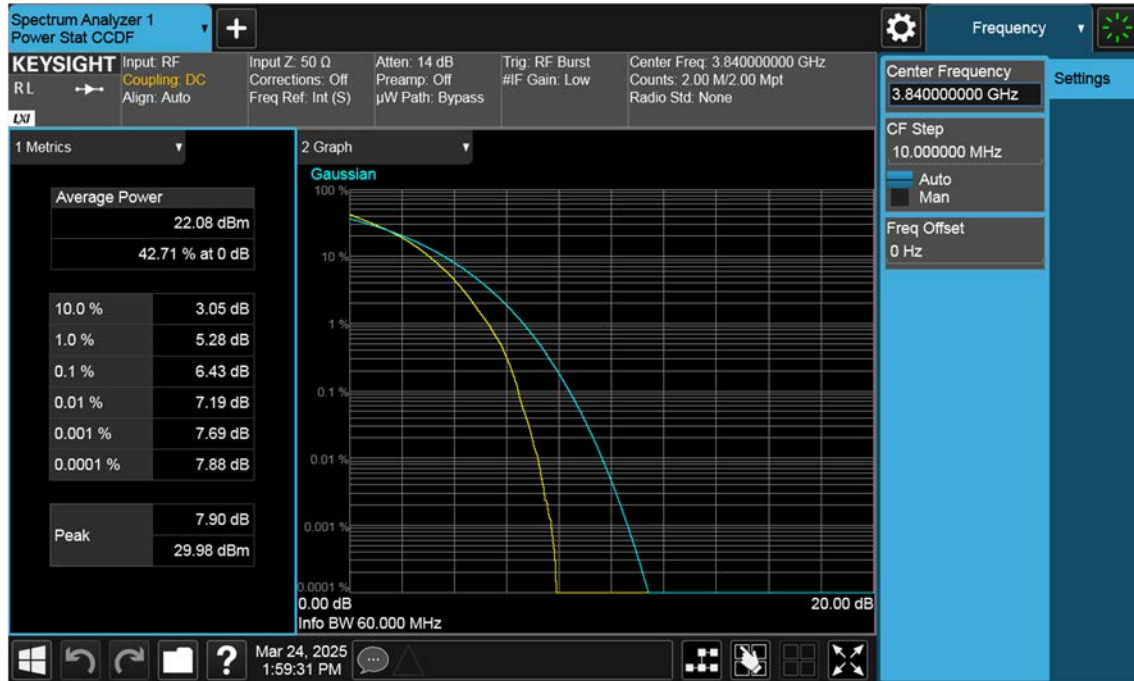
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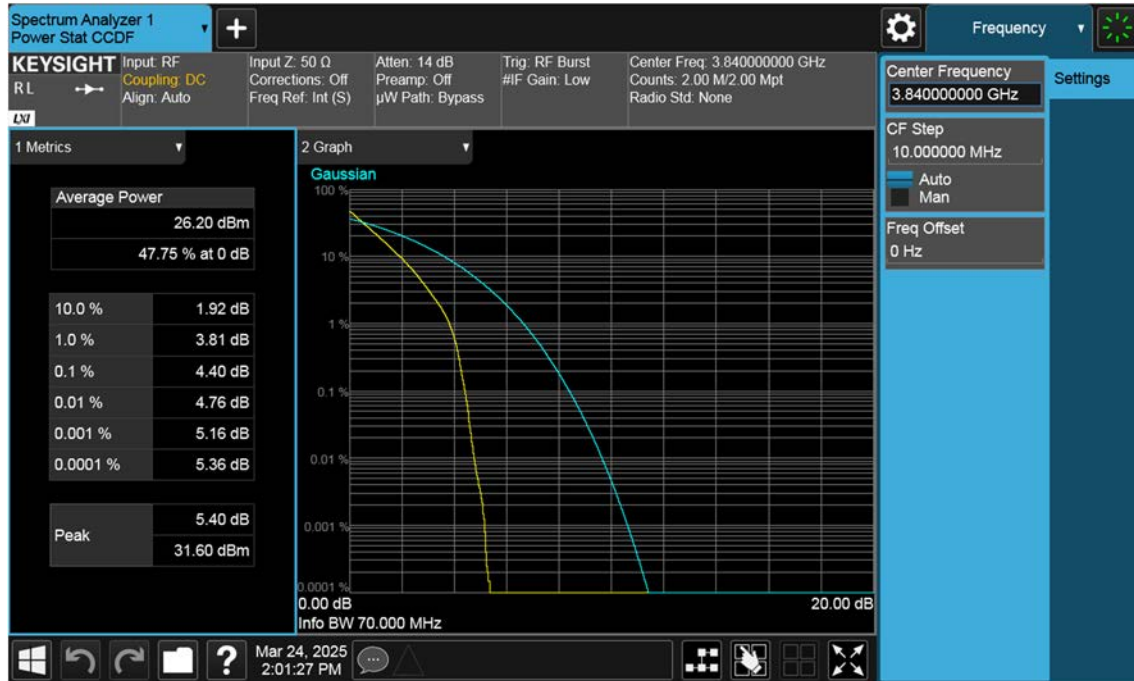
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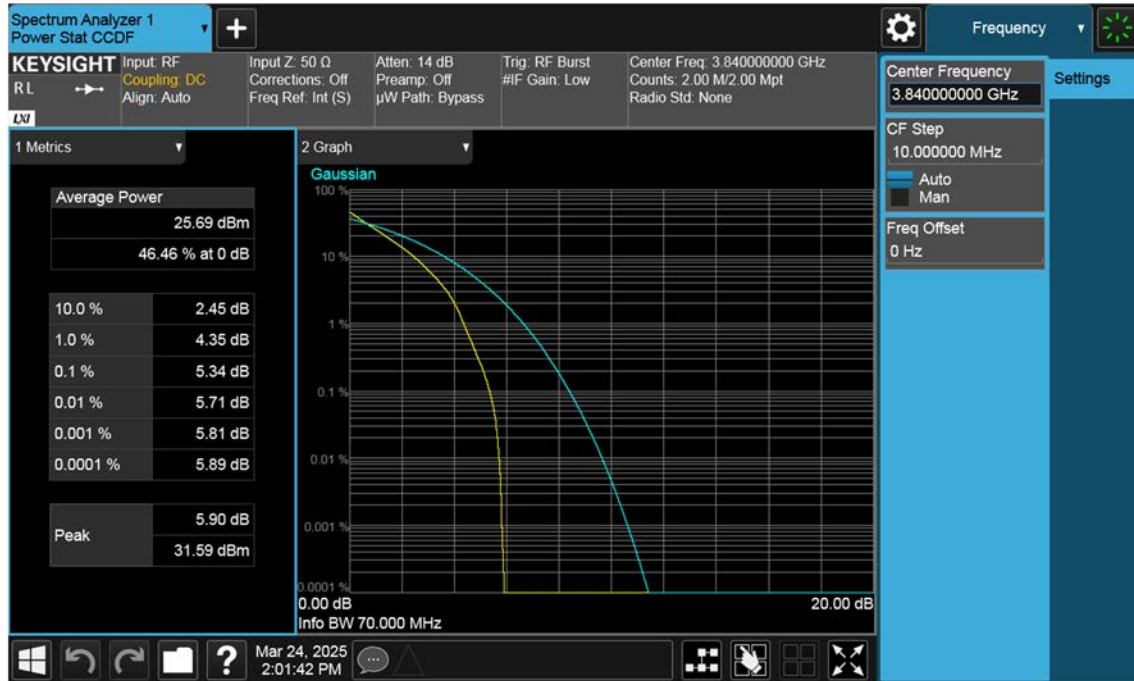
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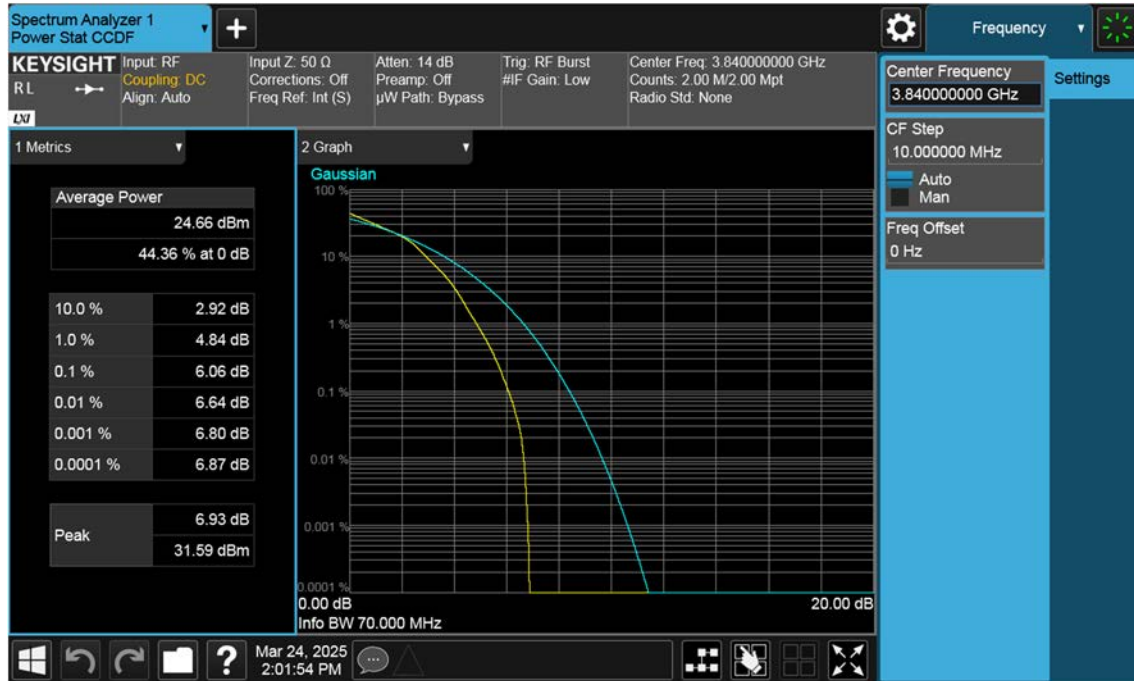
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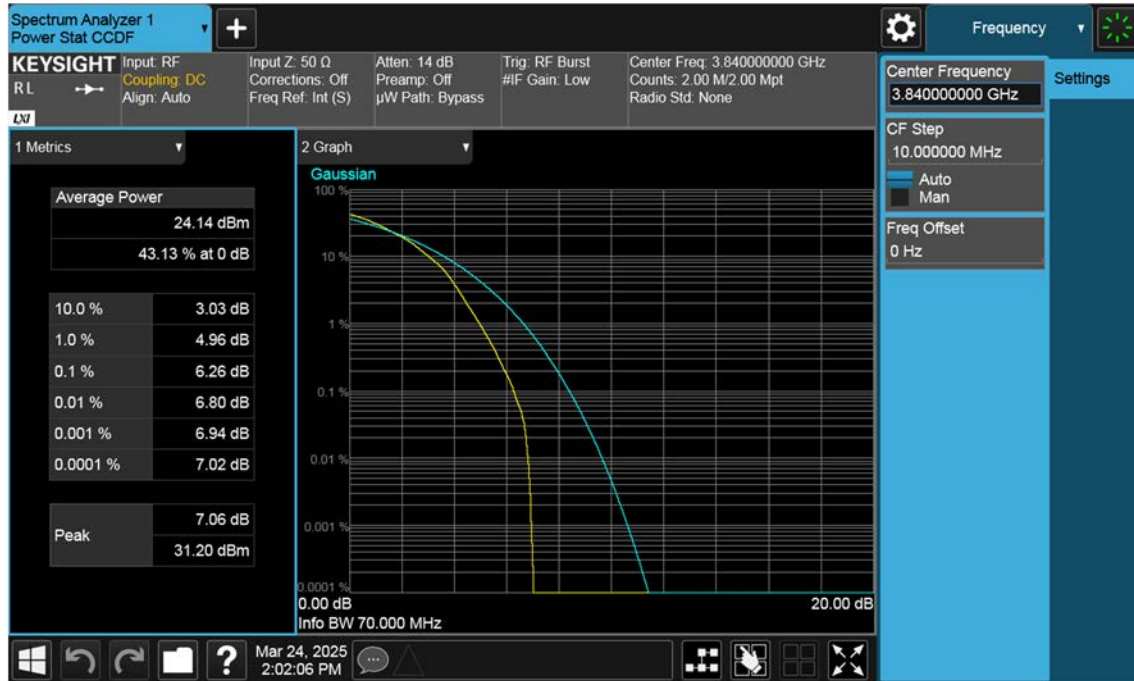
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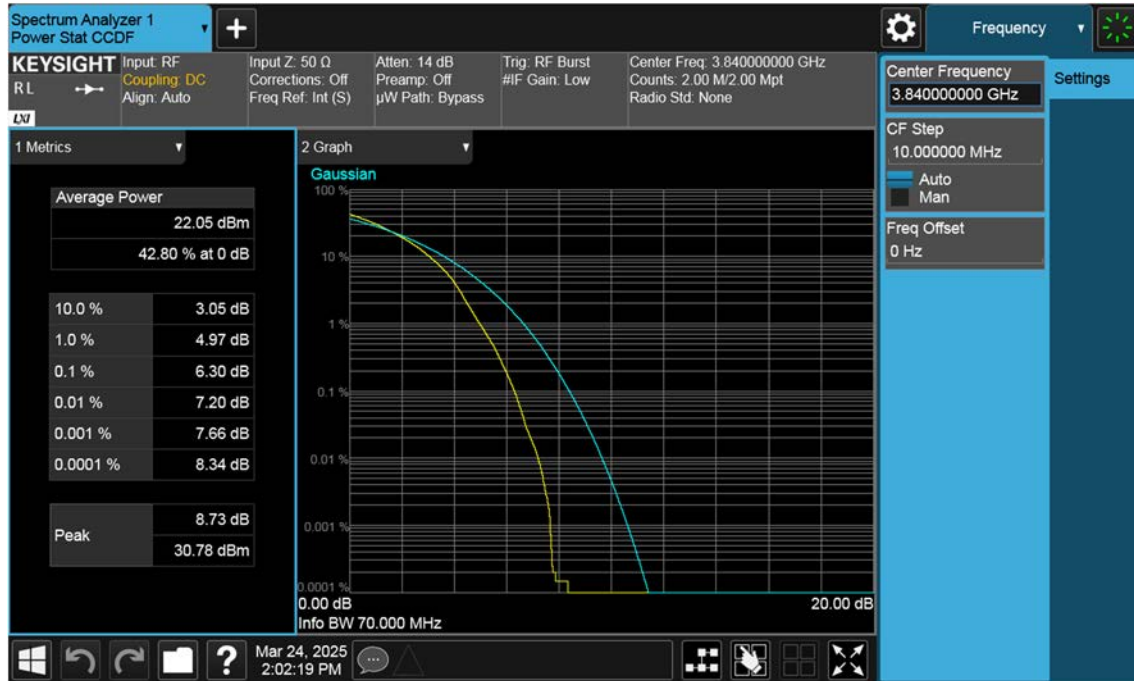
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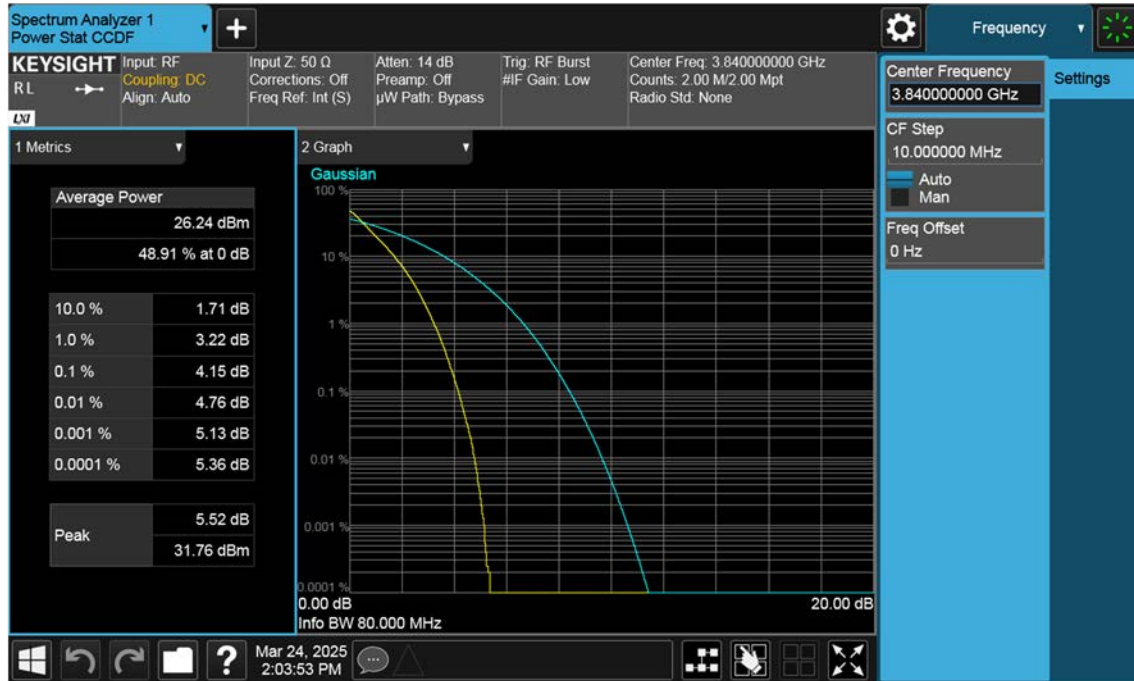
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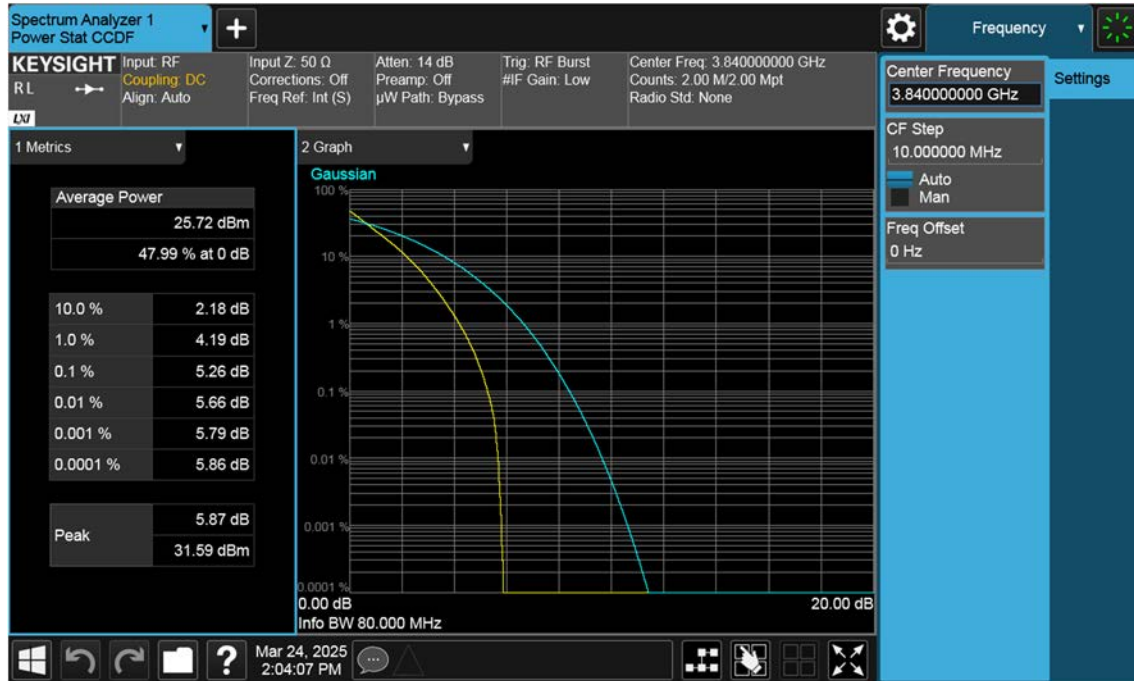
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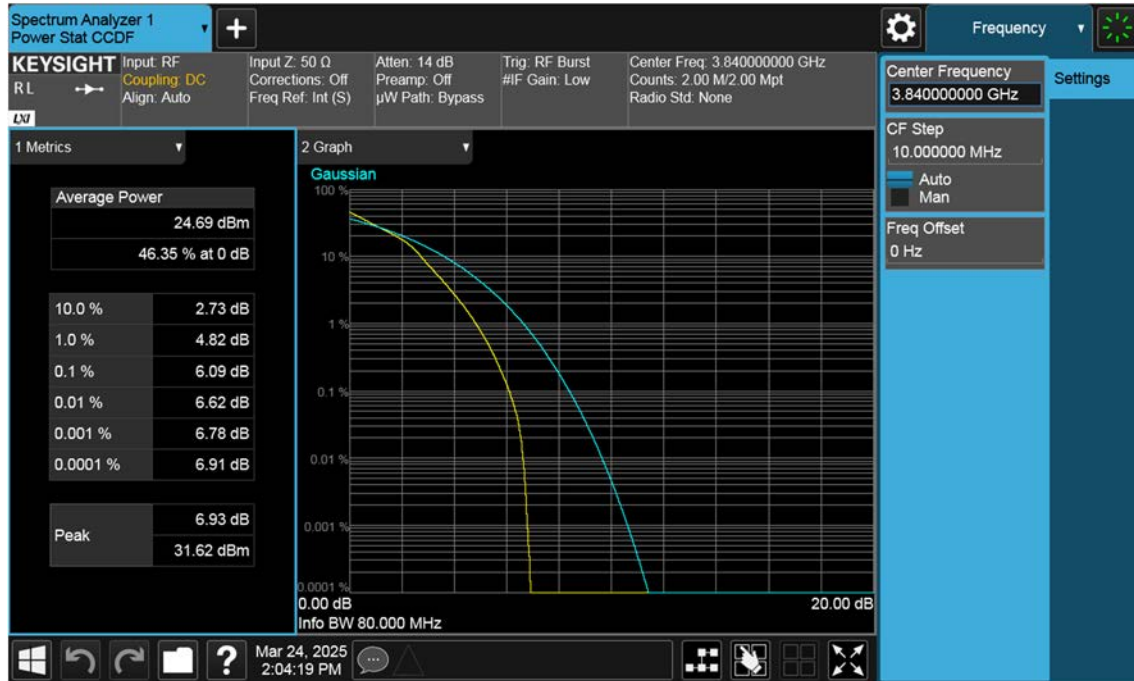
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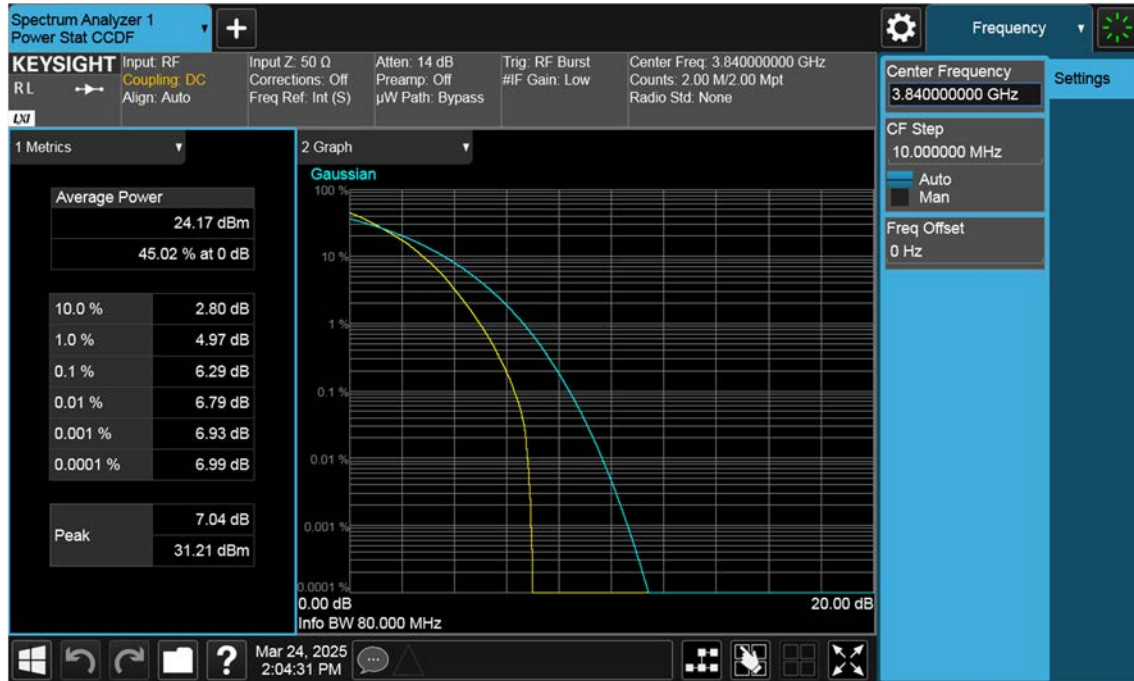
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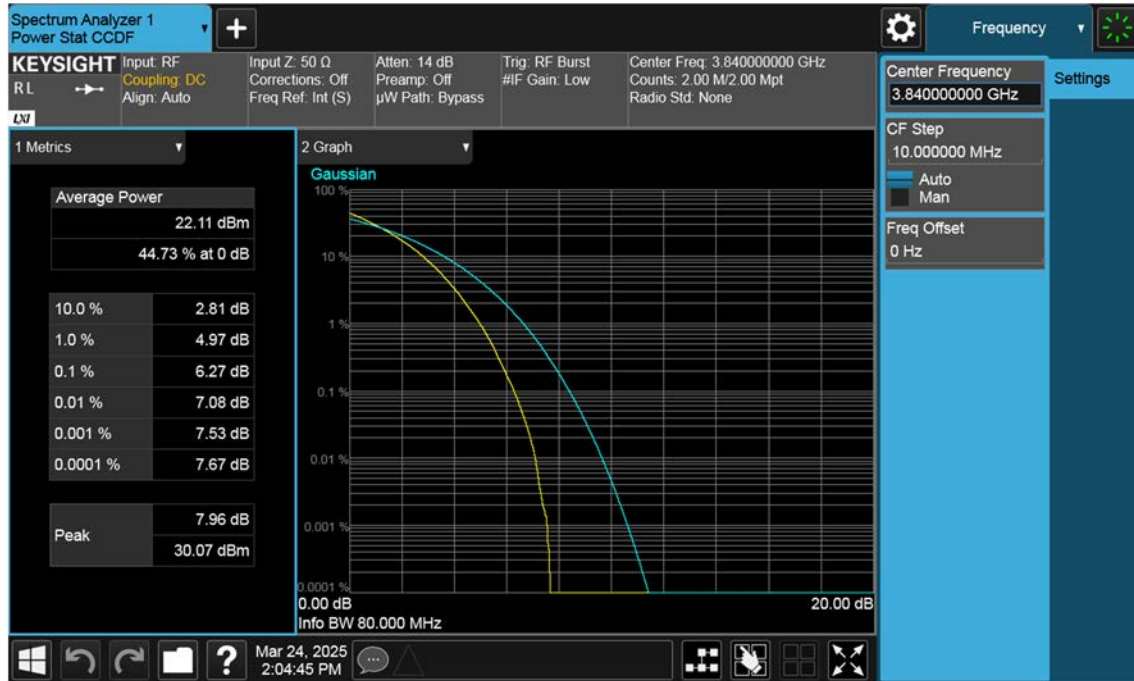
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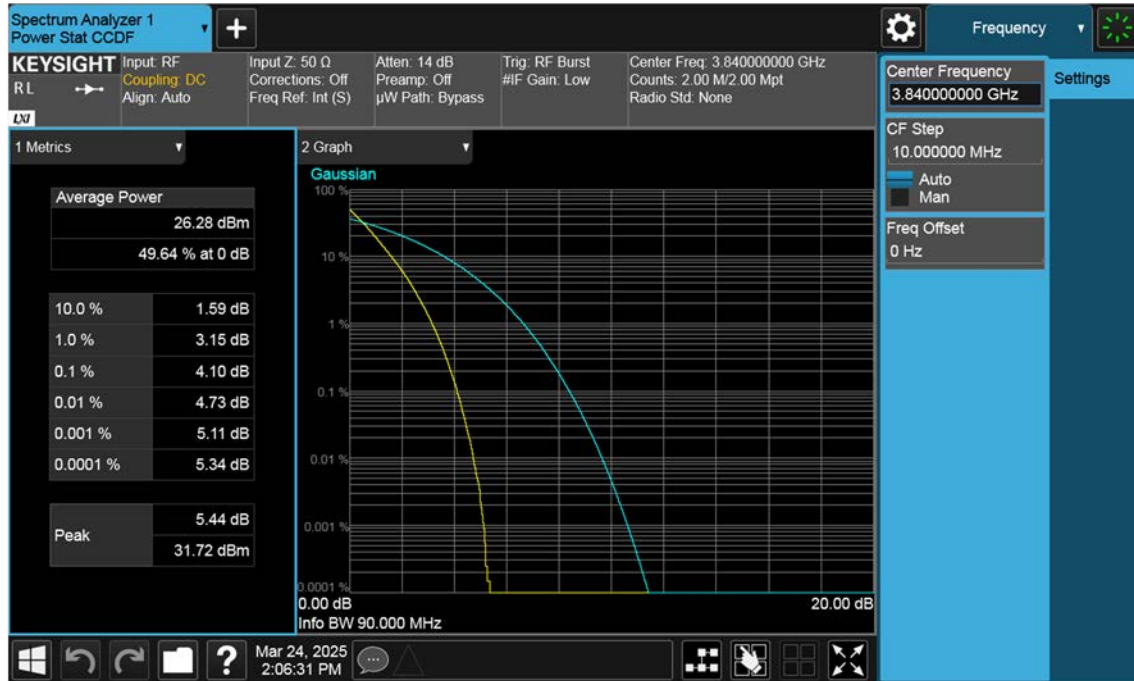
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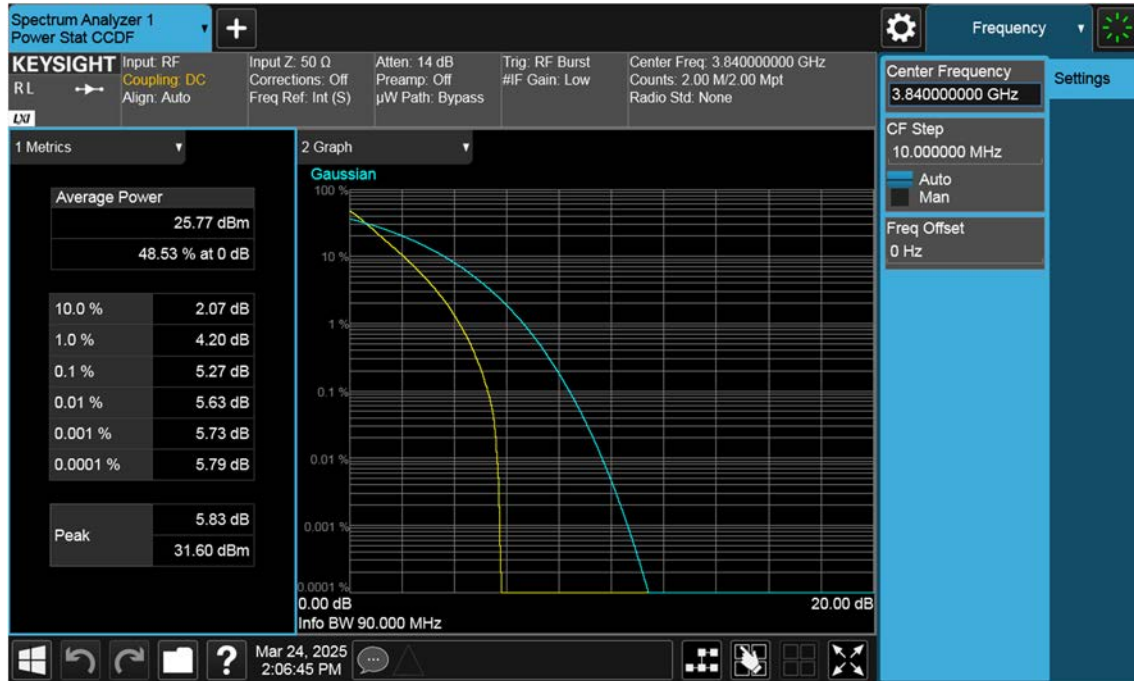
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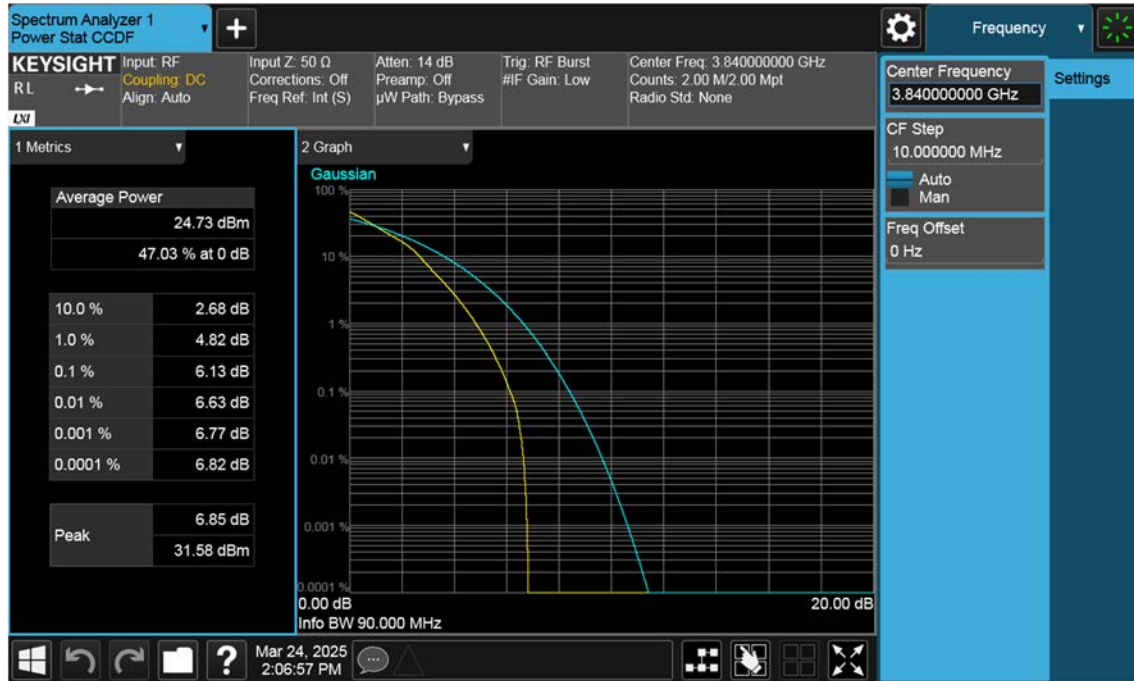
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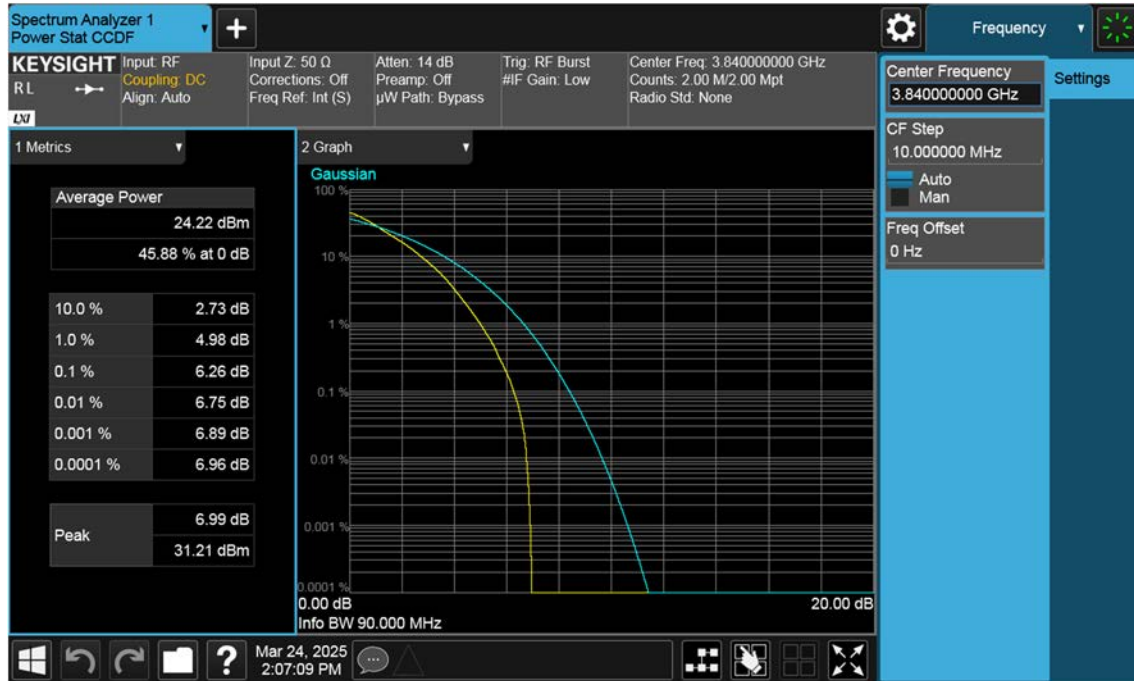
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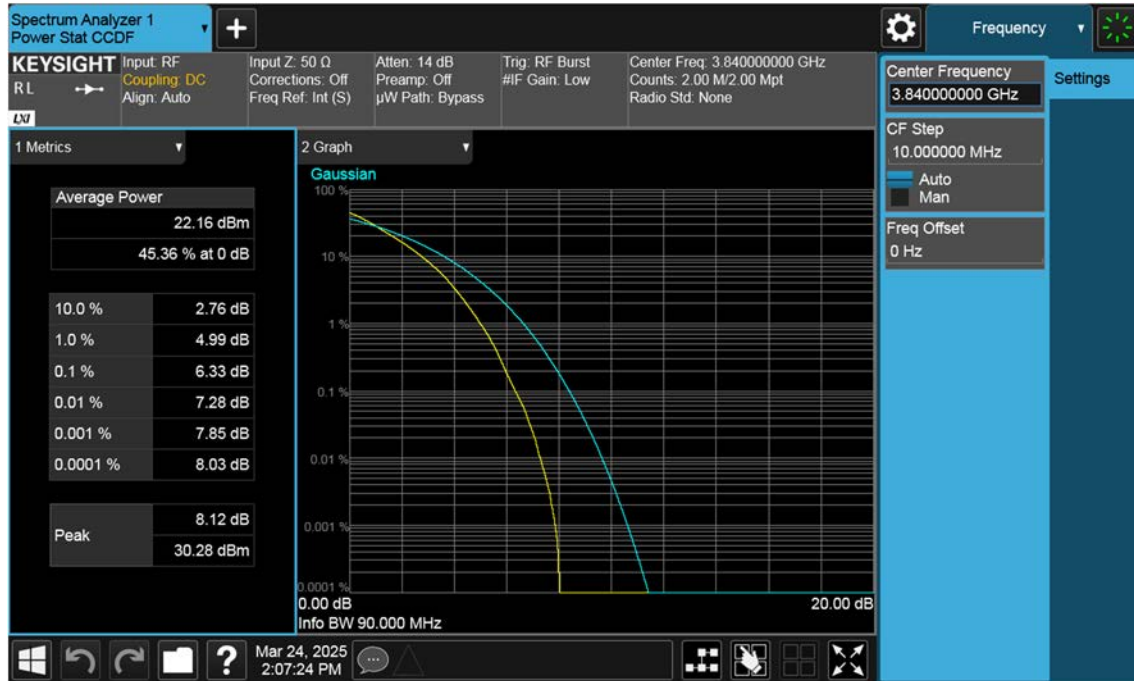
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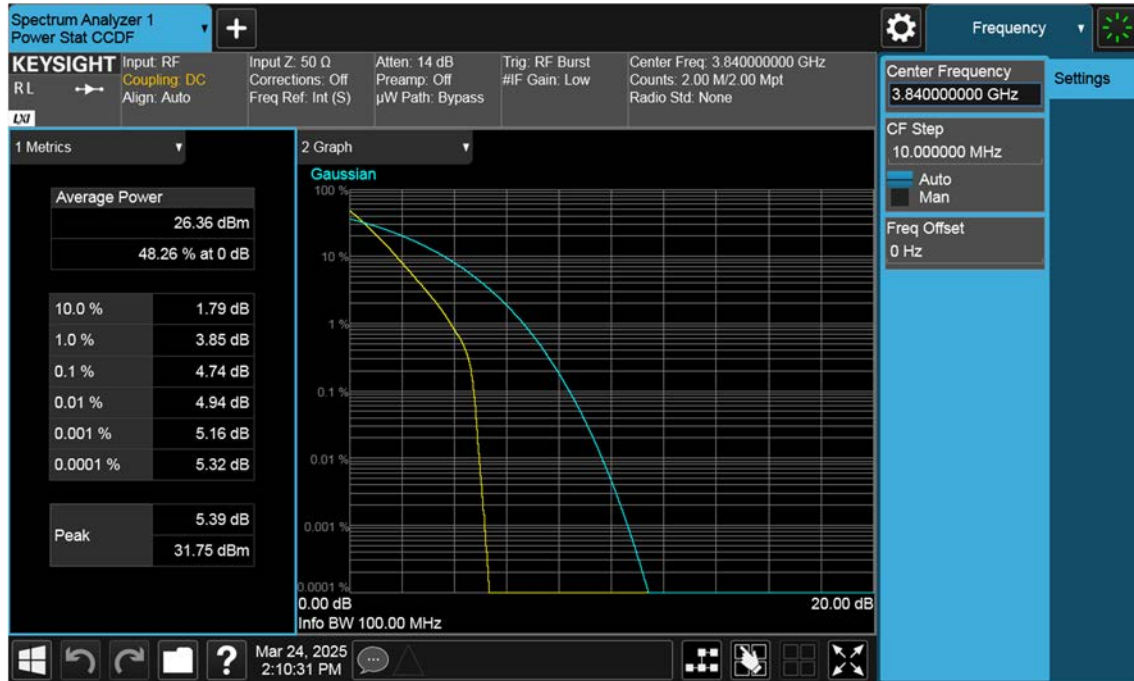
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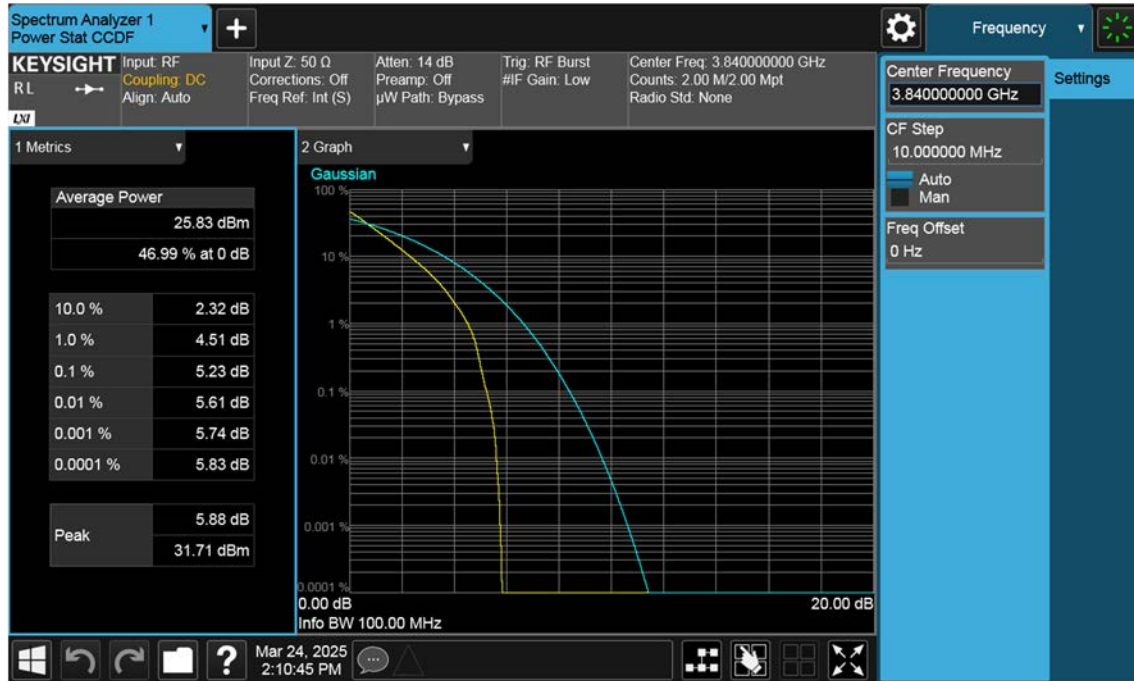
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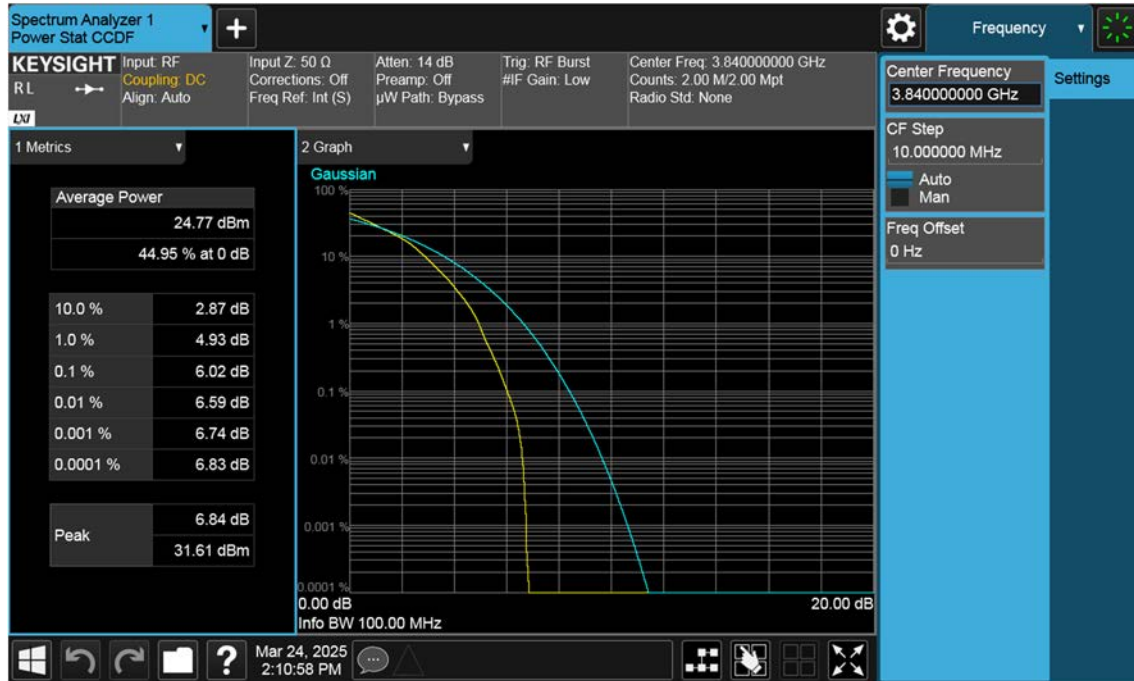
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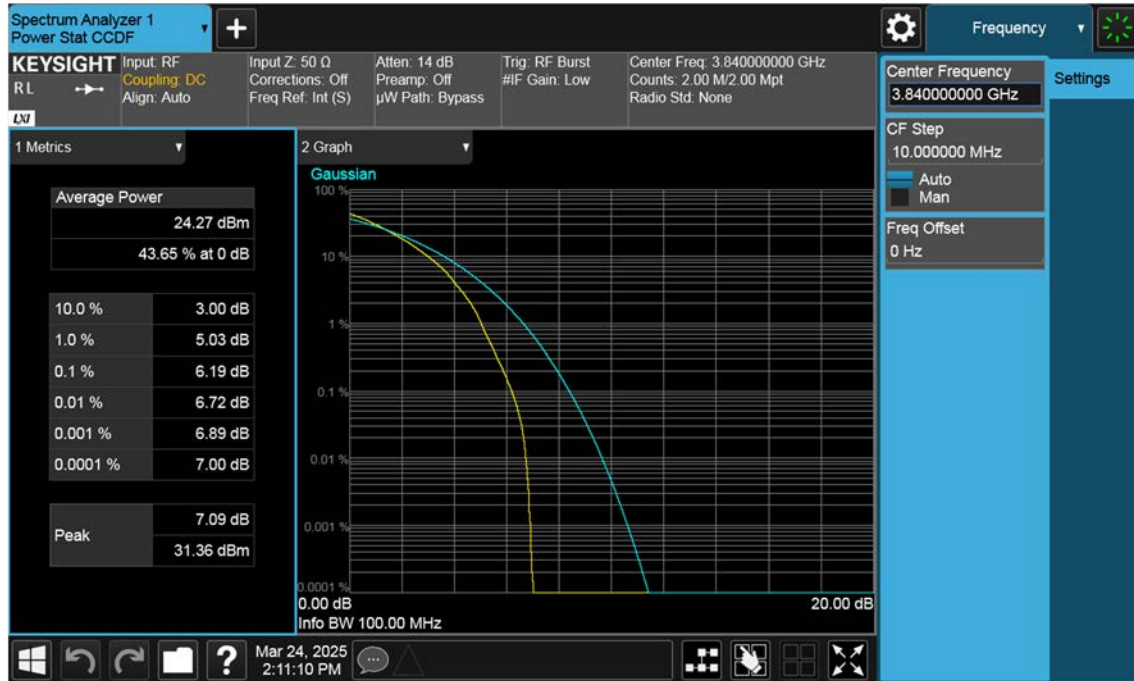
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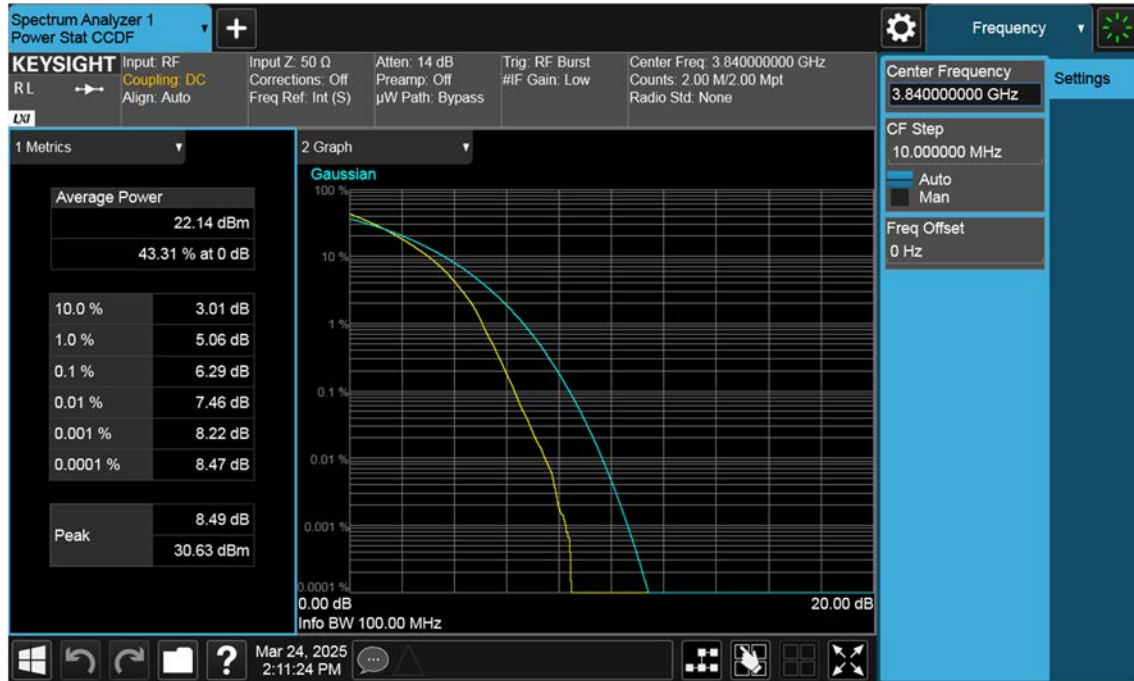
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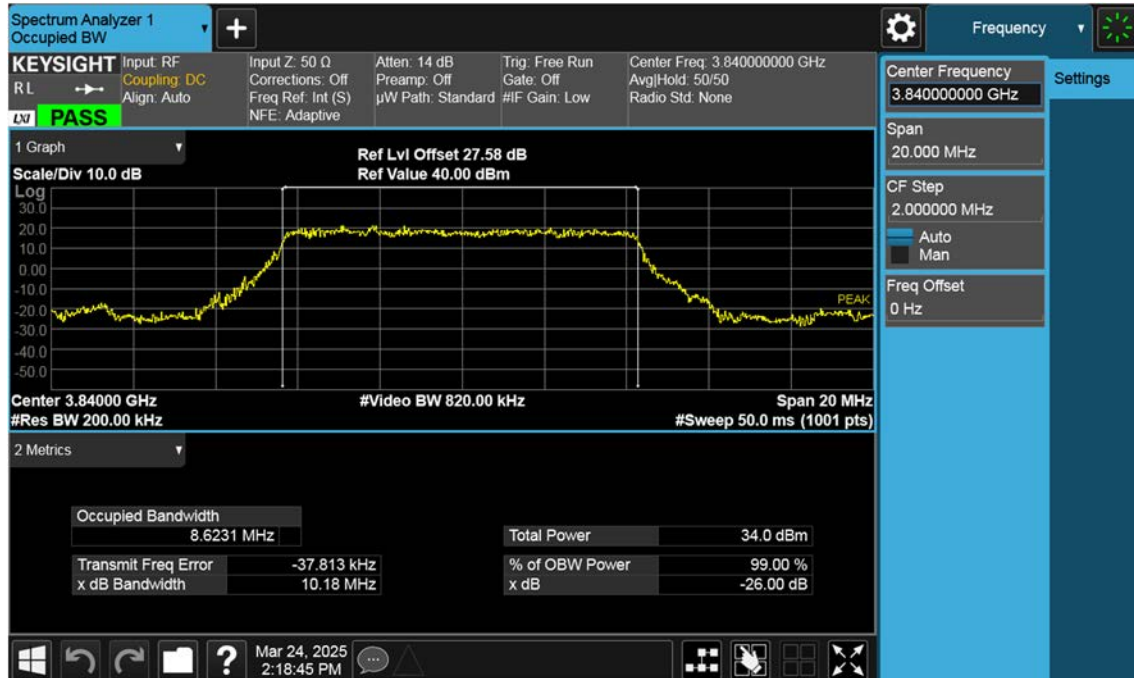
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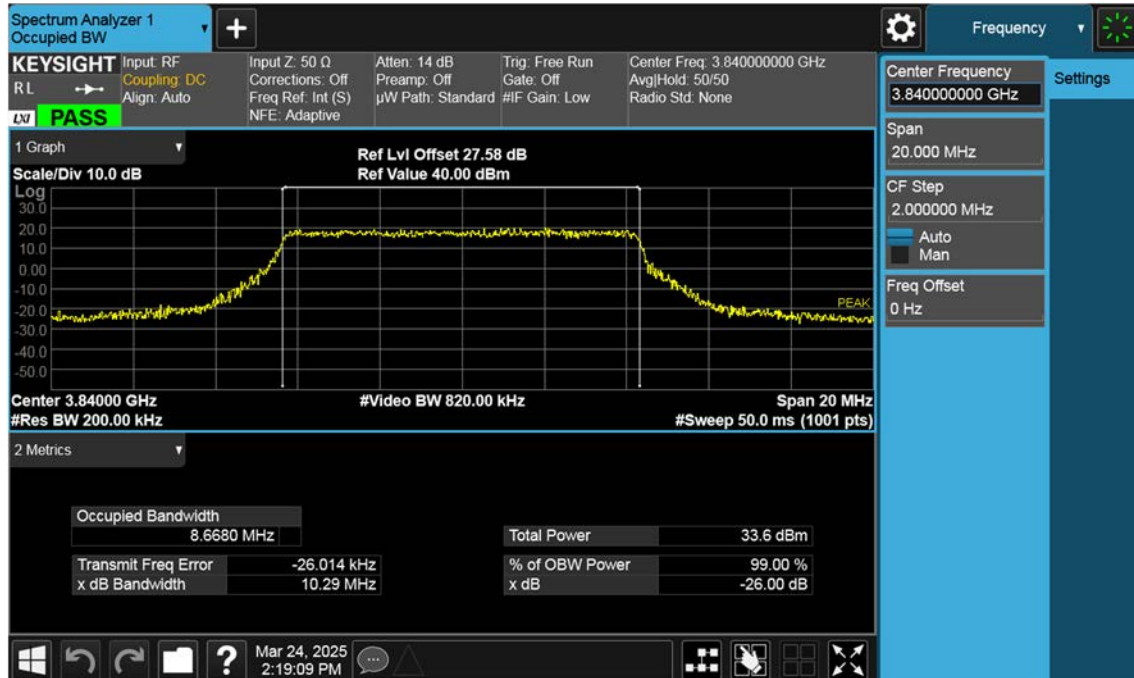
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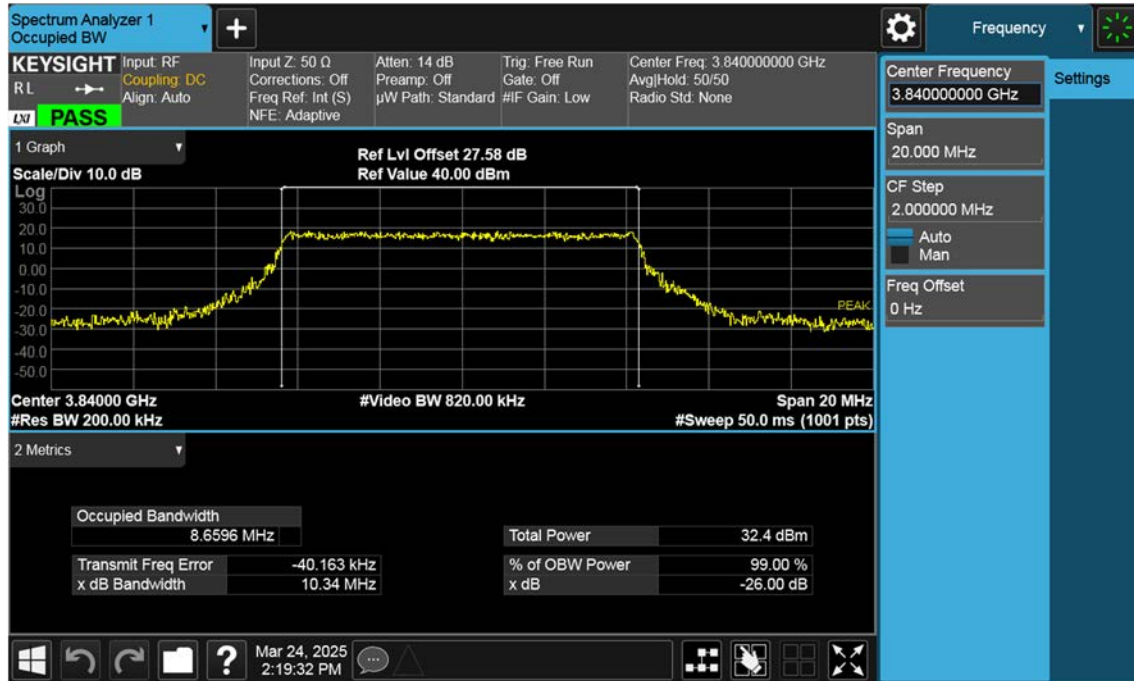
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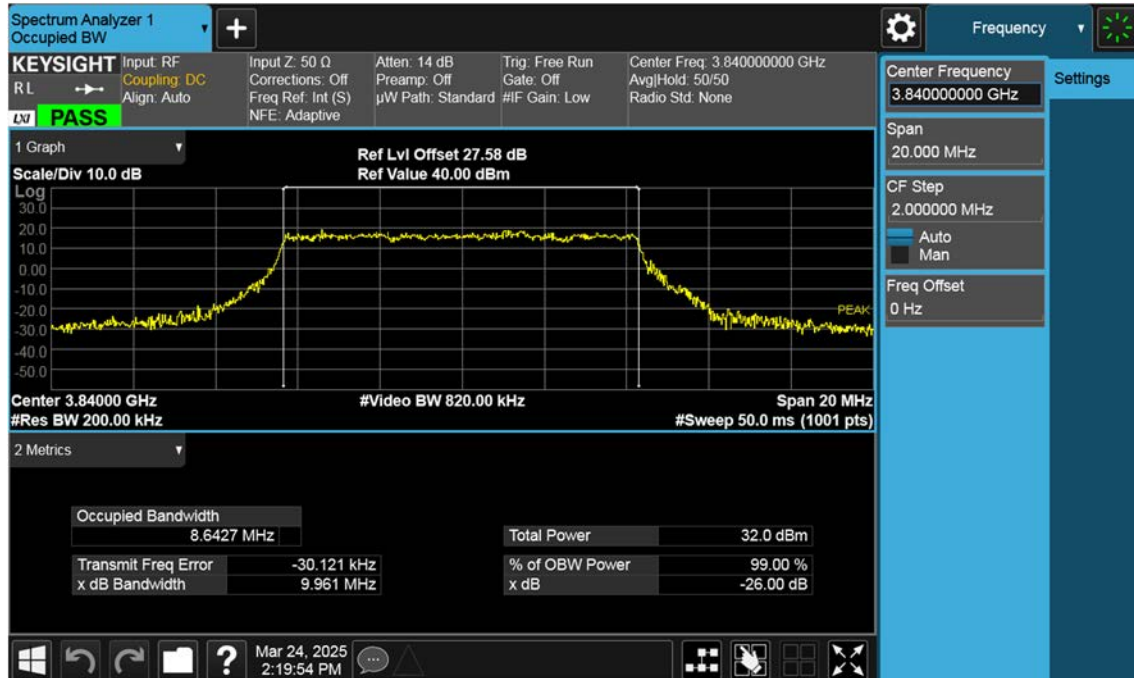
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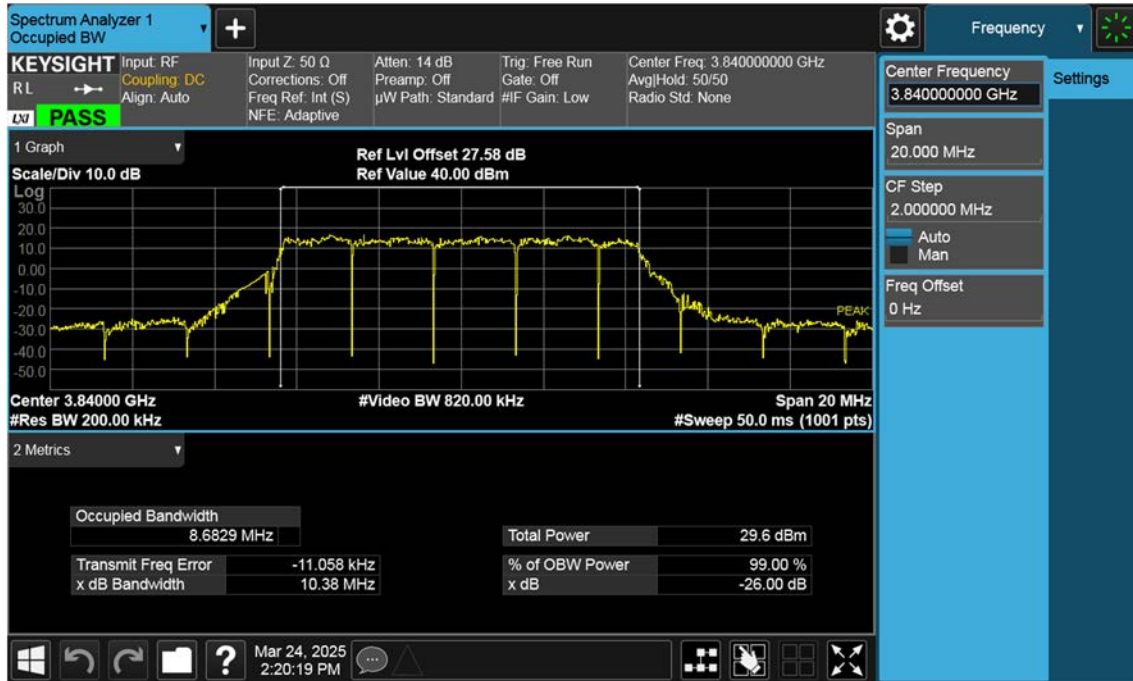
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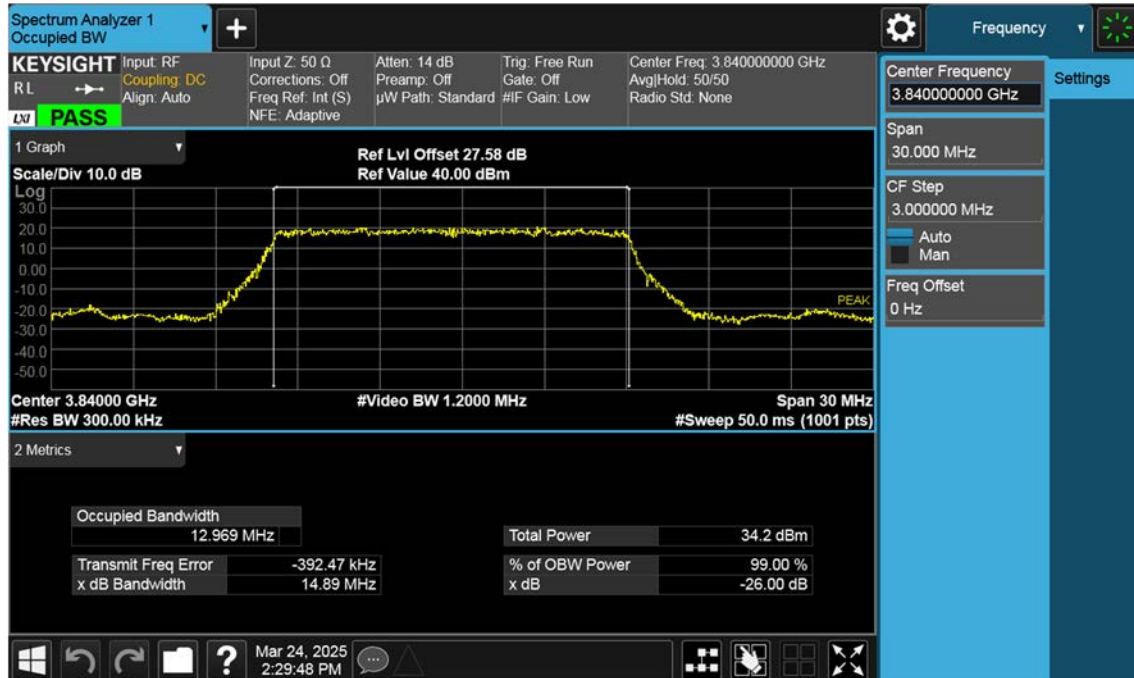
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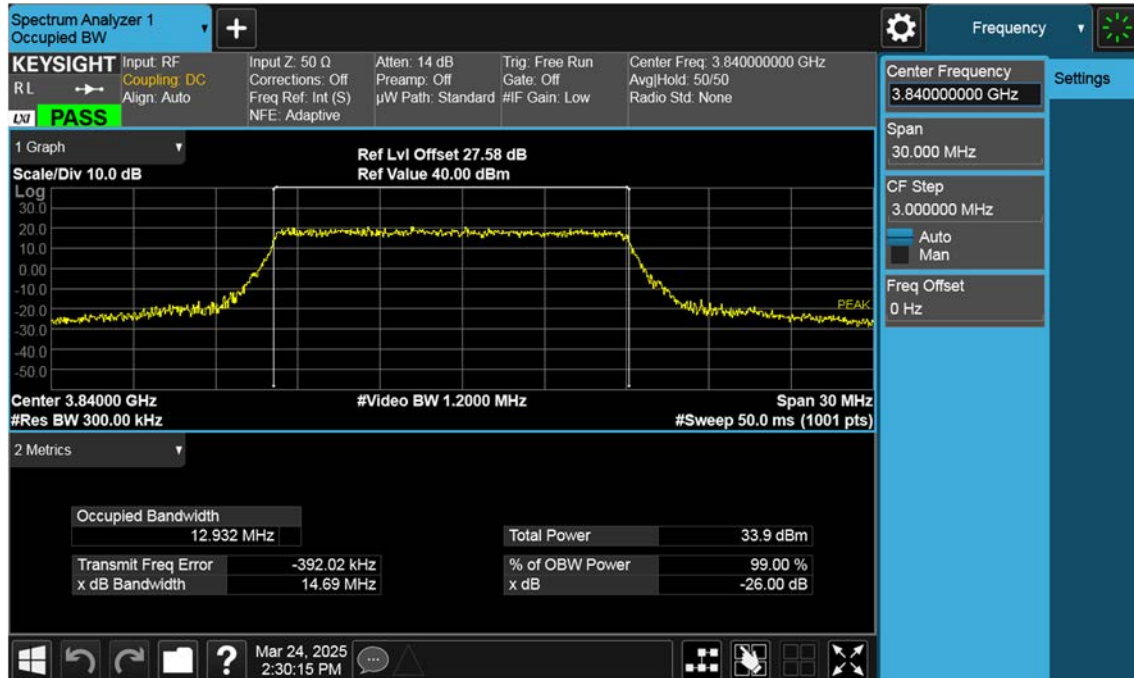
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n77(3700~3980 MHz)_15 M_OBW_Mid_BPSK_FullRB



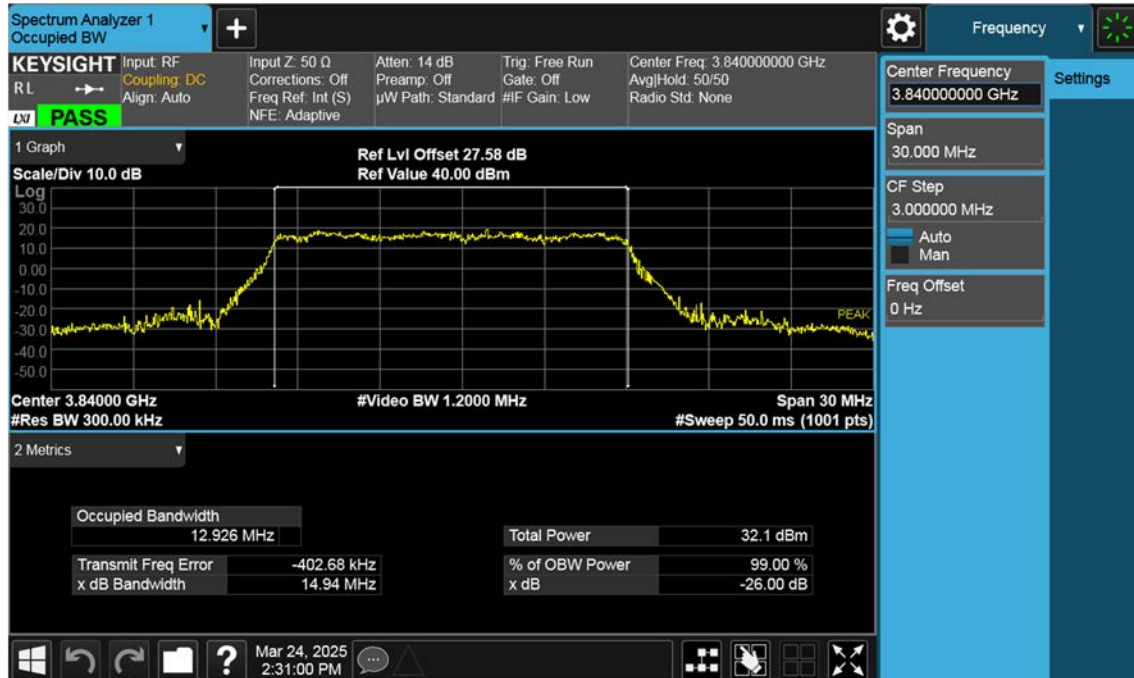
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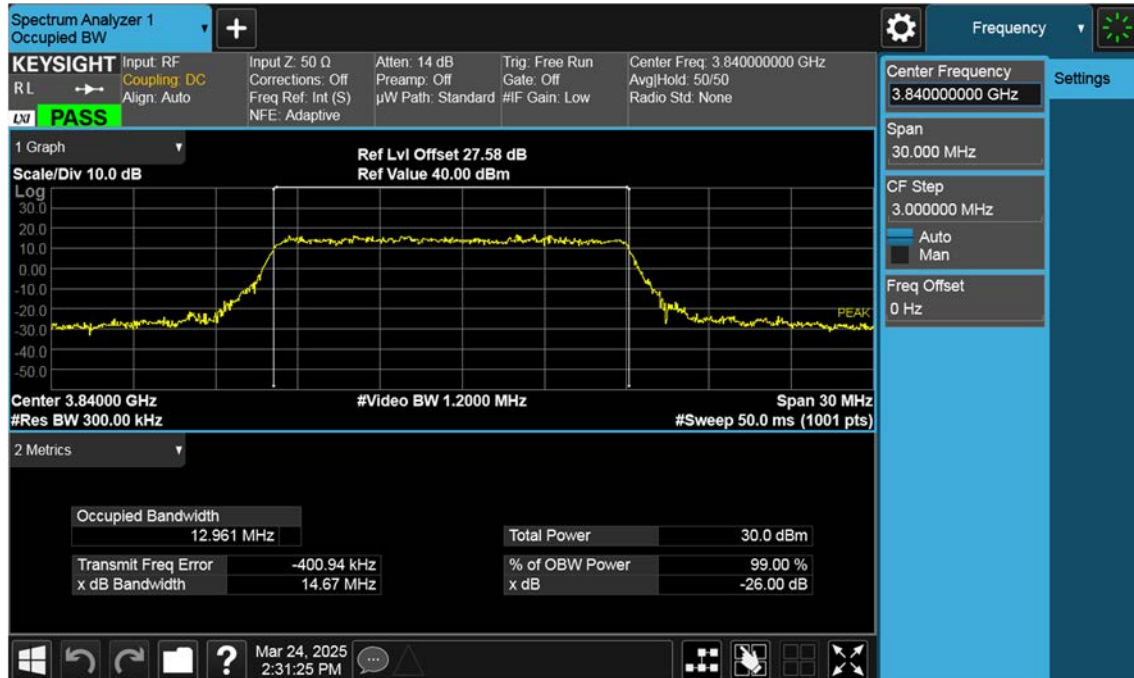
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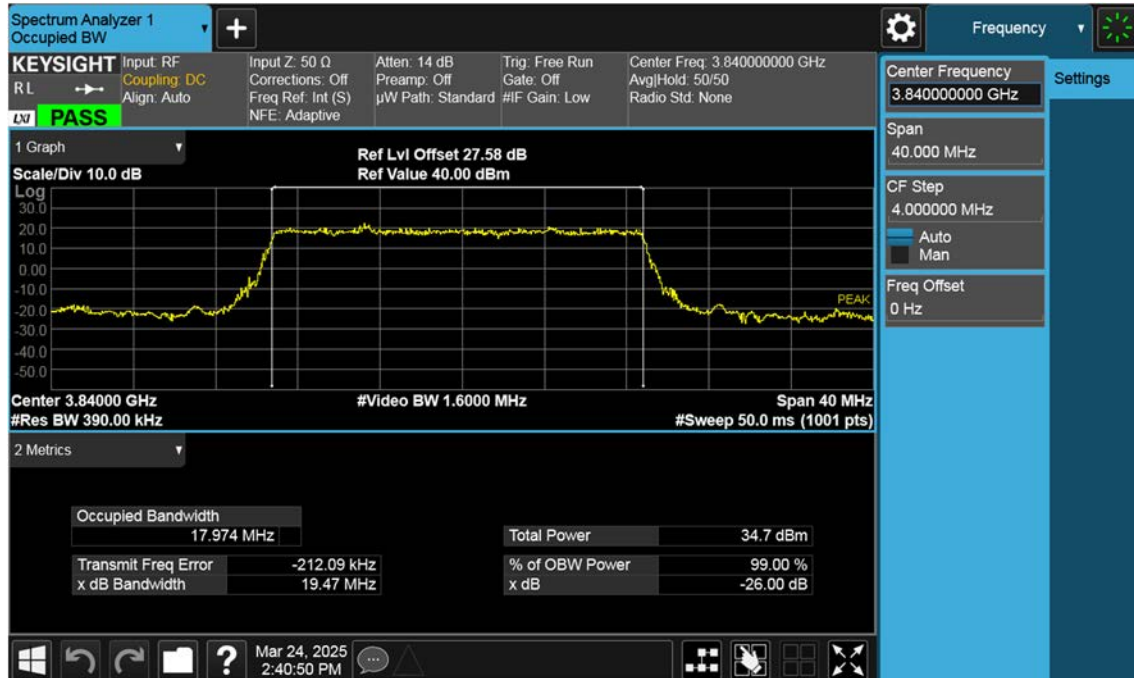
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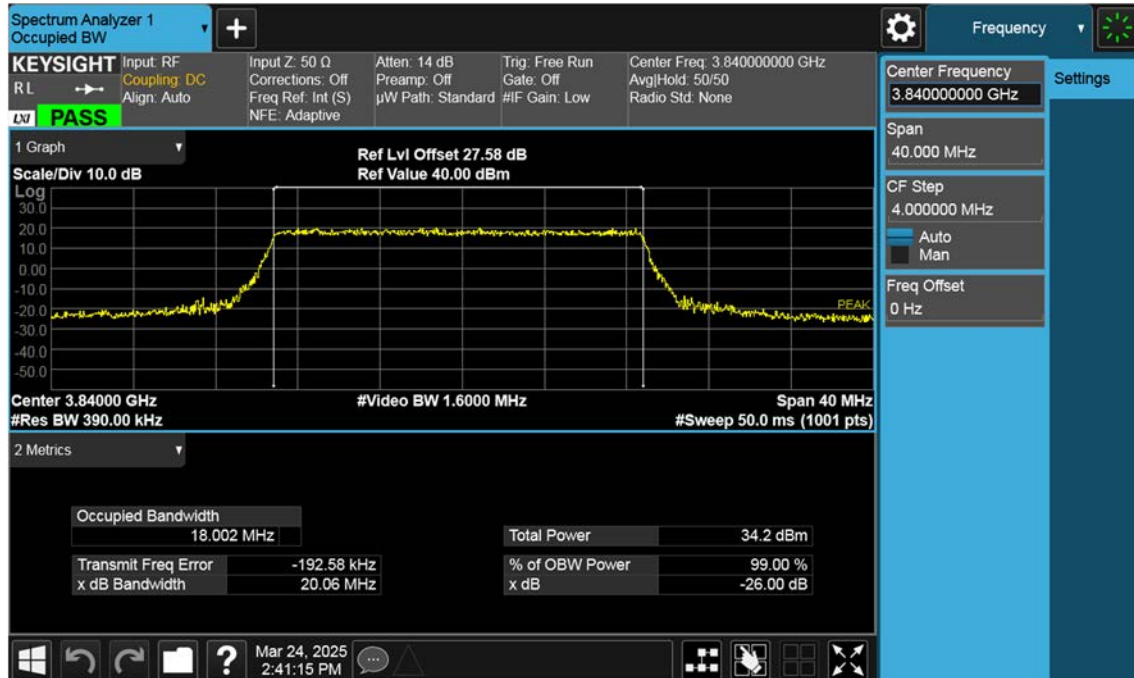
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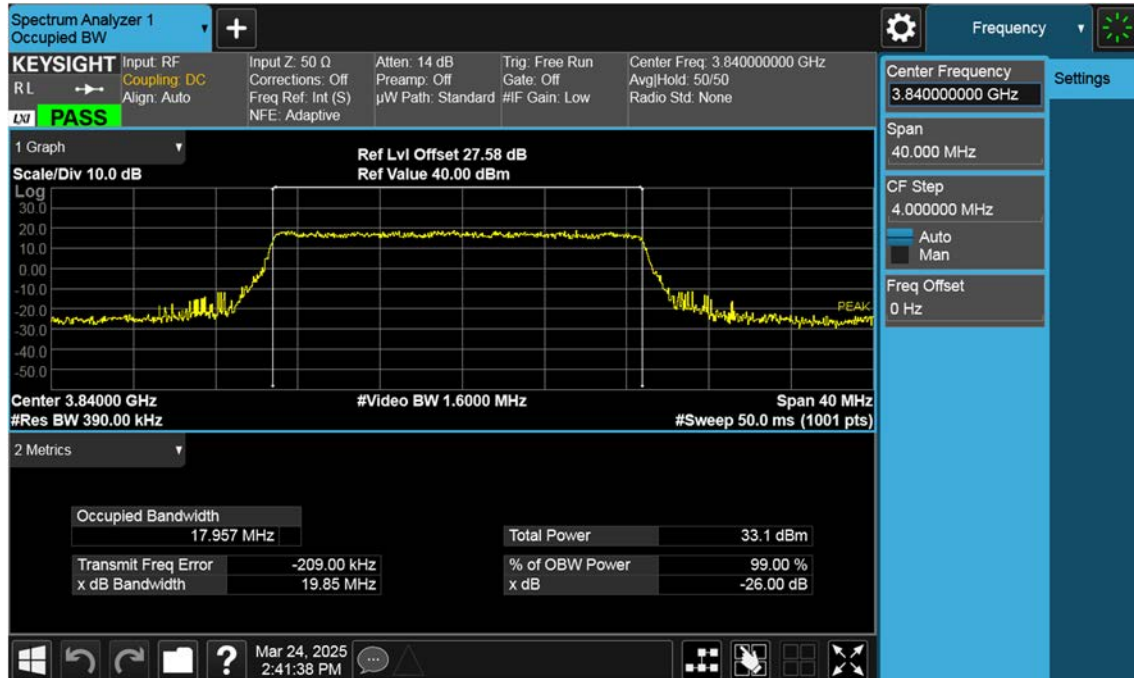
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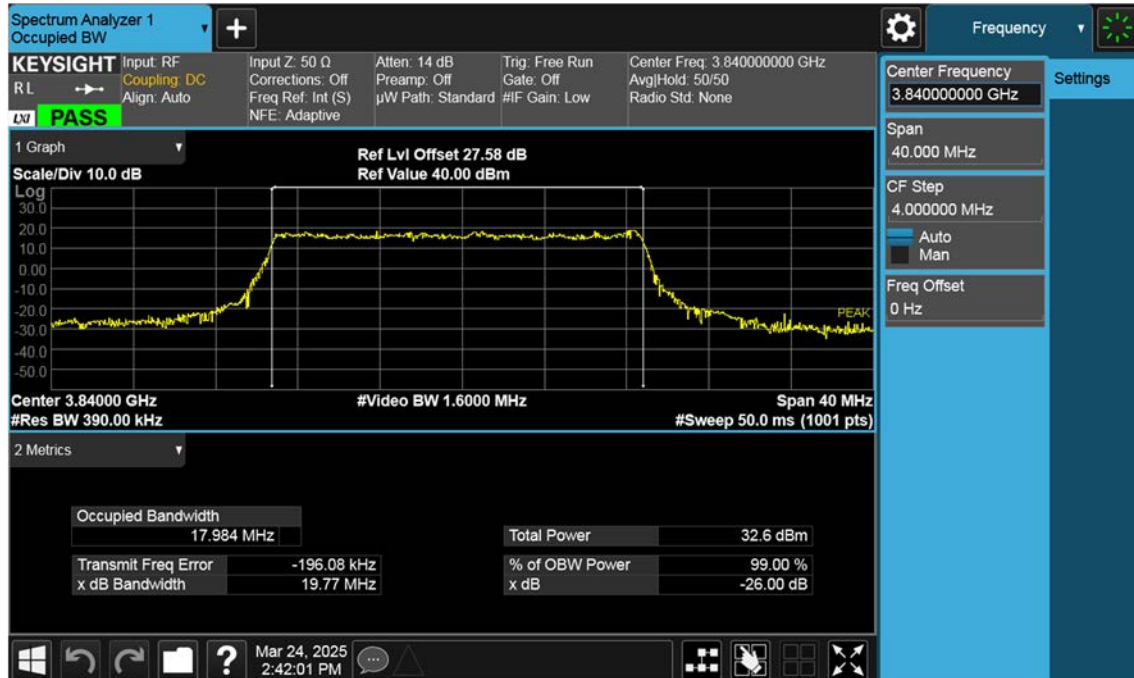
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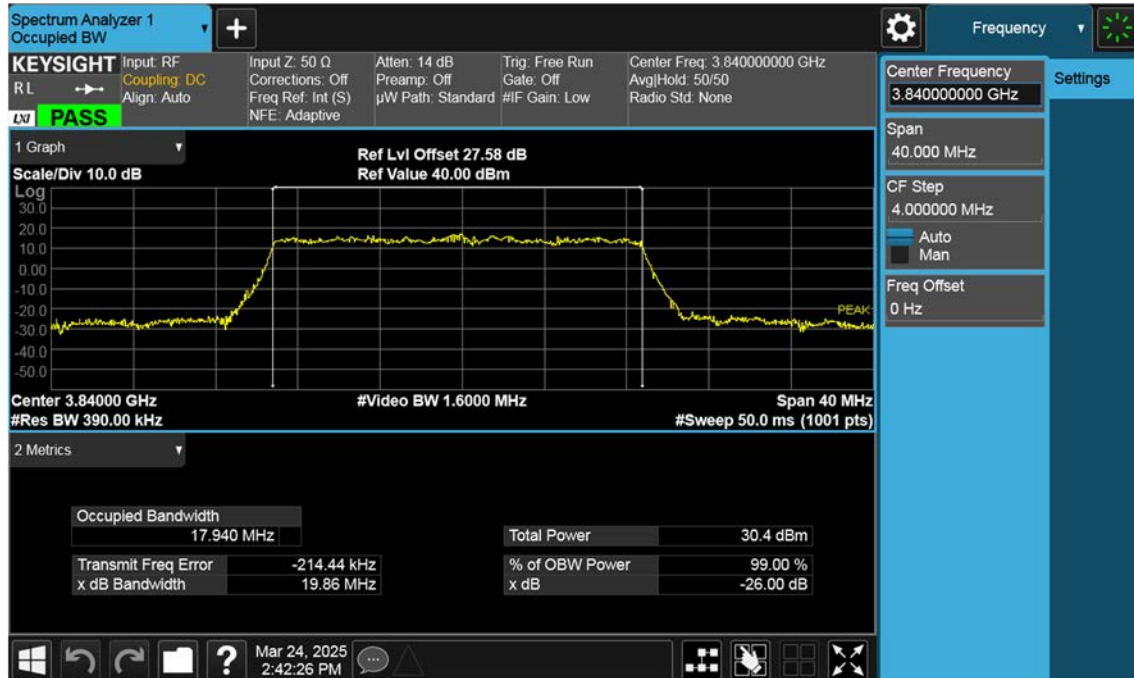
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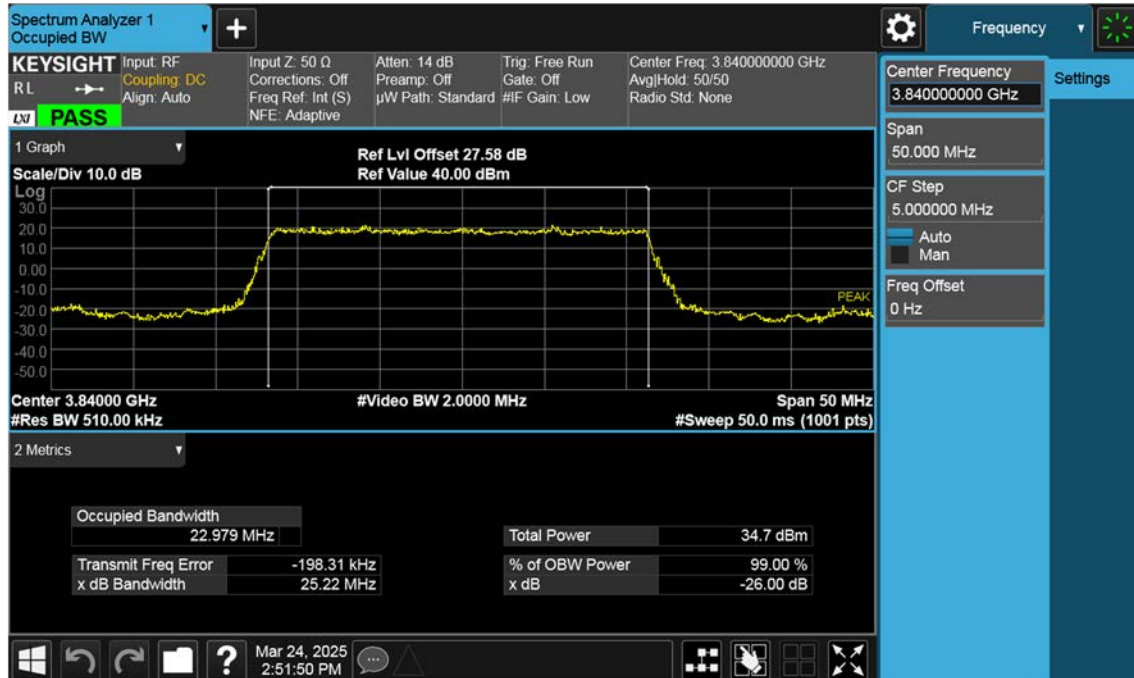
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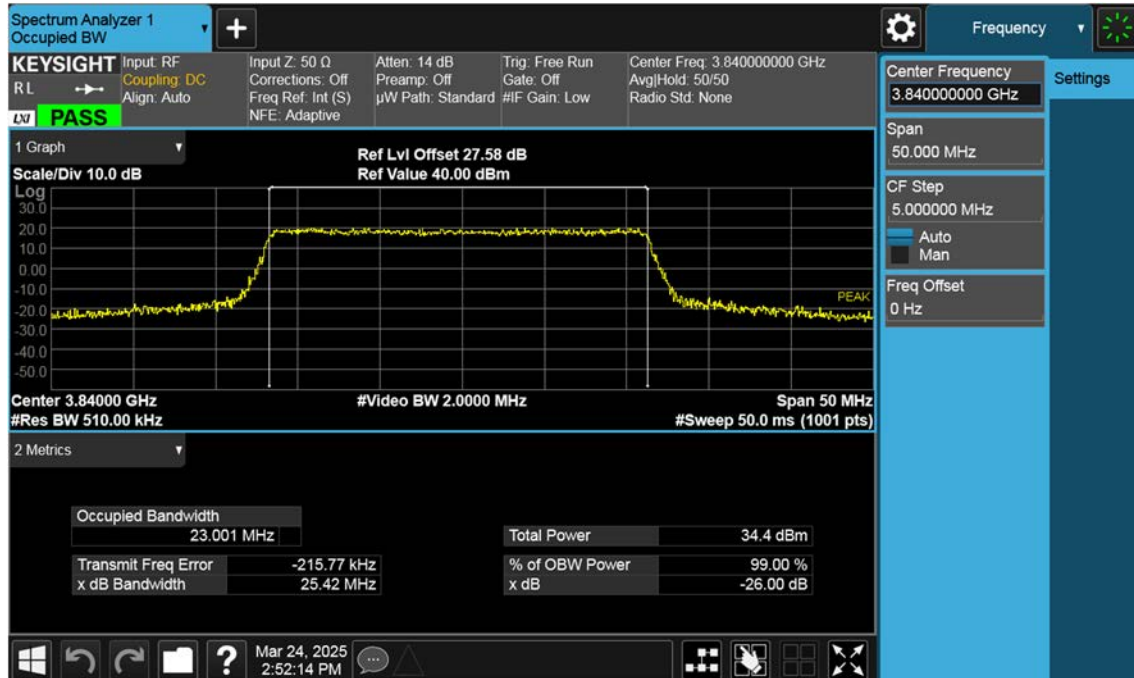
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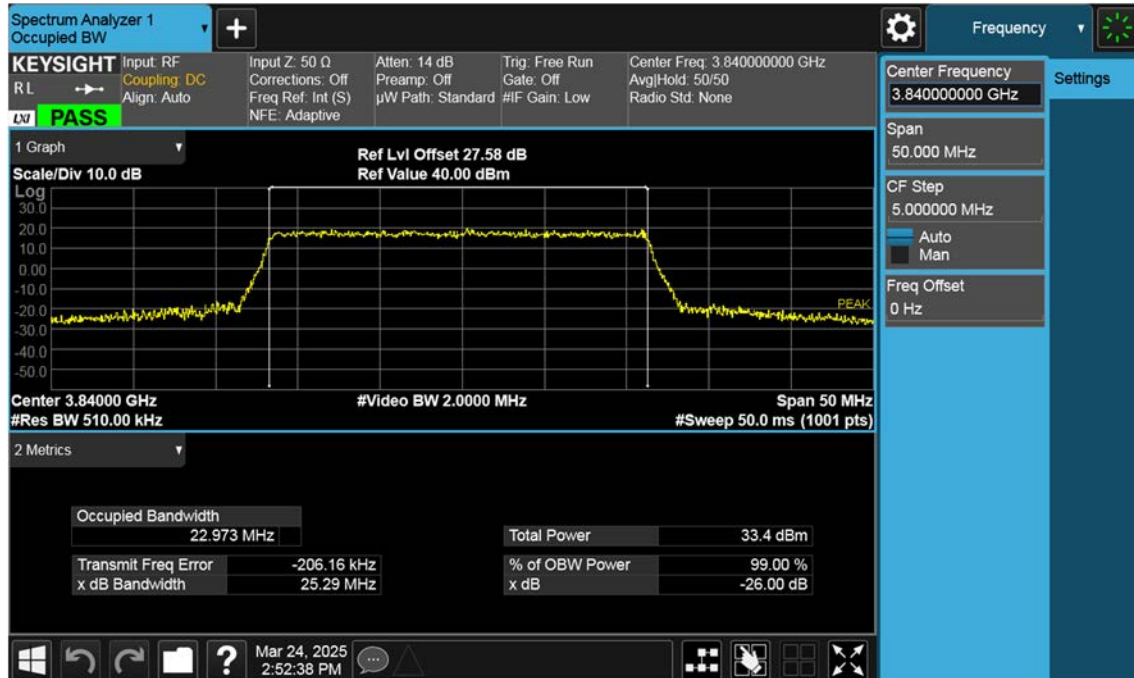
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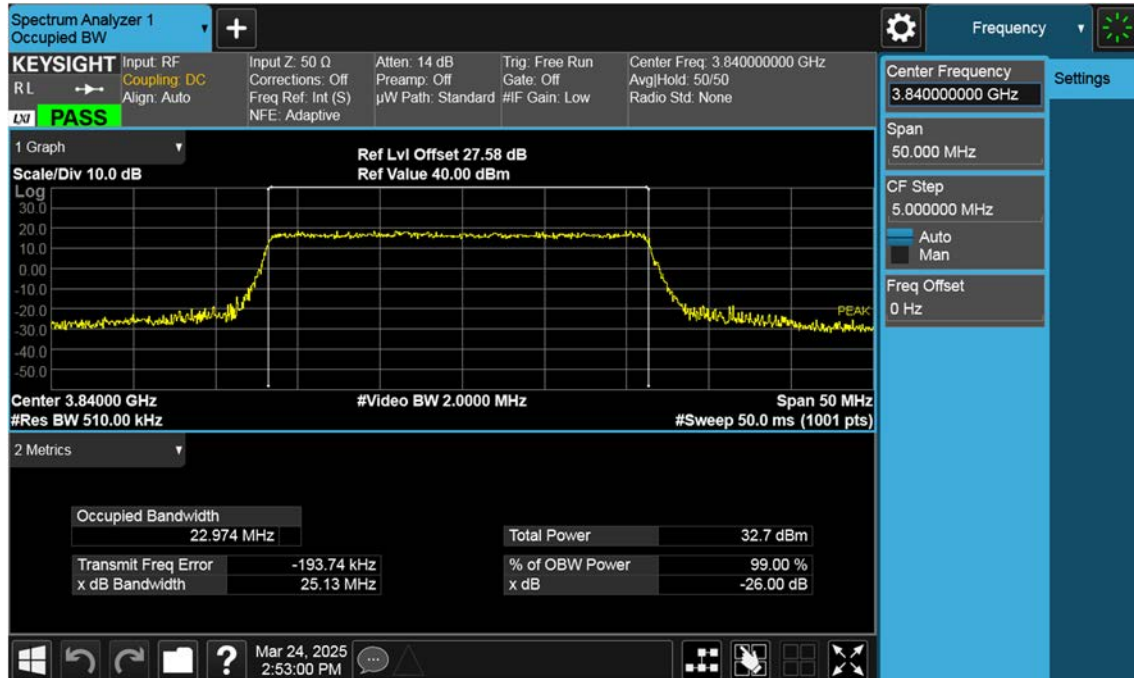
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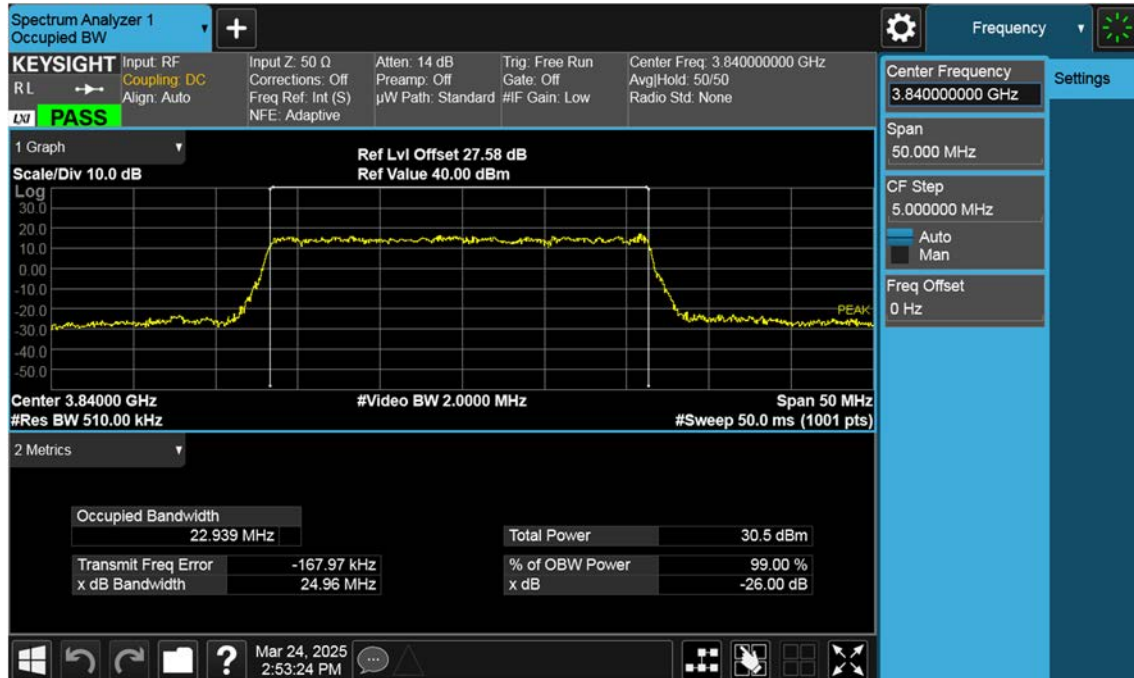
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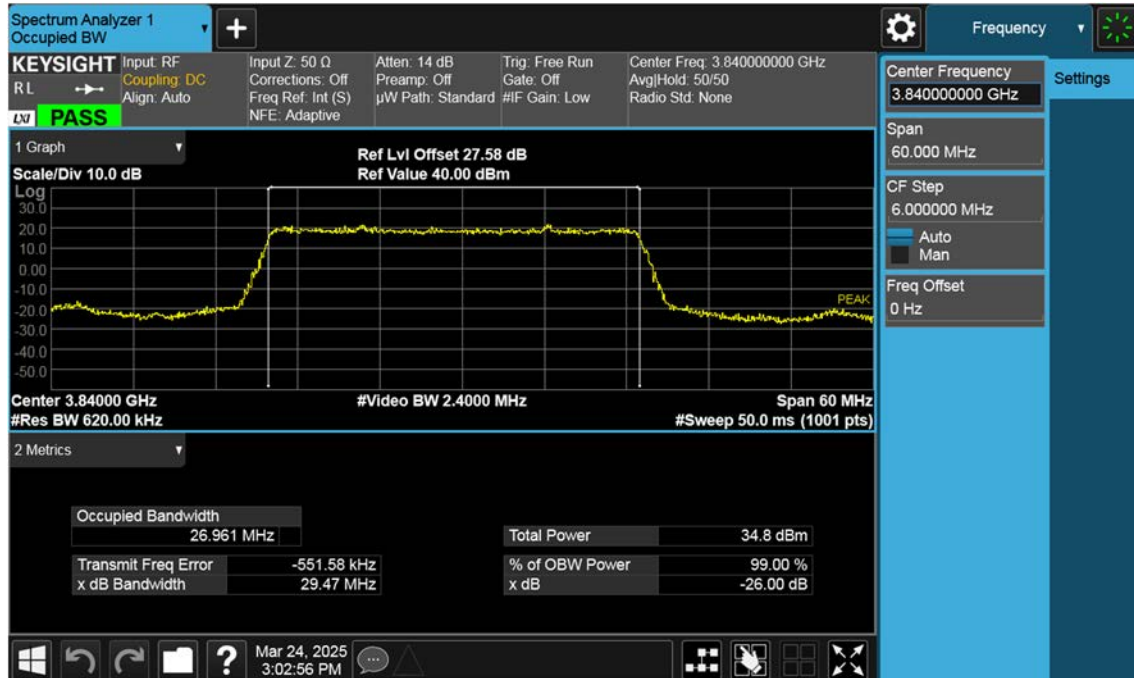
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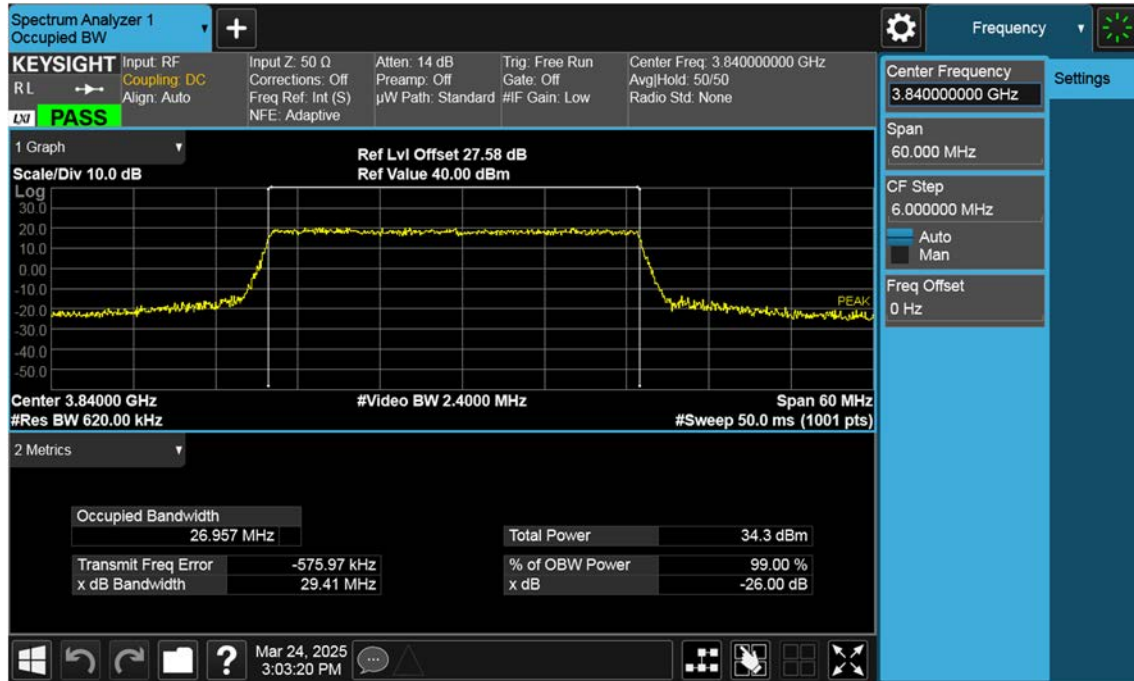
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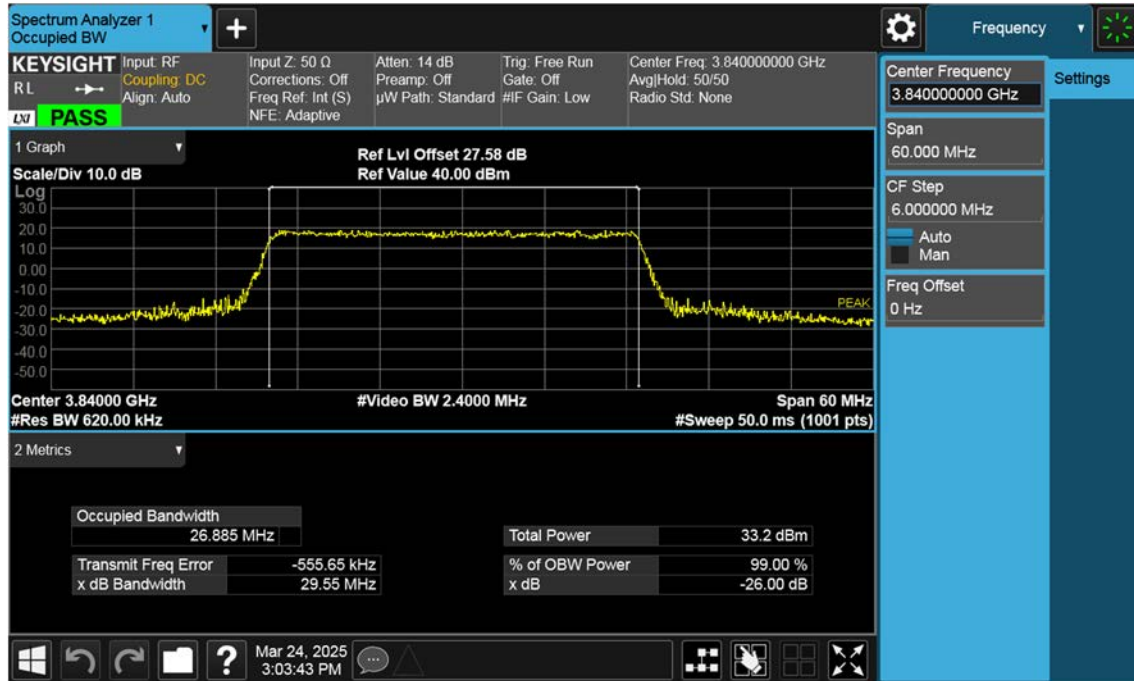
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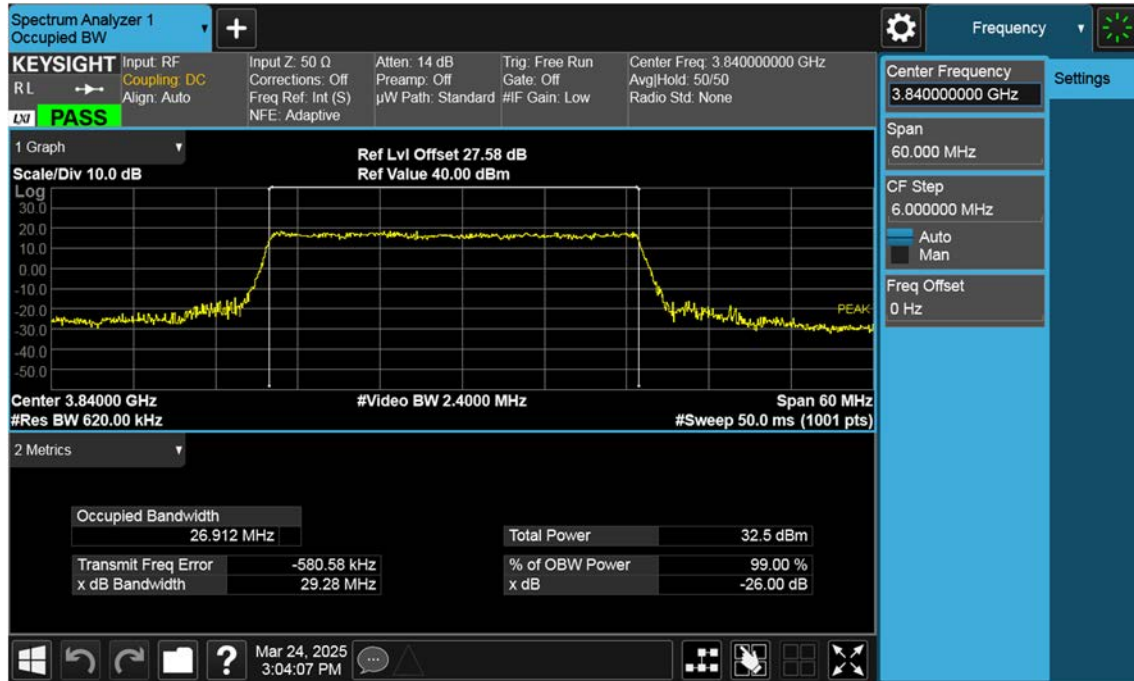
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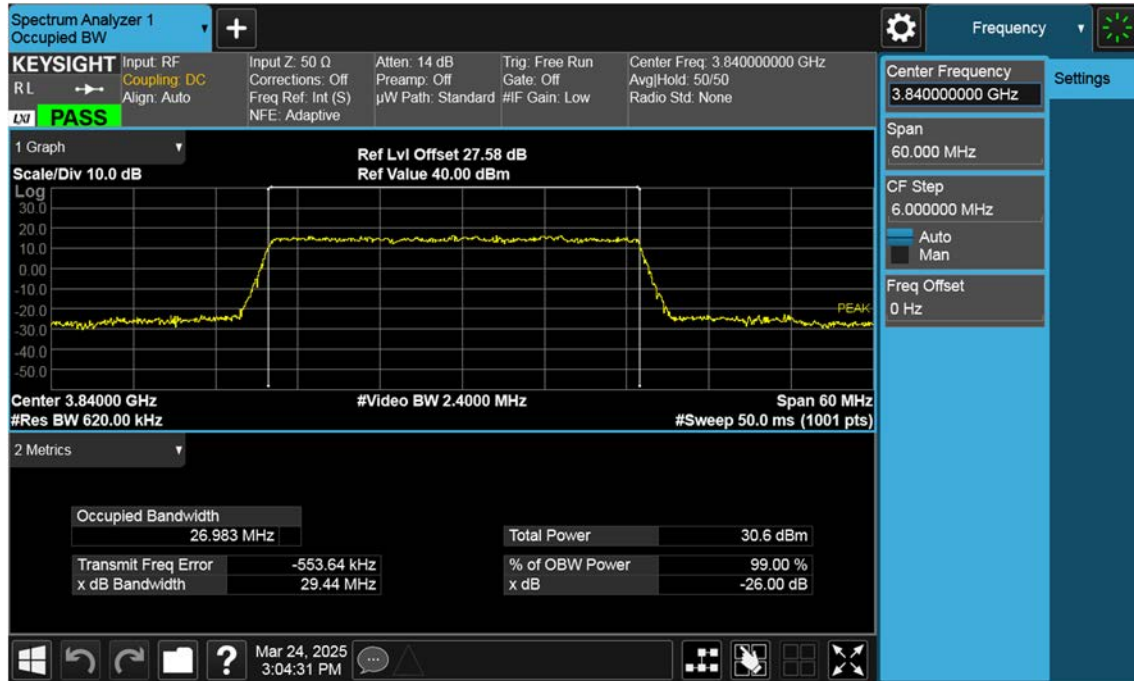
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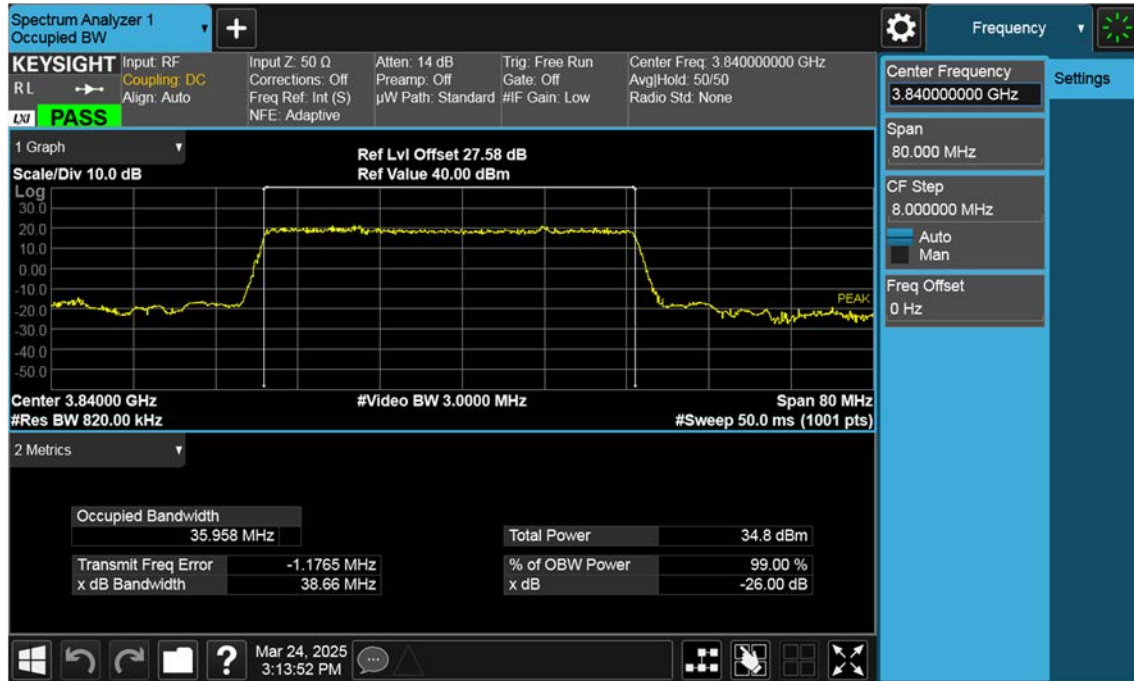
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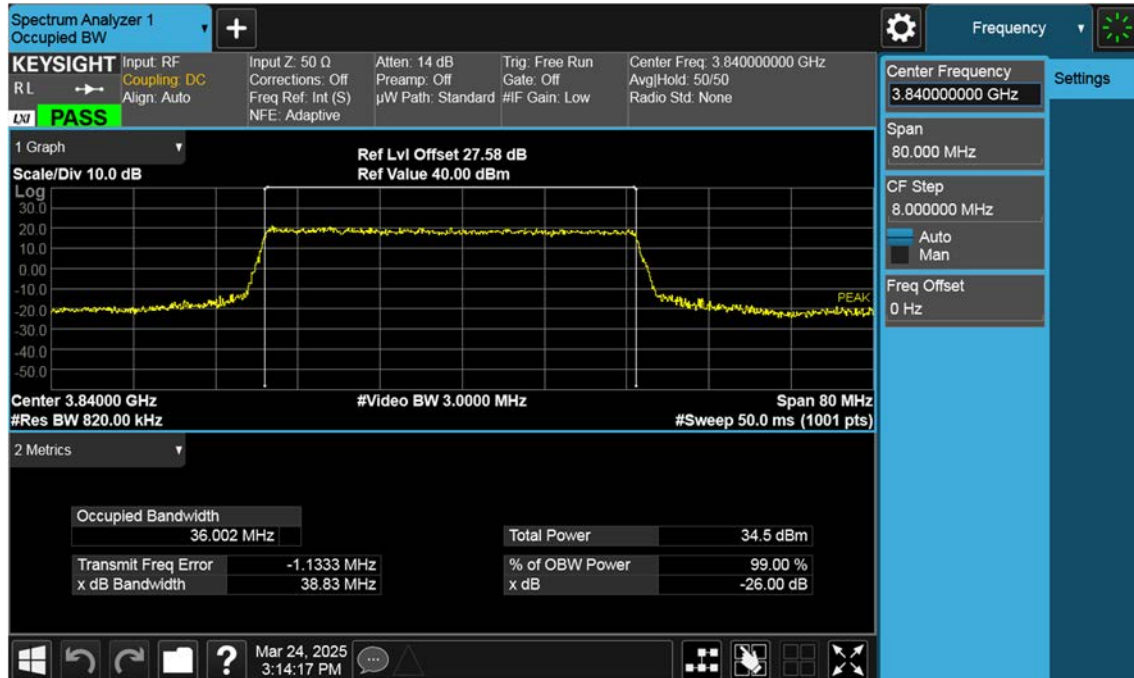
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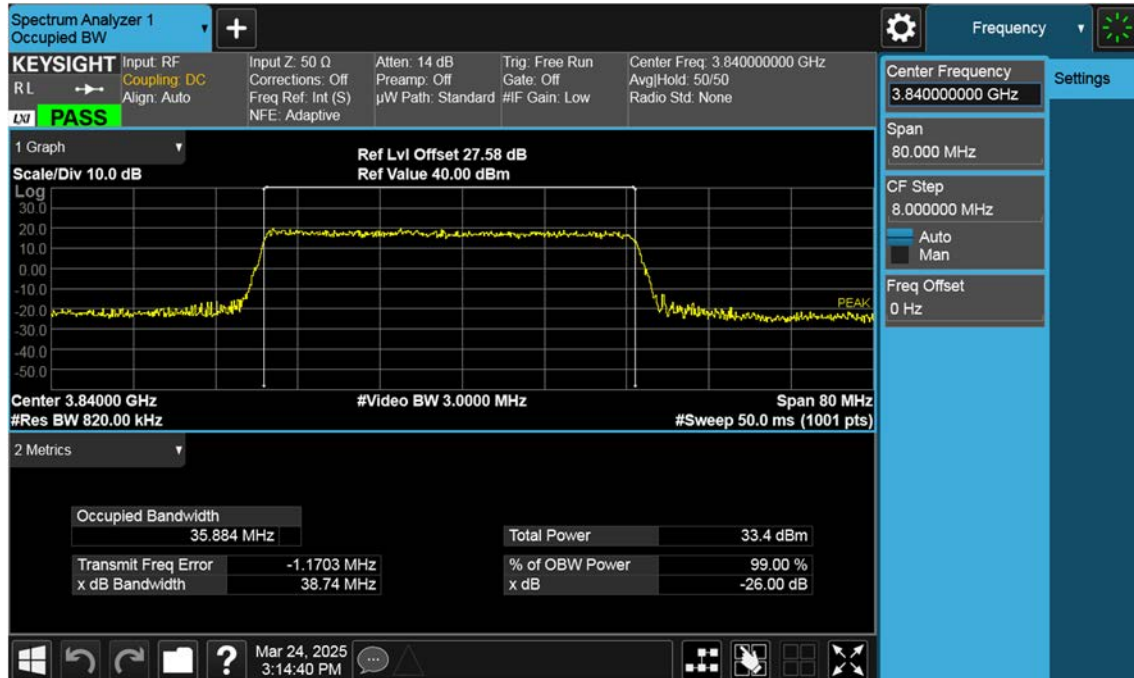
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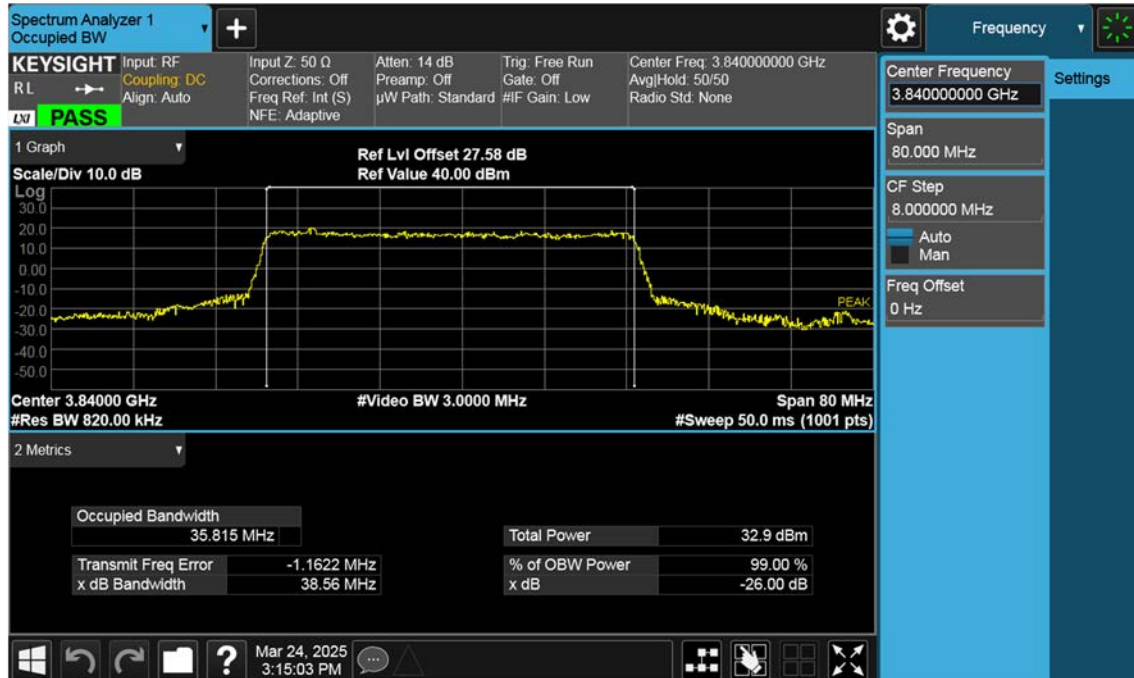
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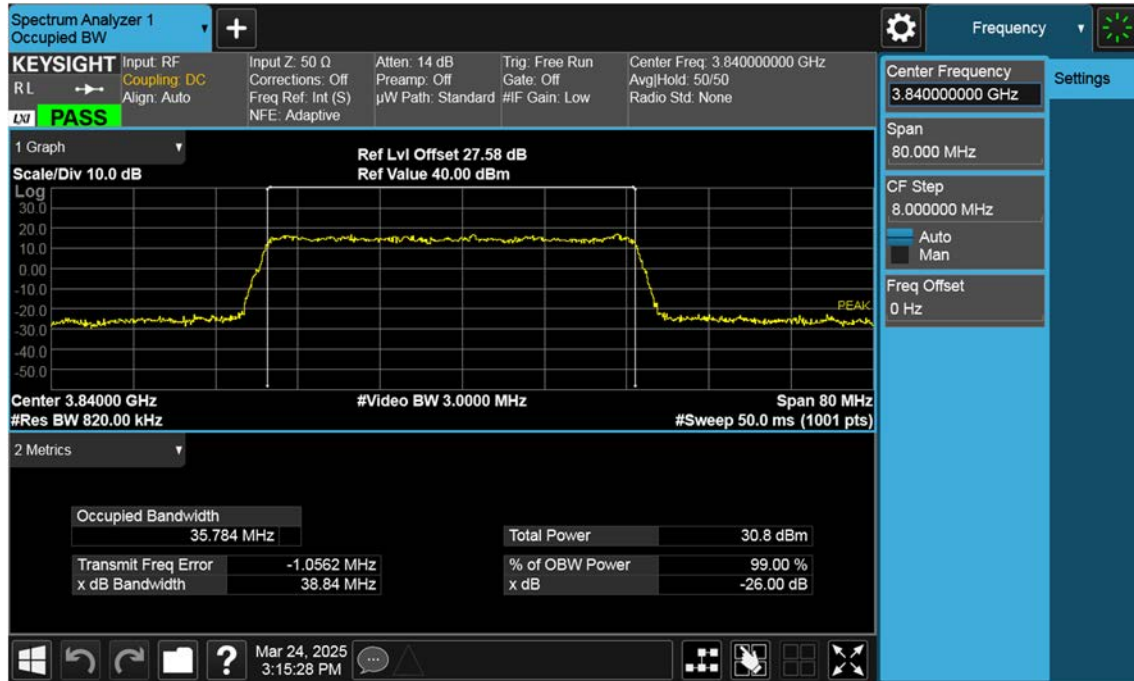
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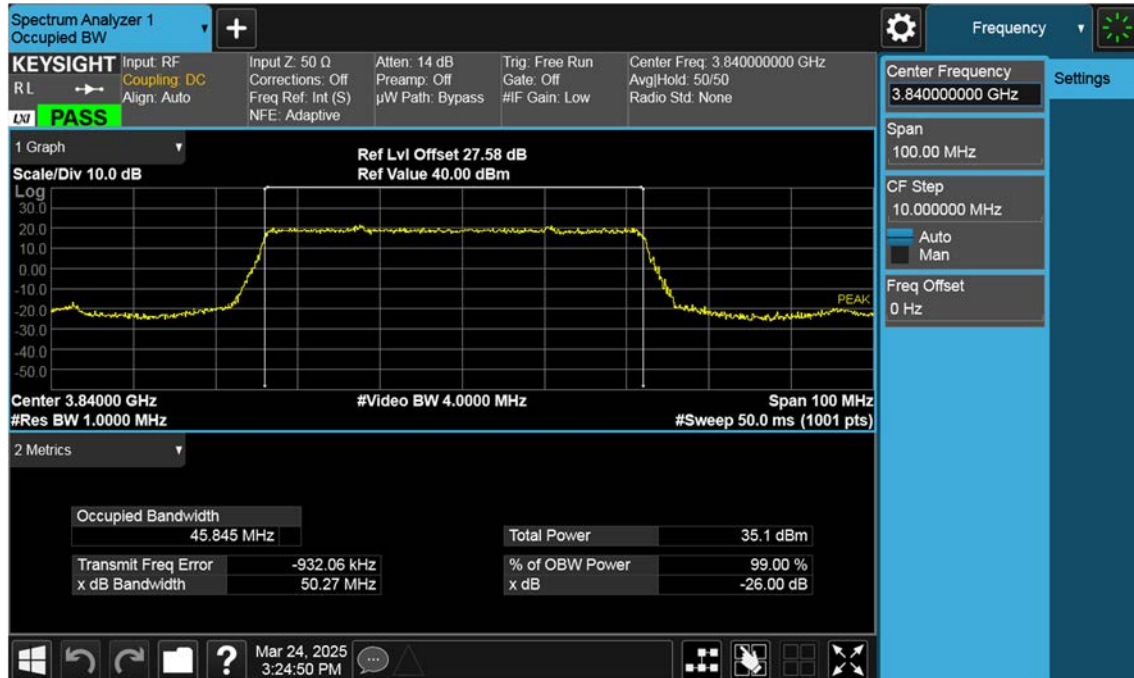
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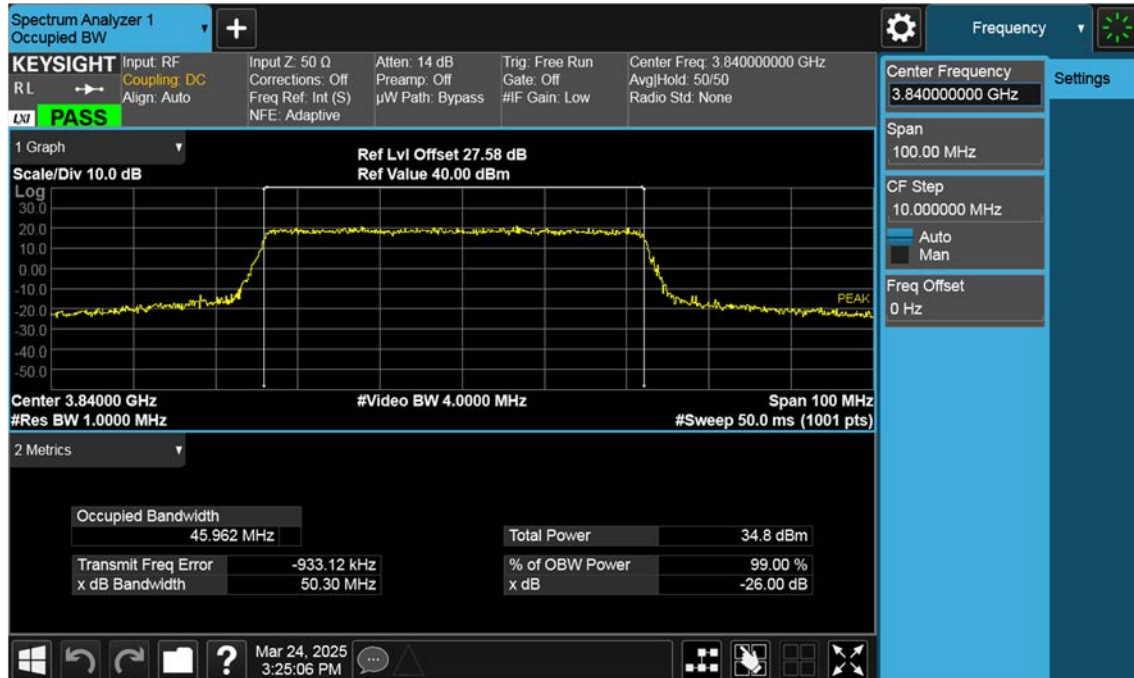
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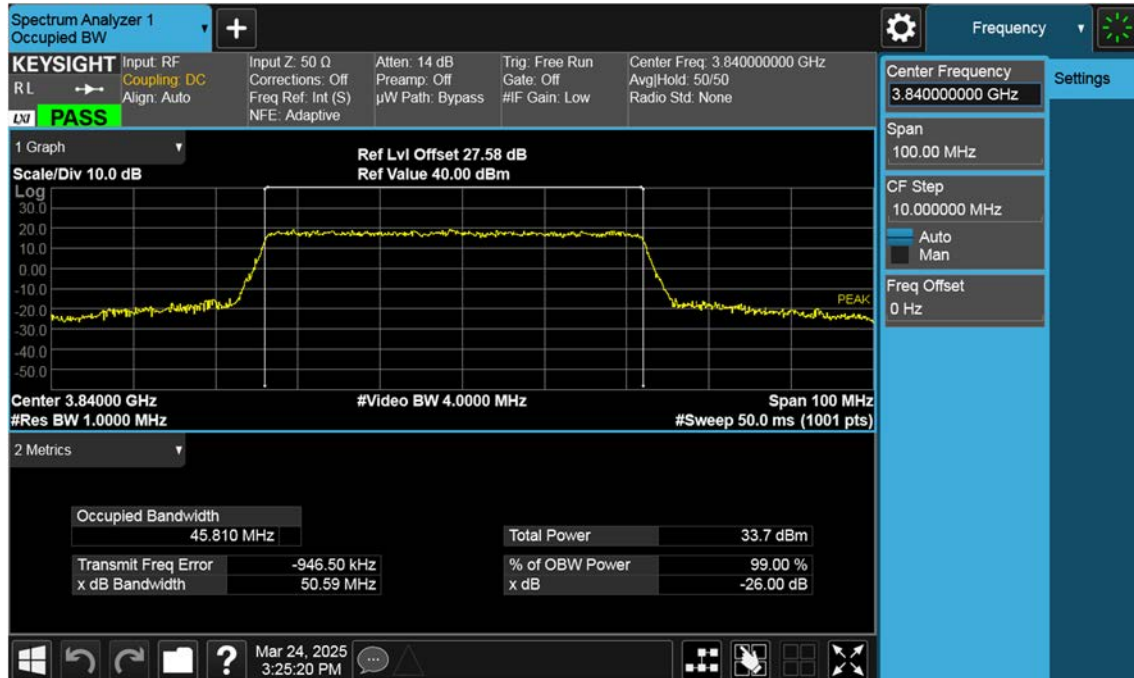
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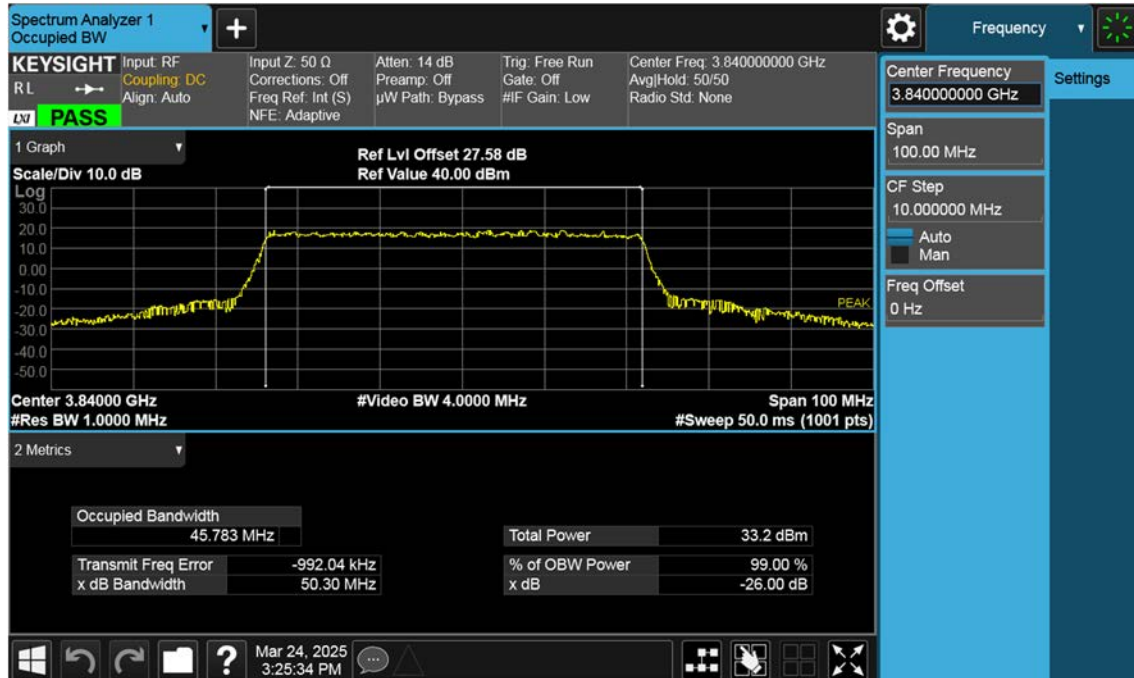
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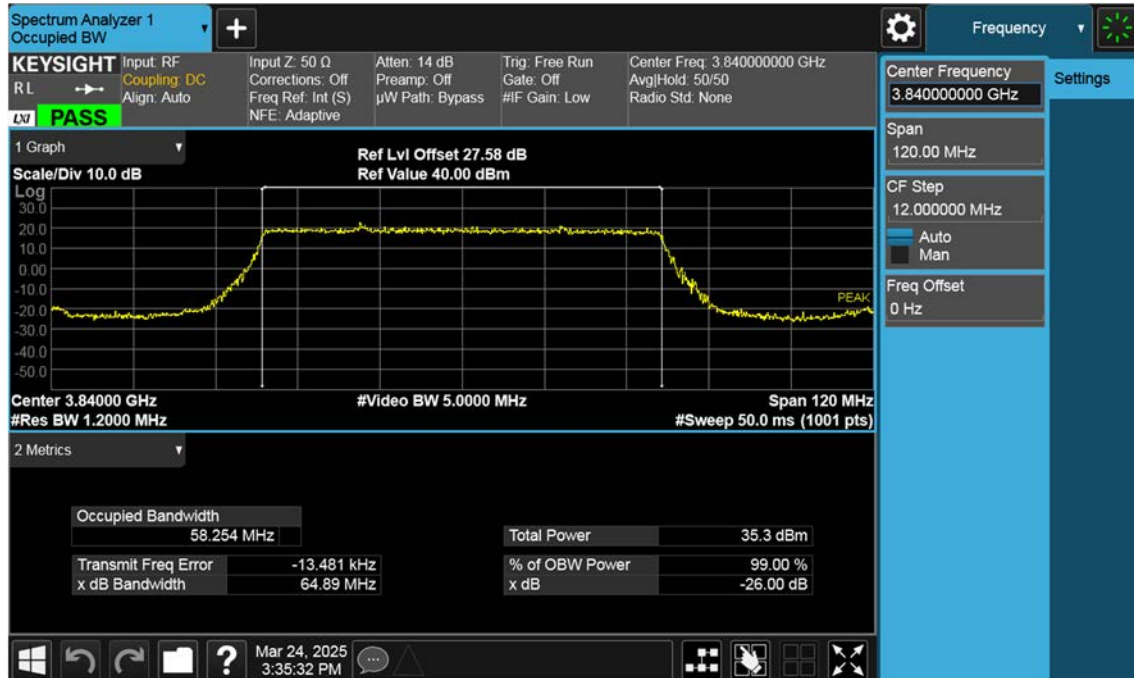
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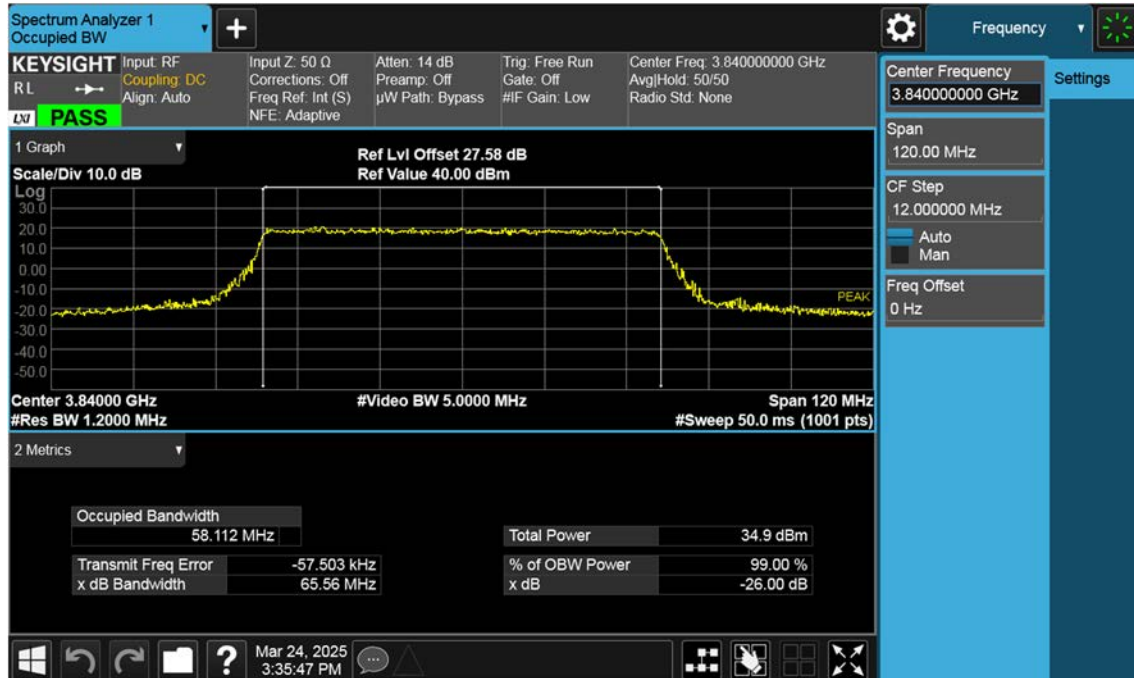
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n77(3700~3980 MHz)_60 M_OBW_Mid_BPSK_FullRB



n77(3700~3980 MHz)_60 M_OBW_Mid_QPSK_FullRB



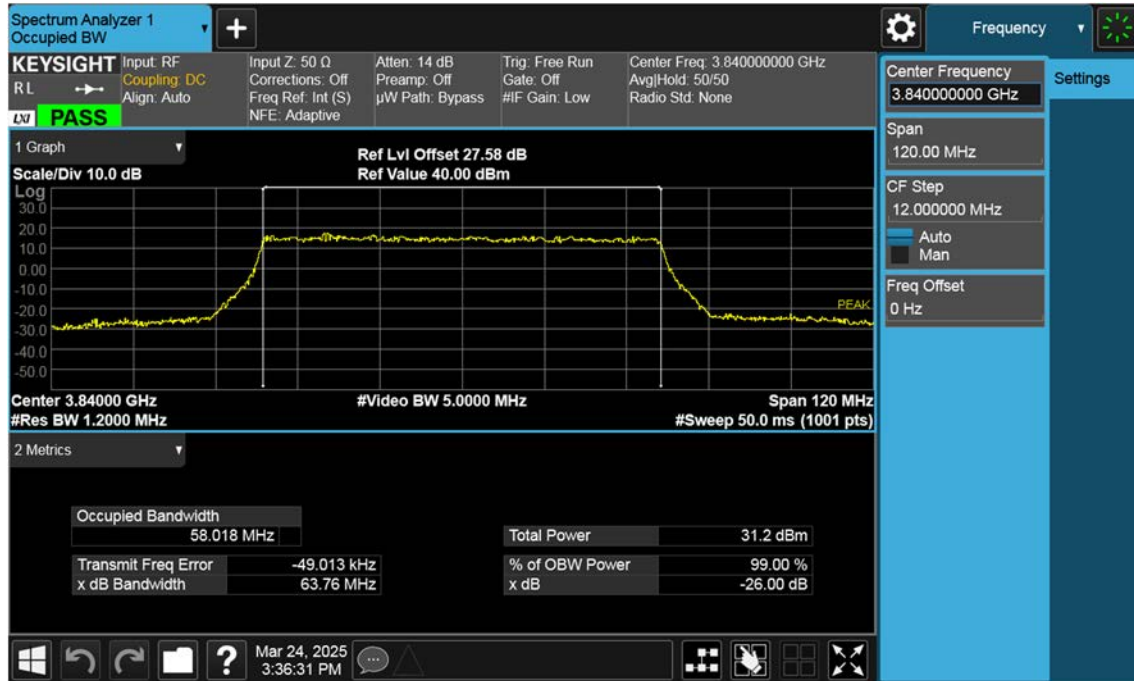
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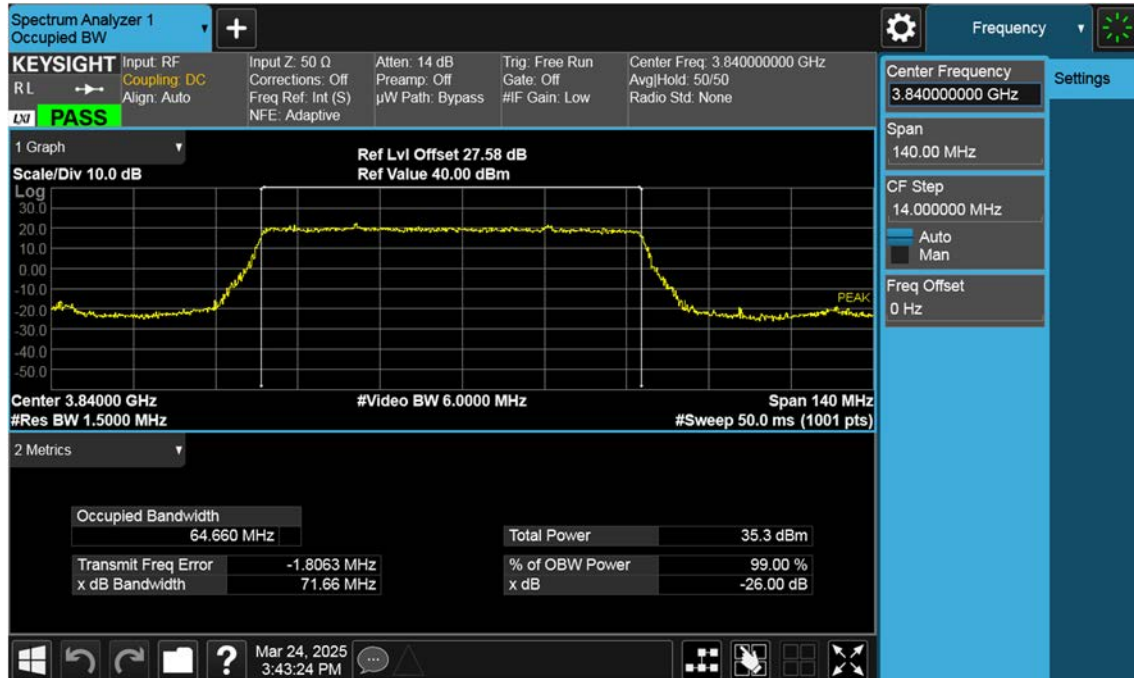
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n77(3700~3980 MHz)_60 M_OBW_Mid_256QAM_FullRB



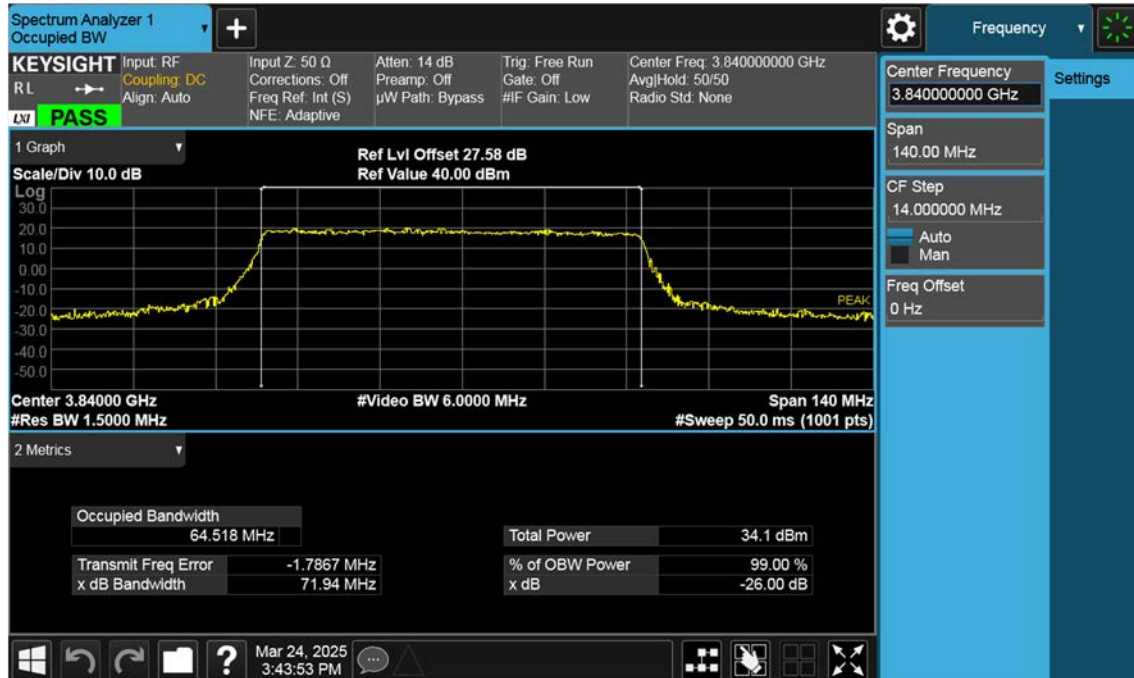
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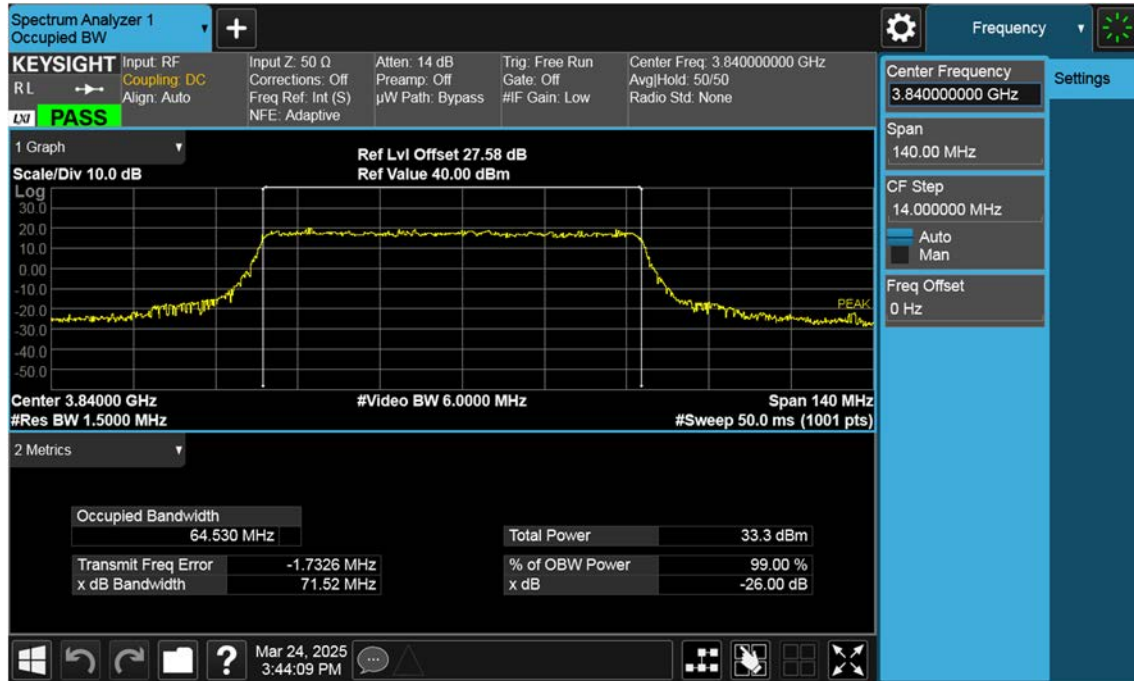
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n77(3700~3980 MHz)_70 M_OBW_Mid_16QAM_FullRB



n77(3700~3980 MHz)_70 M_OBW_Mid_64QAM_FullRB



n77(3700~3980 MHz)_70 M_OBW_Mid_256QAM_FullRB

