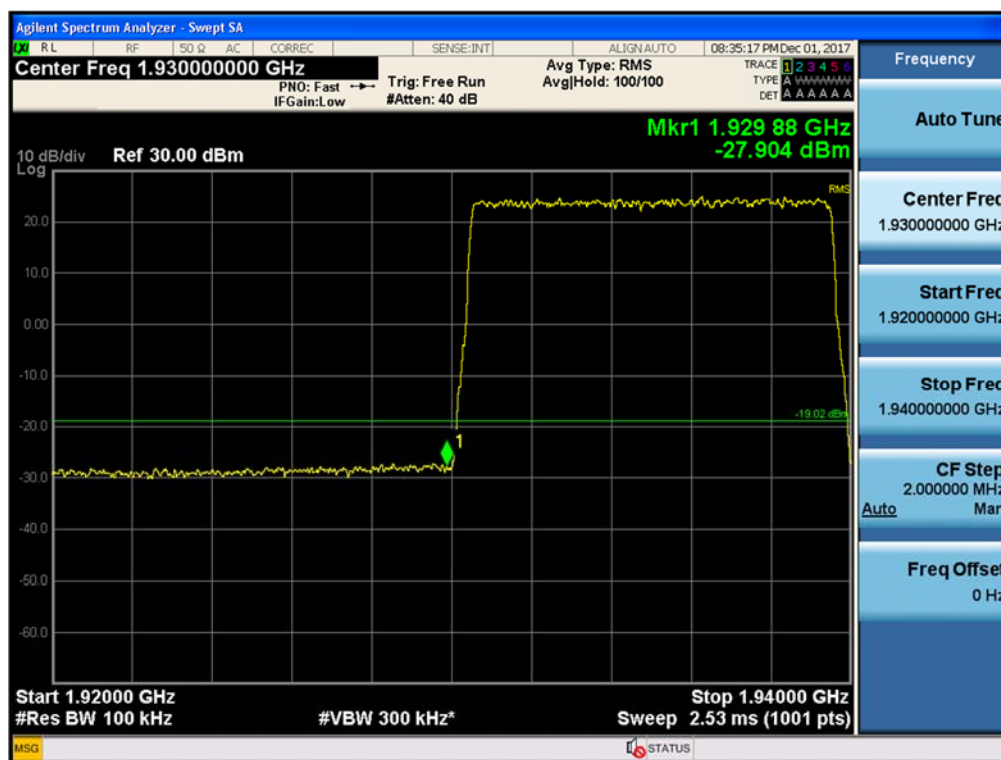
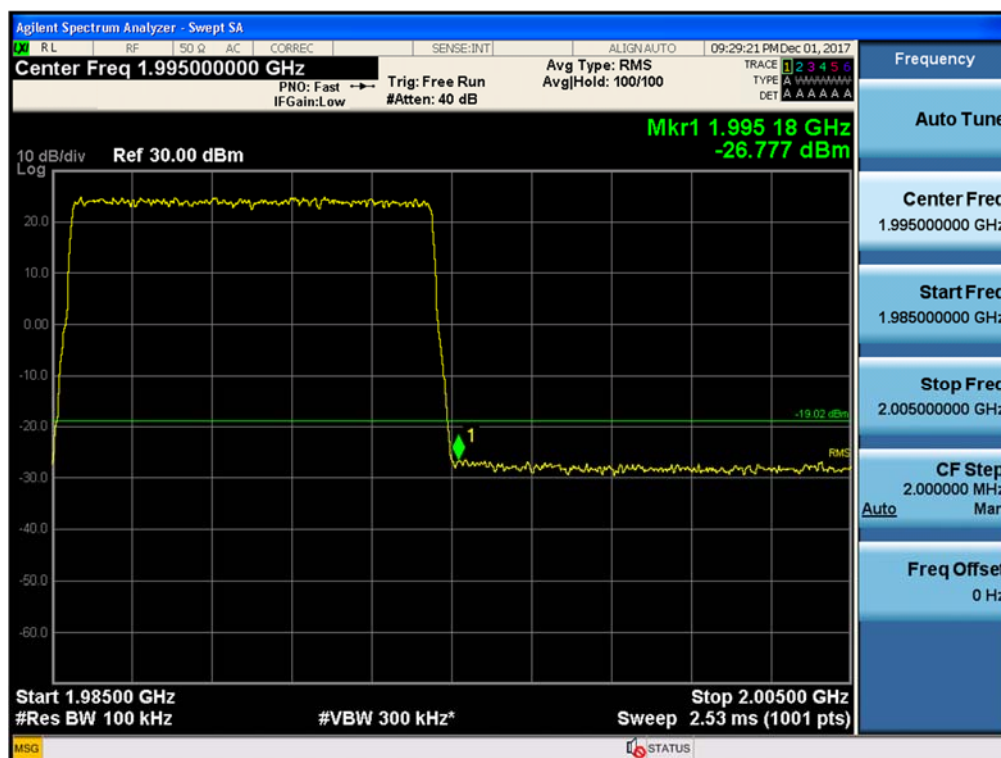


Test Data at Output Port 2_256QAM

[Downlink Low]

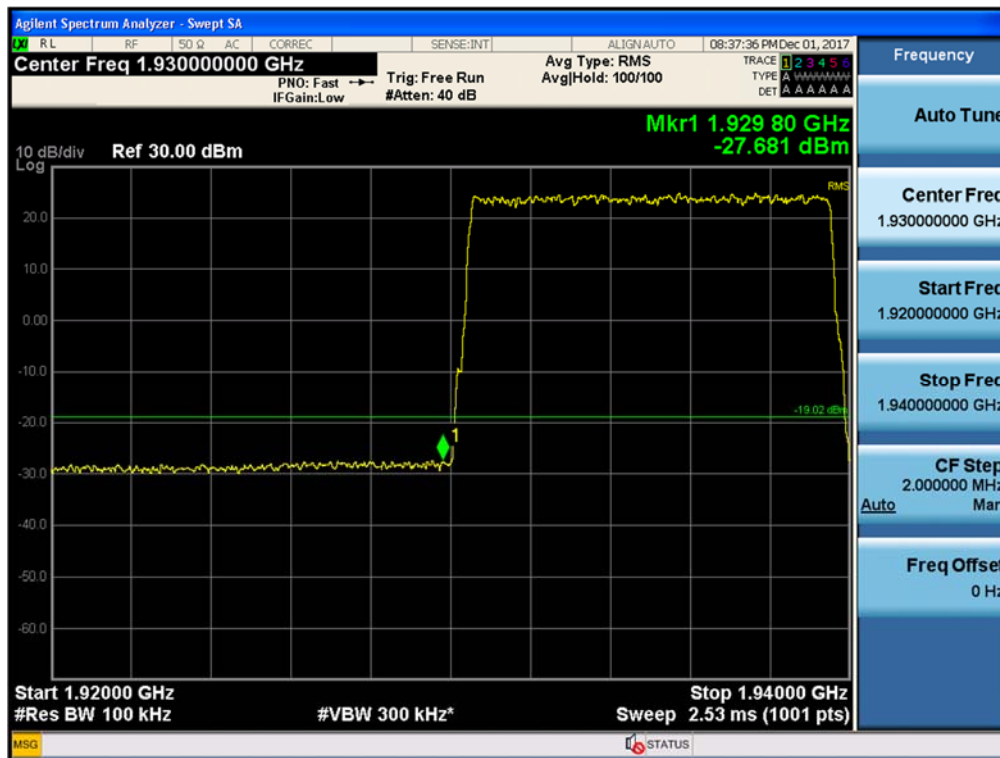


[Downlink High]

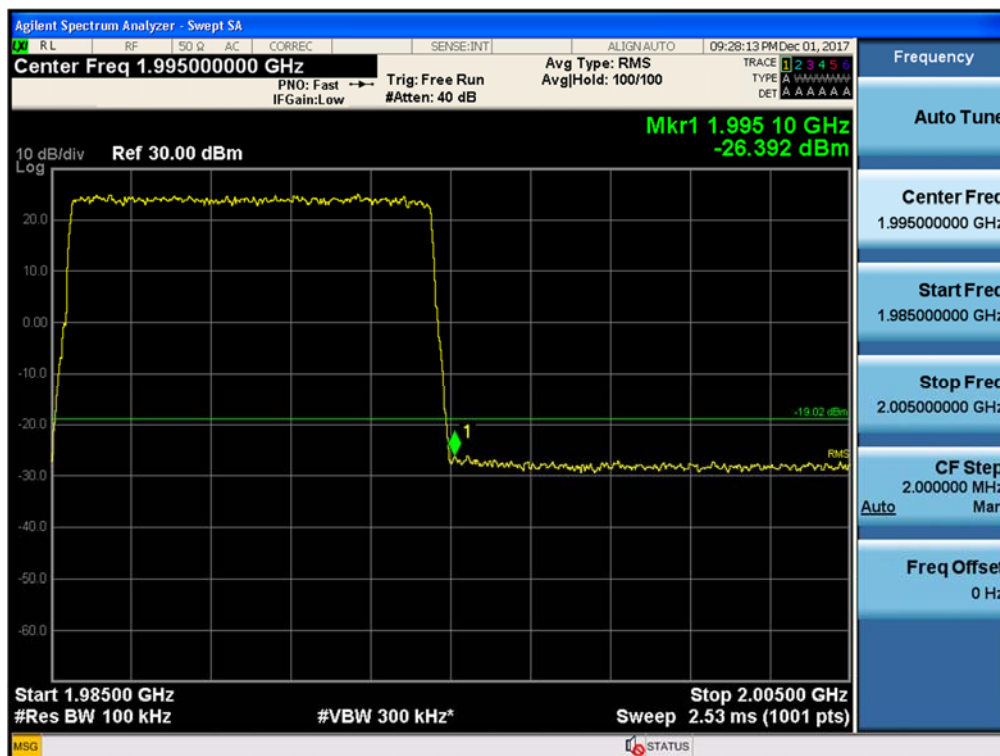


Test Data at Output Port 3_256QAM

[Downlink Low]



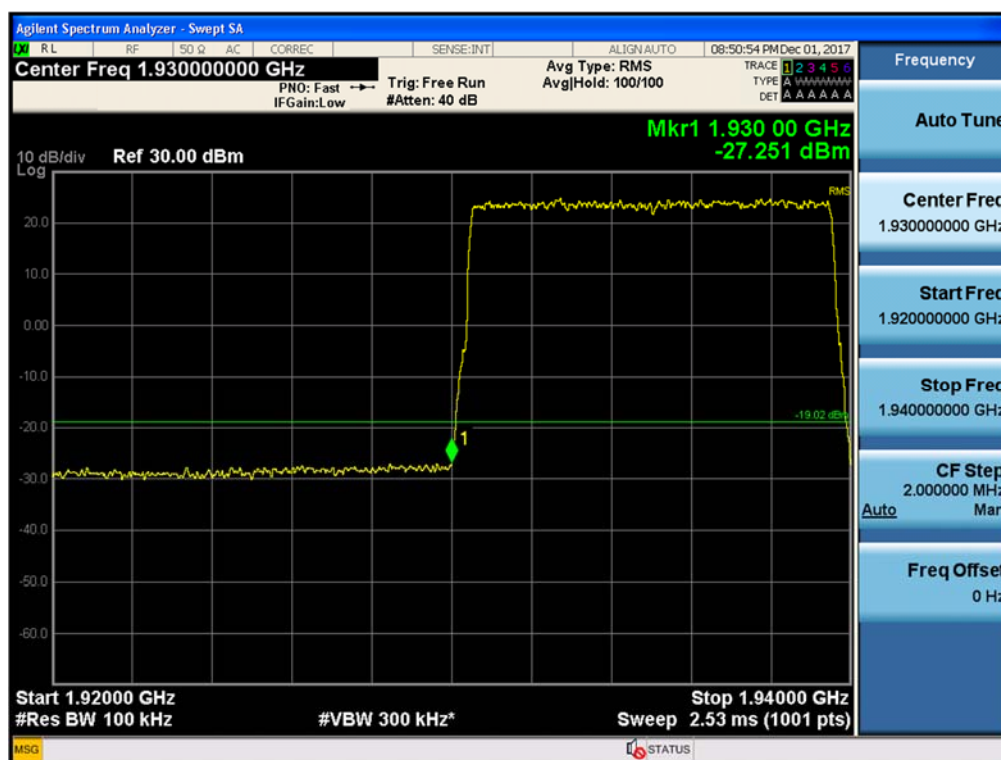
[Downlink High]



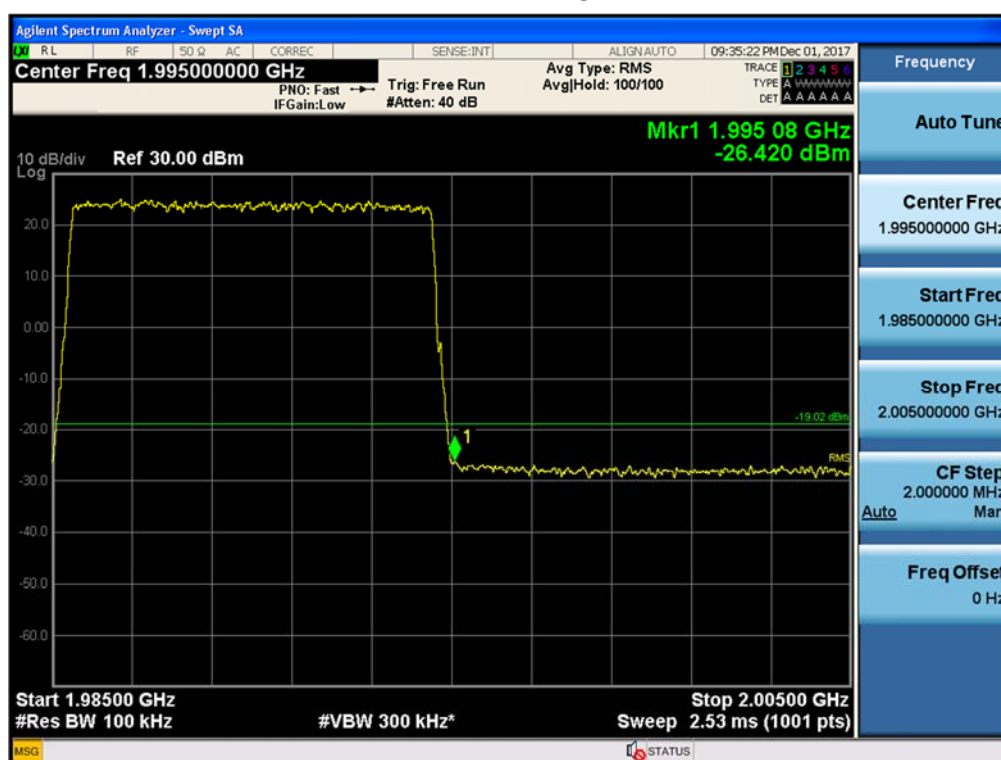
10 MHz + 10 MHz / 2 Carriers (20 W + 20 W)

Test Data at Output Port 0_256QAM

[Downlink Low]

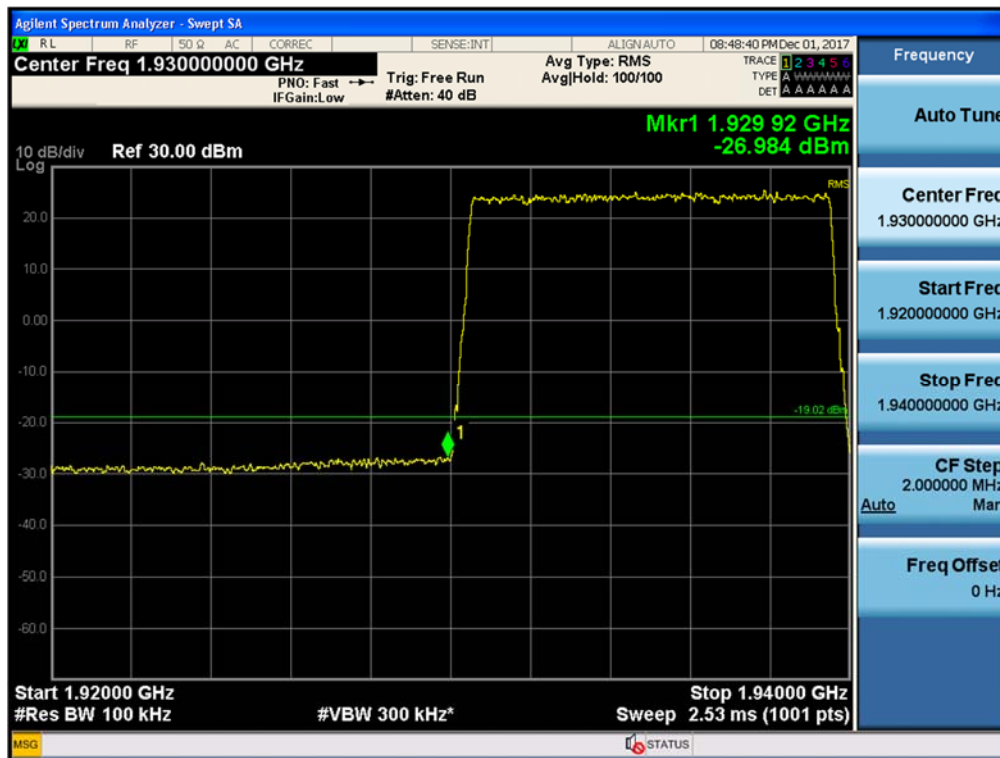


[Downlink High]

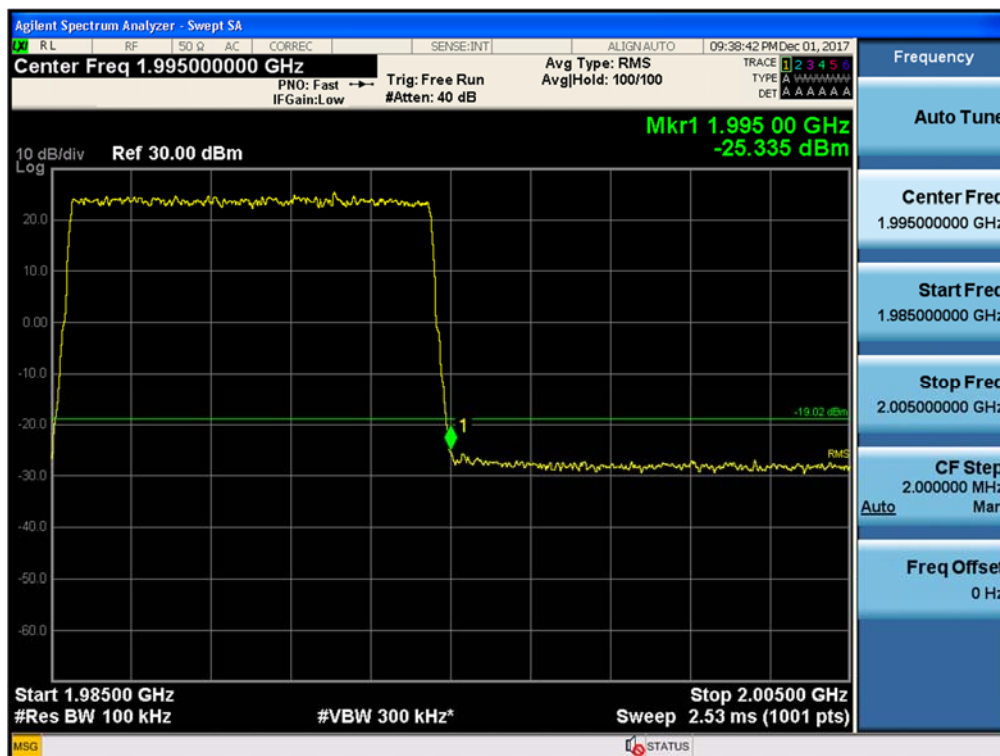


Test Data at Output Port 1_256QAM

[Downlink Low]

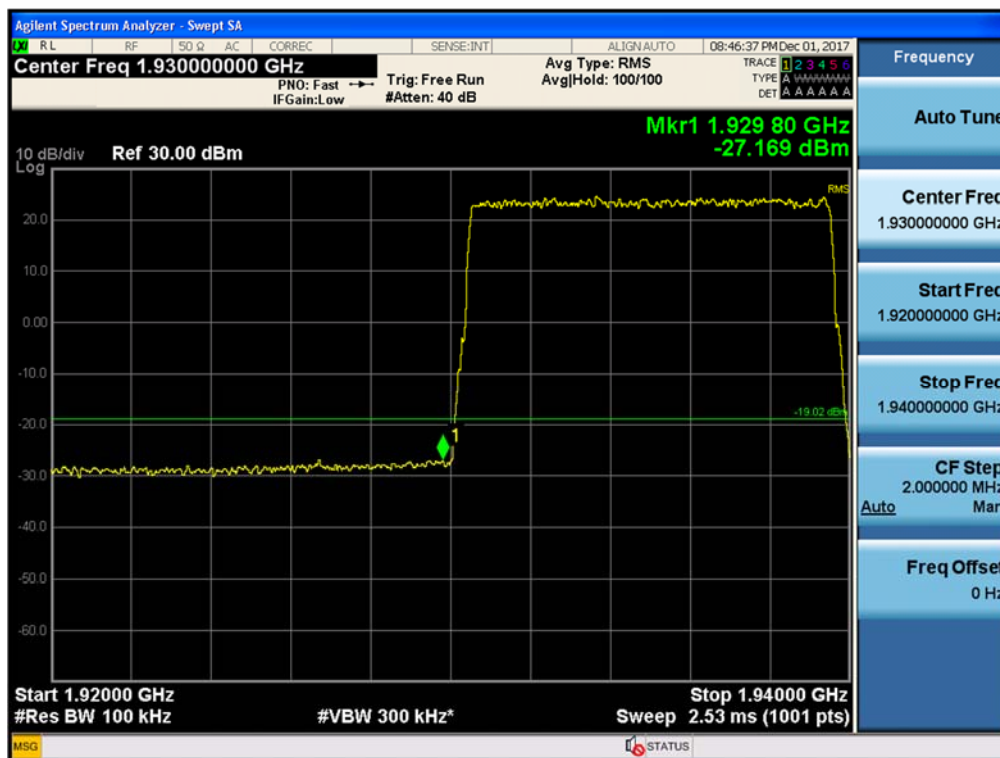


[Downlink High]

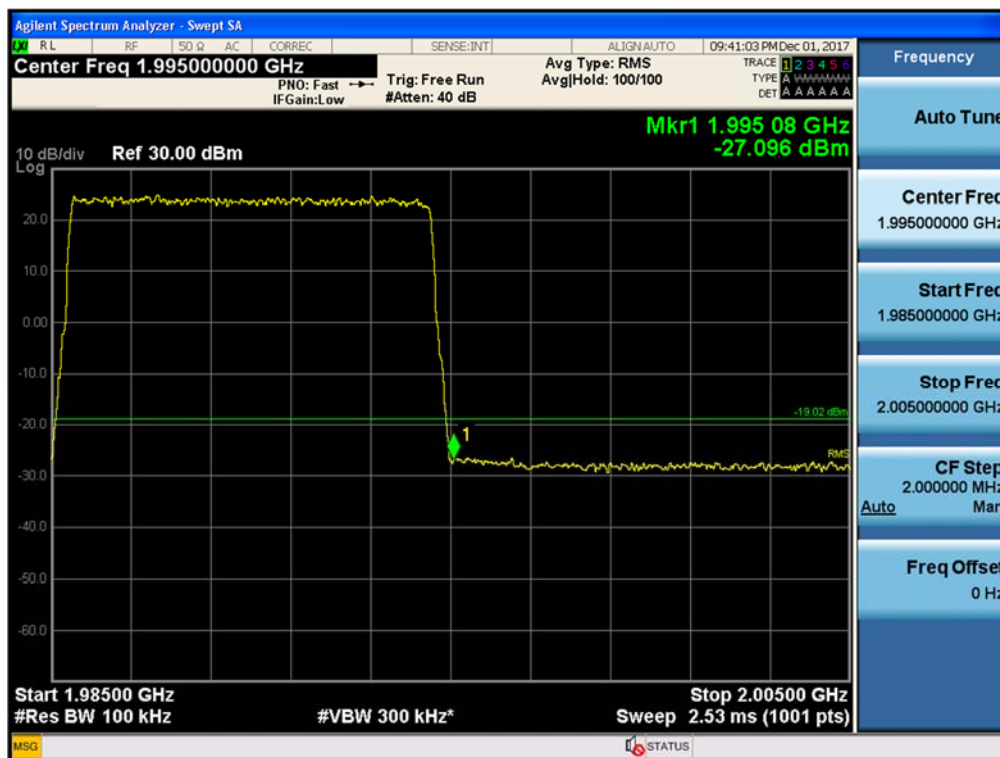


Test Data at Output Port 2_256QAM

[Downlink Low]

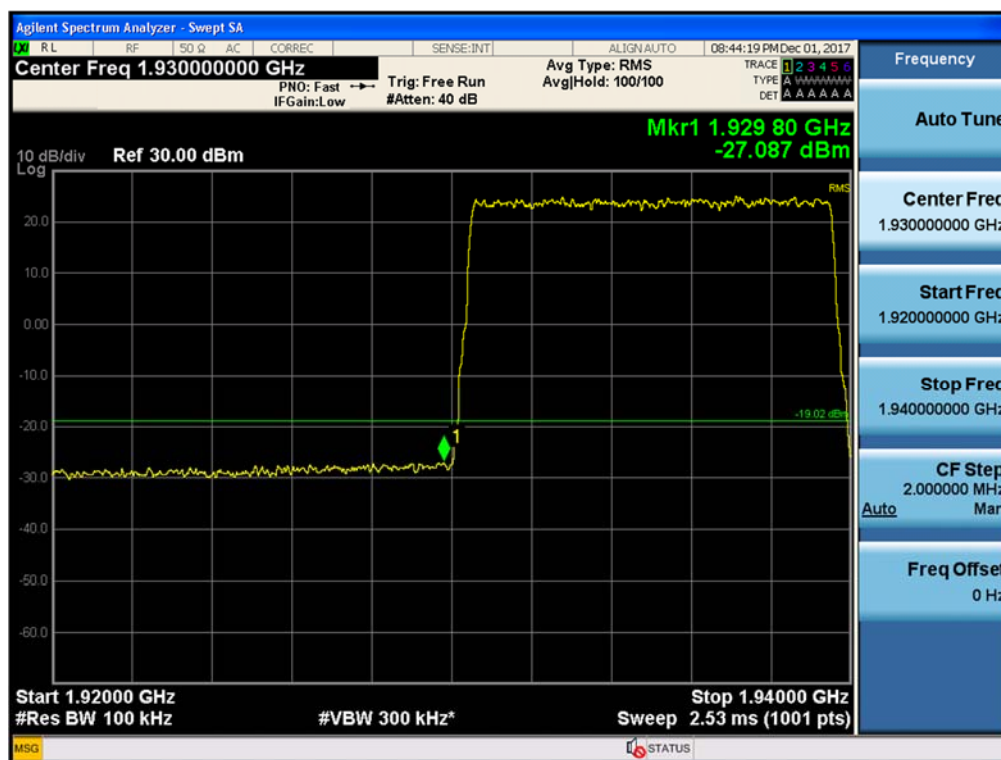


[Downlink High]

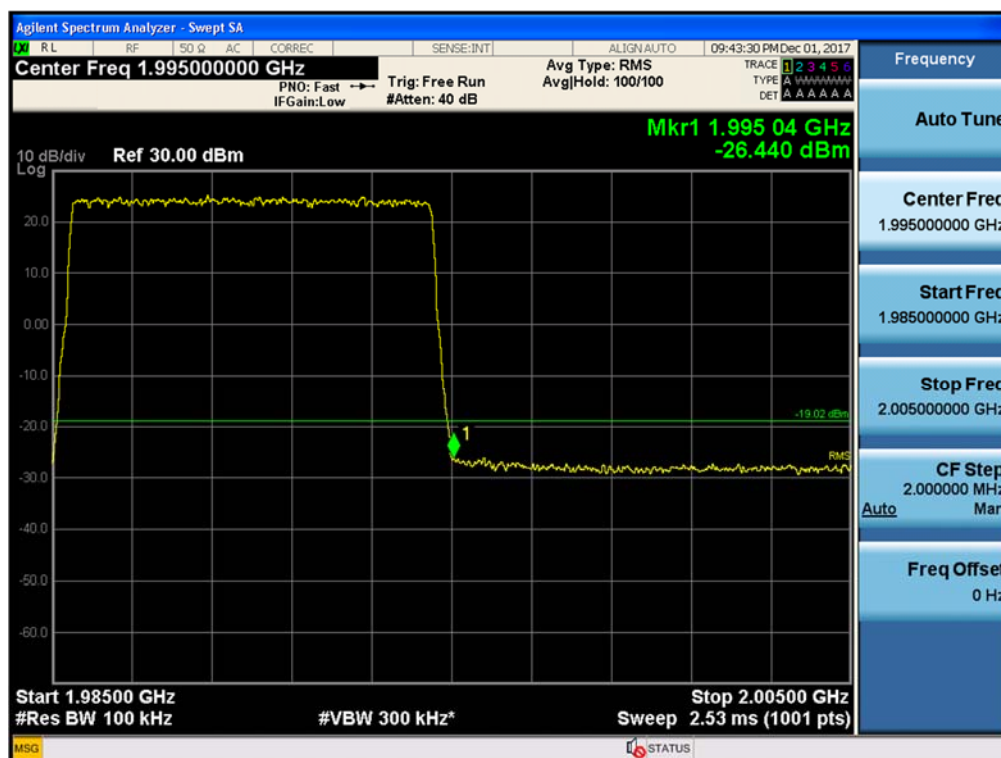


Test Data at Output Port 3_256QAM

[Downlink Low]



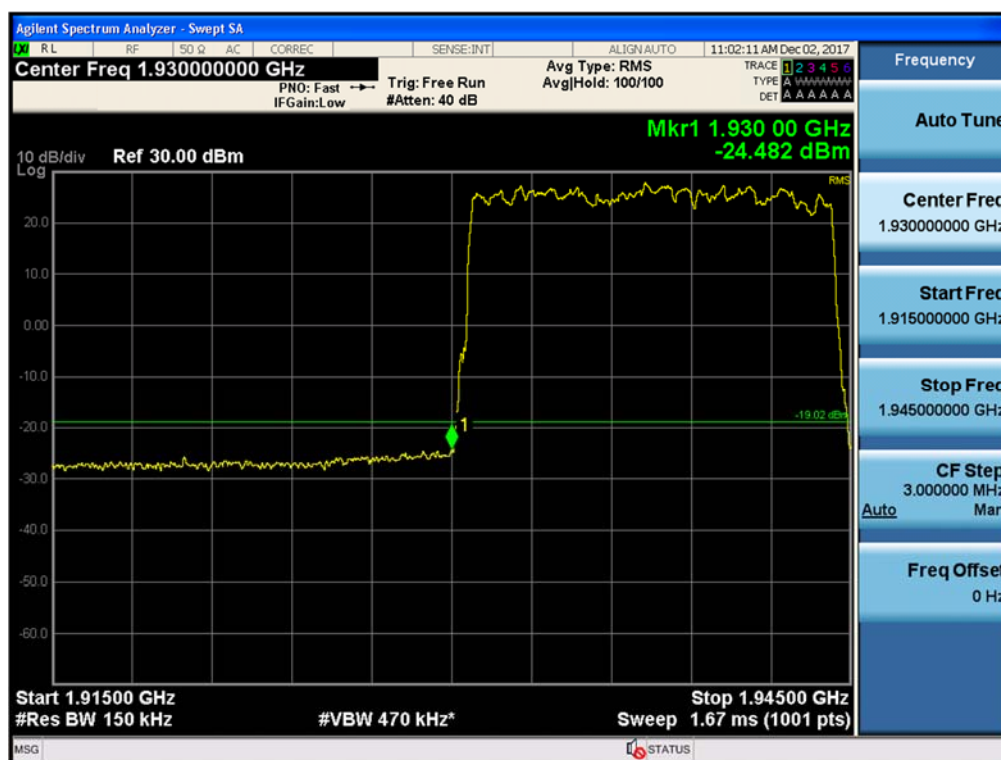
[Downlink High]



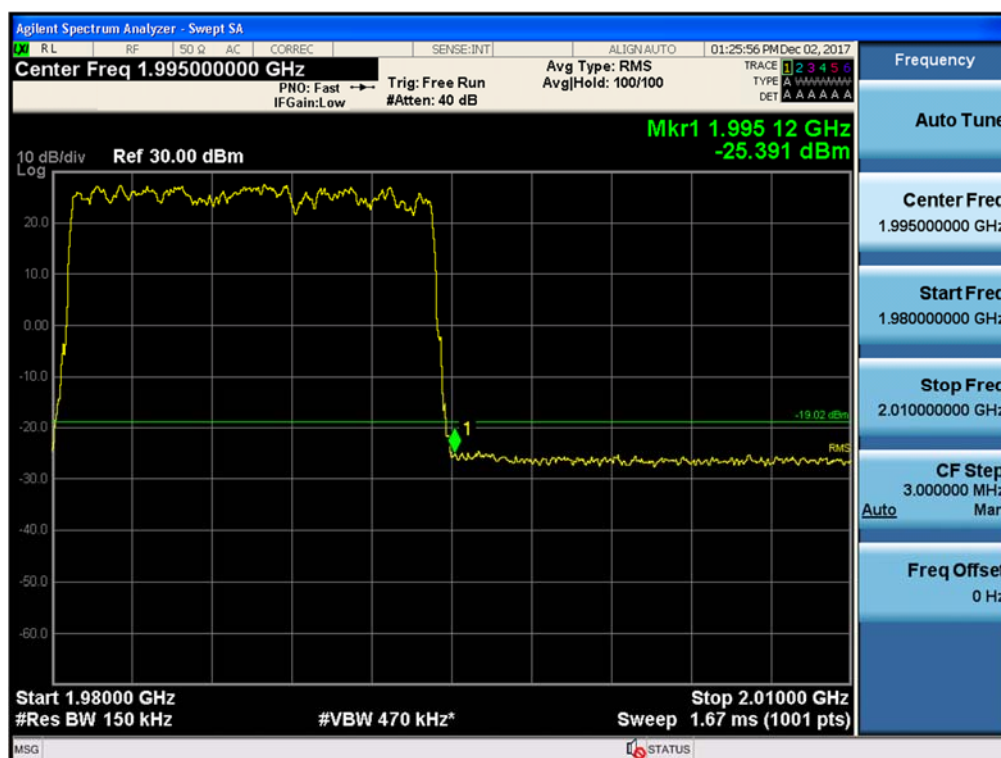
15 MHz / 1 Carrier (30 W)

Test Data at Output Port 0_QPSK

[Downlink Low]

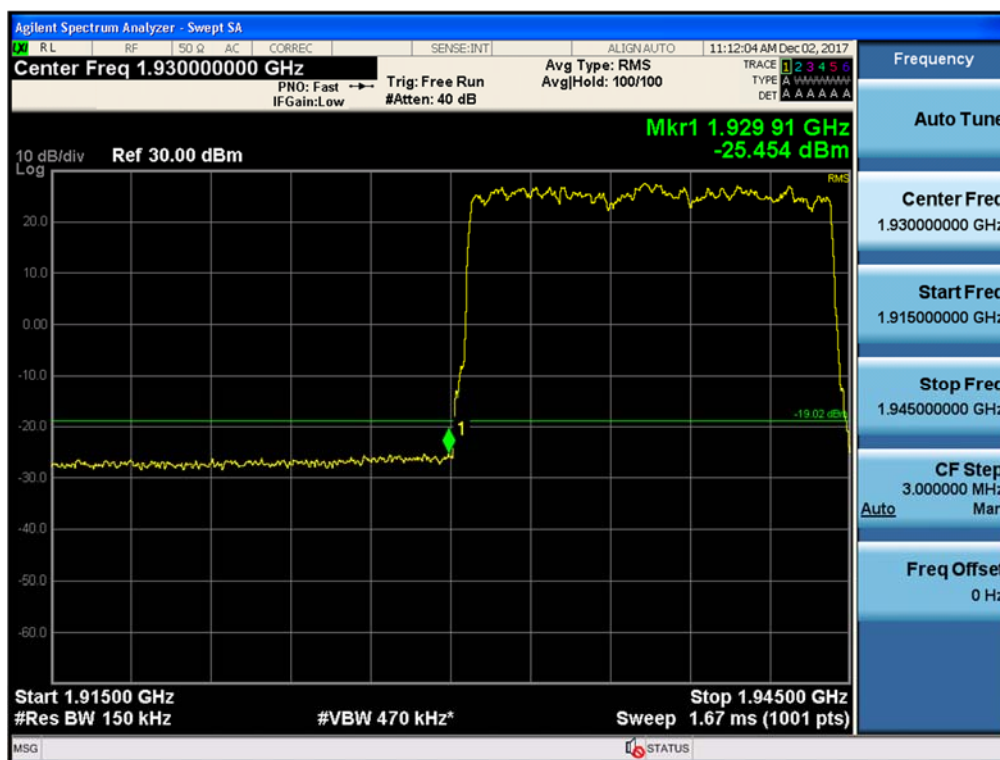


[Downlink High]

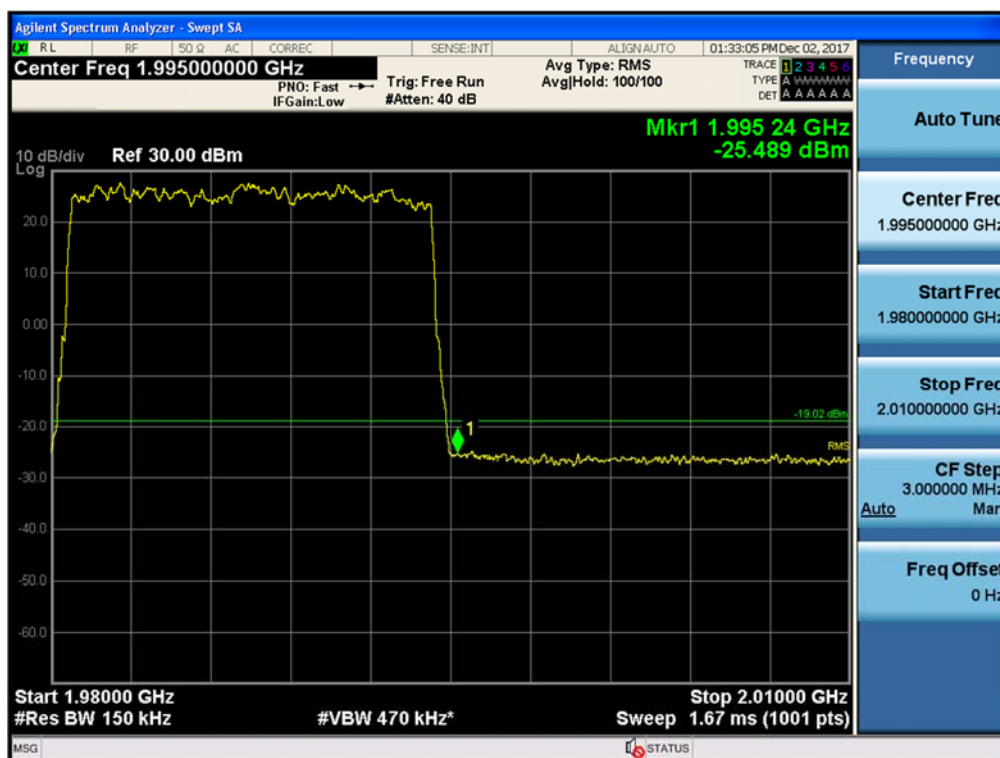


Test Data at Output Port 0_16QAM

[Downlink Low]

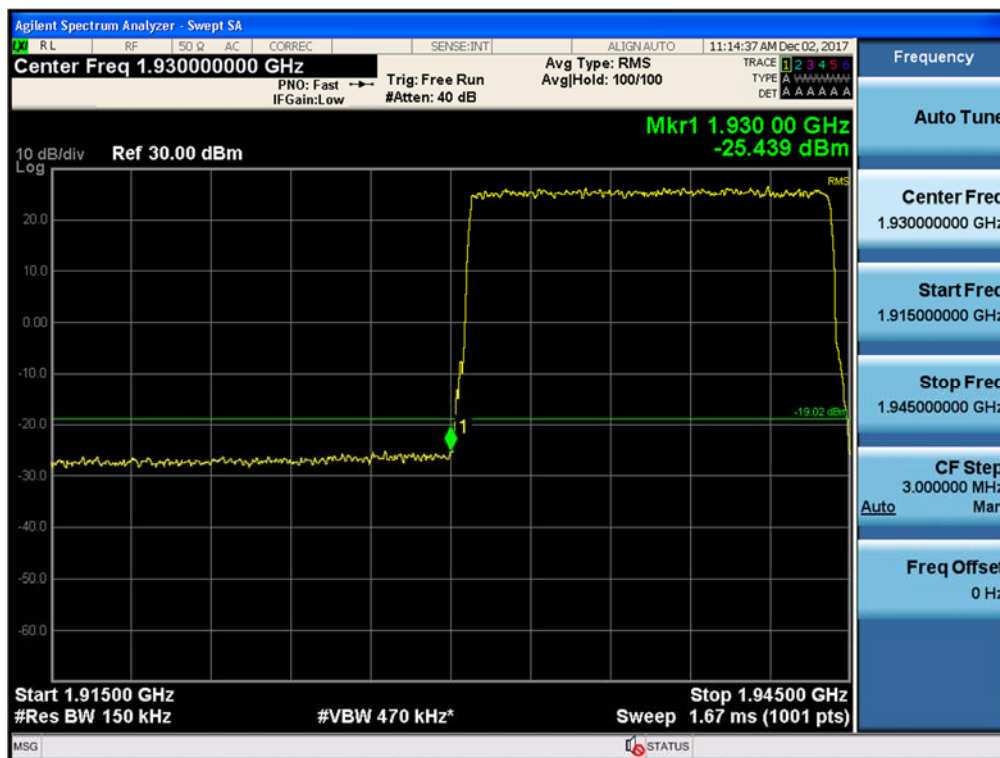


[Downlink High]

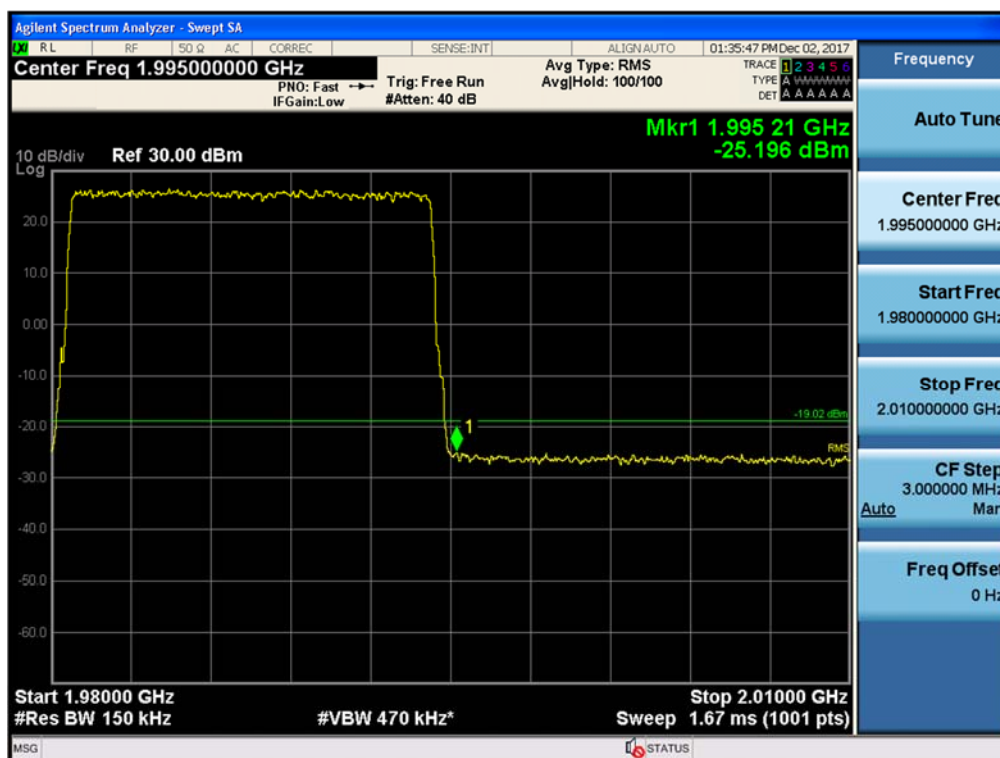


Test Data at Output Port 0_64QAM

[Downlink Low]

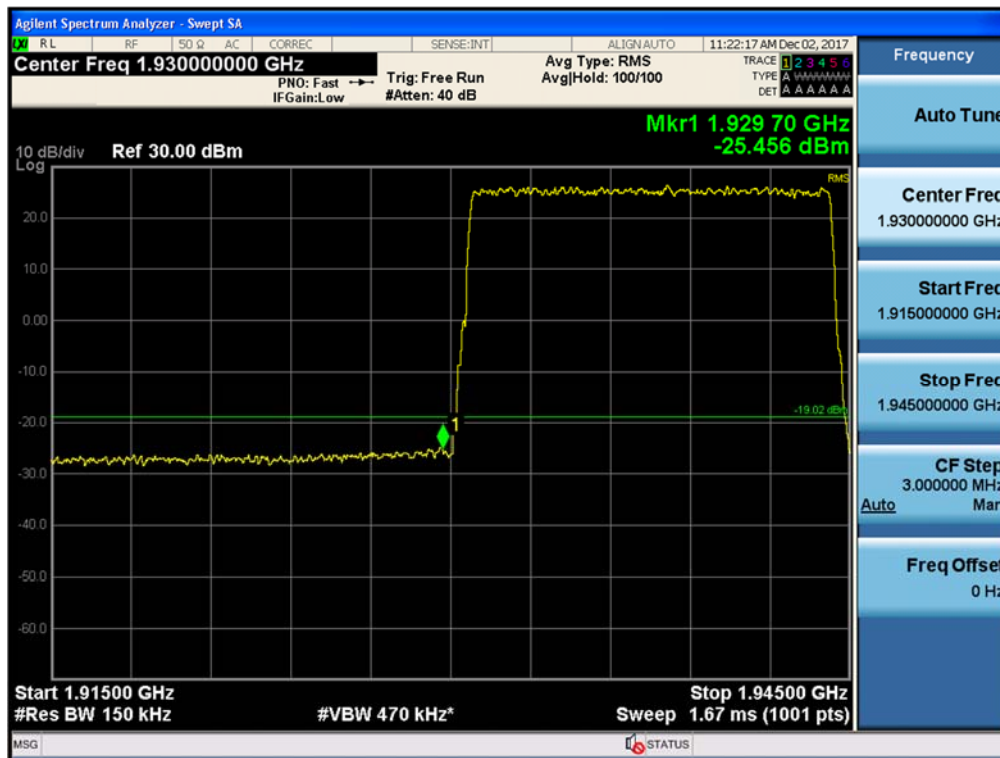


[Downlink High]

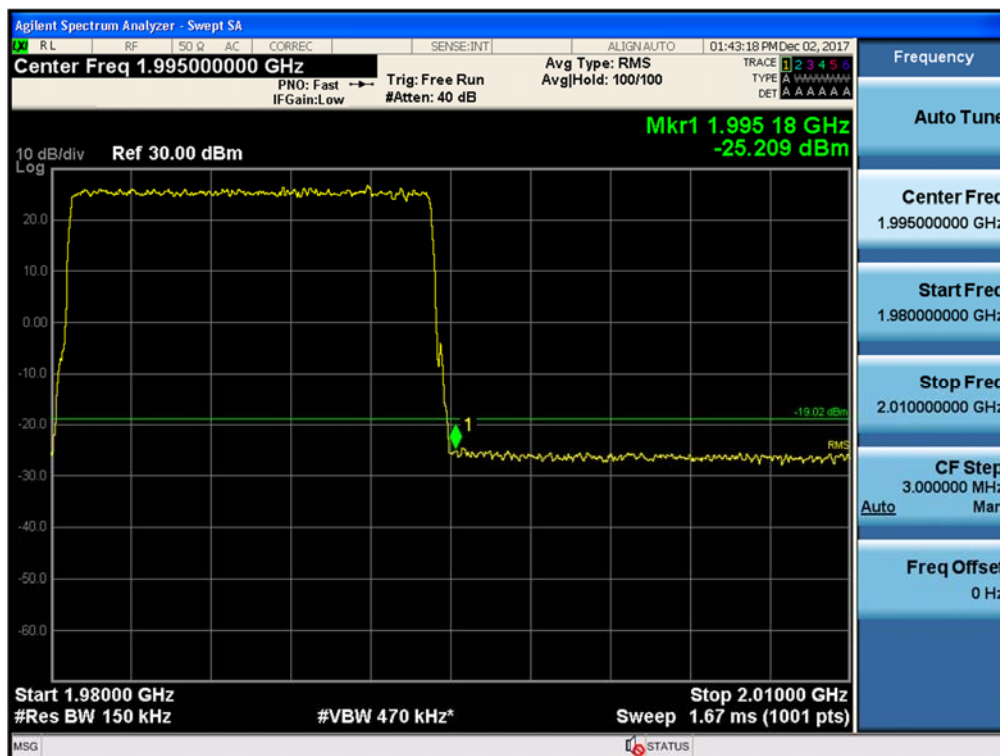


Test Data at Output Port 0_256QAM

[Downlink Low]

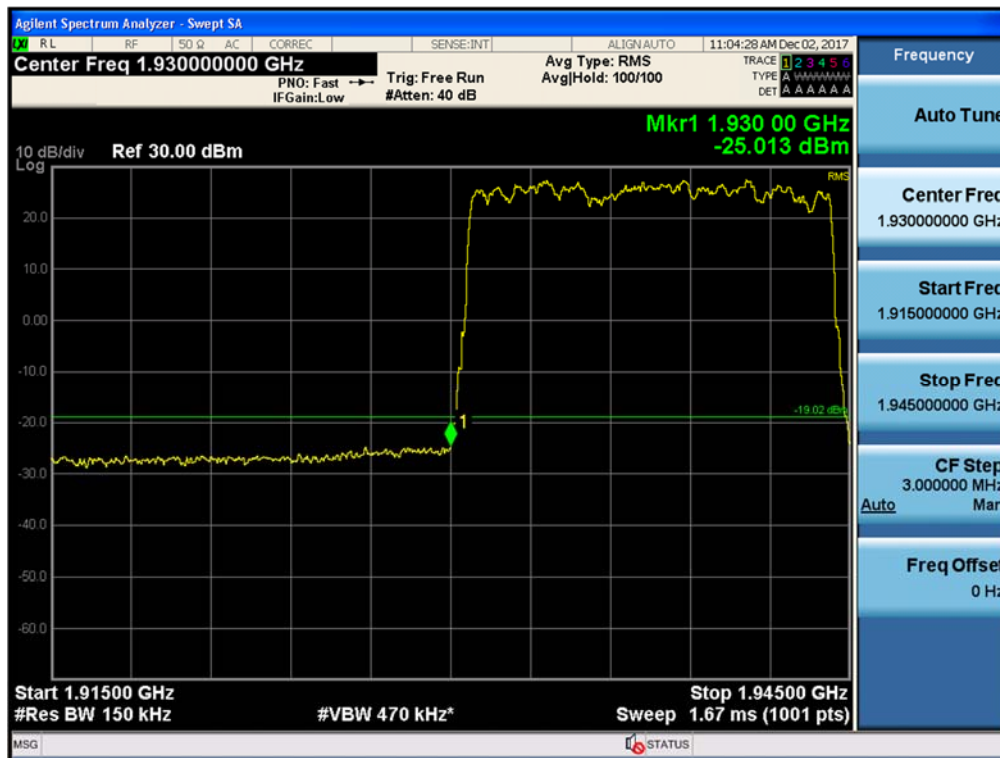


[Downlink High]

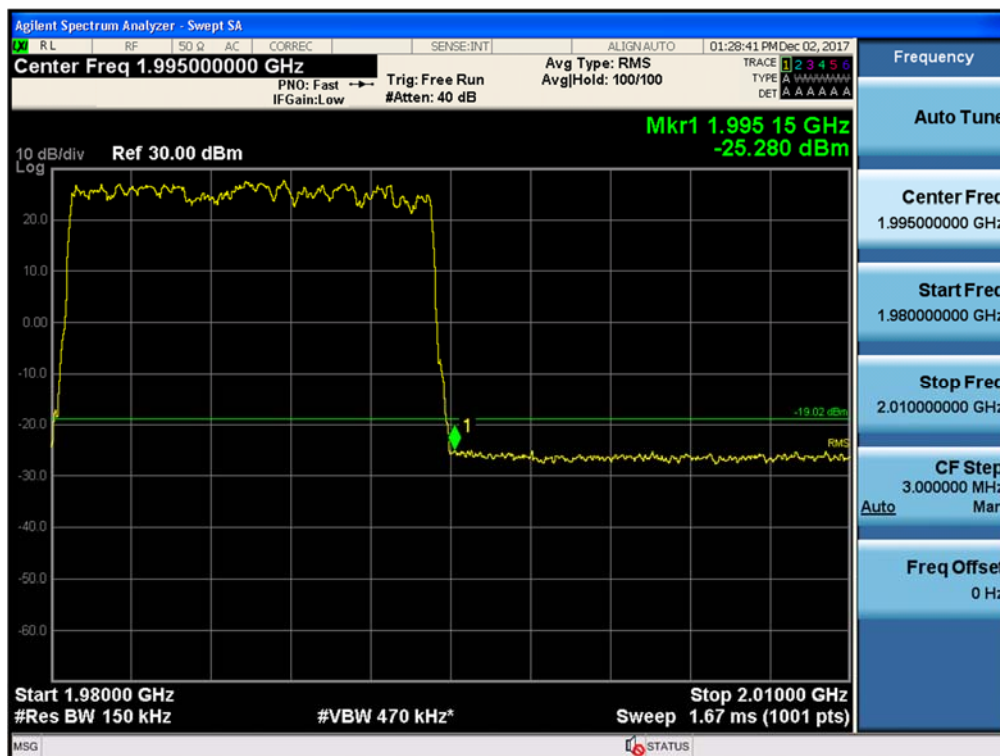


Test Data at Output Port 1_QPSK

[Downlink Low]

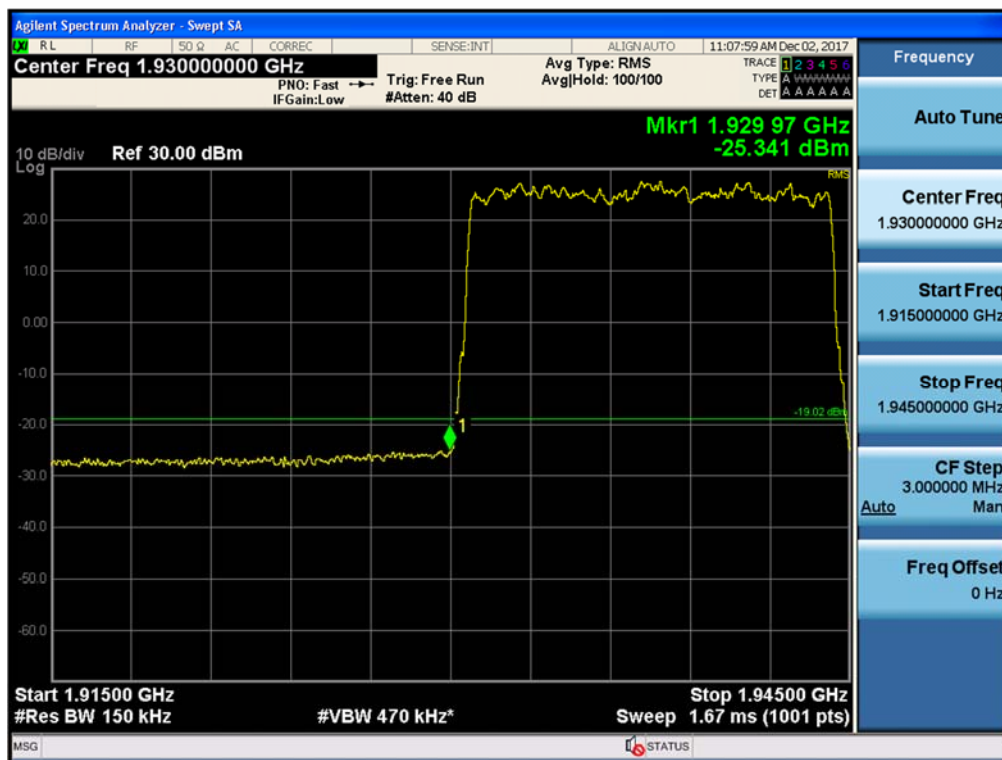


[Downlink High]

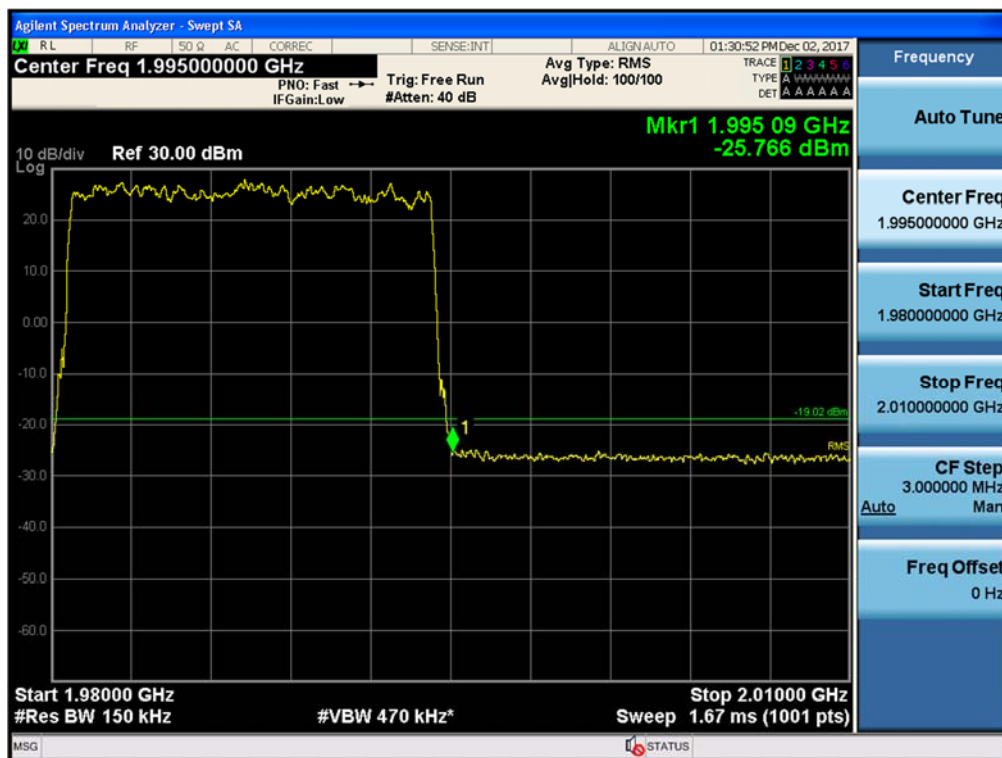


Test Data at Output Port 1_16QAM

[Downlink Low]

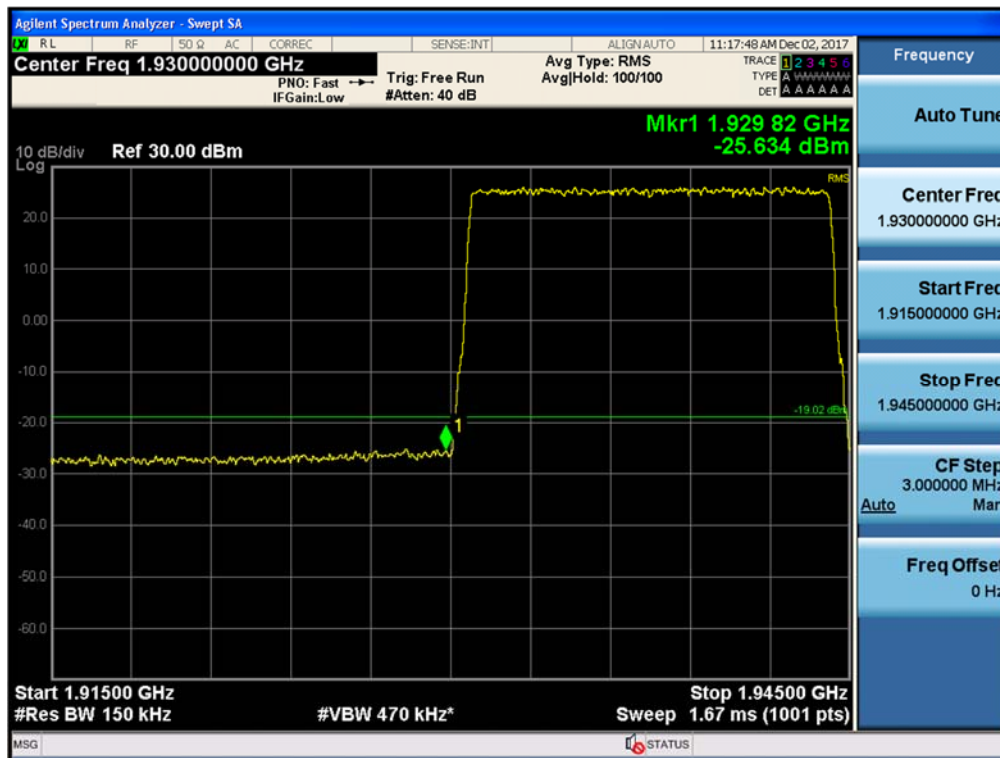


[Downlink High]

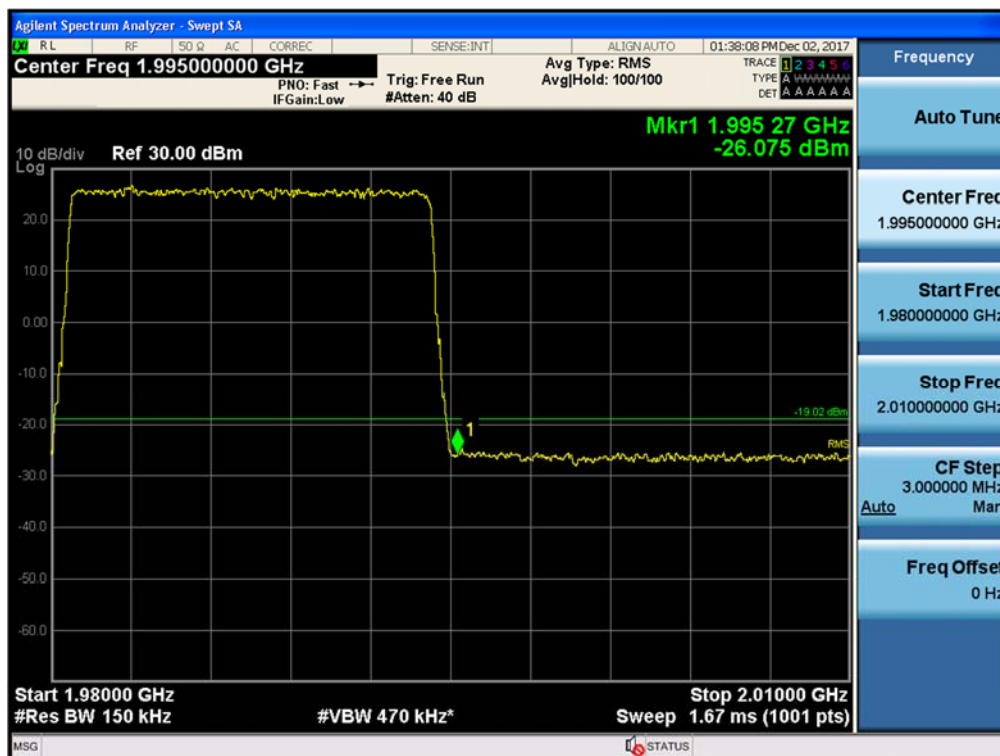


Test Data at Output Port 1_64QAM

[Downlink Low]

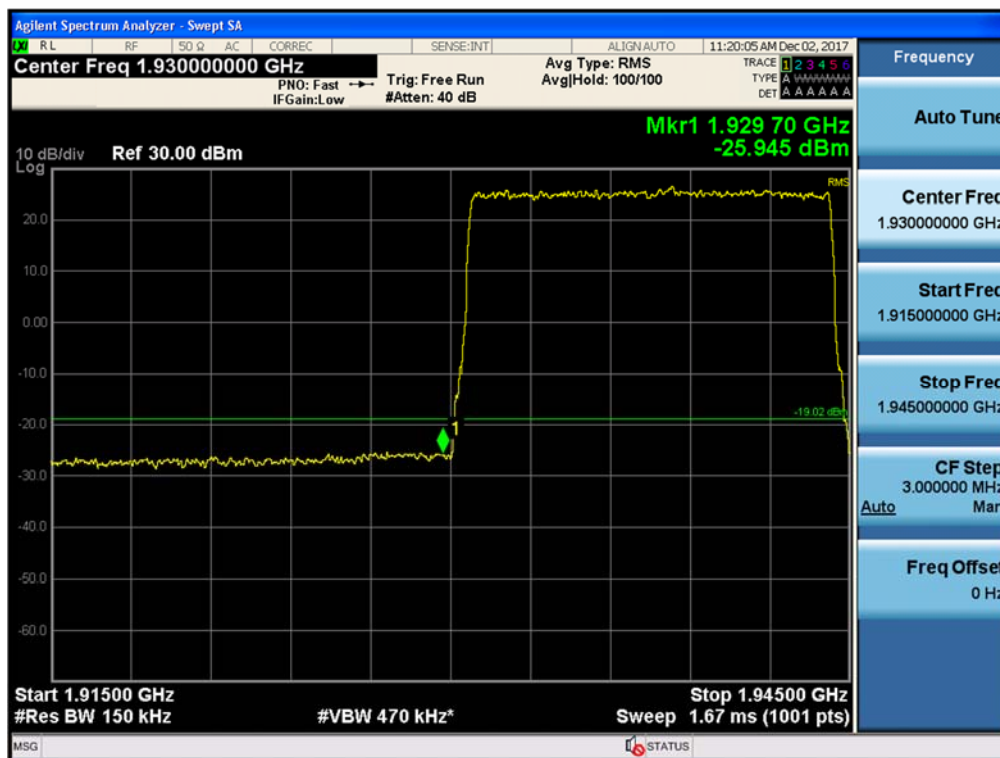


[Downlink High]

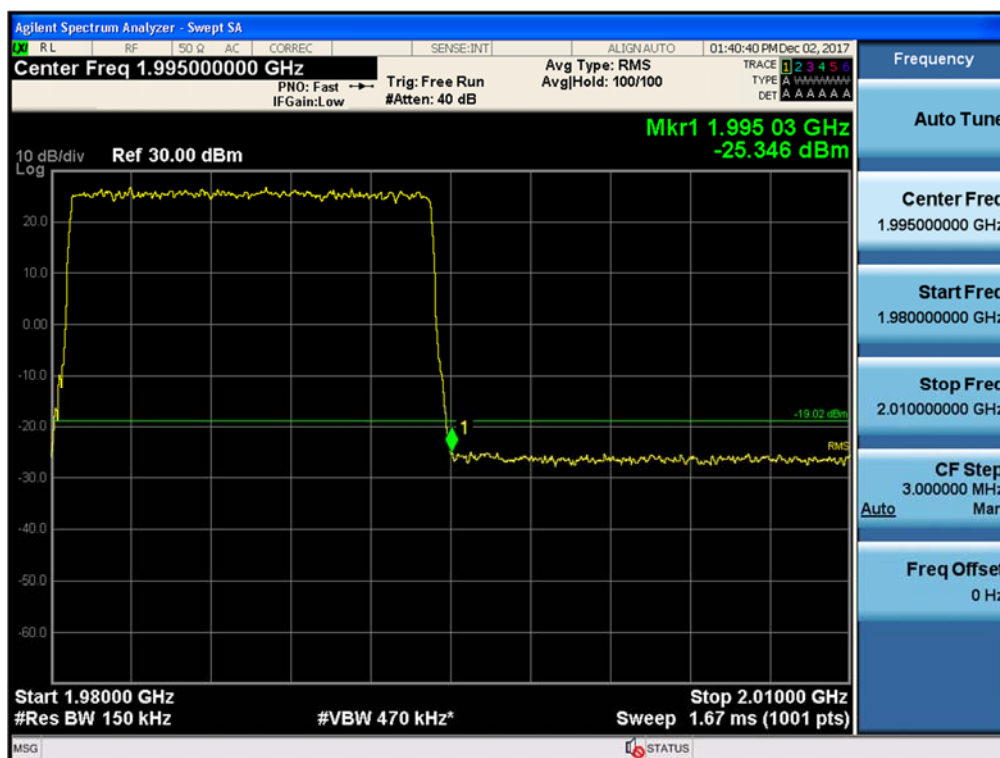


Test Data at Output Port 1_256QAM

[Downlink Low]

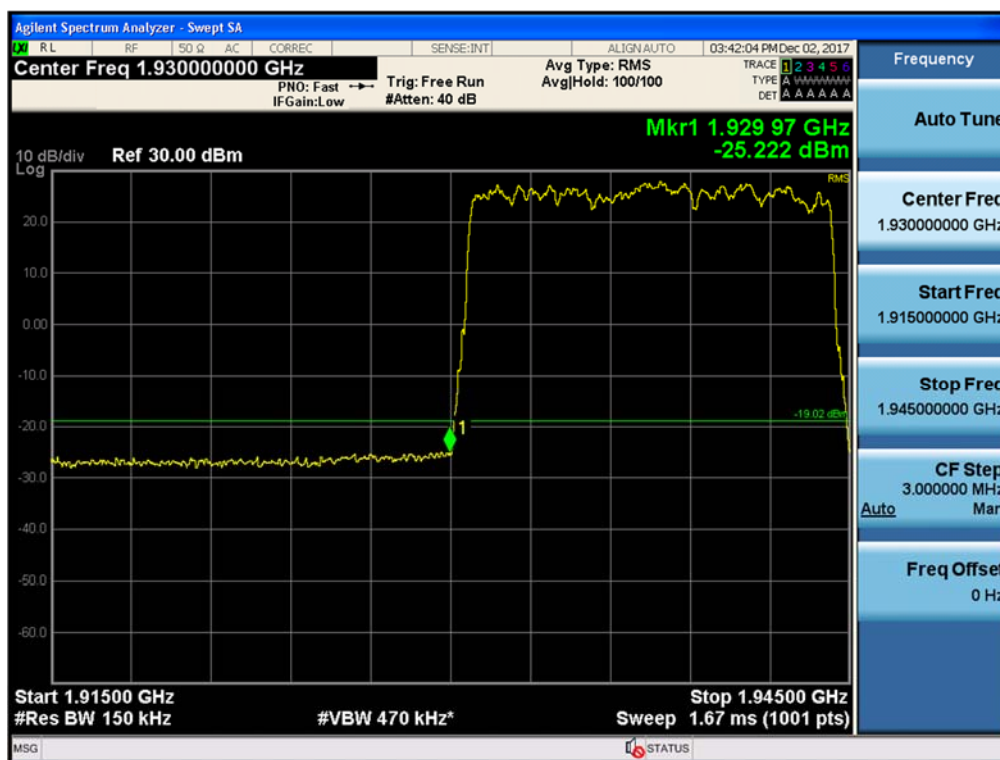


[Downlink High]

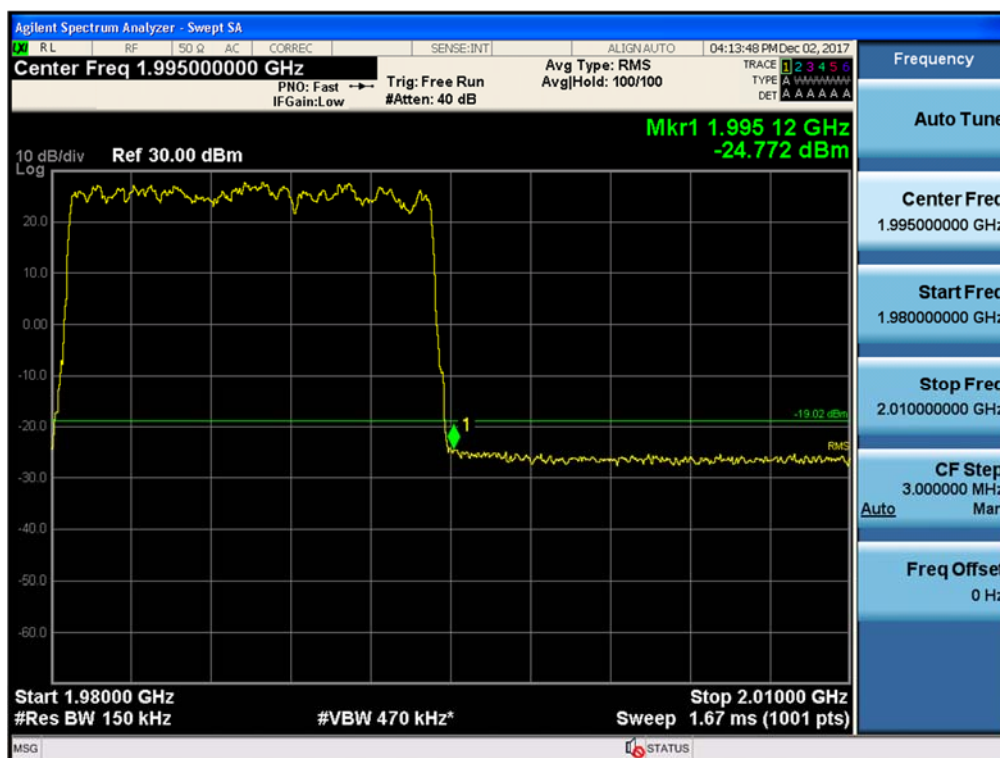


Test Data at Output Port 2_QPSK

[Downlink Low]

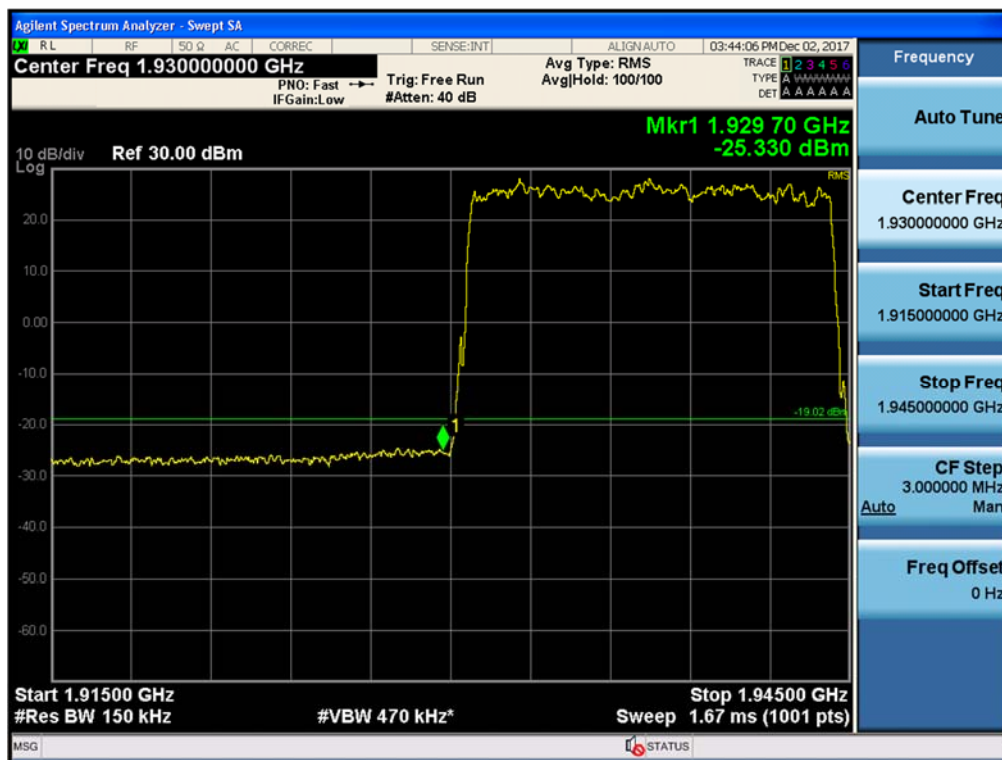


[Downlink High]



Test Data at Output Port 2_16QAM

[Downlink Low]

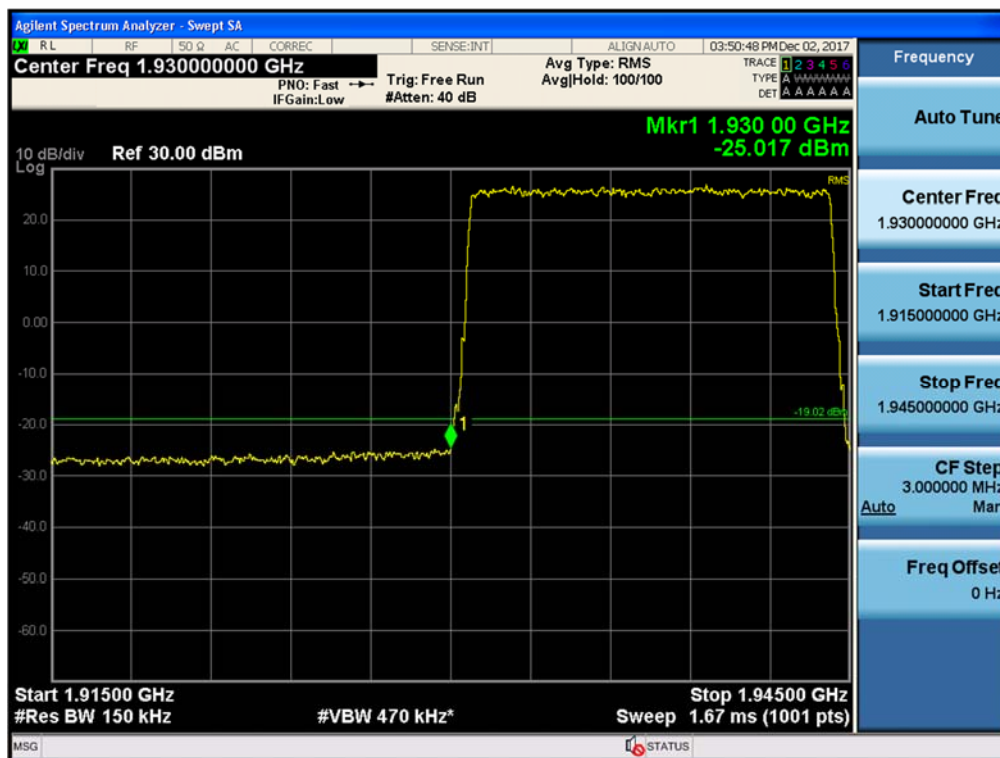


[Downlink High]

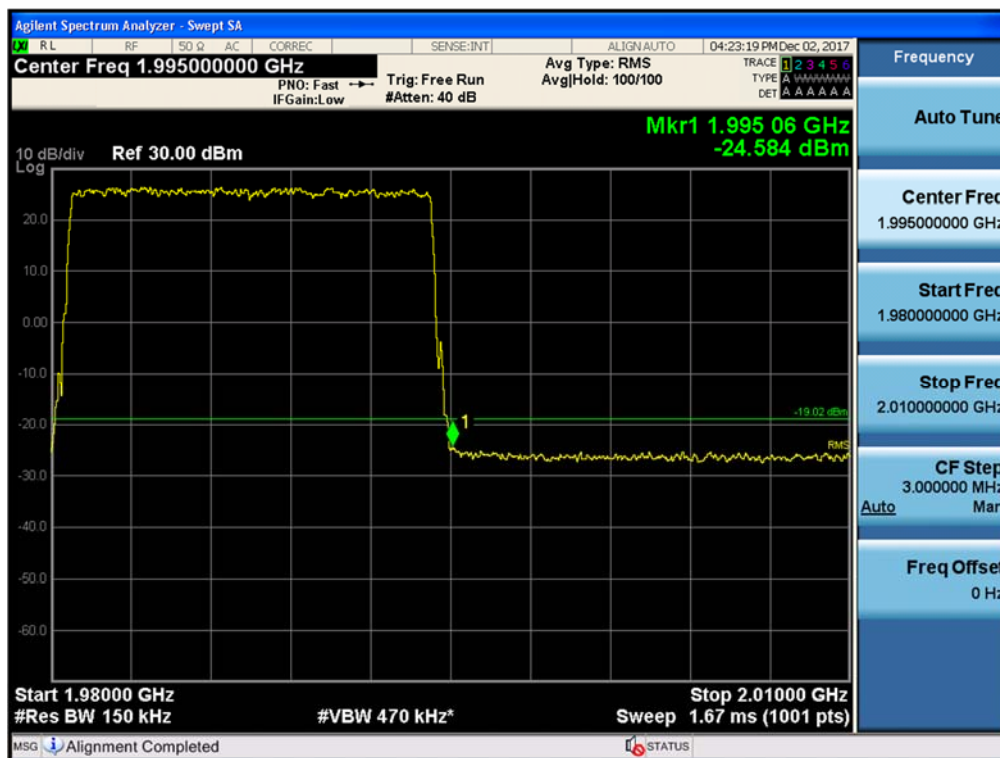


Test Data at Output Port 2_64QAM

[Downlink Low]

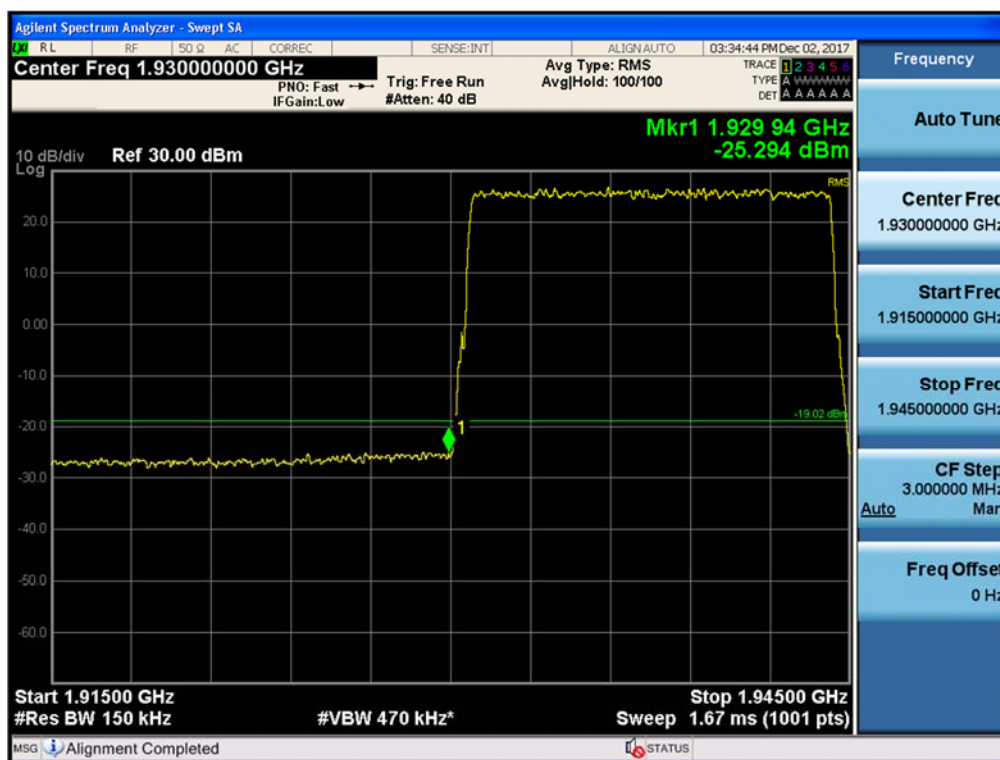


[Downlink High]

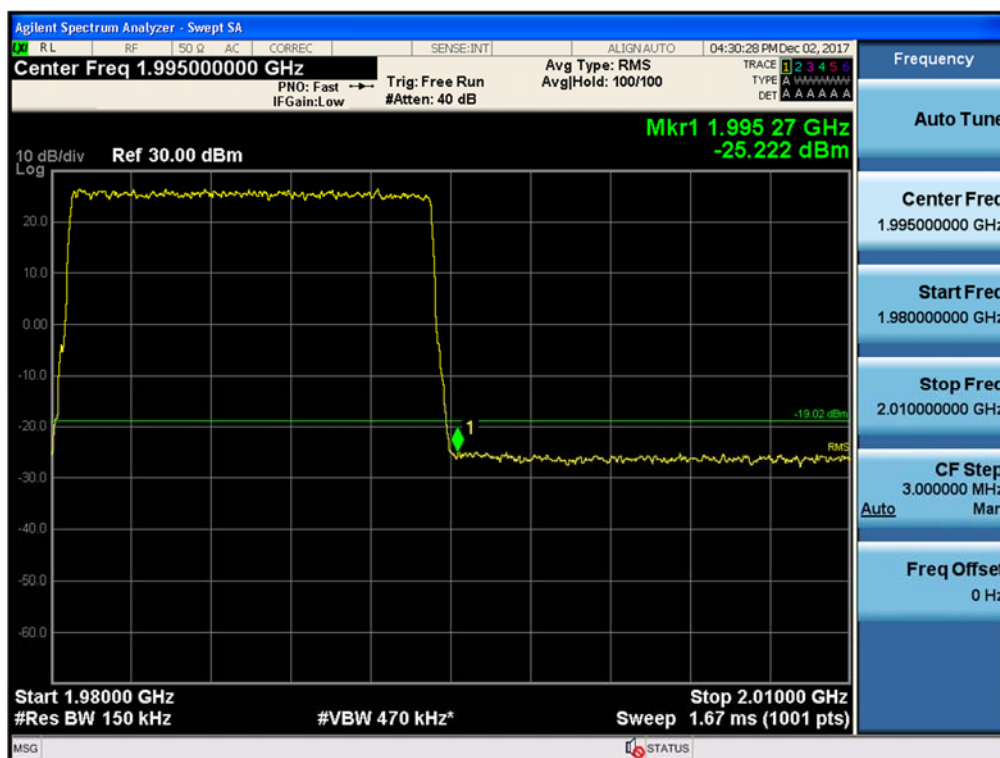


Test Data at Output Port 2_256QAM

[Downlink Low]

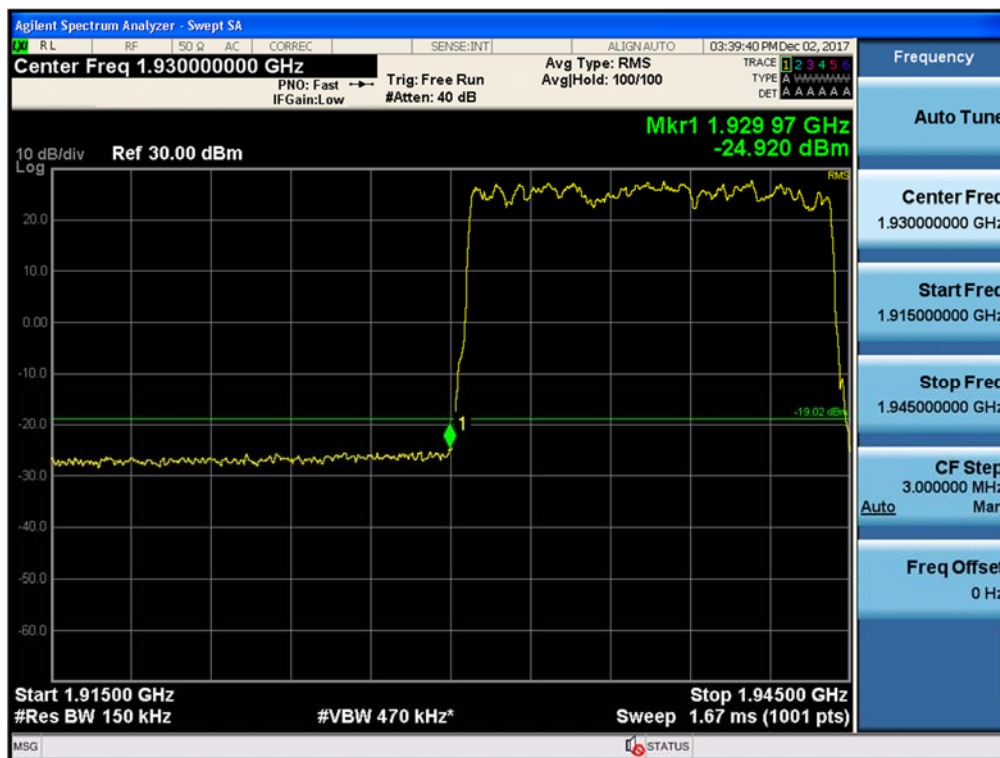


[Downlink High]

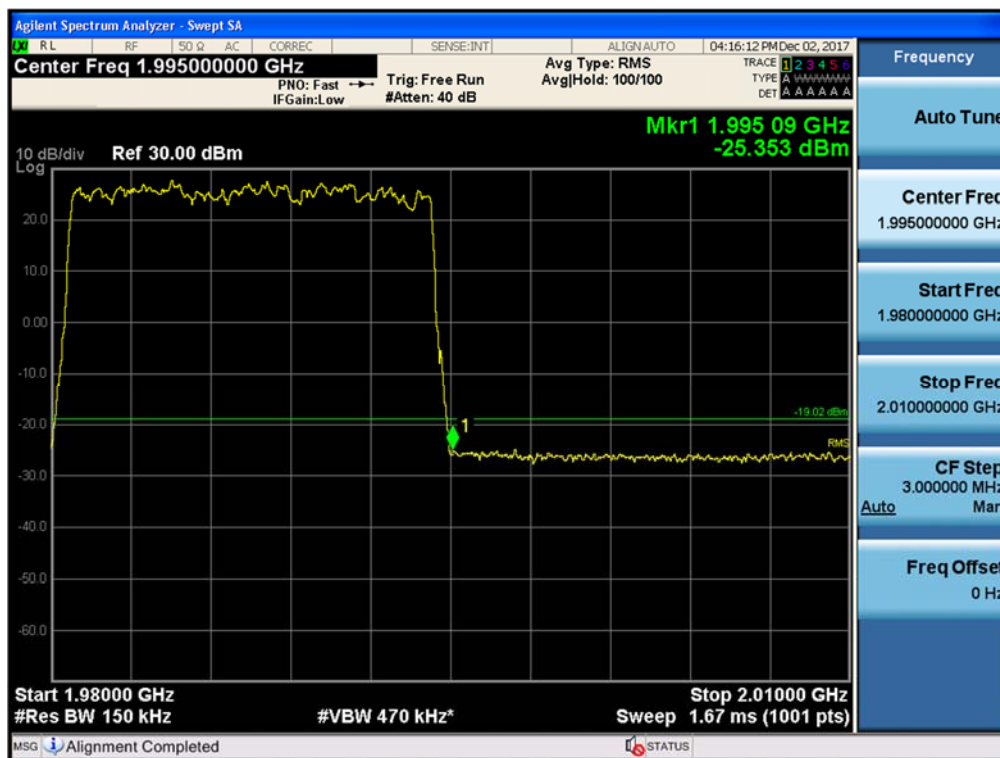


Test Data at Output Port 3_QPSK

[Downlink Low]

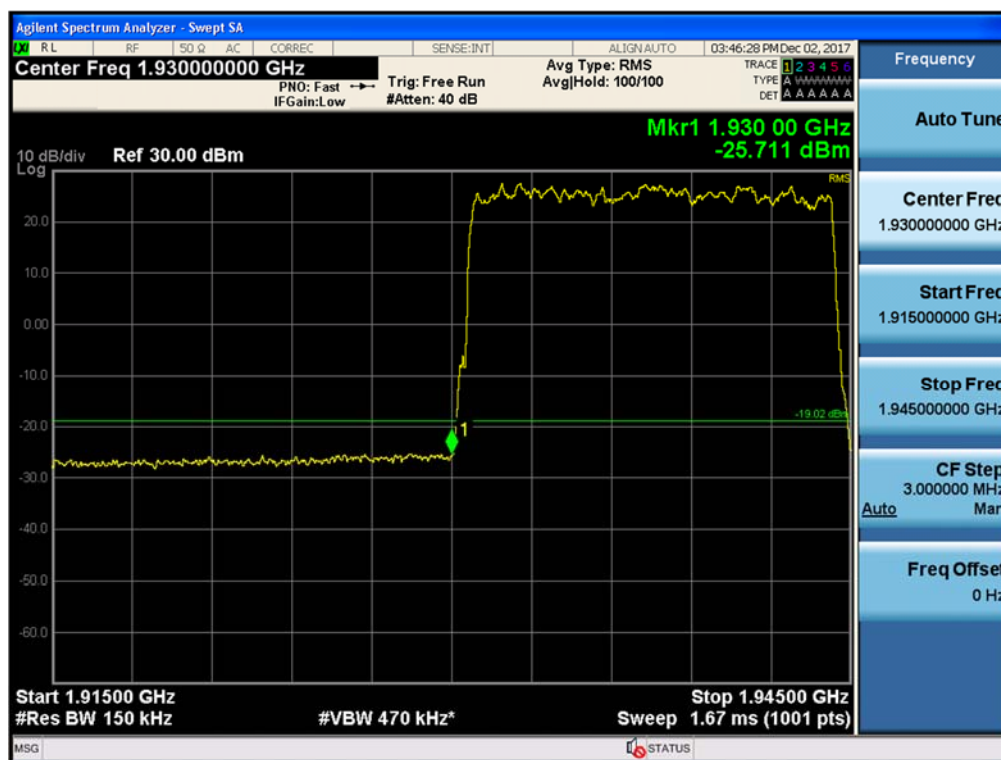


[Downlink High]

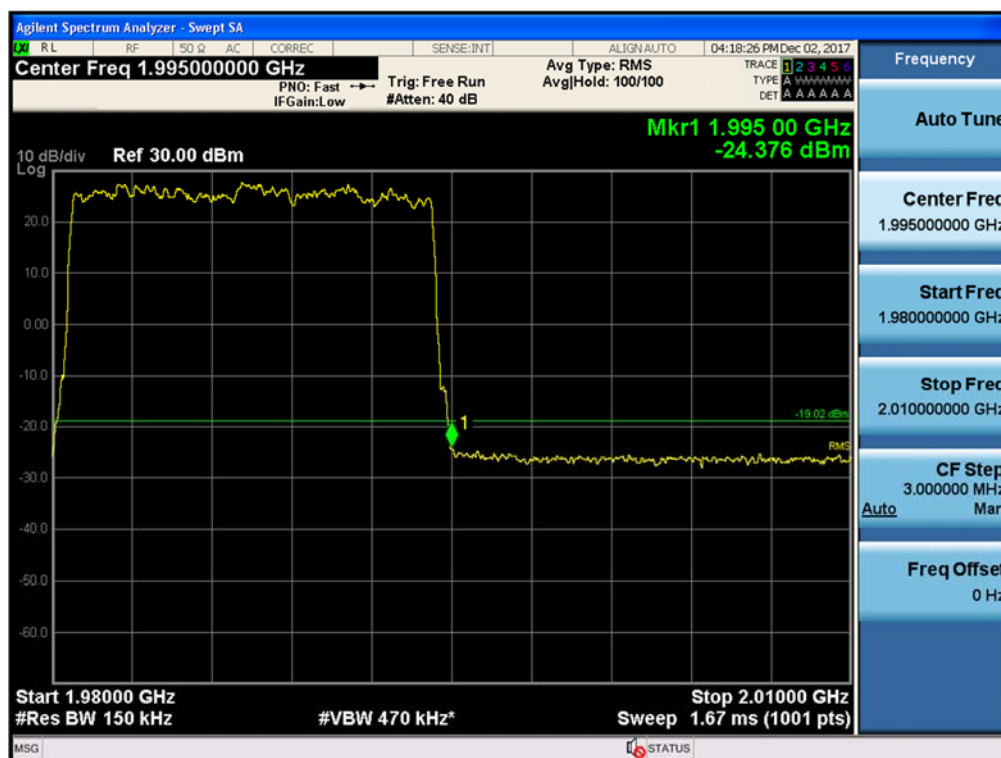


Test Data at Output Port 3_16QAM

[Downlink Low]

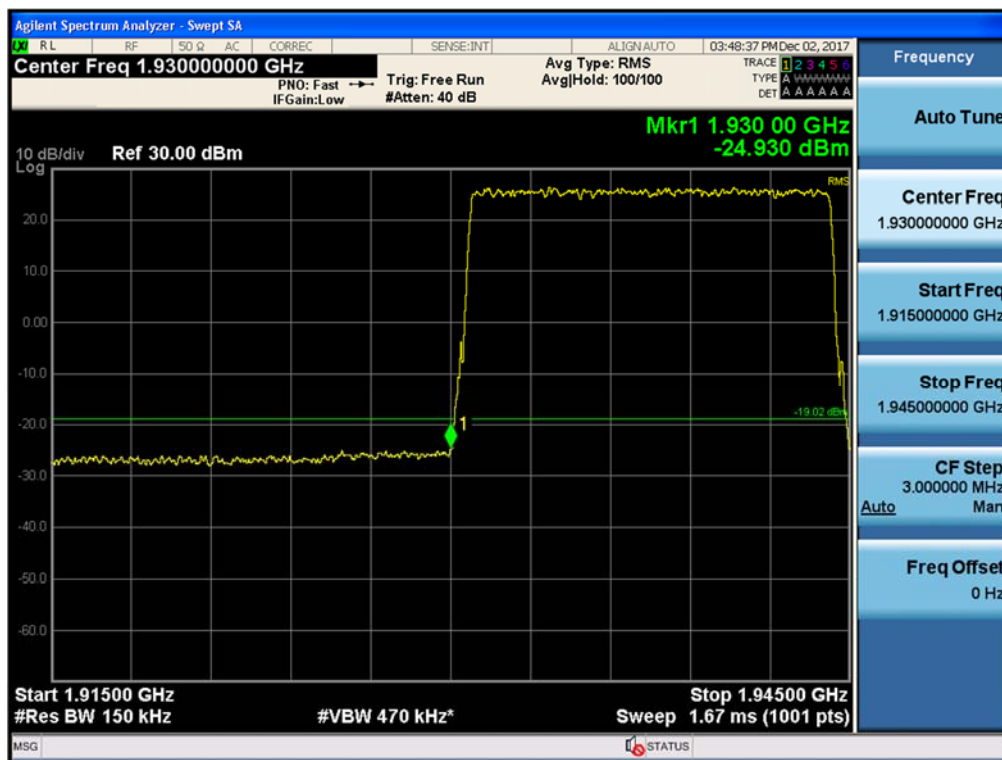


[Downlink High]

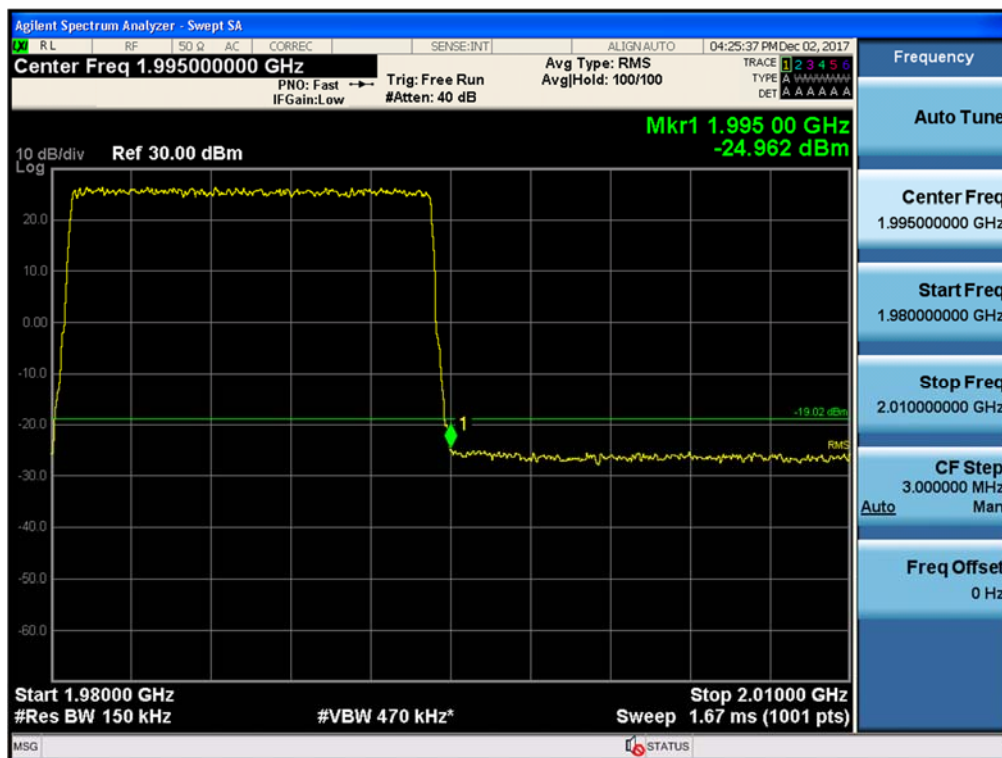


Test Data at Output Port 3_64QAM

[Downlink Low]

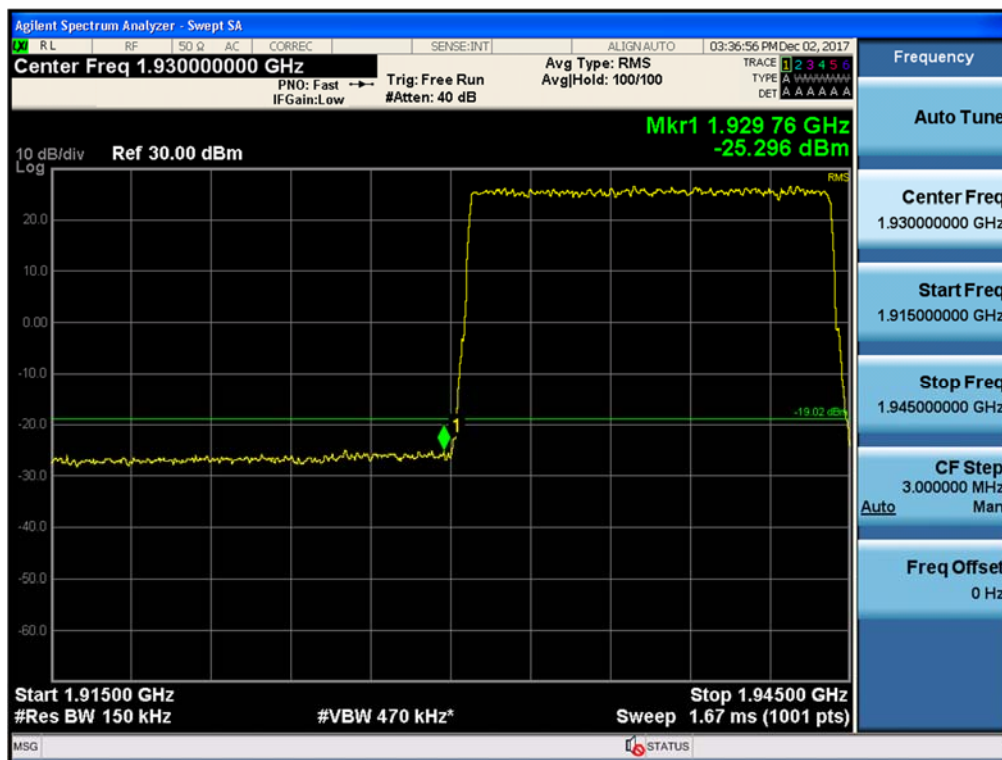


[Downlink High]



Test Data at Output Port 3_256QAM

[Downlink Low]



[Downlink High]

