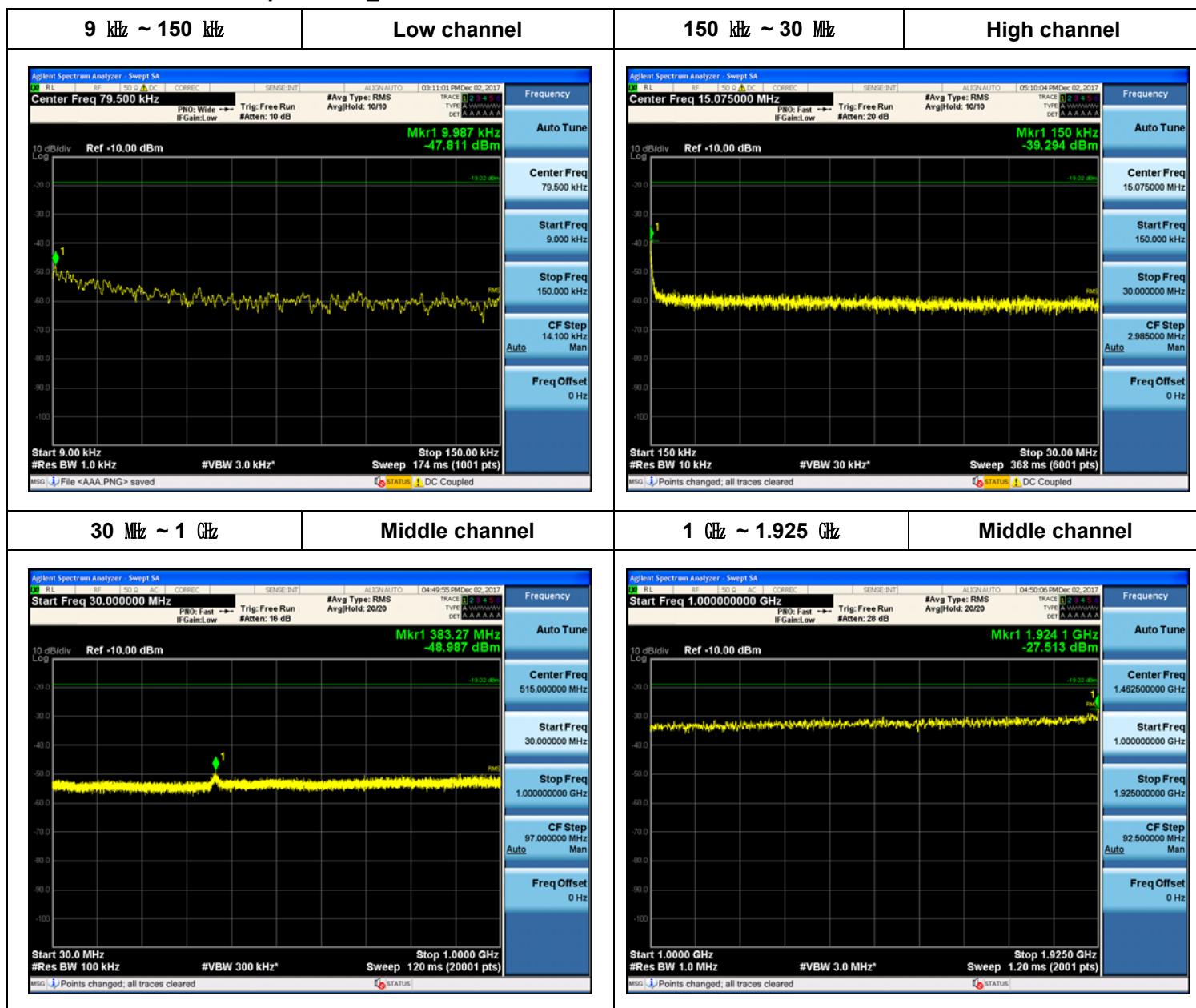
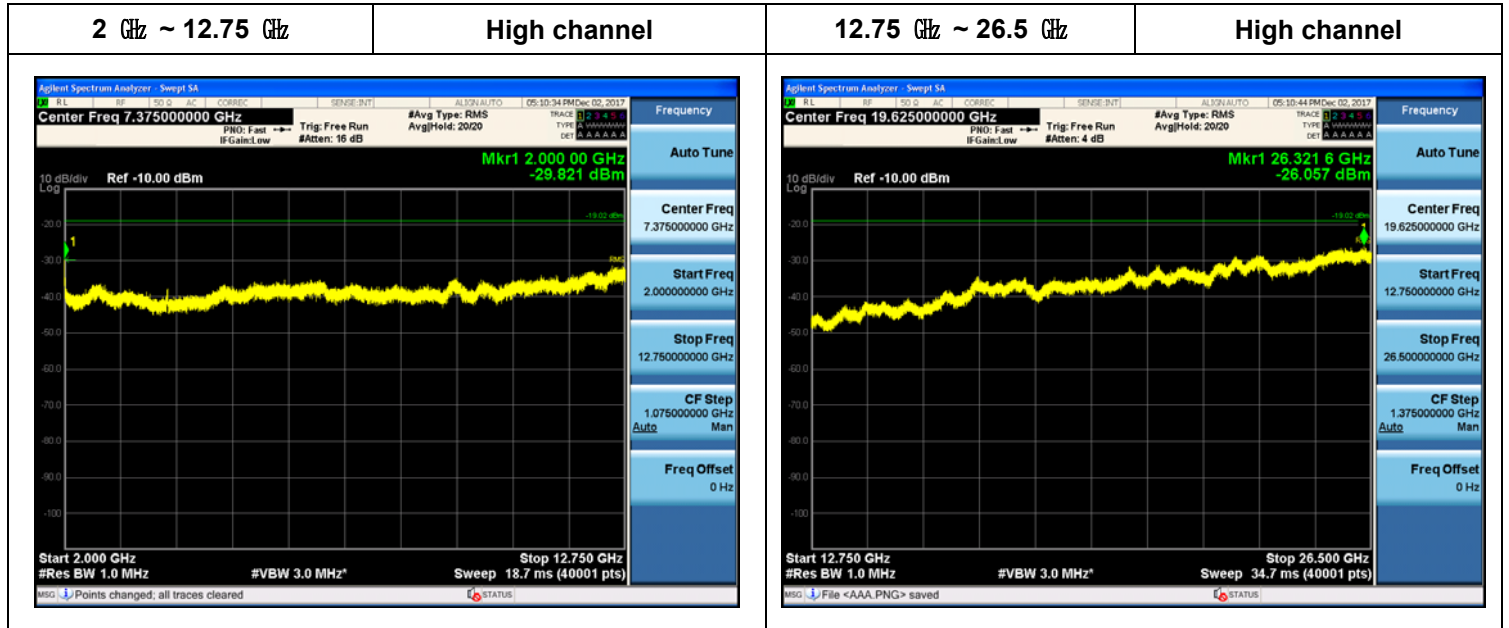
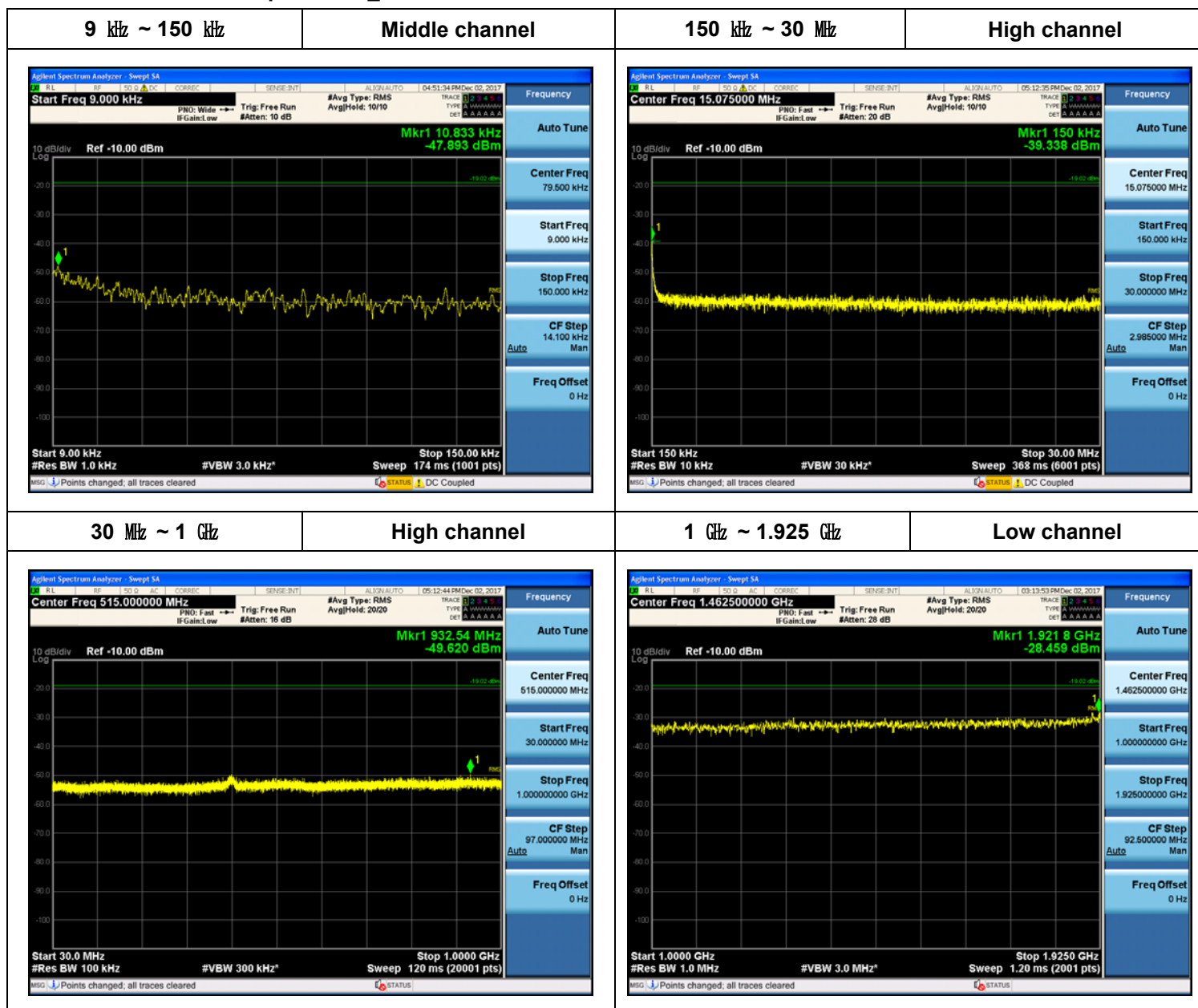


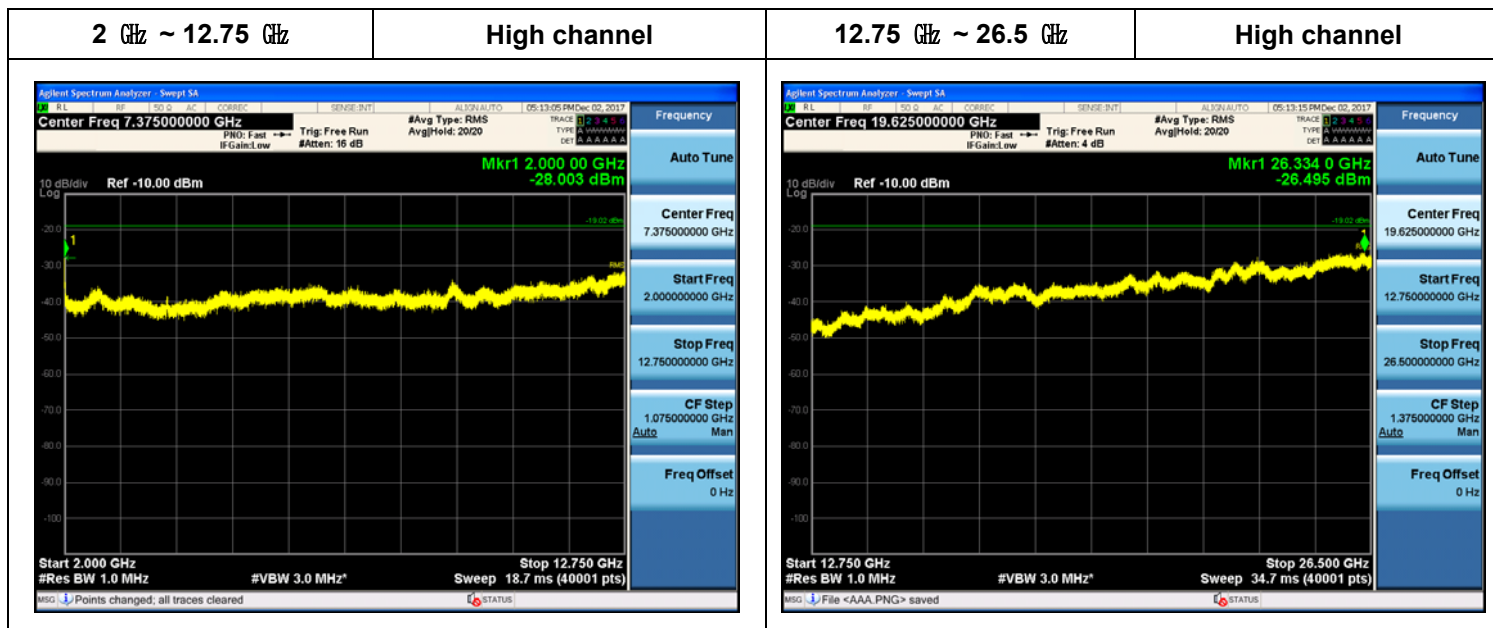
Plot Data for Output Port 3_QPSK



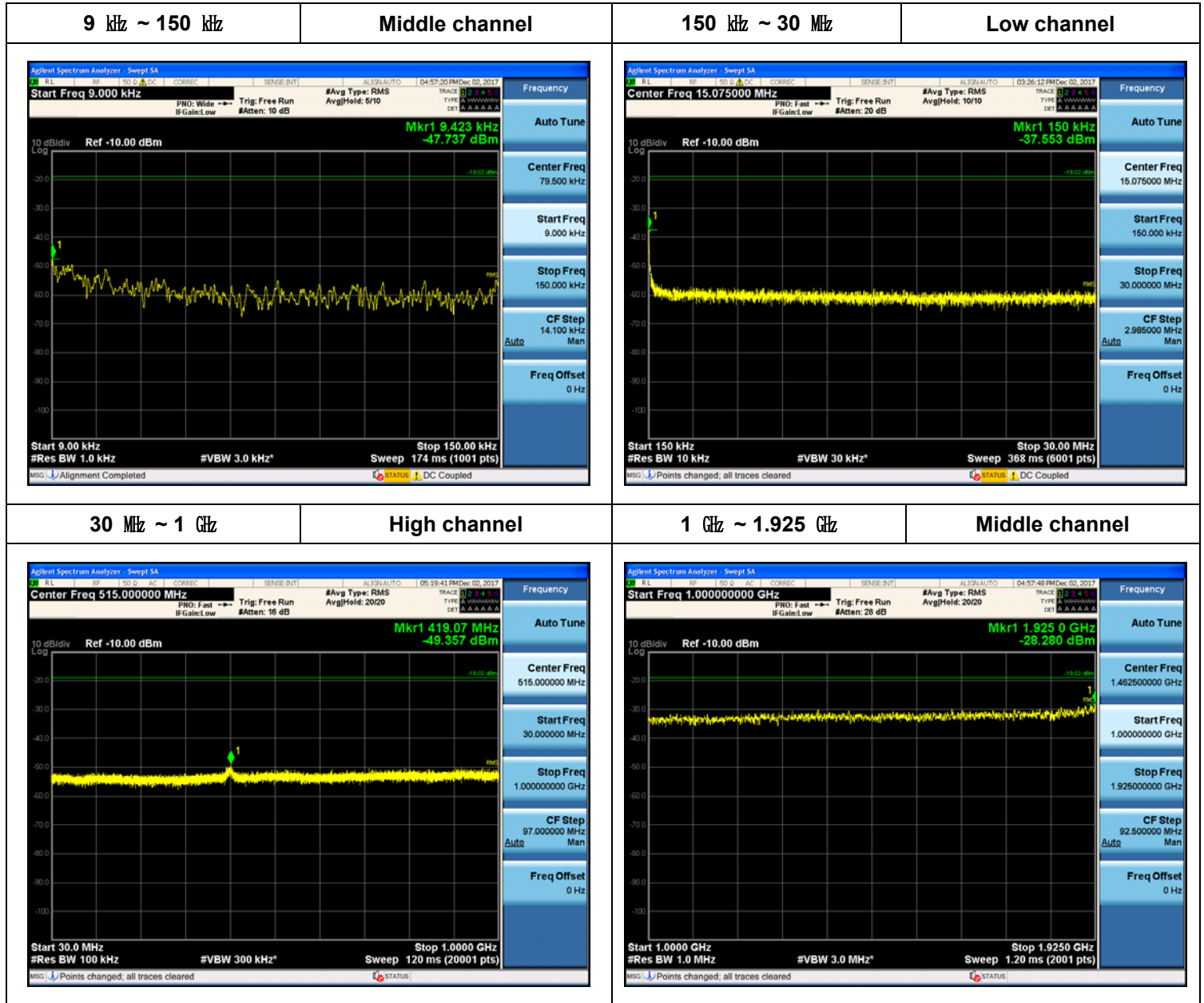


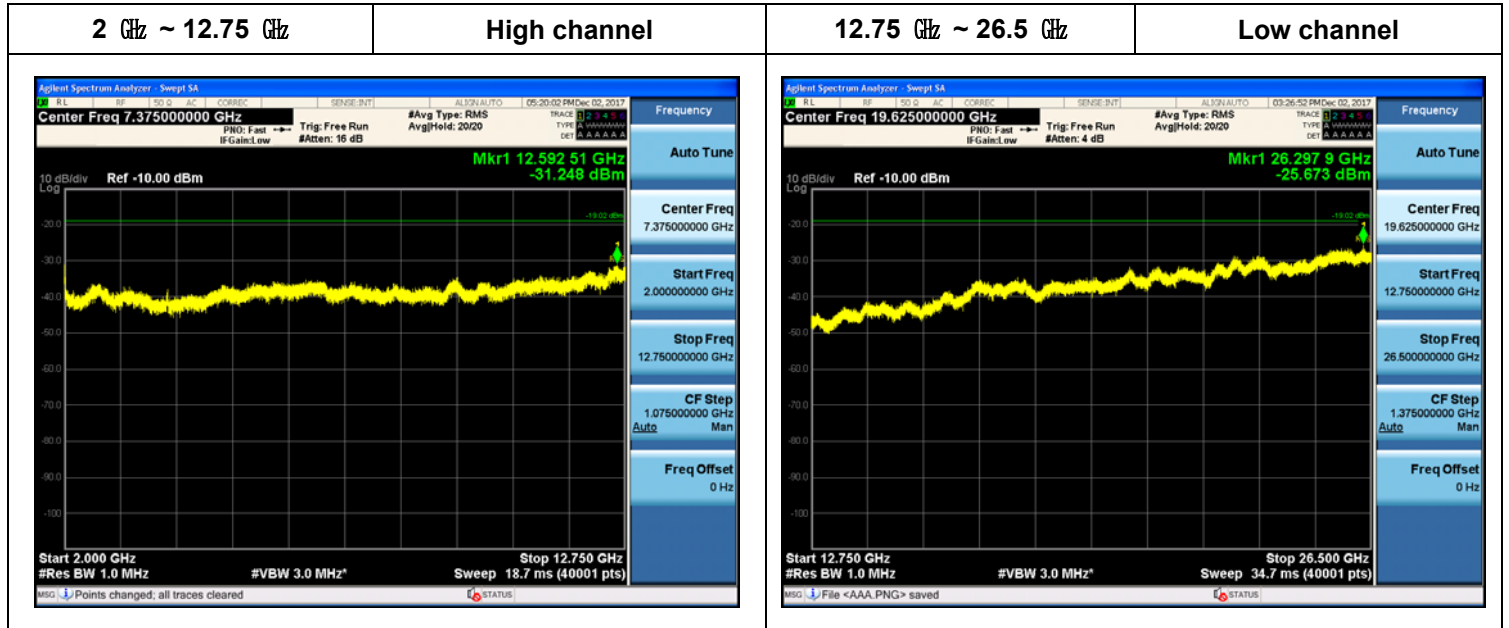
Plot Data for Output Port 3_16QAM



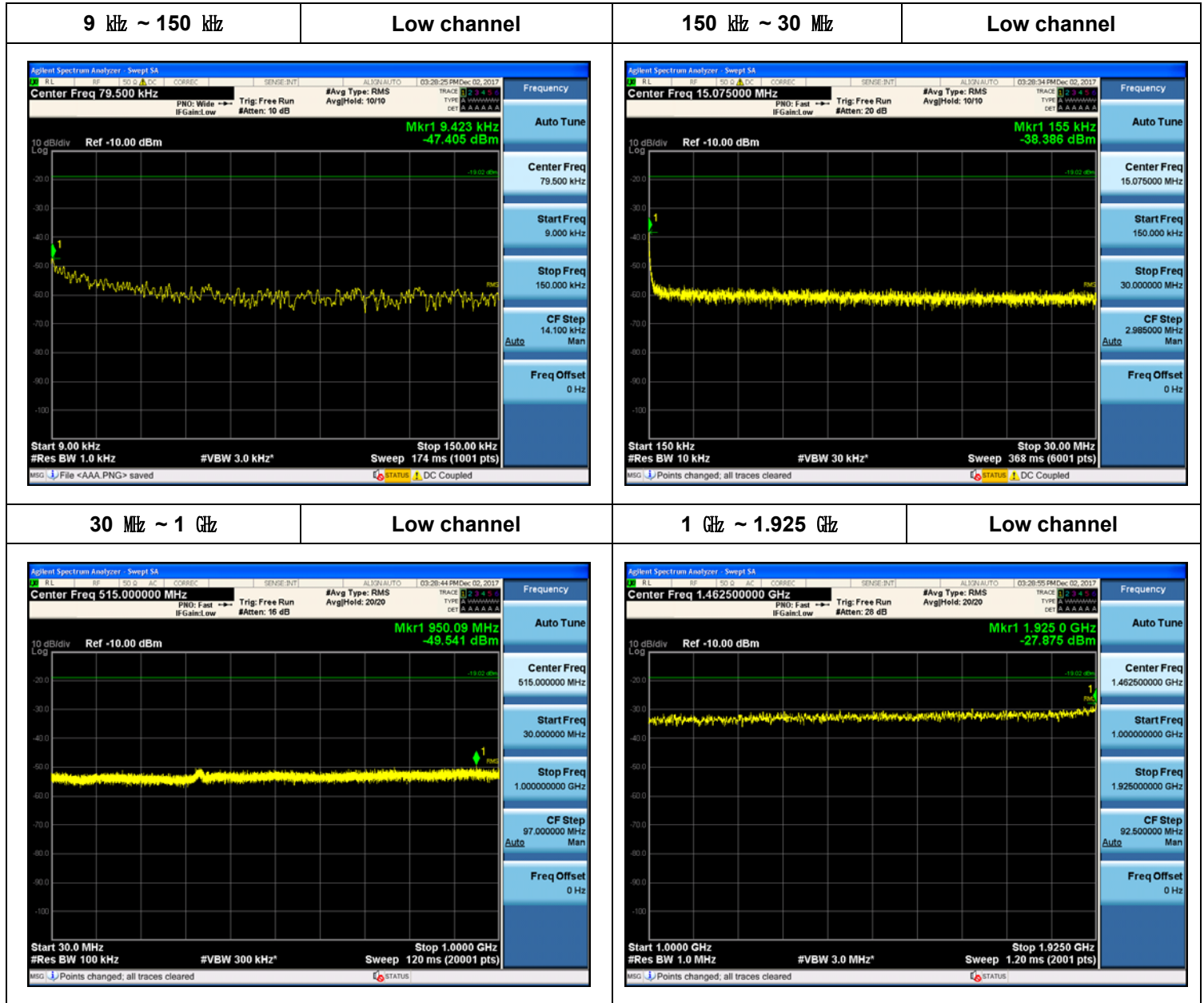


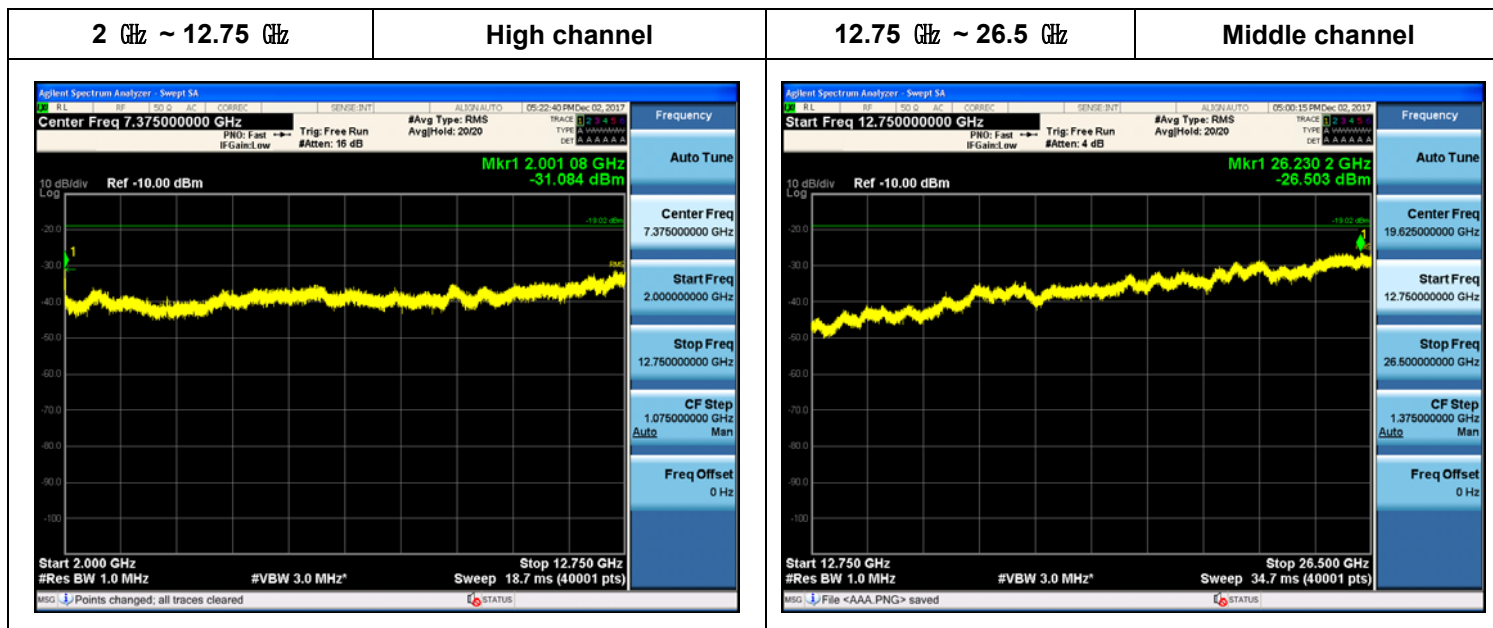
Plot Data for Output Port 3_64QAM





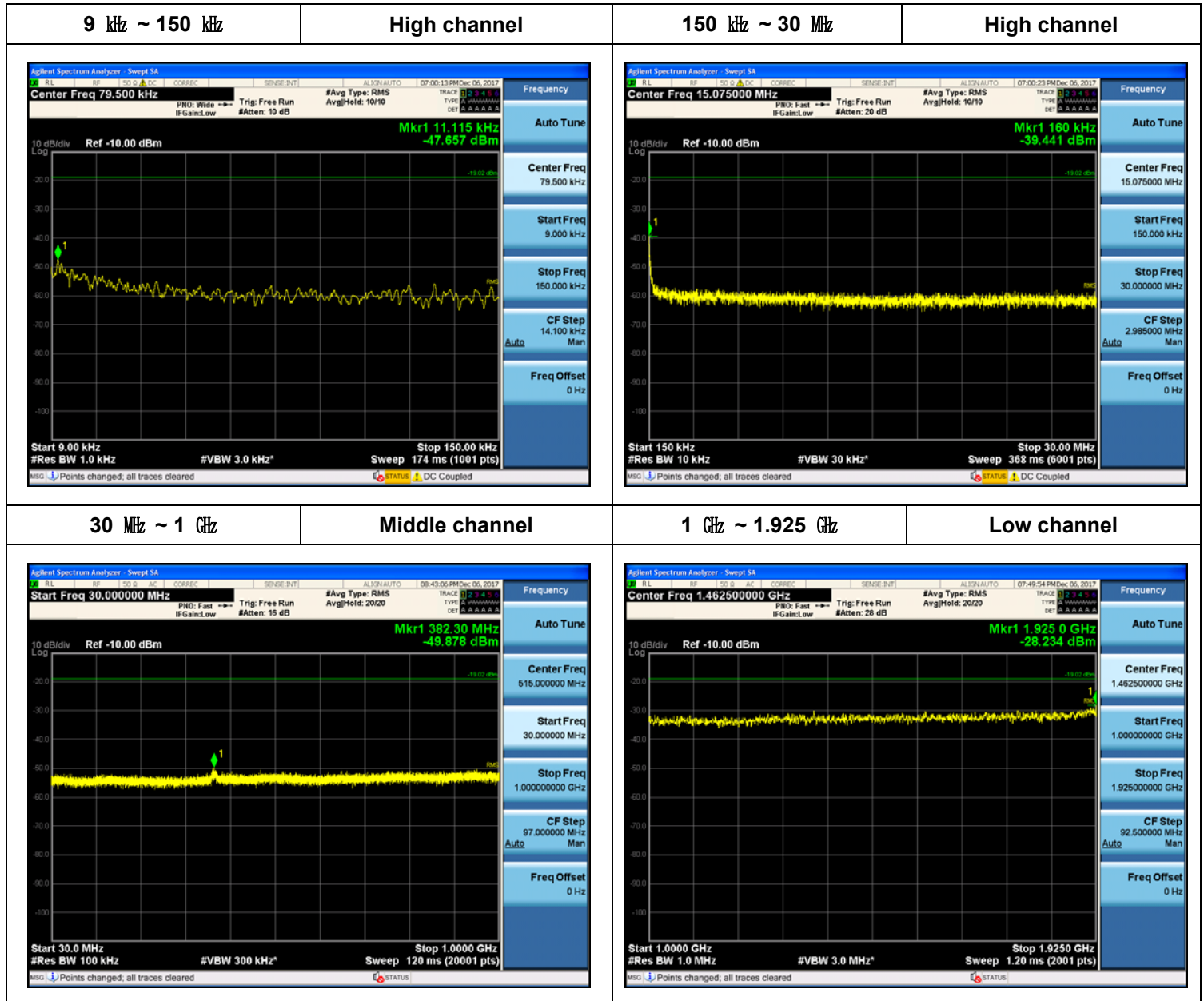
Plot Data for Output Port 3_256QAM

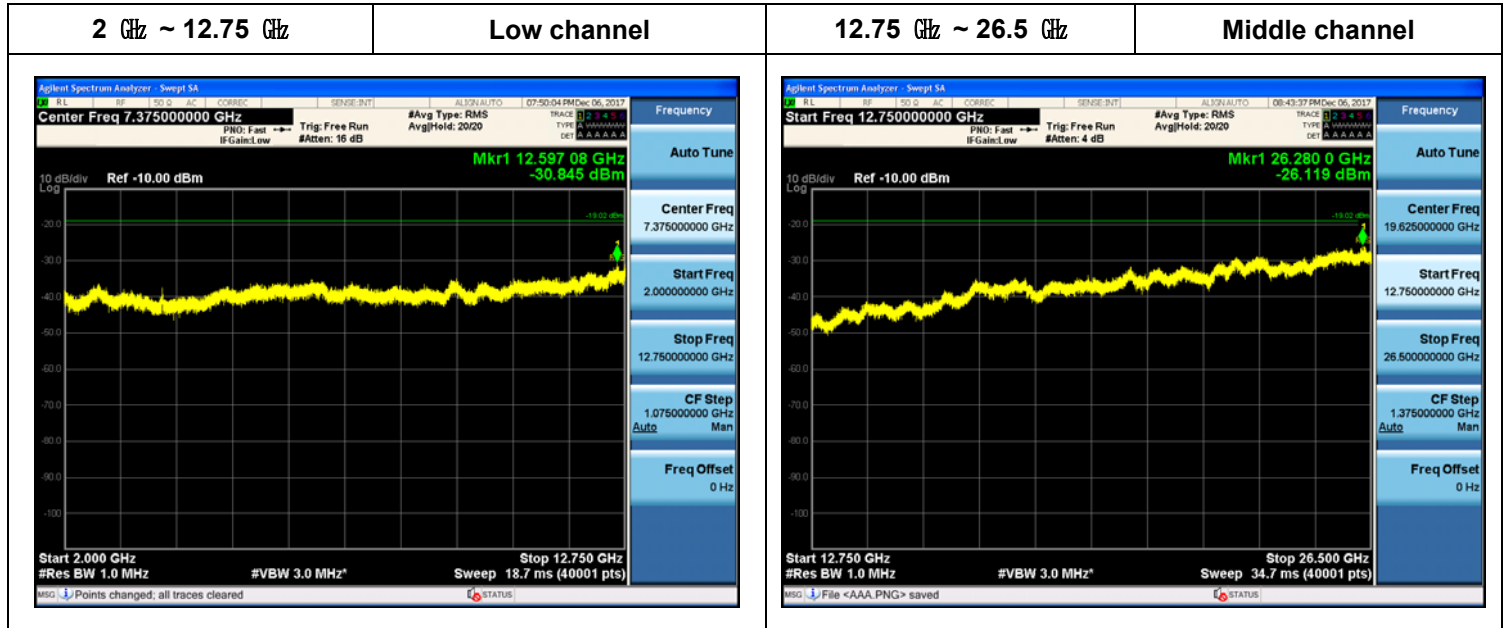




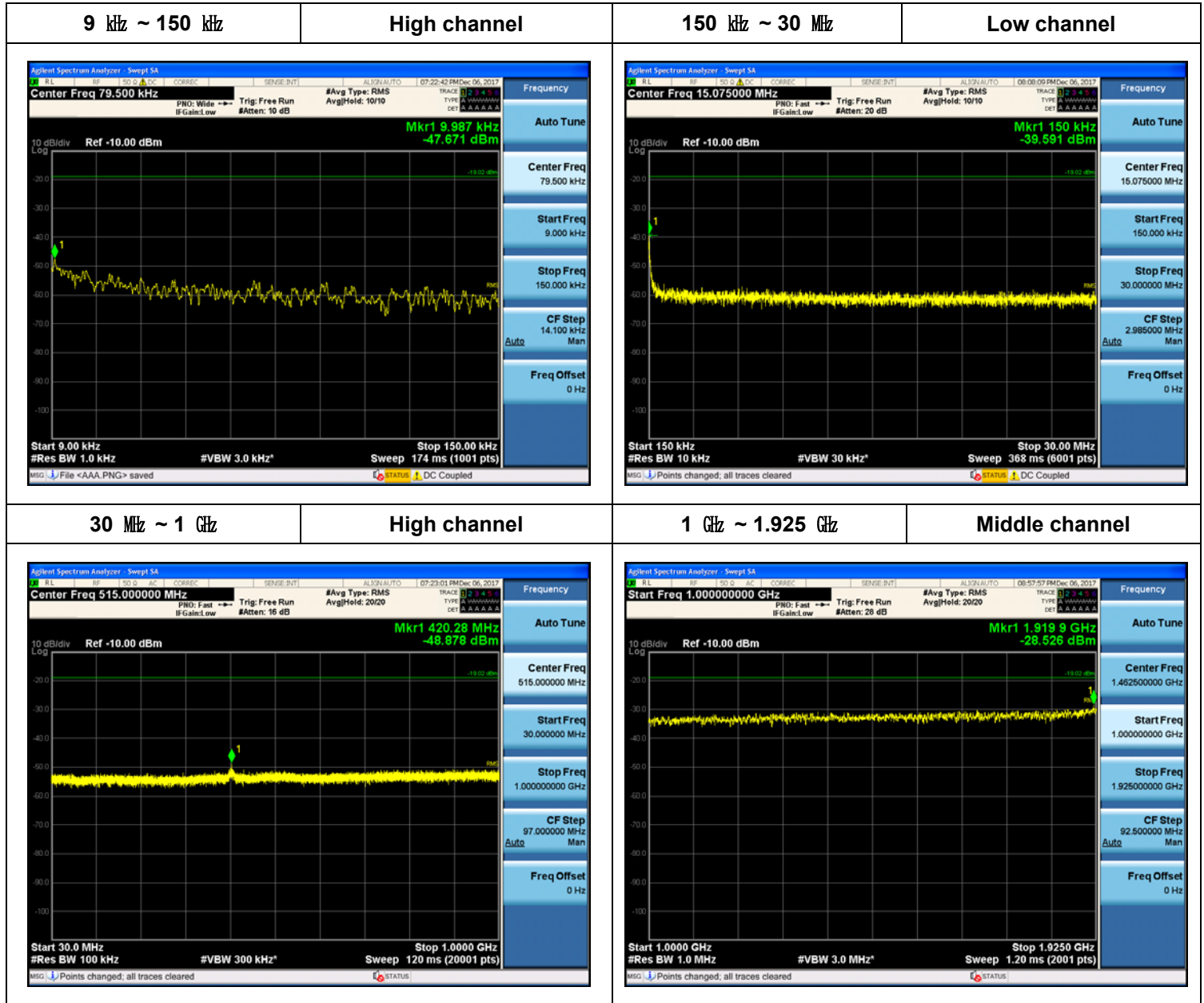
10 MHz + 5 MHz / 2 Carriers (20 W + 10 W)

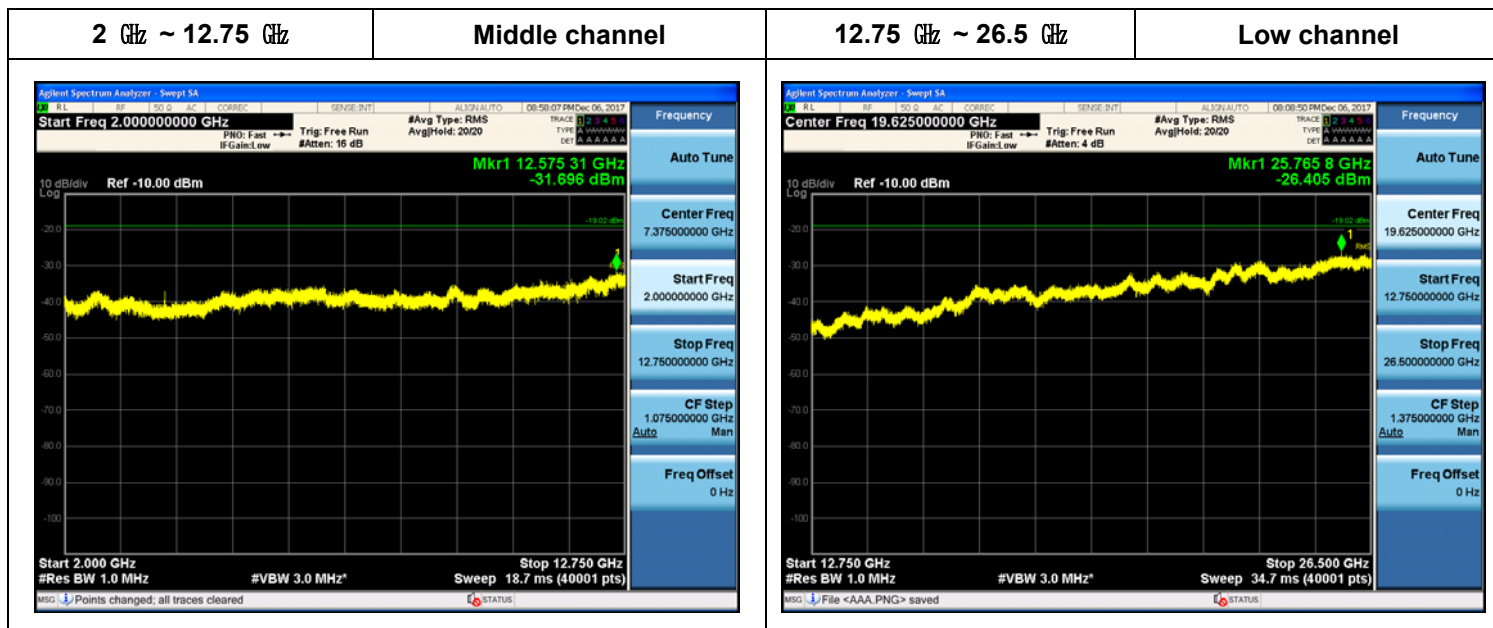
Plot Data for Output Port 0_QPSK



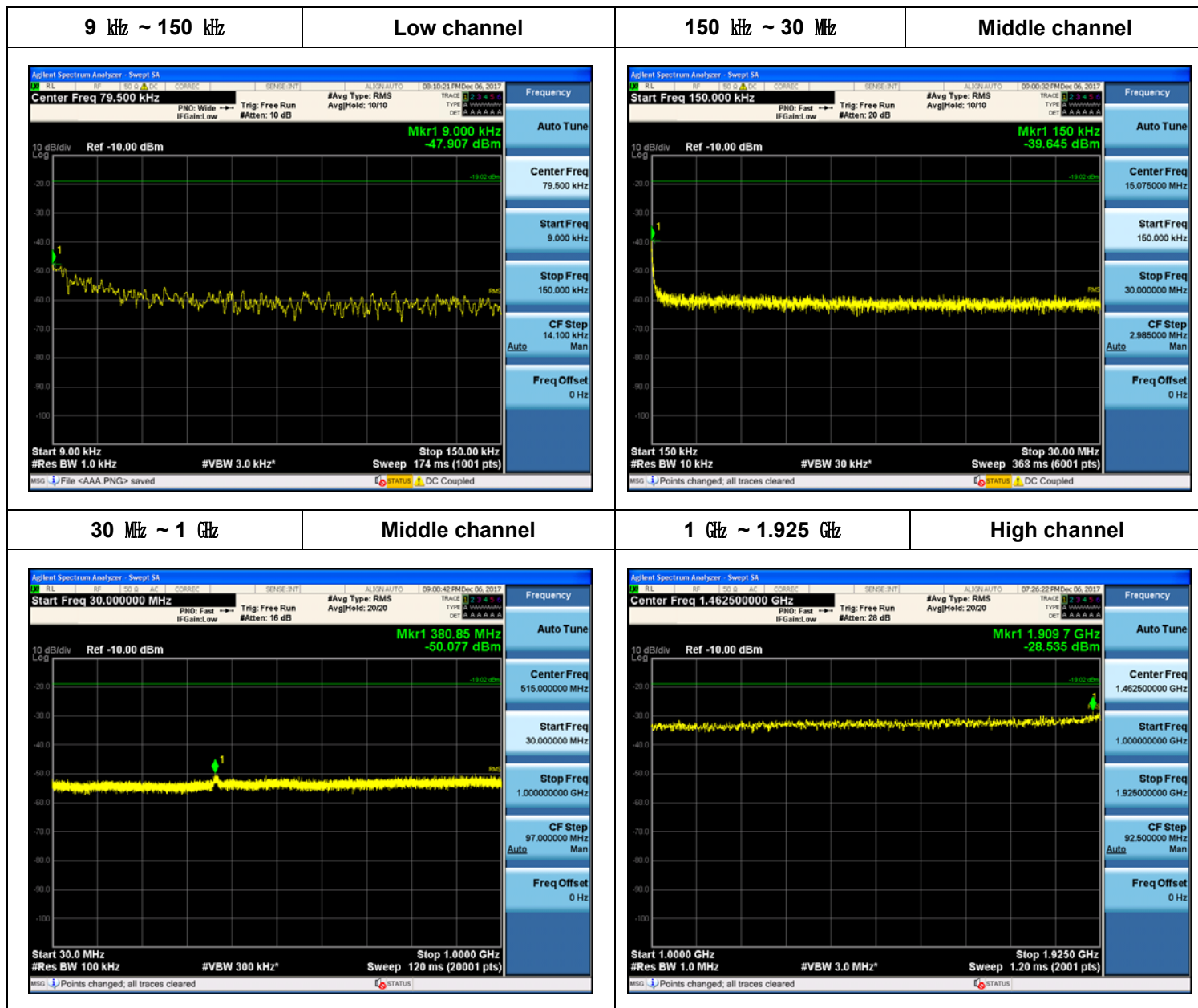


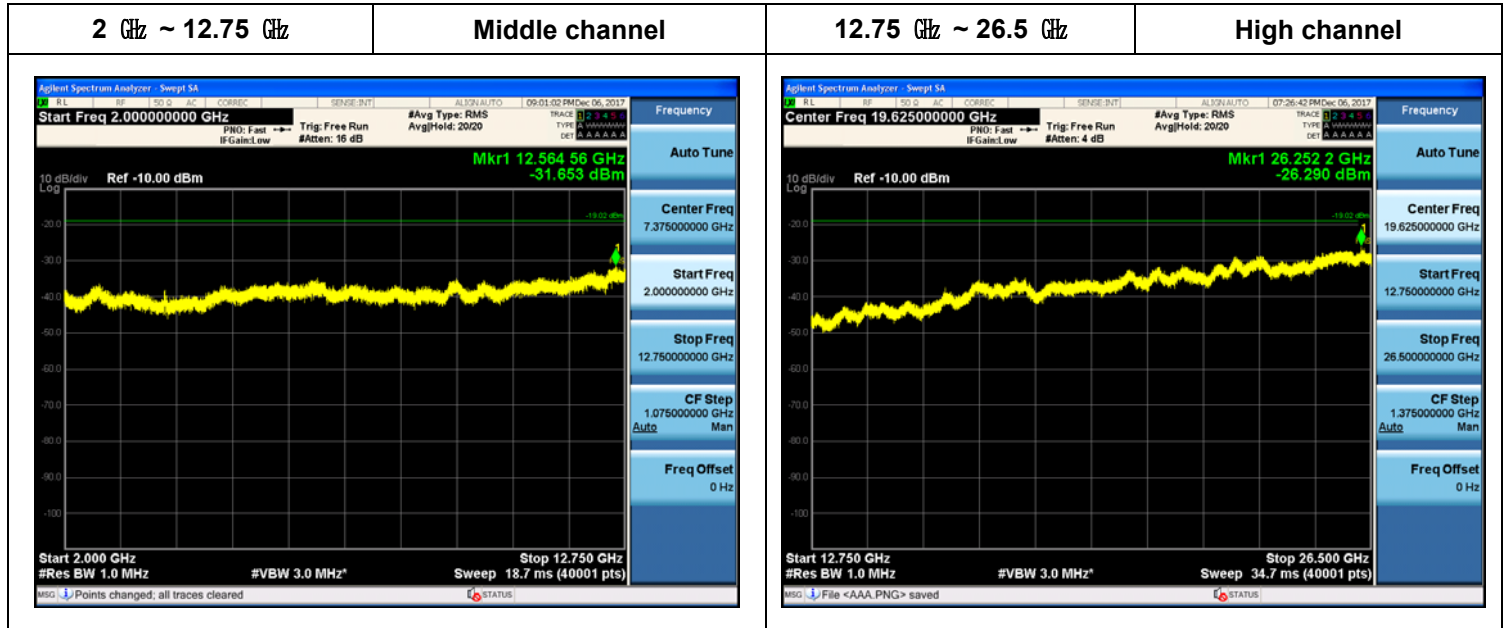
Plot Data for Output Port 0_16QAM



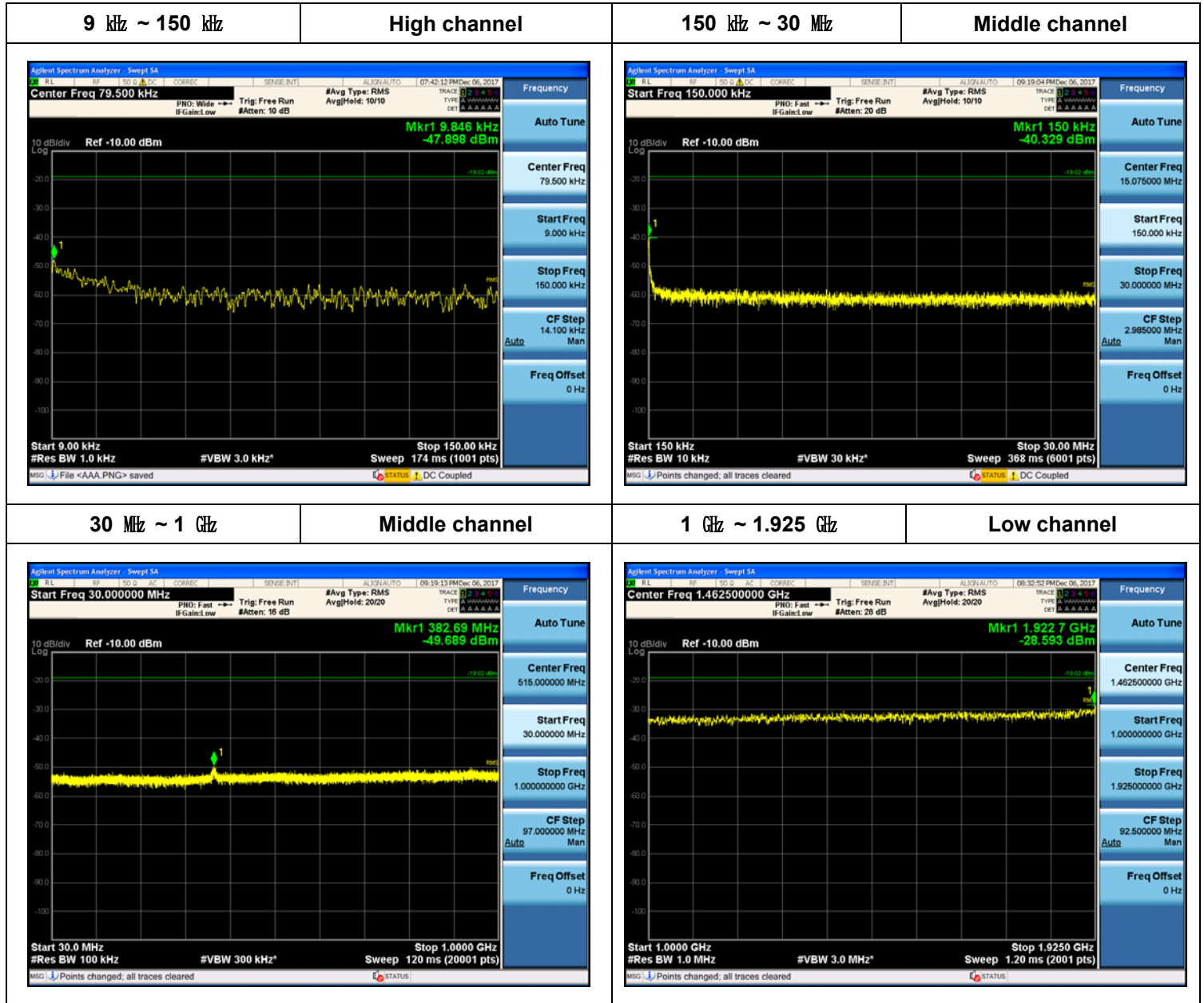


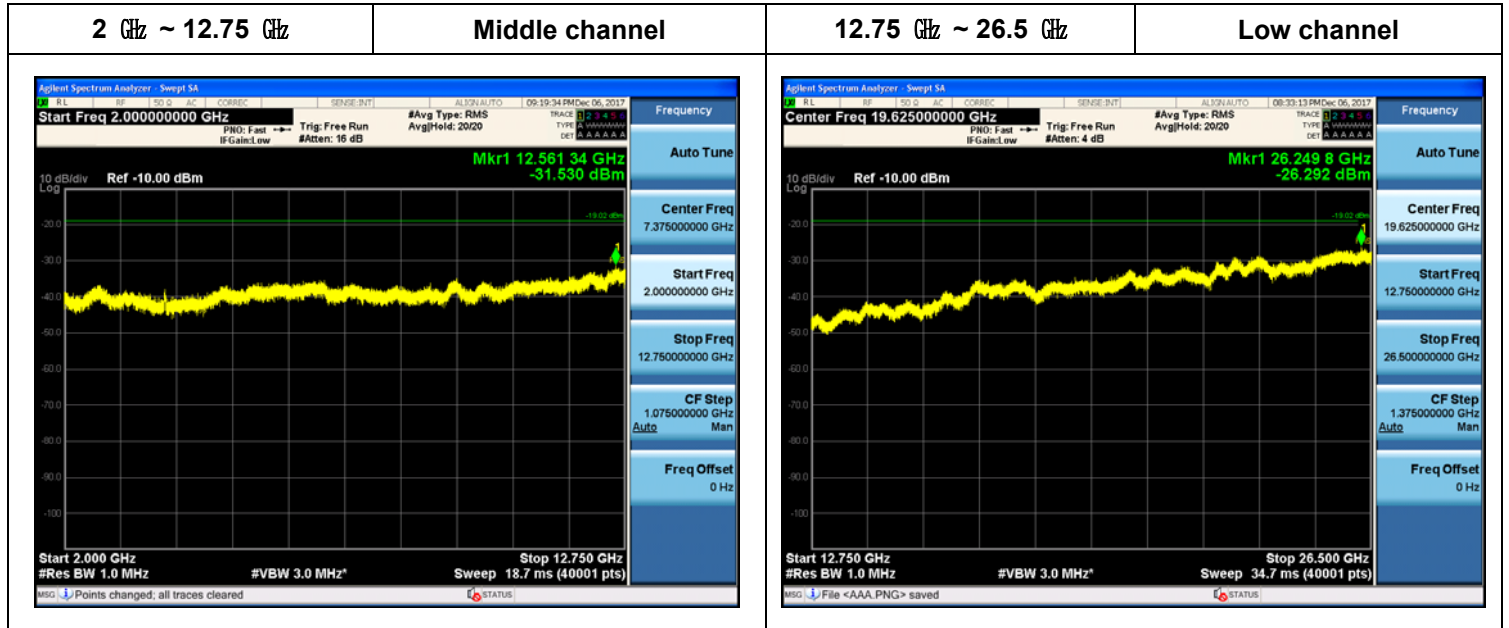
Plot Data for Output Port 0_64QAM



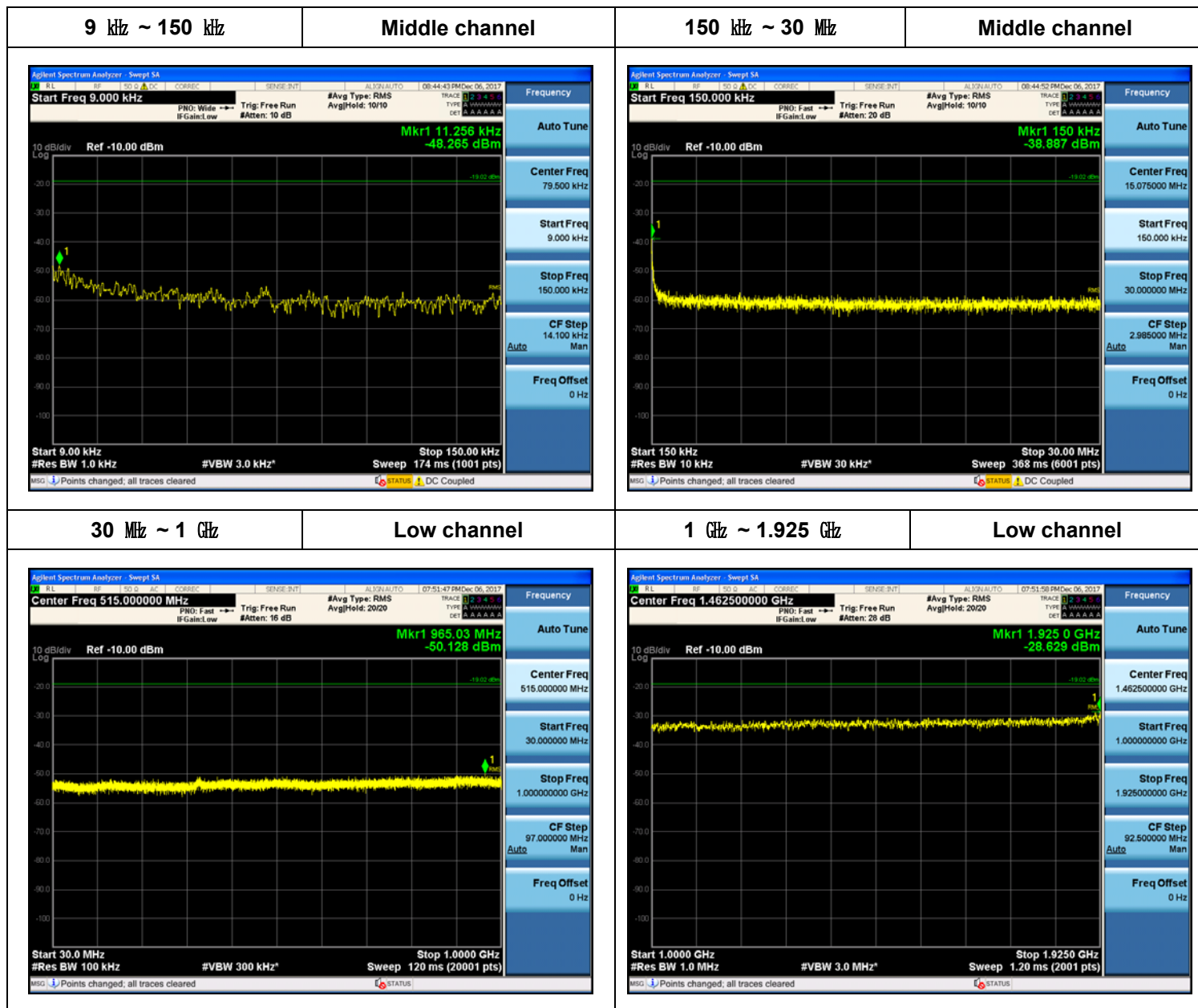


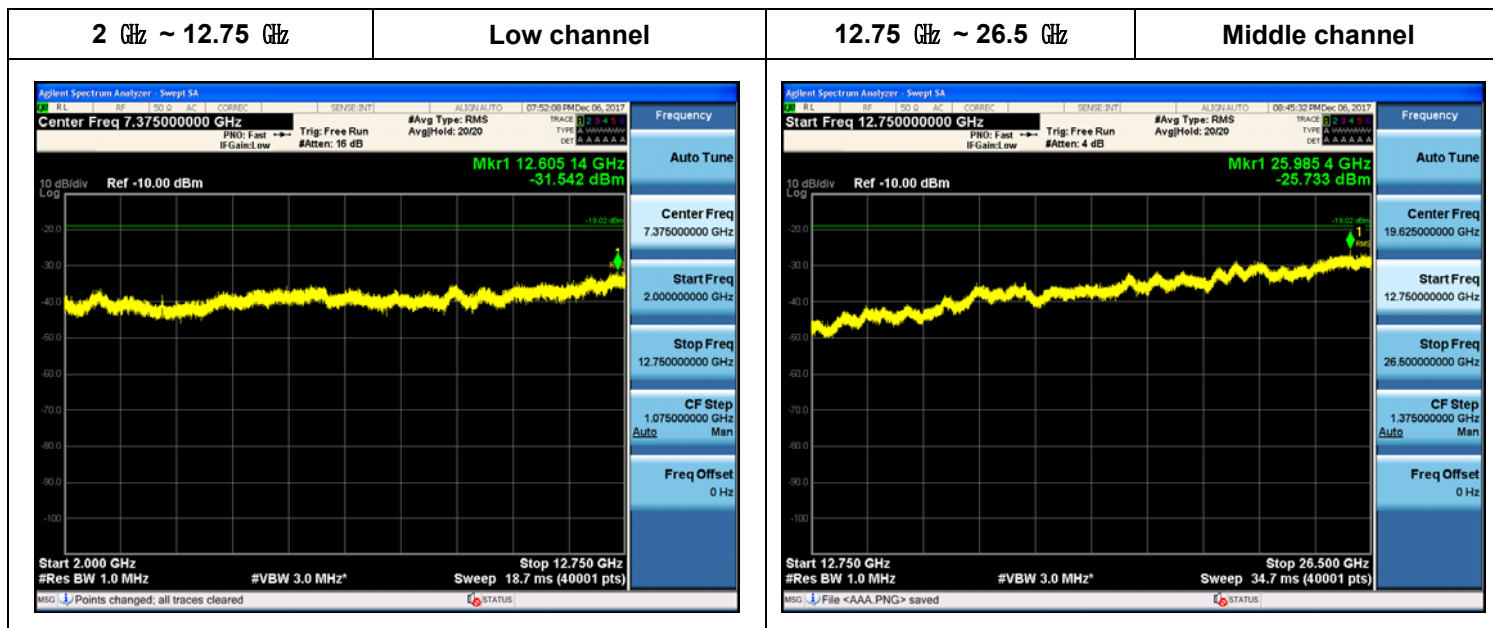
Plot Data for Output Port 0_256QAM



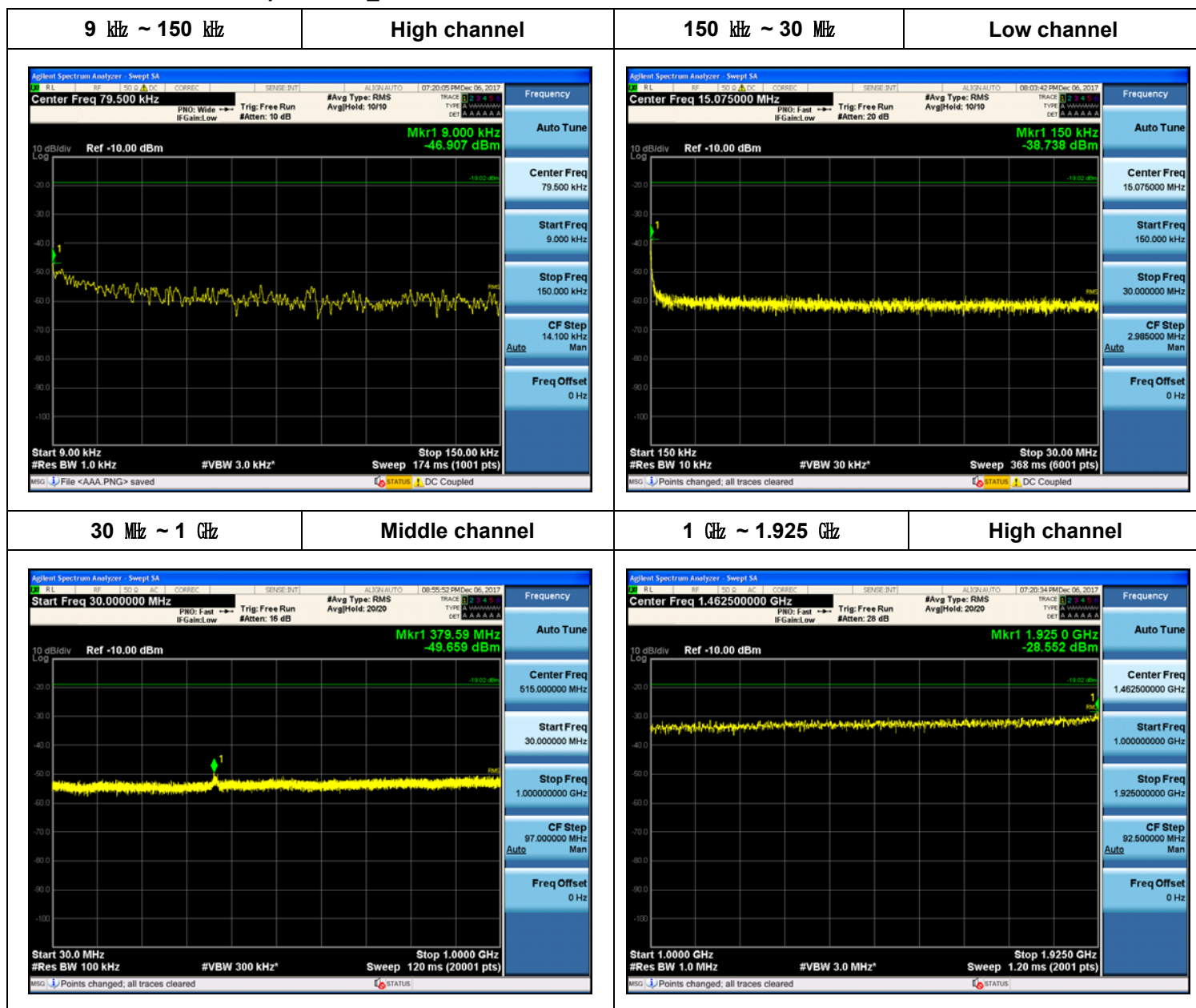


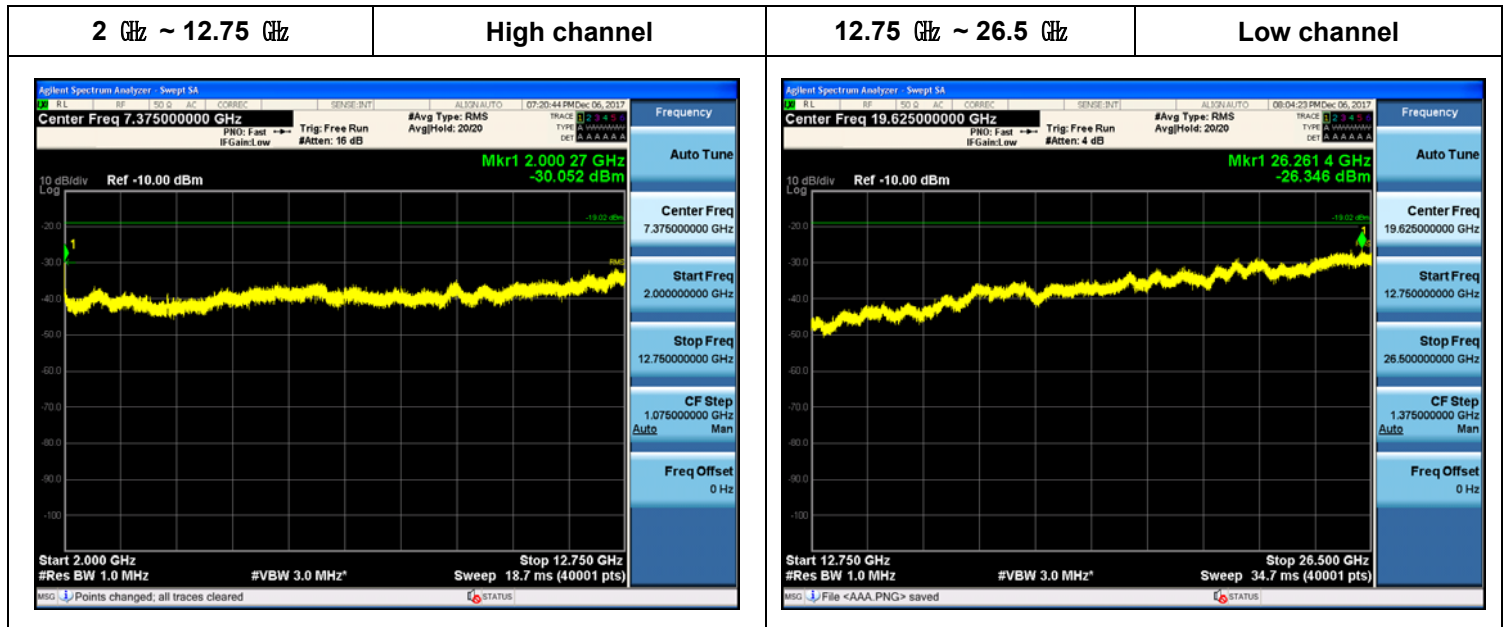
Plot Data for Output Port 1_QPSK





Plot Data for Output Port 1_16QAM





Plot Data for Output Port 1_64QAM

