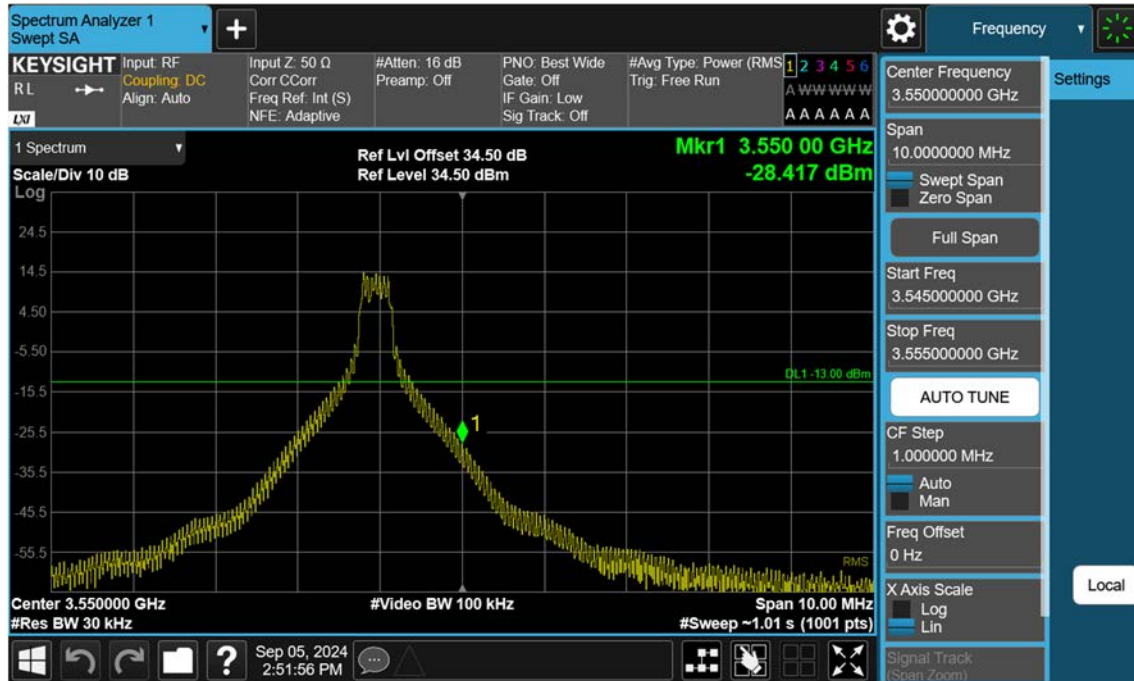


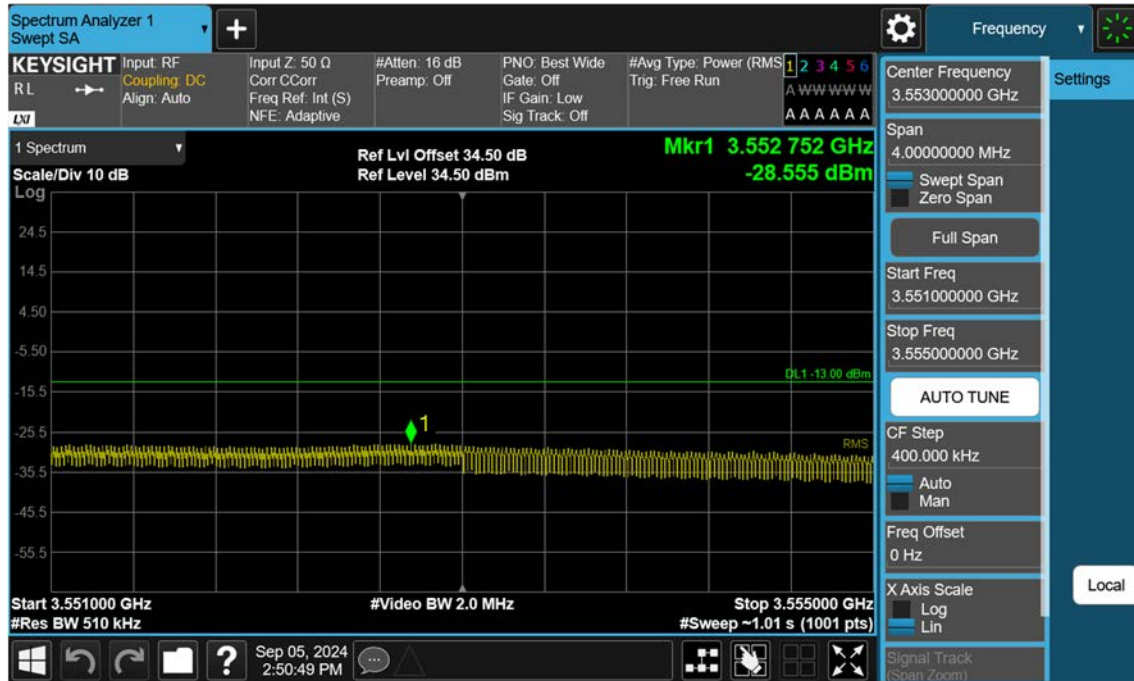
n77(3450~3550 MHz)_100 M_Band Edge_High_BPSK_FullRB(1)



n77(3450~3550 MHz)_100 M_Band Edge_High_BPSK_1RB(1)



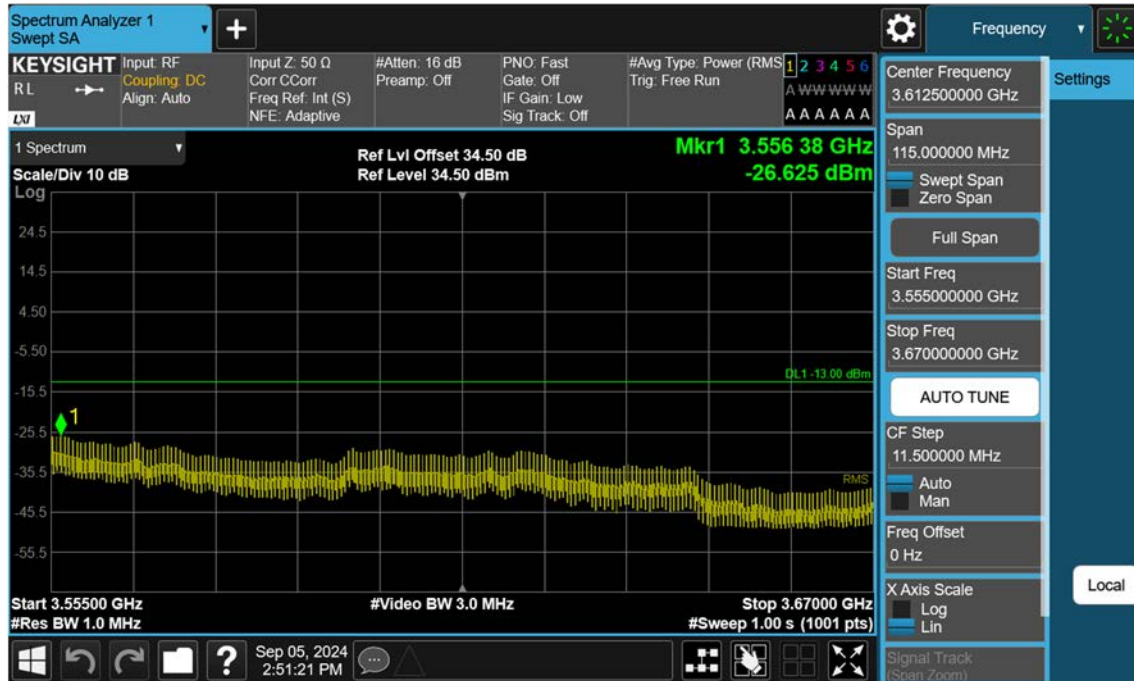
n77(3450~3550 MHz)_100 M_Band Edge_High_BPSK_FullRB(2)



n77(3450~3550 MHz)_100 M_Band Edge_High_BPSK_1RB(2)



n77(3450~3550 MHz)_100 M_Band Edge_High_BPSK_FullRB(3)

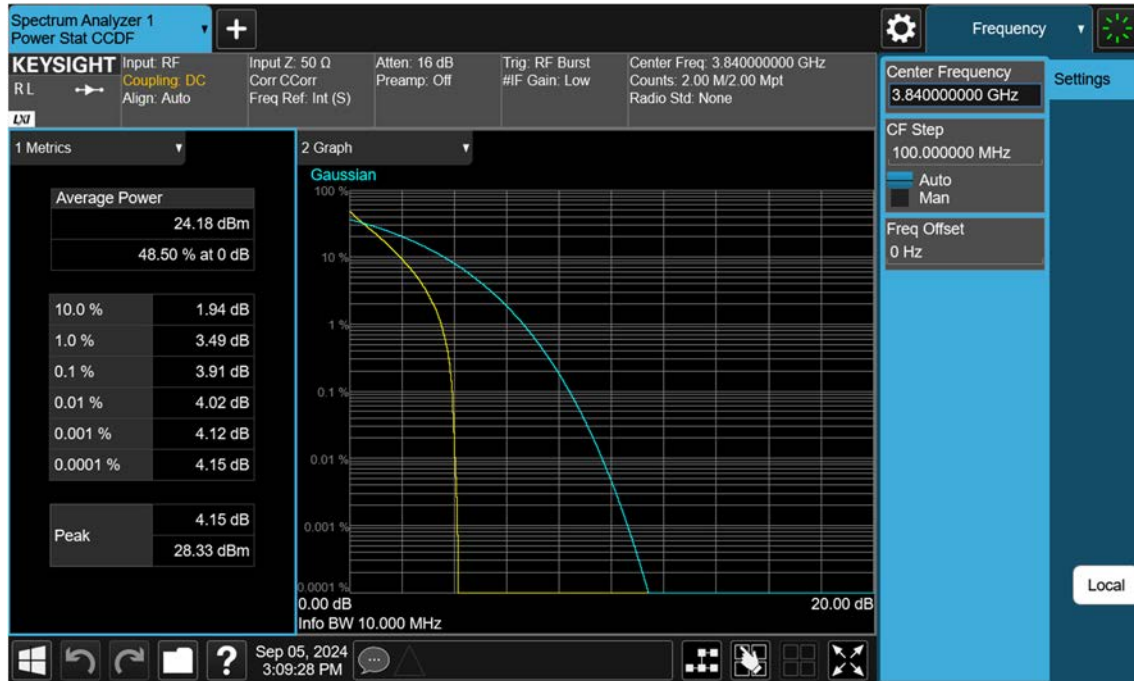


n77(3450~3550 MHz)_100 M_Band Edge_High_BPSK_1RB(3)



11. TEST PLOTS(3700 MHz - 3980 MHz)

n77(3700~3980 MHz)_10 M_PAR_Mid_BPSK_FullRB



n77(3700~3980 MHz)_10 M_PAR_Mid_QPSK_FullRB



n77(3700~3980 MHz)_10 M_PAR_Mid_16QAM_FullRB



n77(3700~3980 MHz)_10 M_PAR_Mid_64QAM_FullRB



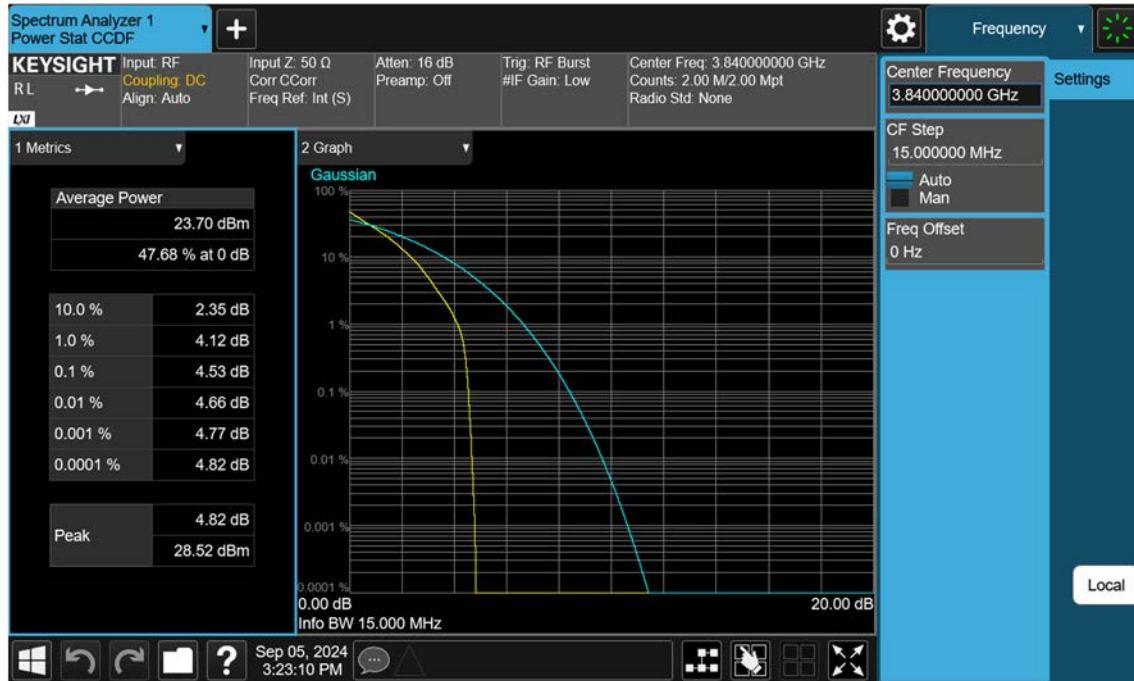
n77(3700~3980 MHz)_10 M_PAR_Mid_256QAM_FullRB



n77(3700~3980 MHz)_15 M_PAR_Mid_BPSK_FullRB



n77(3700~3980 MHz)_15 M_PAR_Mid_QPSK_FullRB



n77(3700~3980 MHz)_15 M_PAR_Mid_16QAM_FullRB



n77(3700~3980 MHz)_15 M_PAR_Mid_64QAM_FullRB



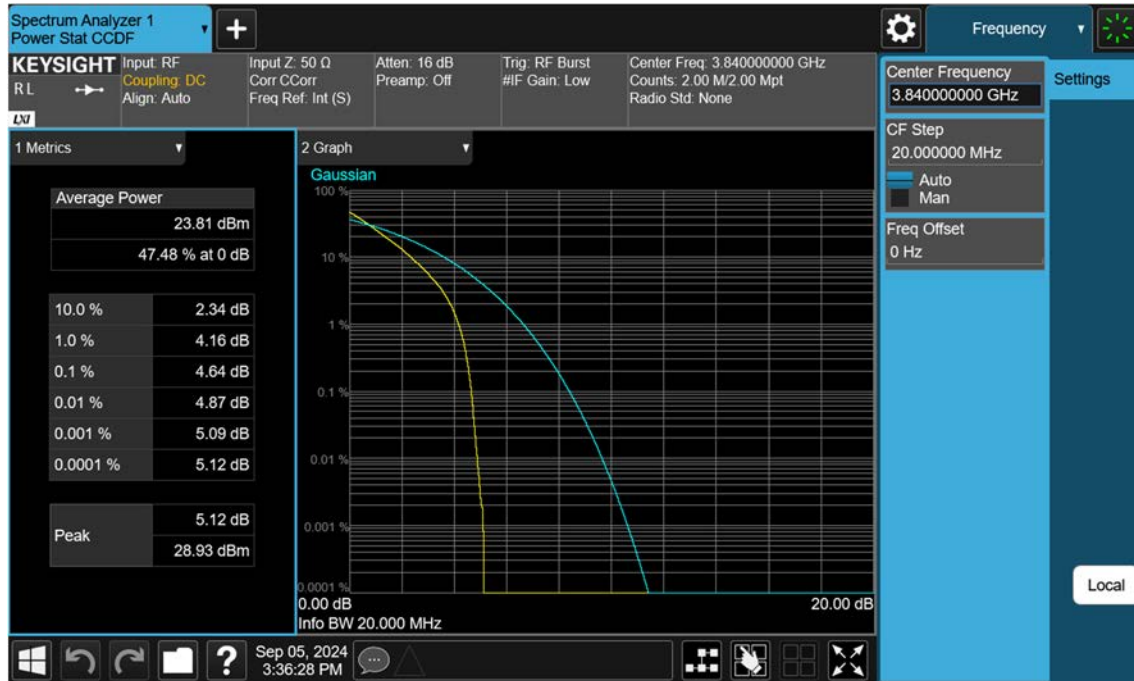
n77(3700~3980 MHz)_15 M_PAR_Mid_256QAM_FullRB



n77(3700~3980 MHz)_20 M_PAR_Mid_BPSK_FullRB



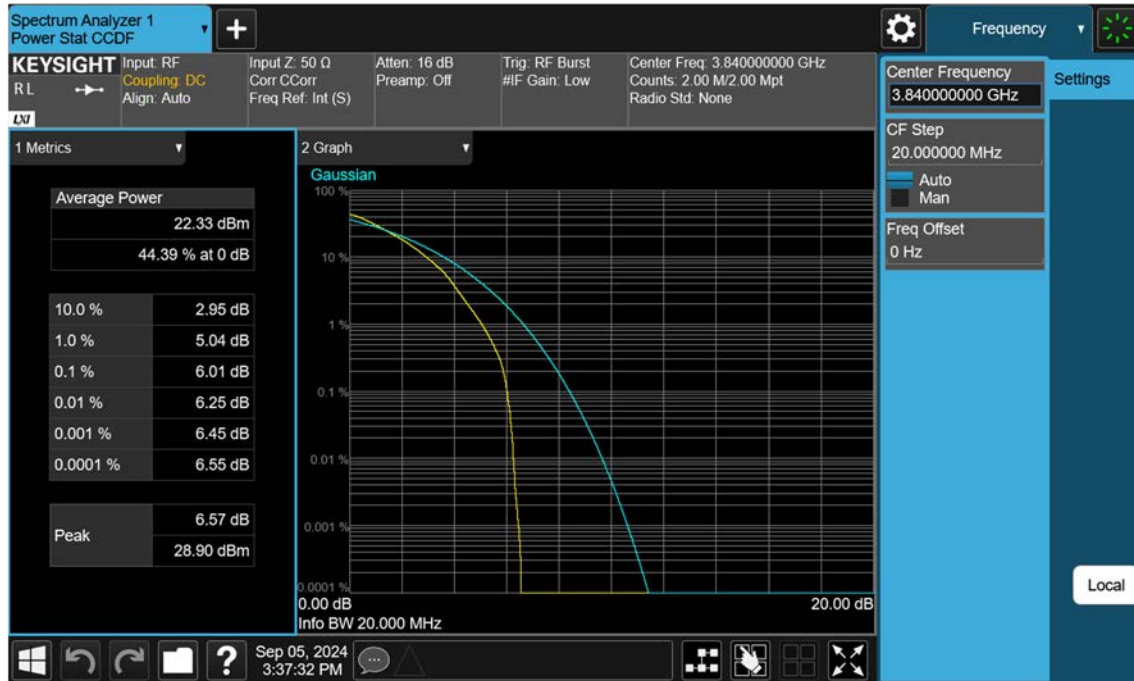
n77(3700~3980 MHz)_20 M_PAR_Mid_QPSK_FullRB



n77(3700~3980 MHz)_20 M_PAR_Mid_16QAM_FullRB



n77(3700~3980 MHz)_20 M_PAR_Mid_64QAM_FullRB



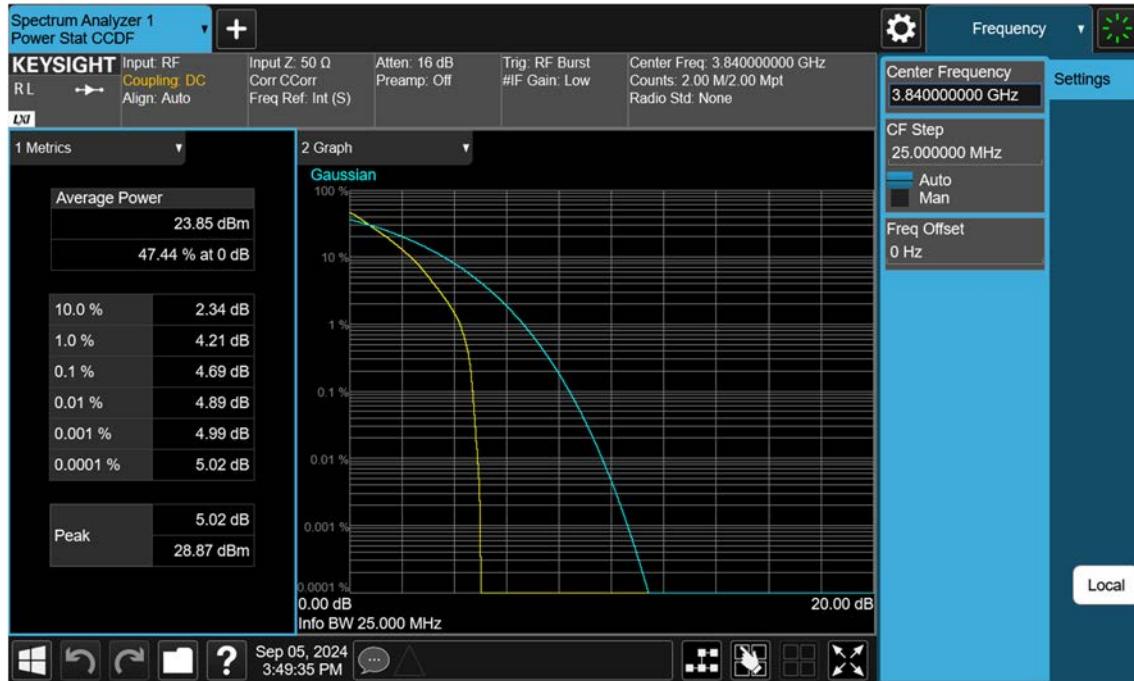
n77(3700~3980 MHz)_20 M_PAR_Mid_256QAM_FullRB



n77(3700~3980 MHz)_25 M_PAR_Mid_BPSK_FullRB



n77(3700~3980 MHz)_25 M_PAR_Mid_QPSK_FullRB



n77(3700~3980 MHz)_25 M_PAR_Mid_16QAM_FullRB



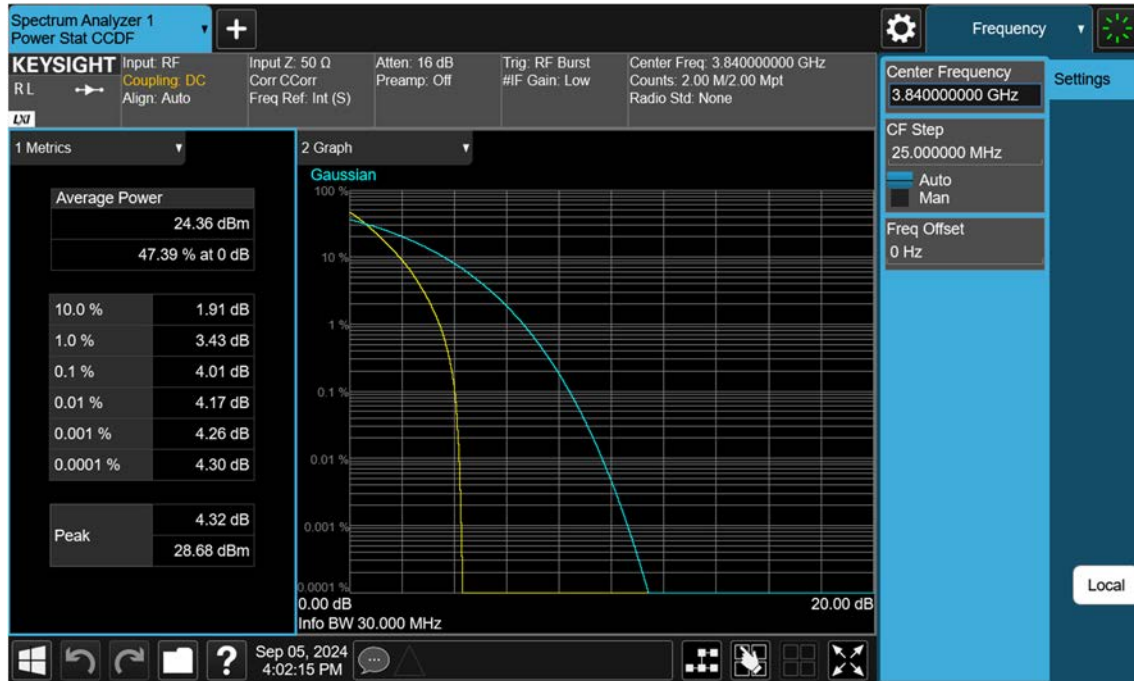
n77(3700~3980 MHz)_25 M_PAR_Mid_64QAM_FullRB



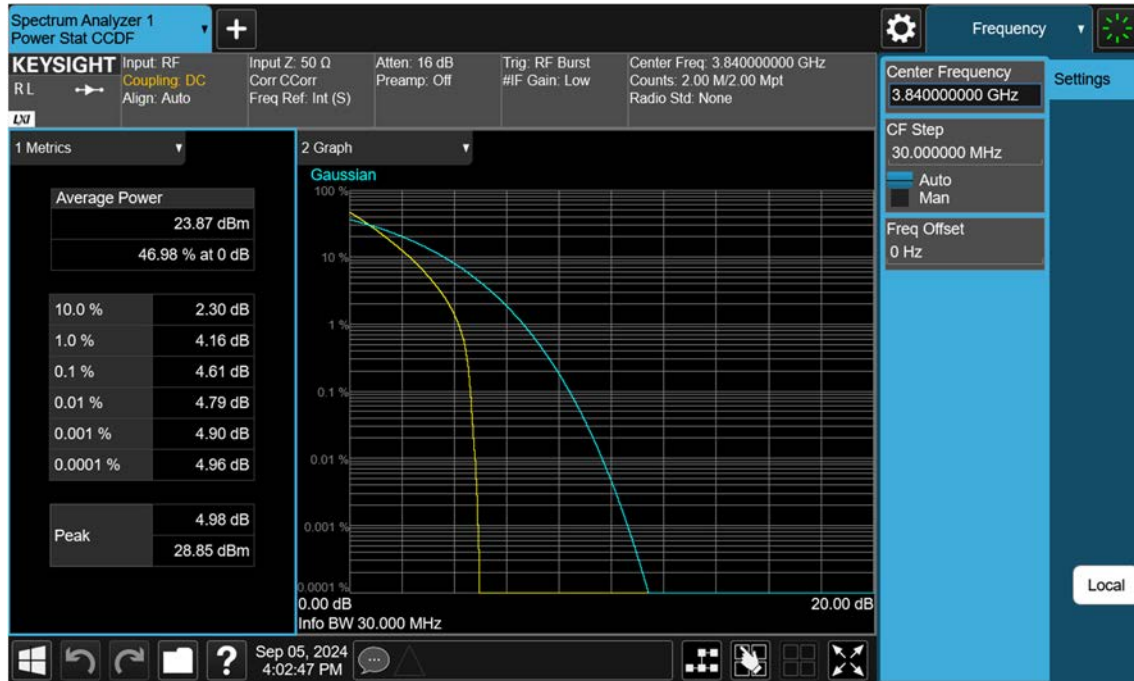
n77(3700~3980 MHz)_25 M_PAR_Mid_256QAM_FullRB



n77(3700~3980 MHz)_30 M_PAR_Mid_BPSK_FullRB



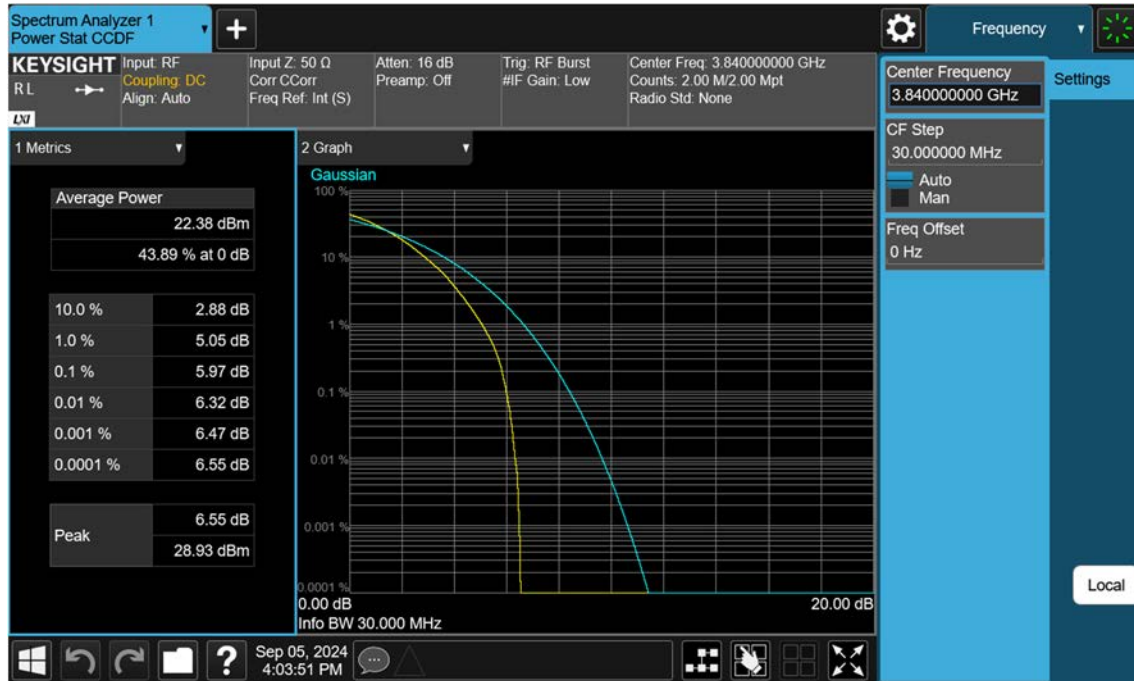
n77(3700~3980 MHz)_30 M_PAR_Mid_QPSK_FullRB



n77(3700~3980 MHz)_30 M_PAR_Mid_16QAM_FullRB



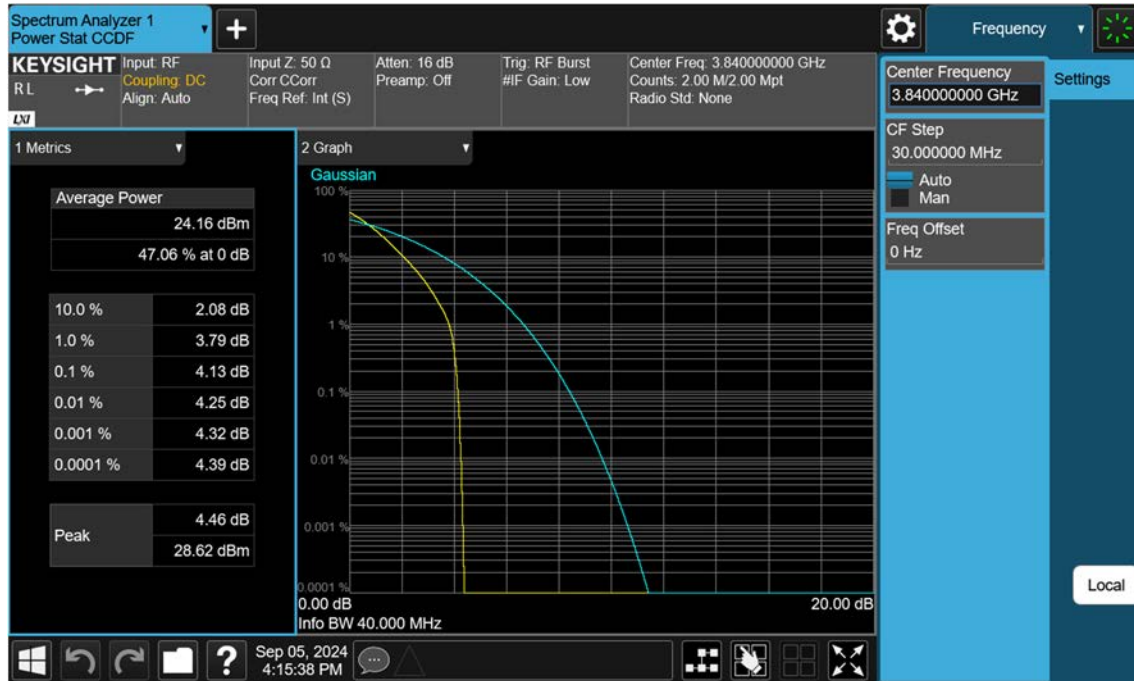
n77(3700~3980 MHz)_30 M_PAR_Mid_64QAM_FullRB



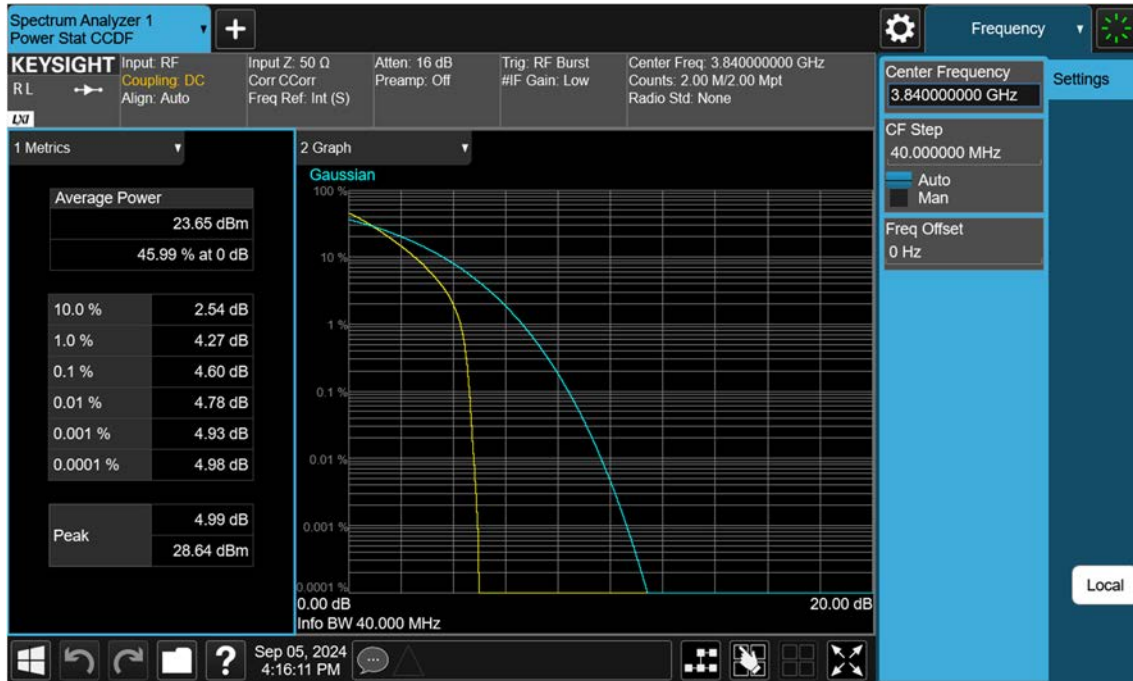
n77(3700~3980 MHz)_30 M_PAR_Mid_256QAM_FullRB



n77(3700~3980 MHz)_40 M_PAR_Mid_BPSK_FullRB



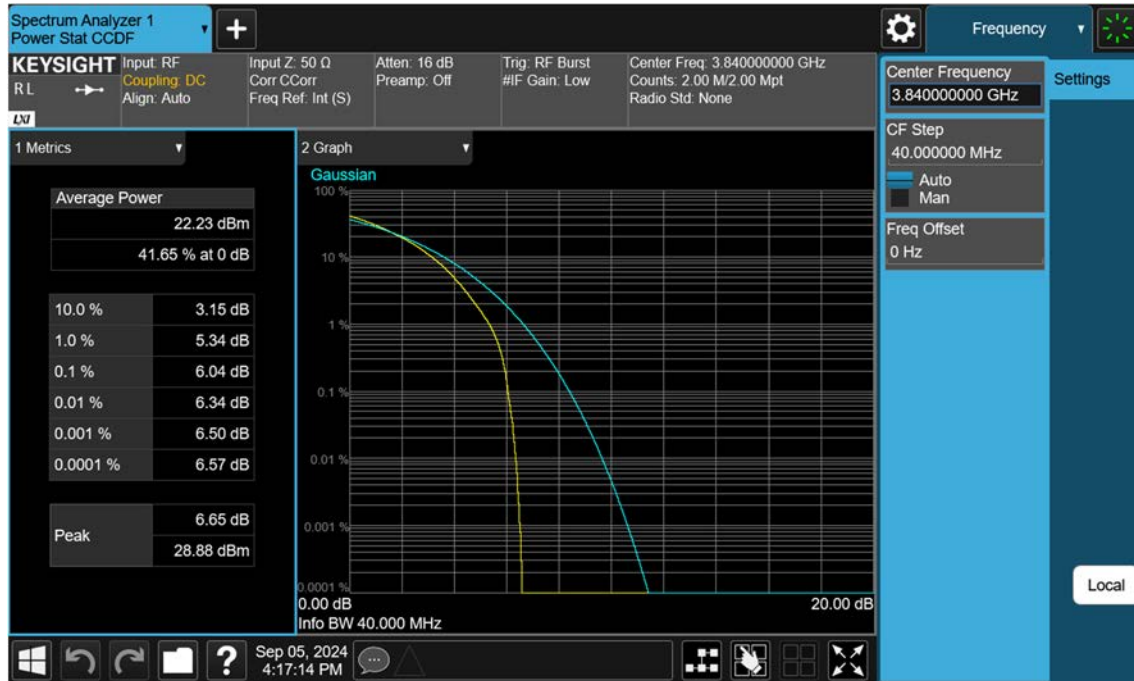
n77(3700~3980 MHz)_40 M_PAR_Mid_QPSK_FullRB



n77(3700~3980 MHz)_40 M_PAR_Mid_16QAM_FullRB



n77(3700~3980 MHz)_40 M_PAR_Mid_64QAM_FullRB



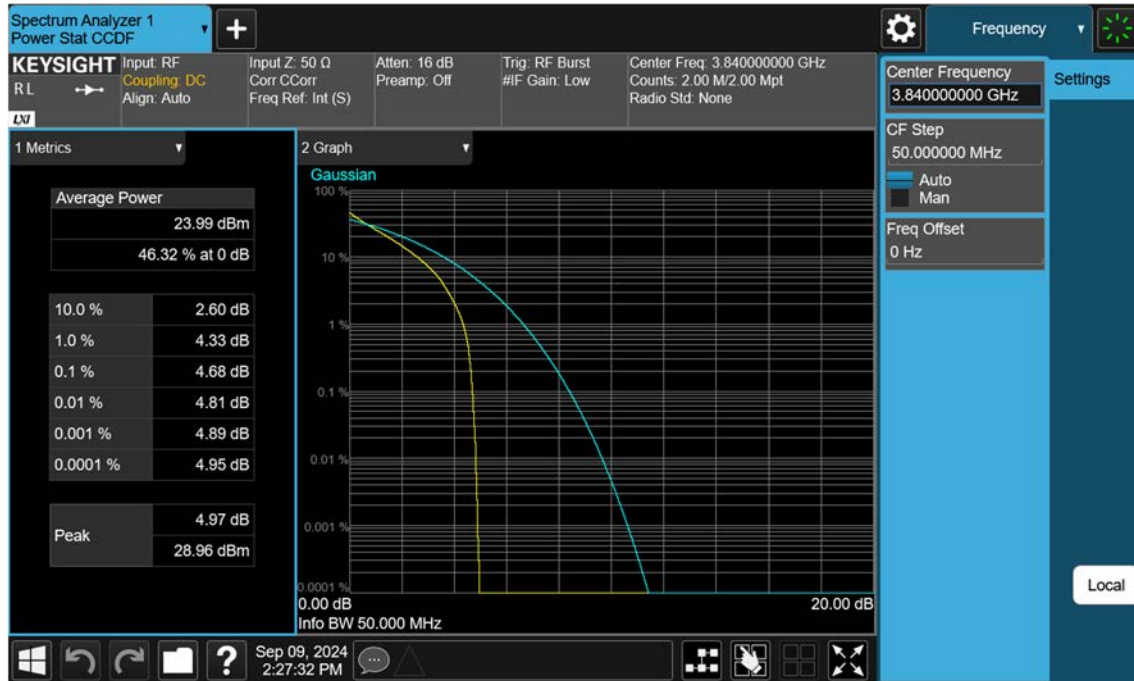
n77(3700~3980 MHz)_40 M_PAR_Mid_256QAM_FullRB



n77(3700~3980 MHz)_50 M_PAR_Mid_BPSK_FullRB



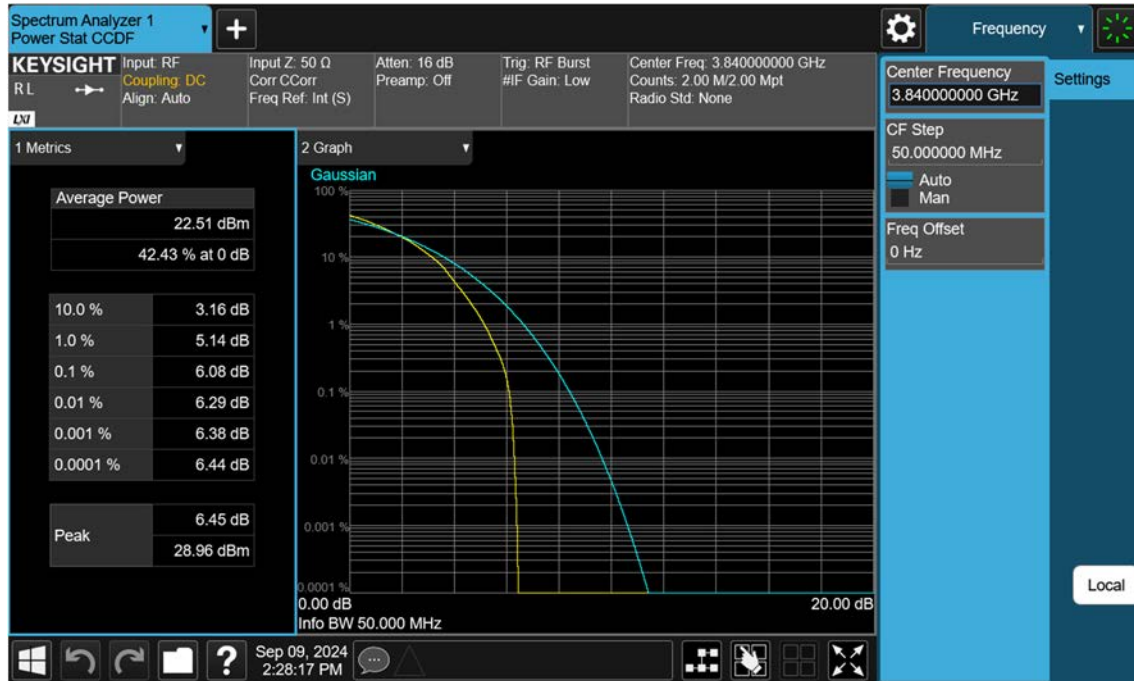
n77(3700~3980 MHz)_50 M_PAR_Mid_QPSK_FullRB



n77(3700~3980 MHz)_50 M_PAR_Mid_16QAM_FullRB



n77(3700~3980 MHz)_50 M_PAR_Mid_64QAM_FullRB



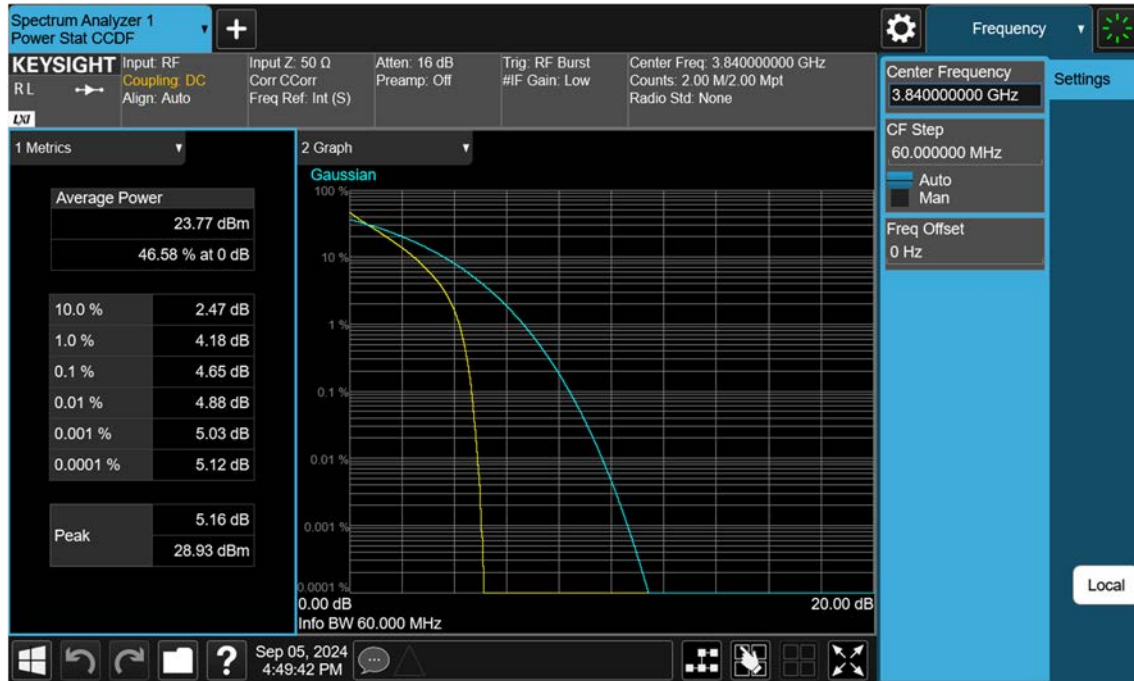
n77(3700~3980 MHz)_50 M_PAR_Mid_256QAM_FullRB



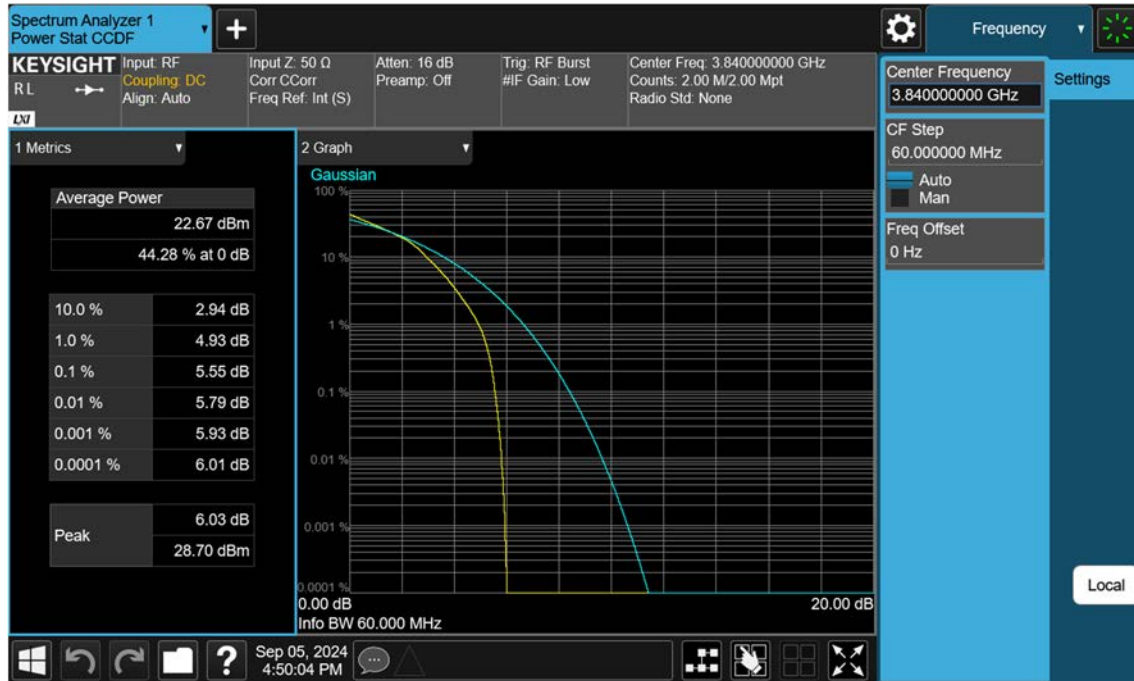
n77(3700~3980 MHz)_60 M_PAR_Mid_BPSK_FullRB



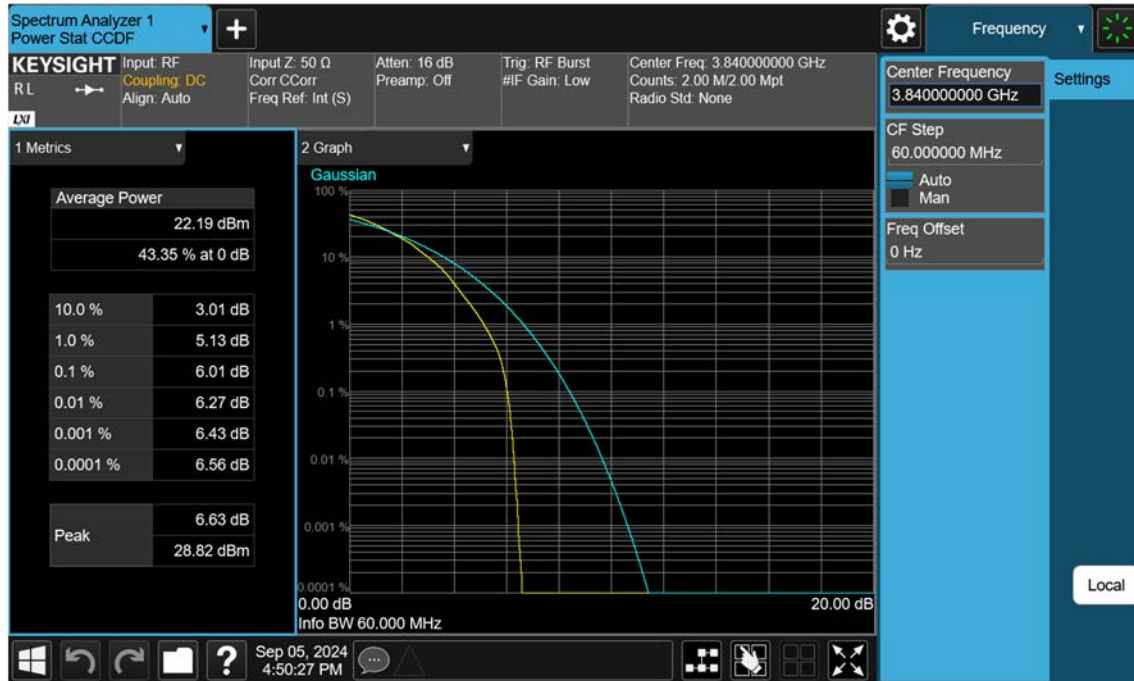
n77(3700~3980 MHz)_60 M_PAR_Mid_QPSK_FullRB



n77(3700~3980 MHz)_60 M_PAR_Mid_16QAM_FullRB



n77(3700~3980 MHz)_60 M_PAR_Mid_64QAM_FullRB



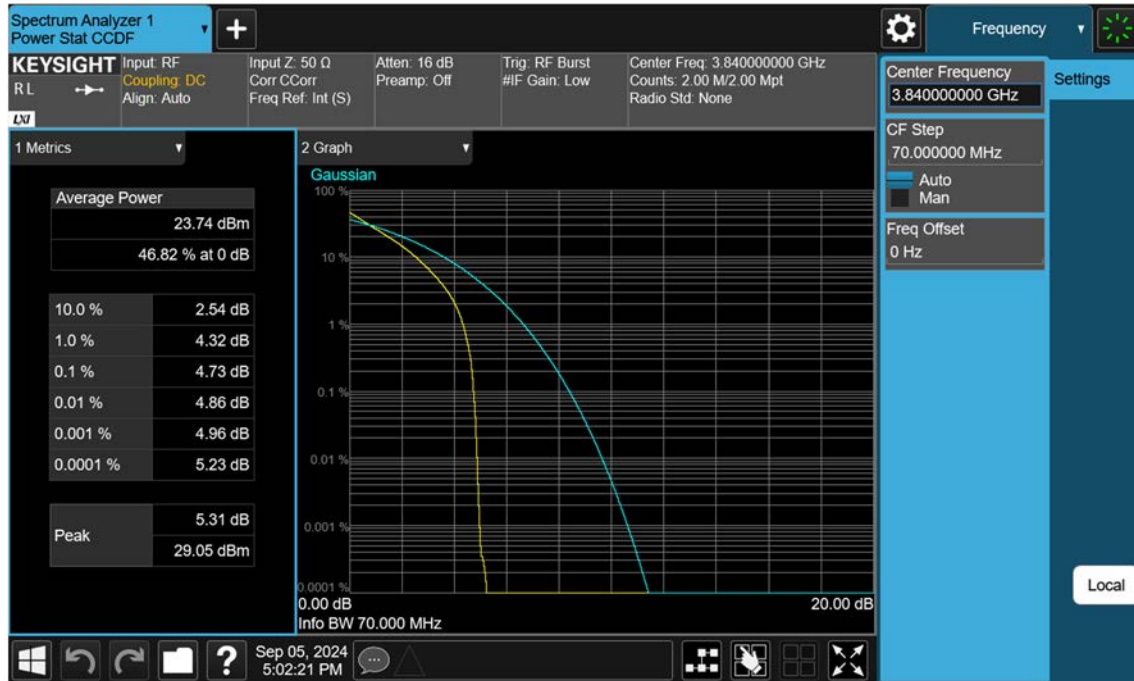
n77(3700~3980 MHz)_60 M_PAR_Mid_256QAM_FullRB



n77(3700~3980 MHz)_70 M_PAR_Mid_BPSK_FullRB



n77(3700~3980 MHz)_70 M_PAR_Mid_QPSK_FullRB



n77(3700~3980 MHz)_70 M_PAR_Mid_16QAM_FullRB



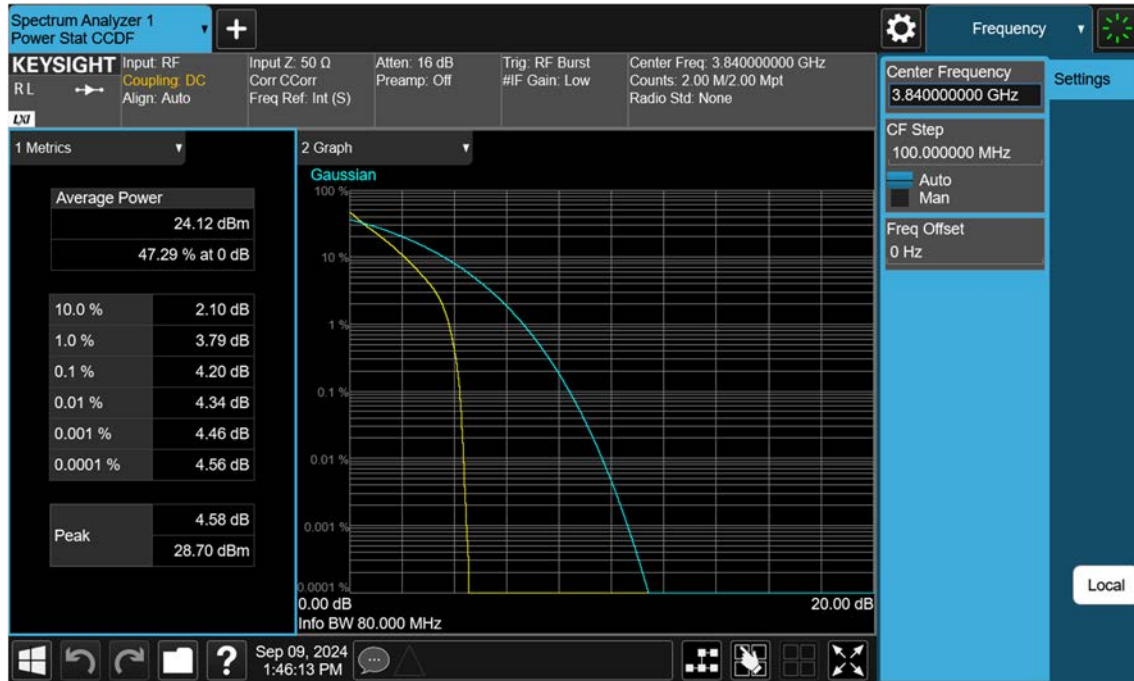
n77(3700~3980 MHz)_70 M_PAR_Mid_64QAM_FullRB



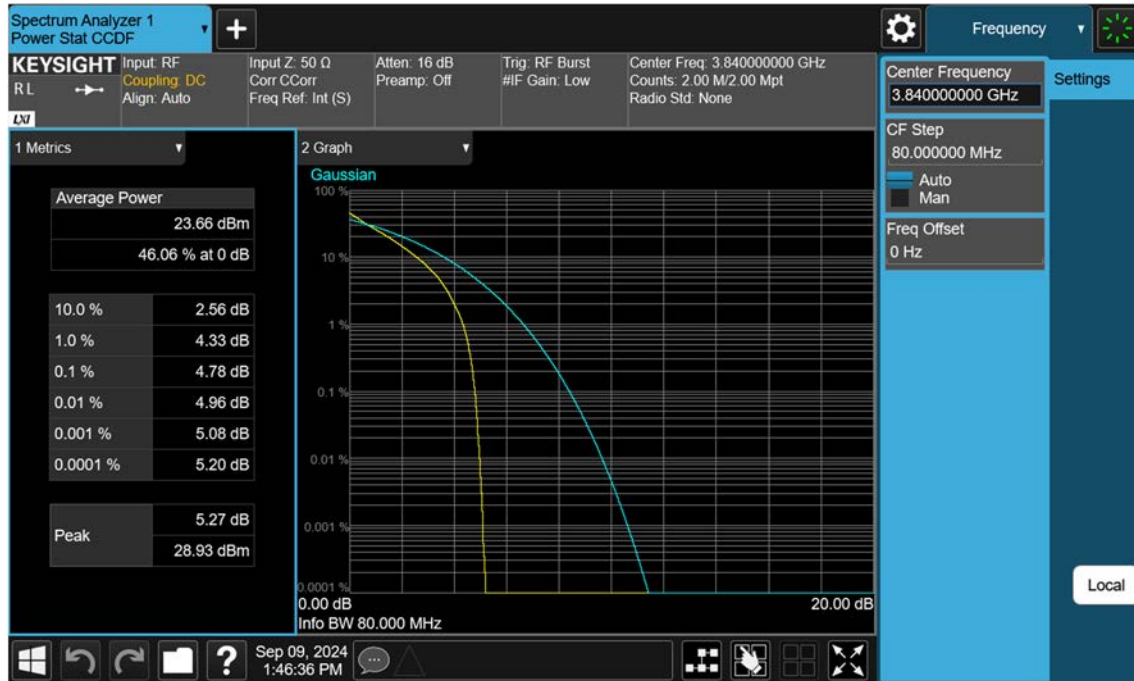
n77(3700~3980 MHz)_70 M_PAR_Mid_256QAM_FullRB



n77(3700~3980 MHz)_80 M_PAR_Mid_BPSK_FullRB



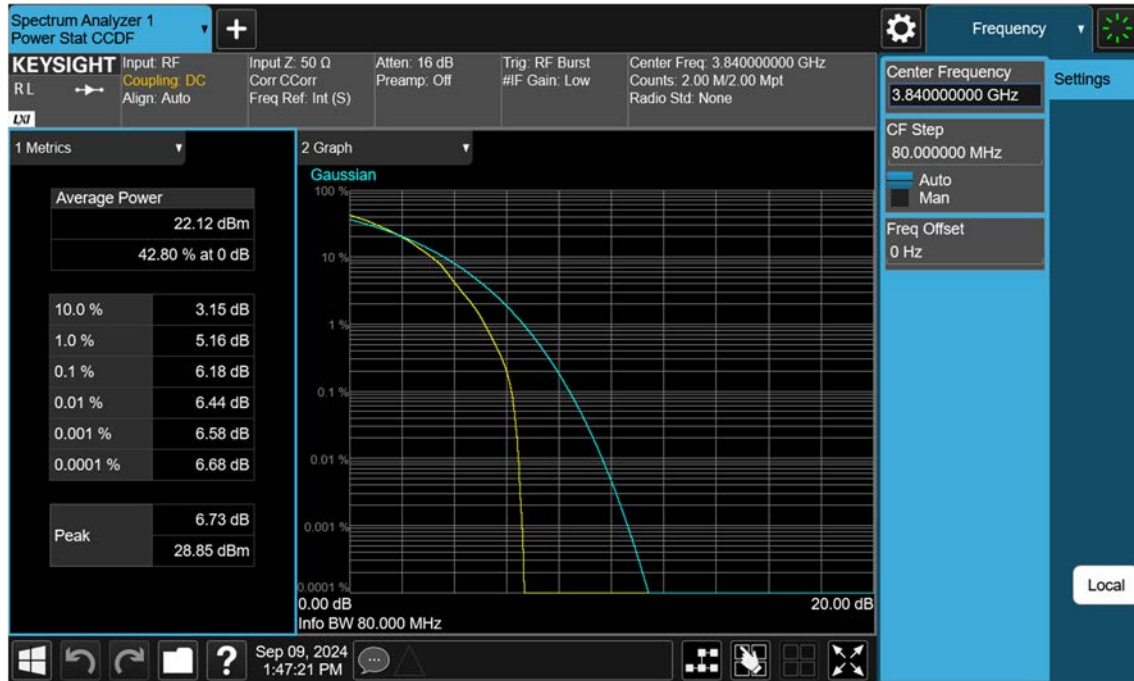
n77(3700~3980 MHz)_80 M_PAR_Mid_QPSK_FullRB



n77(3700~3980 MHz)_80 M_PAR_Mid_16QAM_FullRB



n77(3700~3980 MHz)_80 M_PAR_Mid_64QAM_FullRB



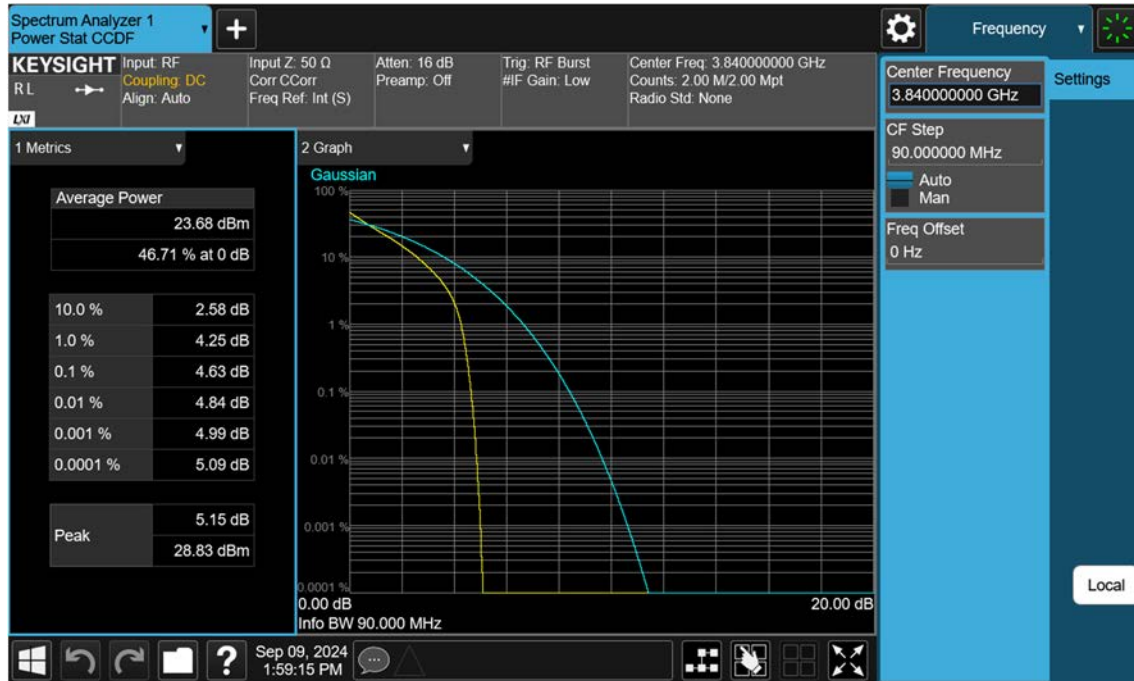
n77(3700~3980 MHz)_80 M_PAR_Mid_256QAM_FullRB



n77(3700~3980 MHz)_90 M_PAR_Mid_BPSK_FullRB



n77(3700~3980 MHz)_90 M_PAR_Mid_QPSK_FullRB



n77(3700~3980 MHz)_90 M_PAR_Mid_16QAM_FullRB



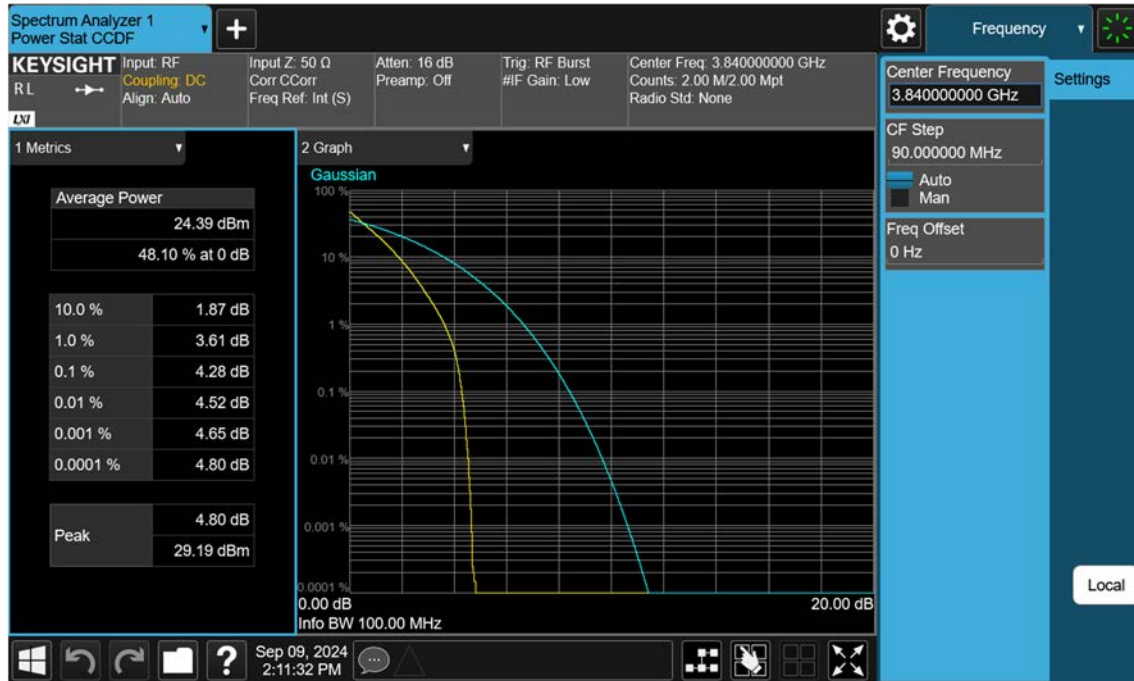
n77(3700~3980 MHz)_90 M_PAR_Mid_64QAM_FullRB



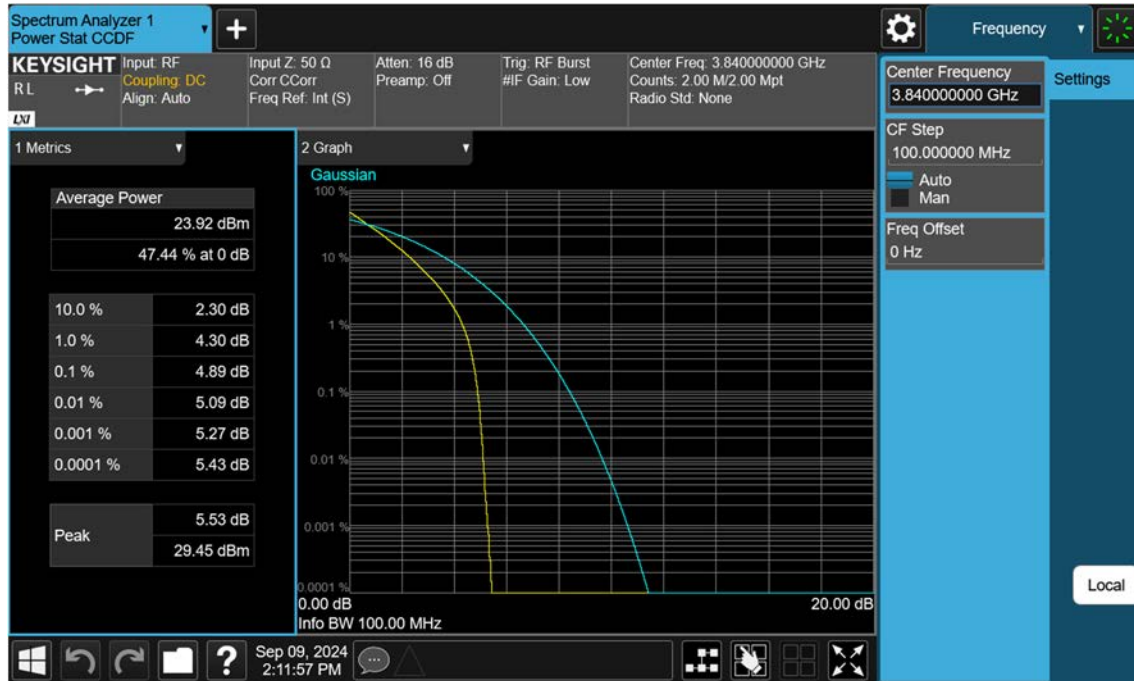
n77(3700~3980 MHz)_90 M_PAR_Mid_256QAM_FullRB



n77(3700~3980 MHz)_100 M_PAR_Mid_BPSK_FullRB



n77(3700~3980 MHz)_100 M_PAR_Mid_QPSK_FullRB



n77(3700~3980 MHz)_100 M_PAR_Mid_16QAM_FullRB



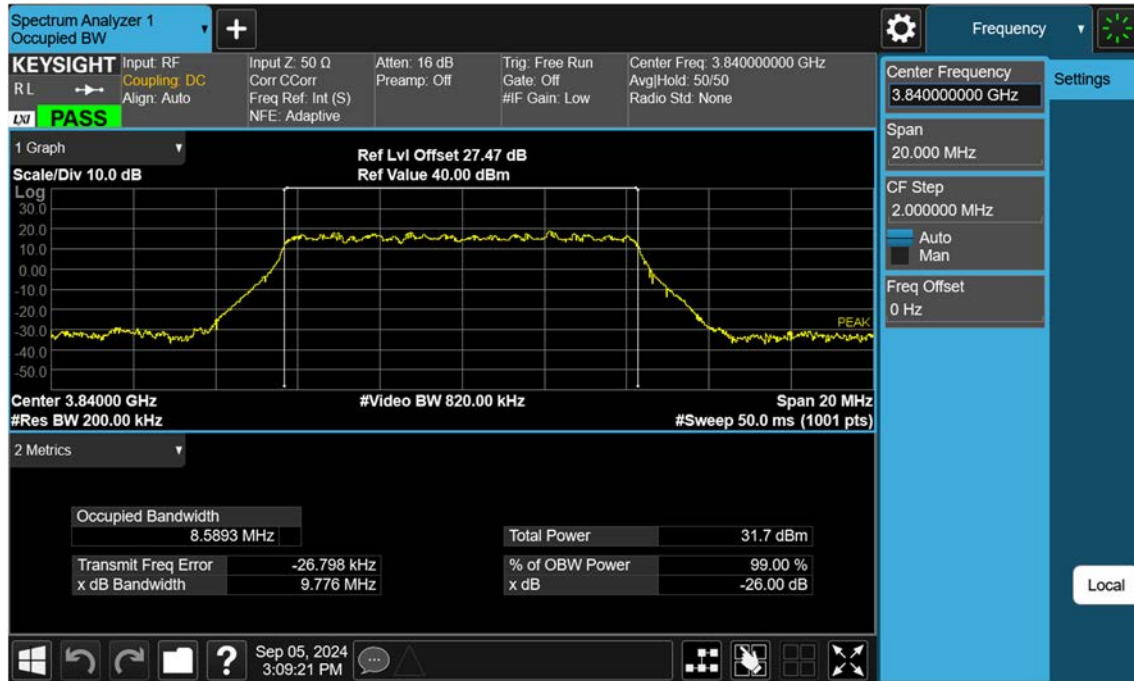
n77(3700~3980 MHz)_100 M_PAR_Mid_64QAM_FullRB



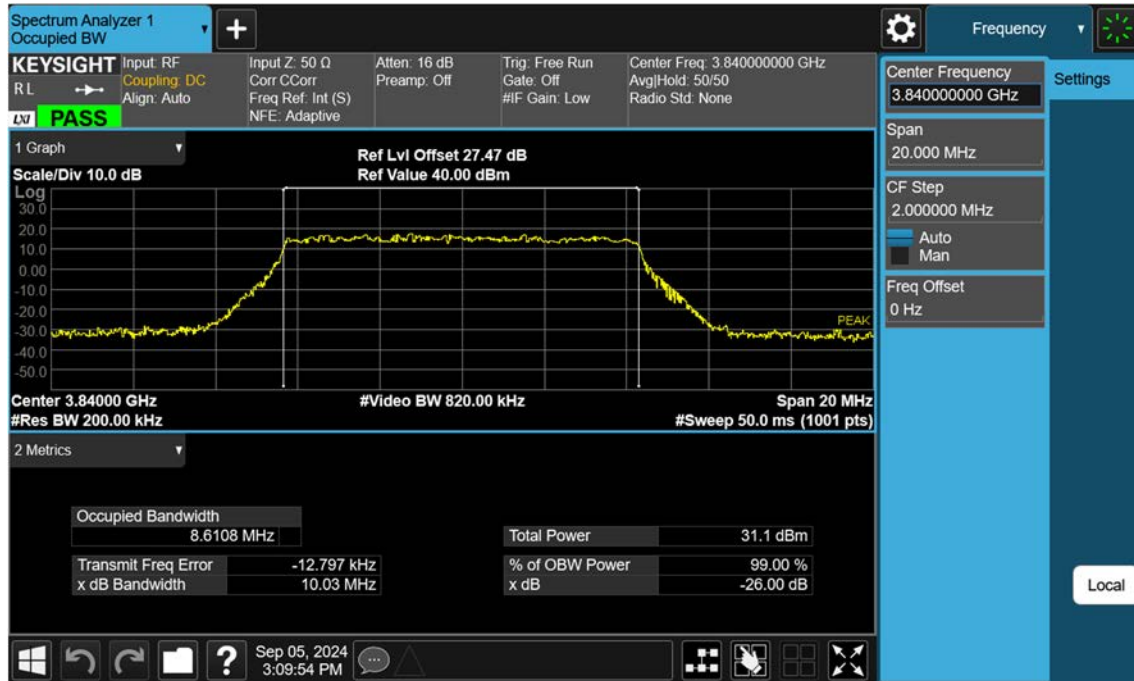
n77(3700~3980 MHz)_100 M_PAR_Mid_256QAM_FullRB



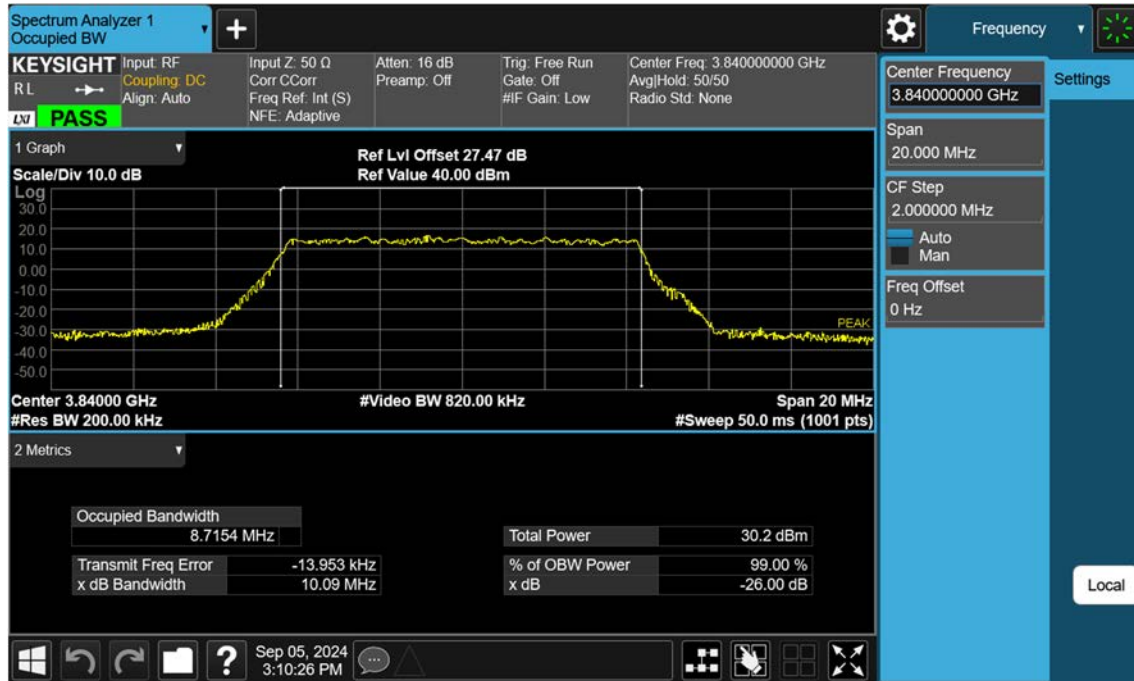
n77(3700~3980 MHz)_10 M_OBW_Mid_BPSK_FullRB



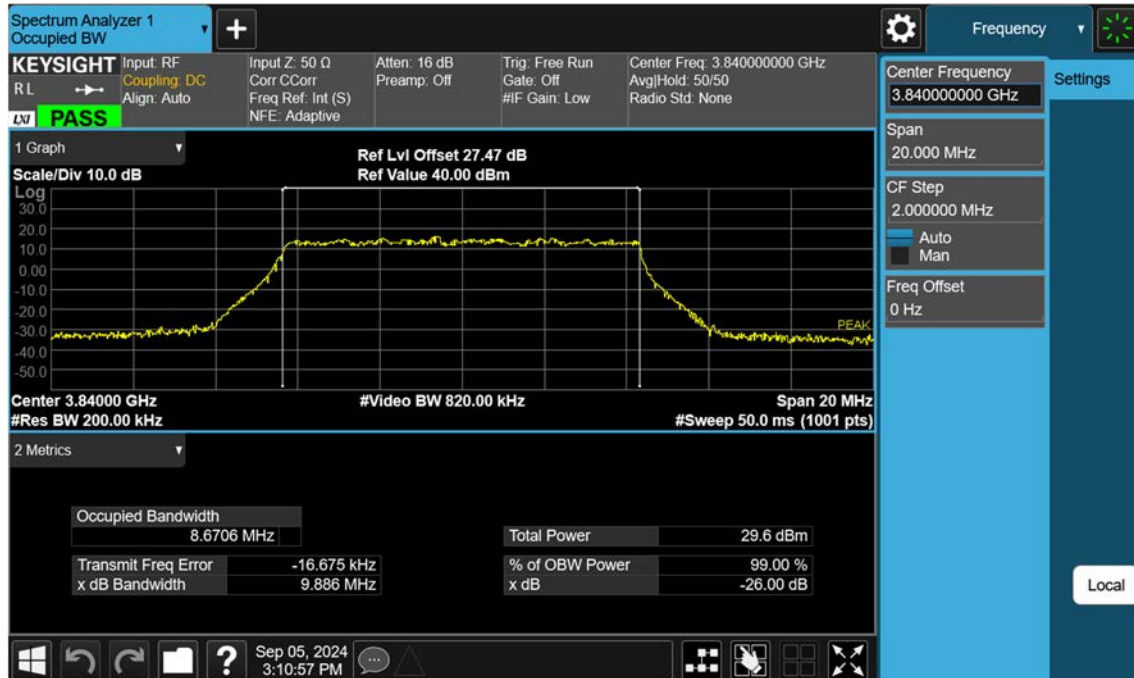
n77(3700~3980 MHz)_10 M_OBW_Mid_QPSK_FullRB



n77(3700~3980 MHz)_10 M_OBW_Mid_16QAM_FullRB



n77(3700~3980 MHz)_10 M_OBW_Mid_64QAM_FullRB



n77(3700~3980 MHz)_10 M_OBW_Mid_256QAM_FullRB

