

APPENDIX B: TEST SEQUENCES

1. Test sequence is generated based on below parameters of the DUT:
 - a. Measured maximum power (P_{max})
 - b. Measured Tx power (P_{limit}) to satisfy SAR_desgin_target
 - c. Setup time to make SARRemaining be full
 - d. Do test according to test sequence

2. Test Sequence A Waveform:

Based on the parameters above, Test Sequence A is generated with one transition between high and low Tx powers. Here, high power = P_{max} ; low power = $P_{lim} + 2dB$, and the transition occurs after 150 seconds at high power P_{max} , and last at least 200s. As long as the power enforcement is taking into effective during one 100s/60s time window, the validation test with this defined test sequence A is valid, otherwise, select other radio configuration (band/RSI within the same technology group) having lower P_{limit} for this test. The Test sequence A waveform is shown below:

S.LSI Test Sequence A

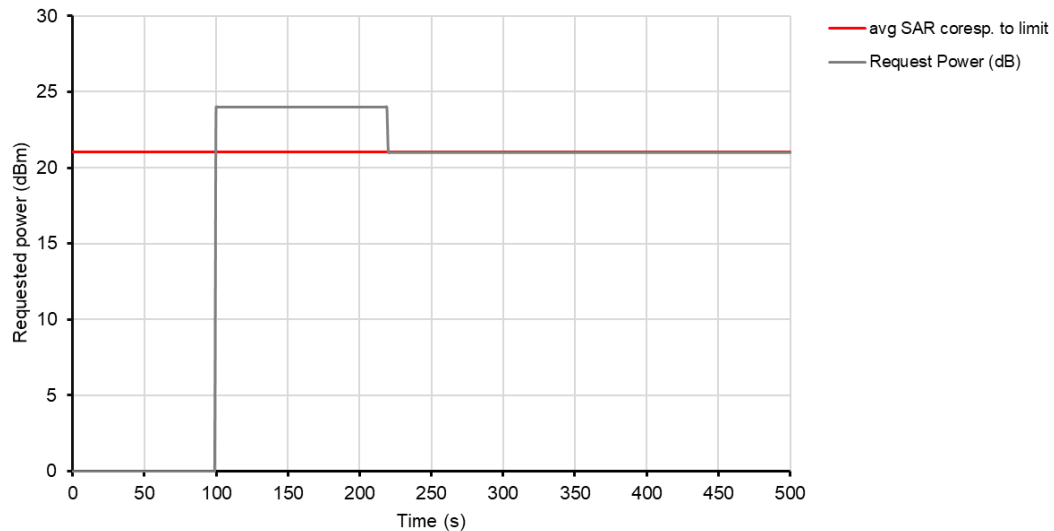


Figure B-1
Test sequence 1 waveform

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3. Test Sequence B Waveform:

Based on the parameters described above, Test Sequence B is generated as described in Table B-1, which contains 360 seconds long sequences, with 10 seconds sleep time, resulting in a total duration of 460 seconds:

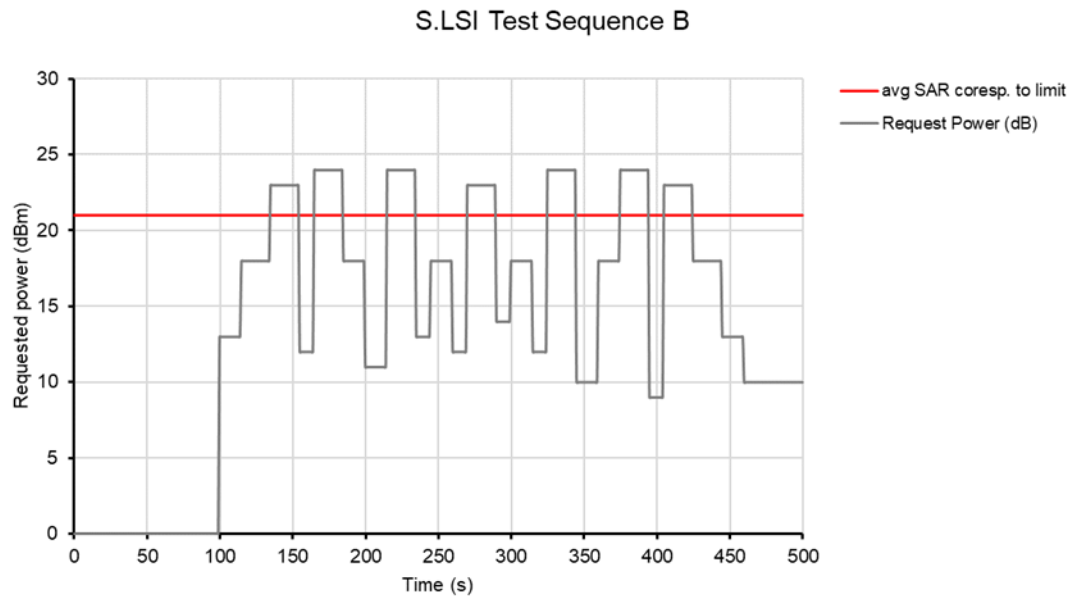
Table B-1
Test Sequence B

Time duration (second)	Power level (dB)
15	Plimit - 5
20	Plimit
20	Plimit + 5
10	Plimit - 6
20	Pmax
15	Plimit
15	Plimit -7
20	Pmax
10	Plimit-5
15	Plimit
10	Plimit-6
20	Plimit + 5
10	Plimit - 4
15	Plimit
10	Plimit - 6
20	Pmax
15	Plimit-8
15	Plimit
20	Pmax
10	Plimit - 9
20	Plimit + 5
20	Plimit
15	Plimit - 5

The Test Sequence 2 waveform is shown in Figure B-1.

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The Test Sequence 2 waveform is shown in Figure B-2.



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