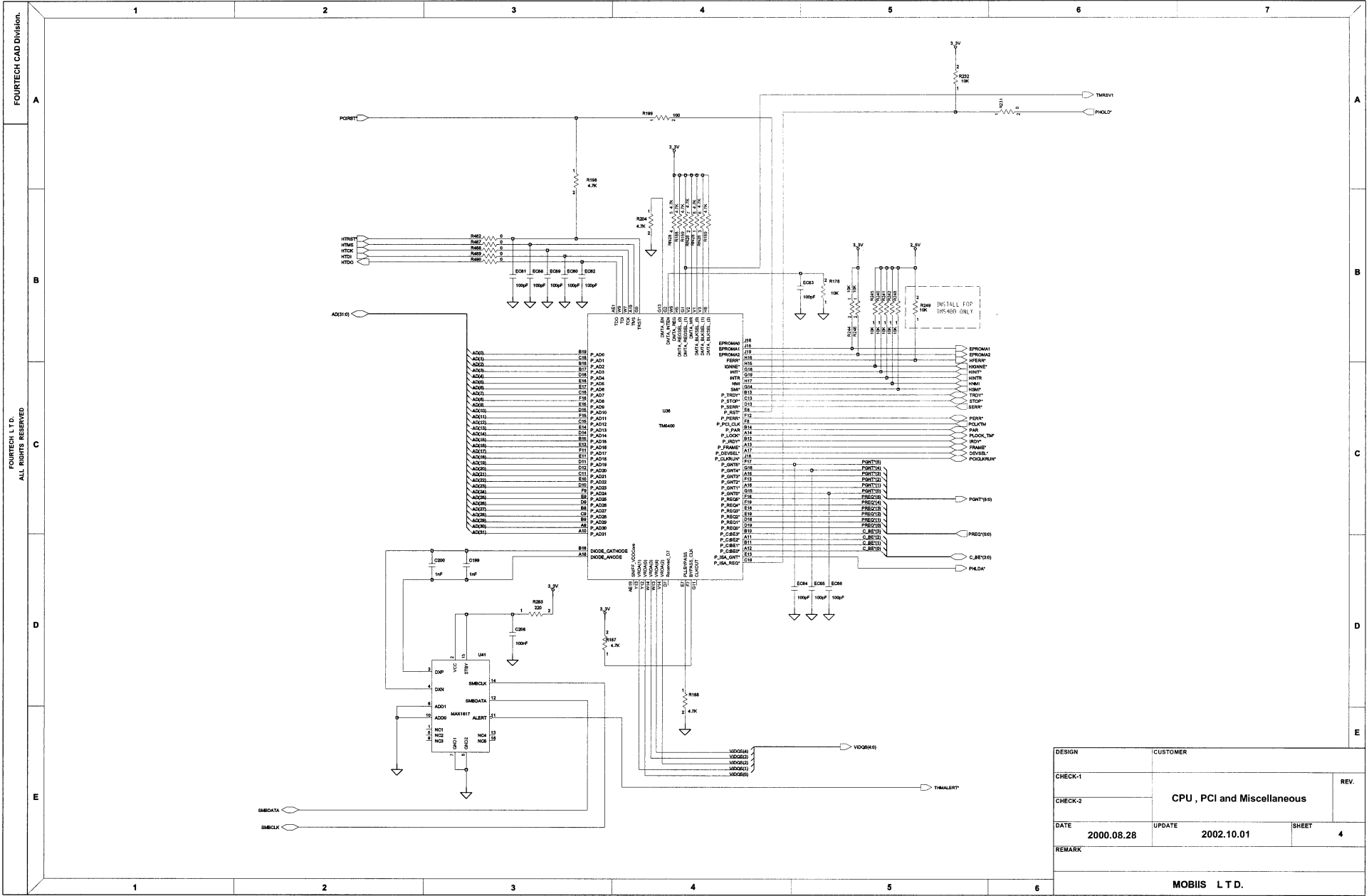


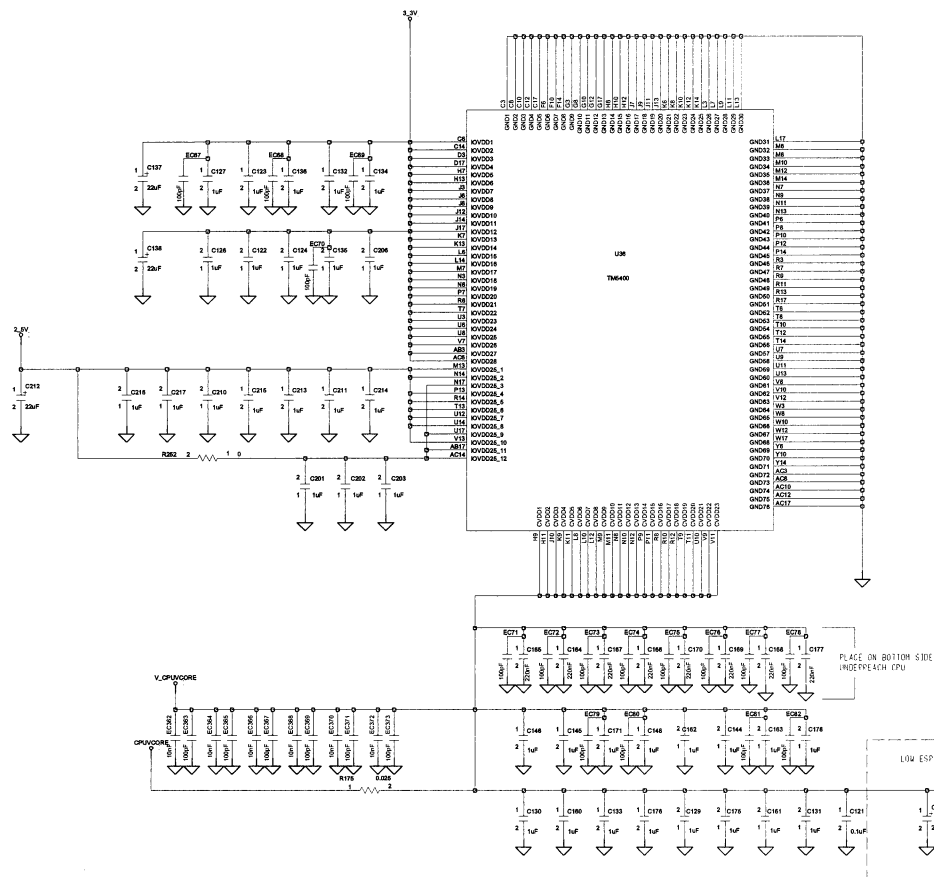


DESIGN		CUSTOMER	
CHECK-1	CPU , PCI and Miscellaneous		REV.
CHECK-2			
DATE	2000.08.28	UPDATE	2002.10.01
		SHEET	4
REMARK			
MOBIS L T D.			

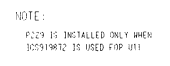
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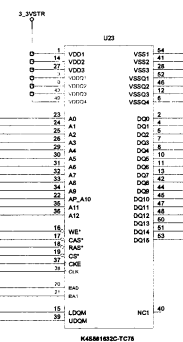
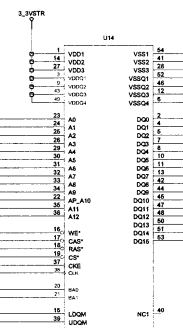
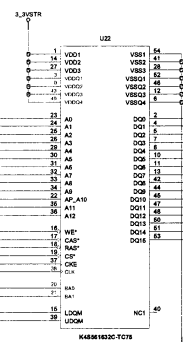
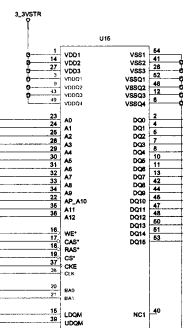
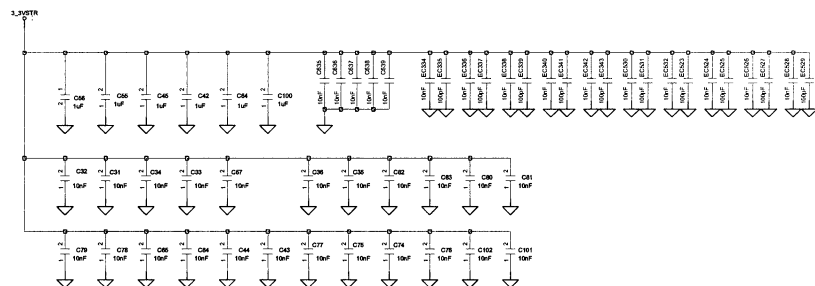
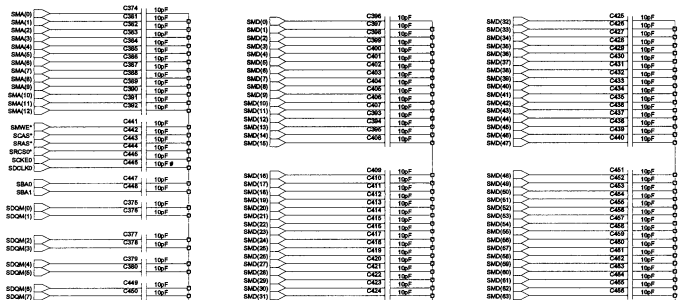


DESIGN		CUSTOMER	
CHECK-1	CPU , POWER and Ground		REV.
CHECK-2			
DATE	2000.08.28	UPDATE	2002.06.12
		SHEET	5
REMARK			
MOBIIS L T D.			

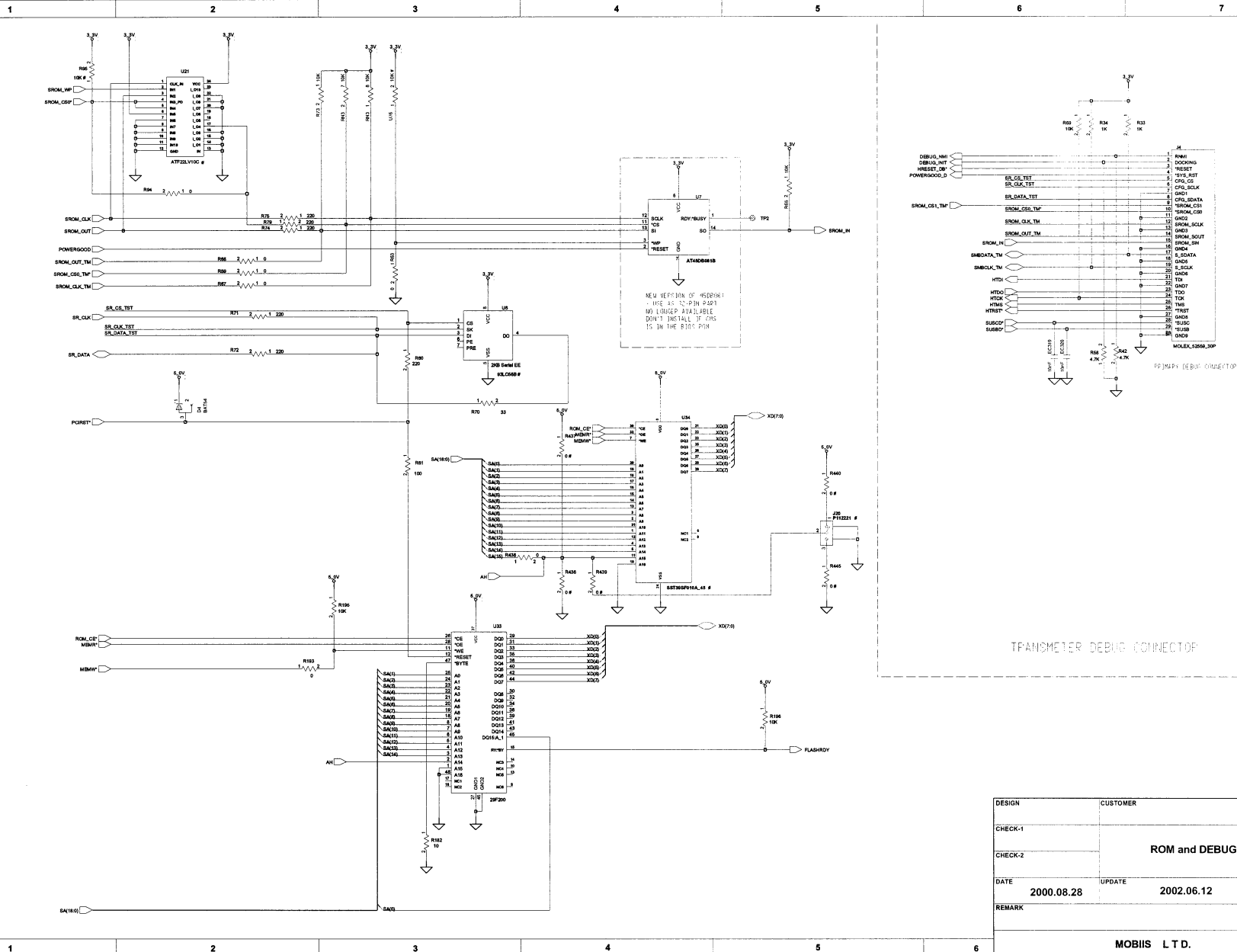


ONLY INSTALL CAPS IF NEEDED TO
REDUCE EDGE RATE AND OVERSHOOT
IN ORDER TO CONTROL EMI

DESIGN	CUSTOMER		
CHECK-1	CLOCK GENERATOR		REV.
CHECK-2			
DATE	UPDATE	SHEET	6
2000.08.28 2002.06.12			
REMARK			
MOBIIS L T D.			



DESIGN		CUSTOMER	
CHECK-1	SDRAM MEMORY		REV.
CHECK-2			
DATE	2000.08.28	UPDATE	2002.06.12
		SHEET	7
REMARK			
MOBIIS L T D.			



DESIGN		CUSTOMER	
CHECK-1	ROM and DEBUG		REV.
CHECK-2			
DATE	2000.08.28	UPDATE	2002.06.12
		SHEET	8
REMARK			
MOBII S L T D.			

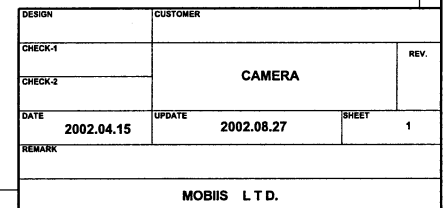
REV.

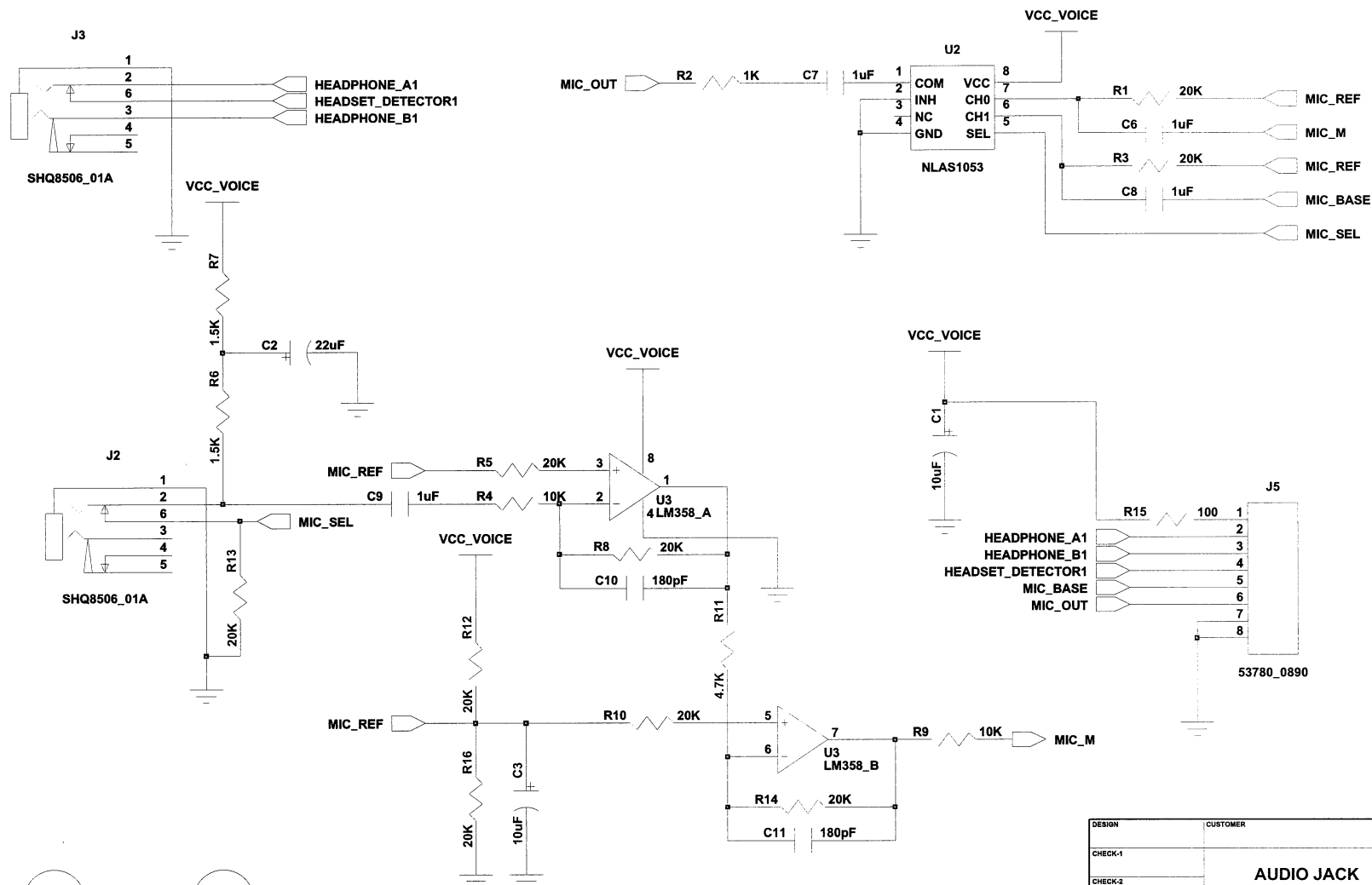
SHEET

8

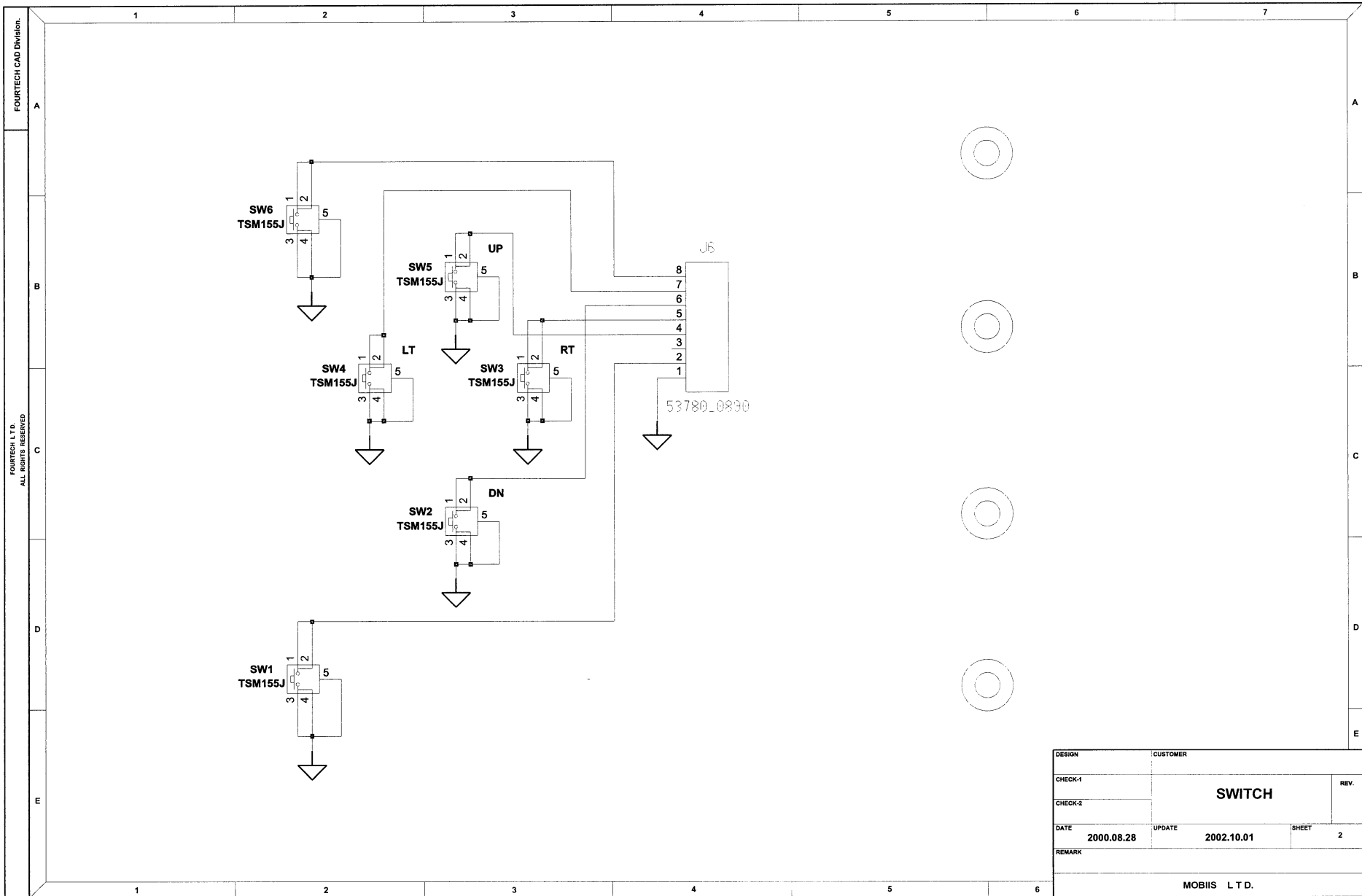


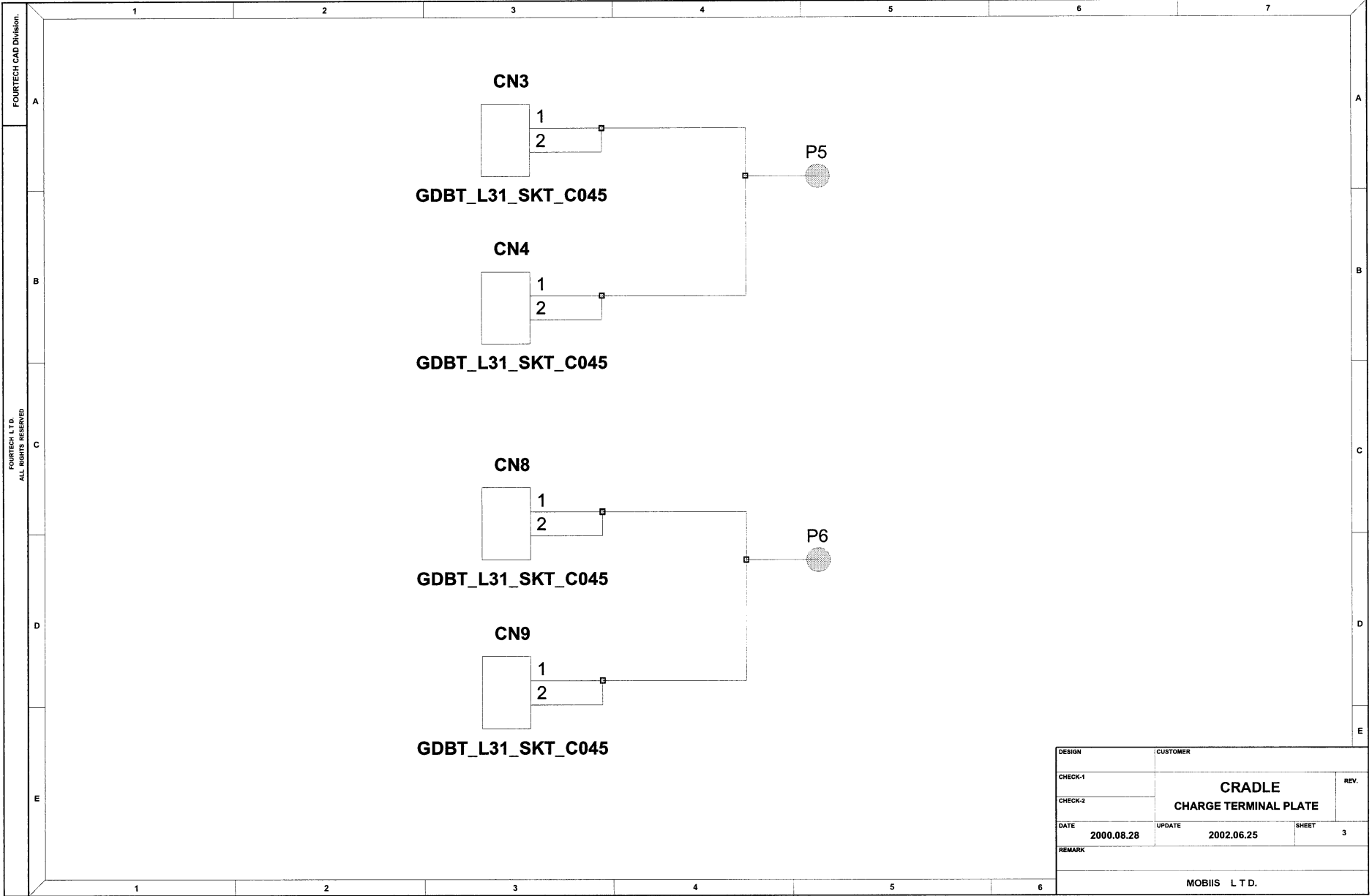
DESIGN	CUSTOMER		
CHECK-1	REV.		
CHECK-2	MEMORY DECODE and PLDS		
DATE	2000.08.28	UPDATE	2002.06.12
REMARK	SHEET 9		
MOBIS L T D.			





DESIGN	CUSTOMER		
CHECK-1	AUDIO JACK		REV.
CHECK-2			
DATE	2000.08.28	UPDATE	2002.06.25
		SHEET	1
REMARK			
MOBIS L T D.			





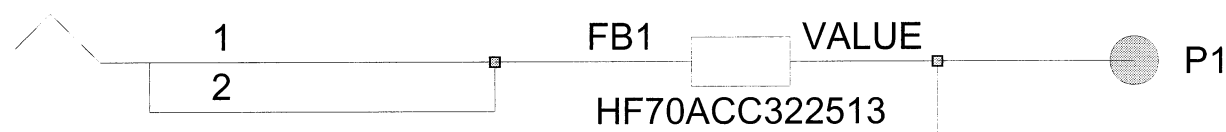
DESIGN	CUSTOMER		
CHECK-1	CRADLE CHARGE TERMINAL PLATE		REV.
CHECK-2			
DATE	2000.08.28	UPDATE	2002.06.25
		SHEET	3
REMARK			
MOBIS L T D.			

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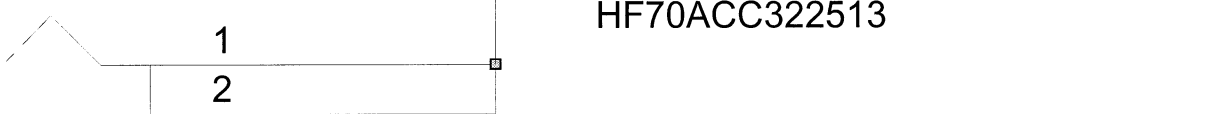
1 2 3 4 5 6 7

CN1



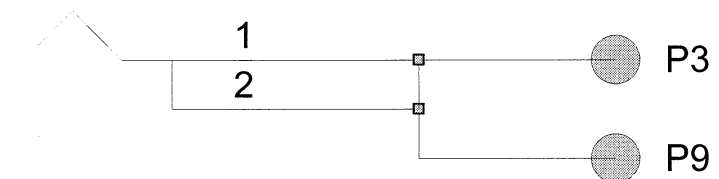
GDBTL_M710B0021

CN2



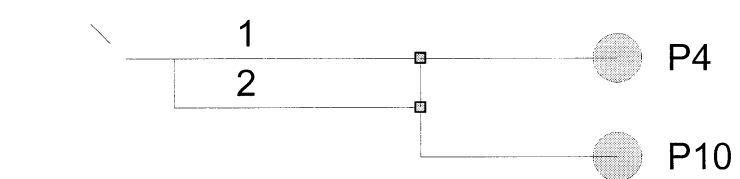
GDBTL_M710B0021

CN6



GDBTL_M710B0021

CN7



GDBTL_M710B0021

DESIGN		CUSTOMER	
CHECK-1	MAIN CHARGE TERMINAL PLATE		REV.
CHECK-2			
DATE	2000.08.28	UPDATE	2002.10.01
		SHEET	4
REMARK			
MOBIS L T D.			

1 2 3 4 5 6

A

B

C

D

E

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