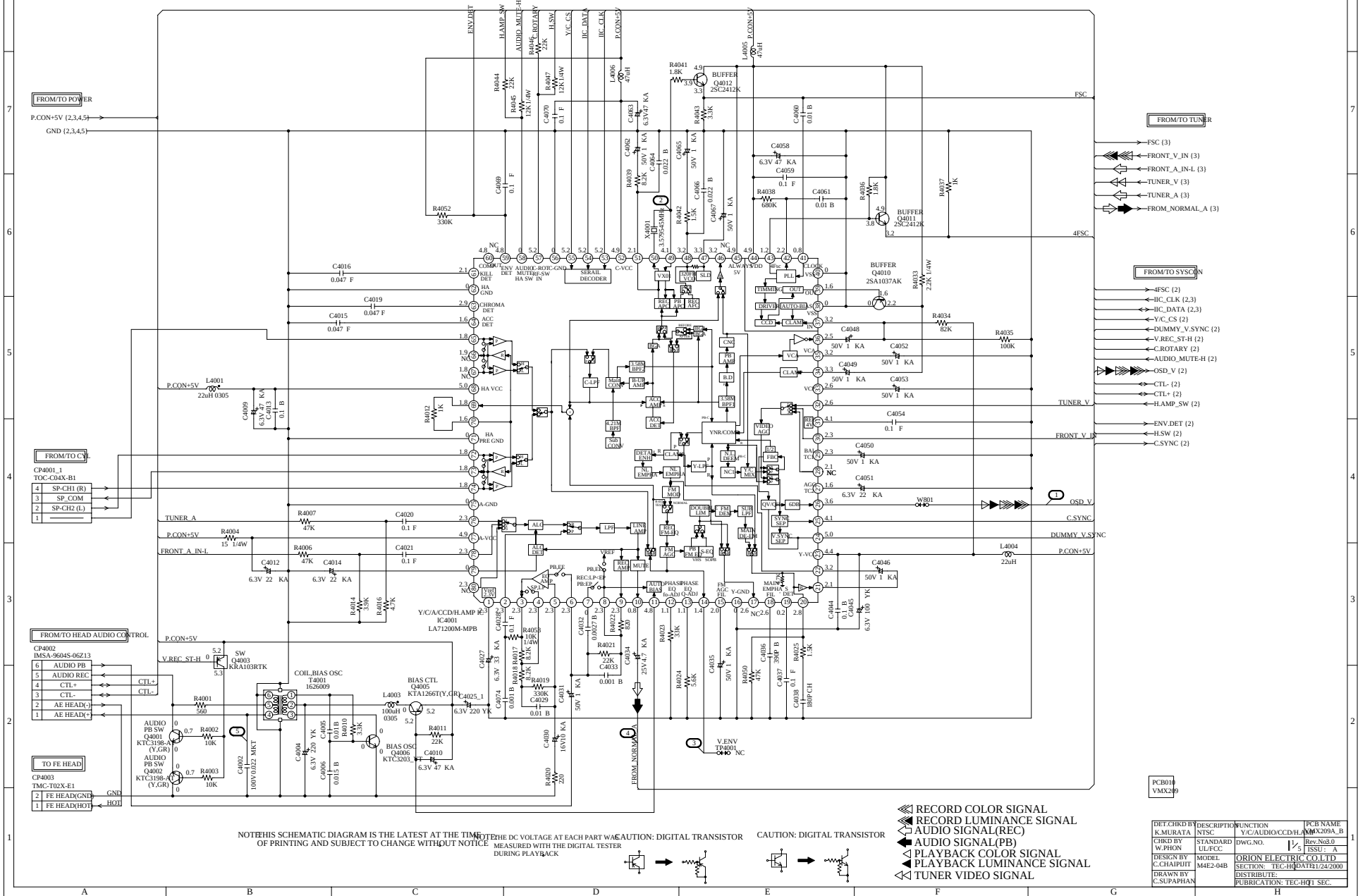


Y/C/AUDIO/CCD/HEAD AMP SCHEMATIC DIAGRAM (SYSCON PCB)



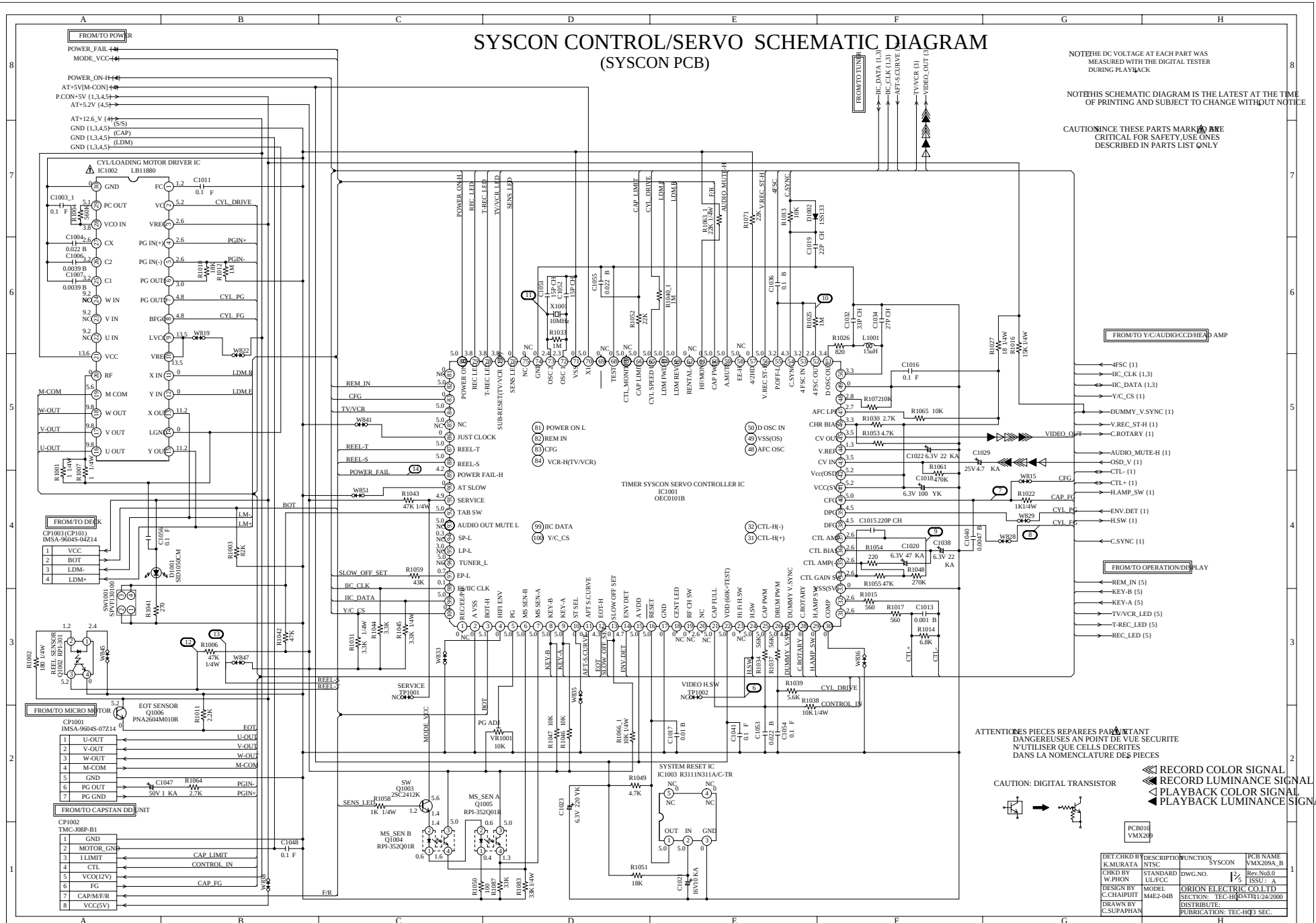
DET. CHKD BY K. MURATA	DESCRIPTION NTSC	FUNCTION Y/C/AUDIO/CCD/HEAD AMP	PCB NAME M4E2-04B
CHKD BY W. PHON	STANDARD UL/FCC	DWG. NO. 1/2	Rev. No. 3.0
DESIGN BY C. CHAIPIIT	MODEL M4E2-04B	ORION ELECTRIC CO., LTD.	TSU: A
DRAWN BY C. SUPAPHAN	SECTION TEC-HQ1/24/2000	DISTRIBUTE	PUBLICATION: TEC-HQ1 SEC.

SYSCON CONTROL/SERVO SCHEMATIC DIAGRAM (SYSCON PCB)

NOTE THE DC VOLTAGE AT EACH PART WAS MEASURED WITH THE DIGITAL TESTER DURING PLAYBACK

NOTE THIS SCHEMATIC DIAGRAM IS THE LATEST AT THE TIME OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE

CAUTION SINCE THESE PARTS MARKED ARE CRITICAL FOR SAFETY, USE ONES DESCRIBED IN PARTS LIST ONLY



ATTENTION: PIÈCES RÉPARÉES PAR UN
DANGEREUSES À UN POINT DE VUE SÉCURITÉ
N'UTILISER QUE CELLES DÉCRITES
DANS LA NOMÉNCLATURE DES PIÈCES

CAUTION: DIGITAL TRANSISTOR

RECORD COLOR SIGNAL
RECORD LUMINANCE SIGNAL
PLAYBACK COLOR SIGNAL
PLAYBACK LUMINANCE SIGNAL

PCB010
VMX209

DET: CHD BY	DESCRIPTION	FUNCTION	PCB NAME
K. MURATA	NTSC	SYSCON	VMX209A_B
CHD BY	STANDARD	DWG. NO.	Rev. No.
W. PHON	ULFPC	1/2	3.0
DESIGN BY	MODEL	SECTION	DATE
C. CHAIPIIT	M4E2-04B	TEC-HQ3 SEC.	1/24/2000
DRAWN BY	DISTRIBUTE	PUBLICATION	SEC.
C. SUPAPHAN		TEC-HQ3 SEC.	

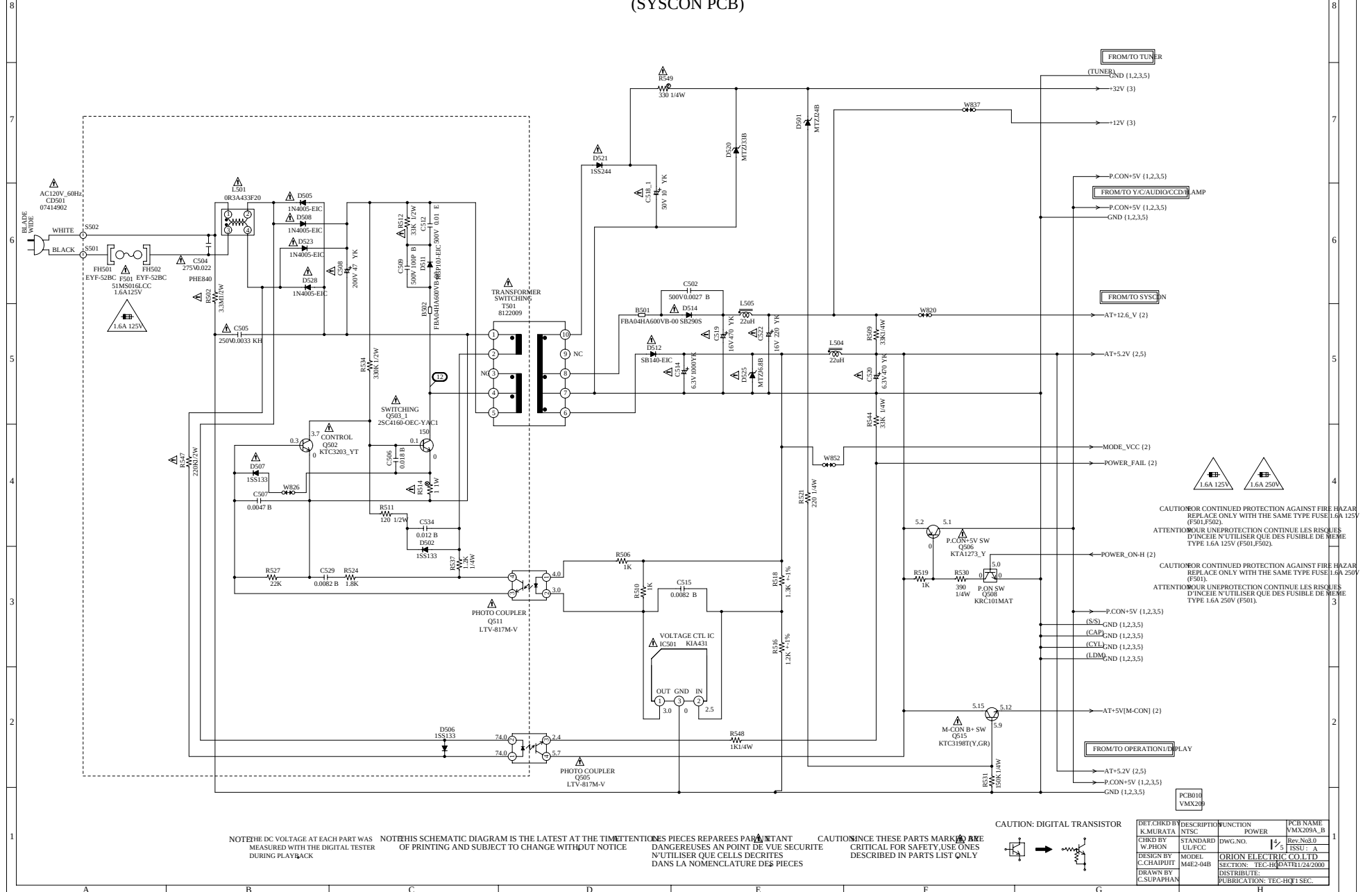
NOTE: THE DC VOLTAGE AT EACH PART MEASURED WITH THE DIGITAL TESTER DURING PLAYBACK. OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE. CRITICAL FOR SAFETY, USE ONES DESCRIBED IN PARTS LIST ONLY.

ATTENTION: LES PIÈCES RÉPARÉES PAR UN
DANGEREUSES AN POINT DE VUE SECURITE
N'UTILISER QUE CELLES DECRITES
DANS LA NOMENCLATURE DES PIÈCES

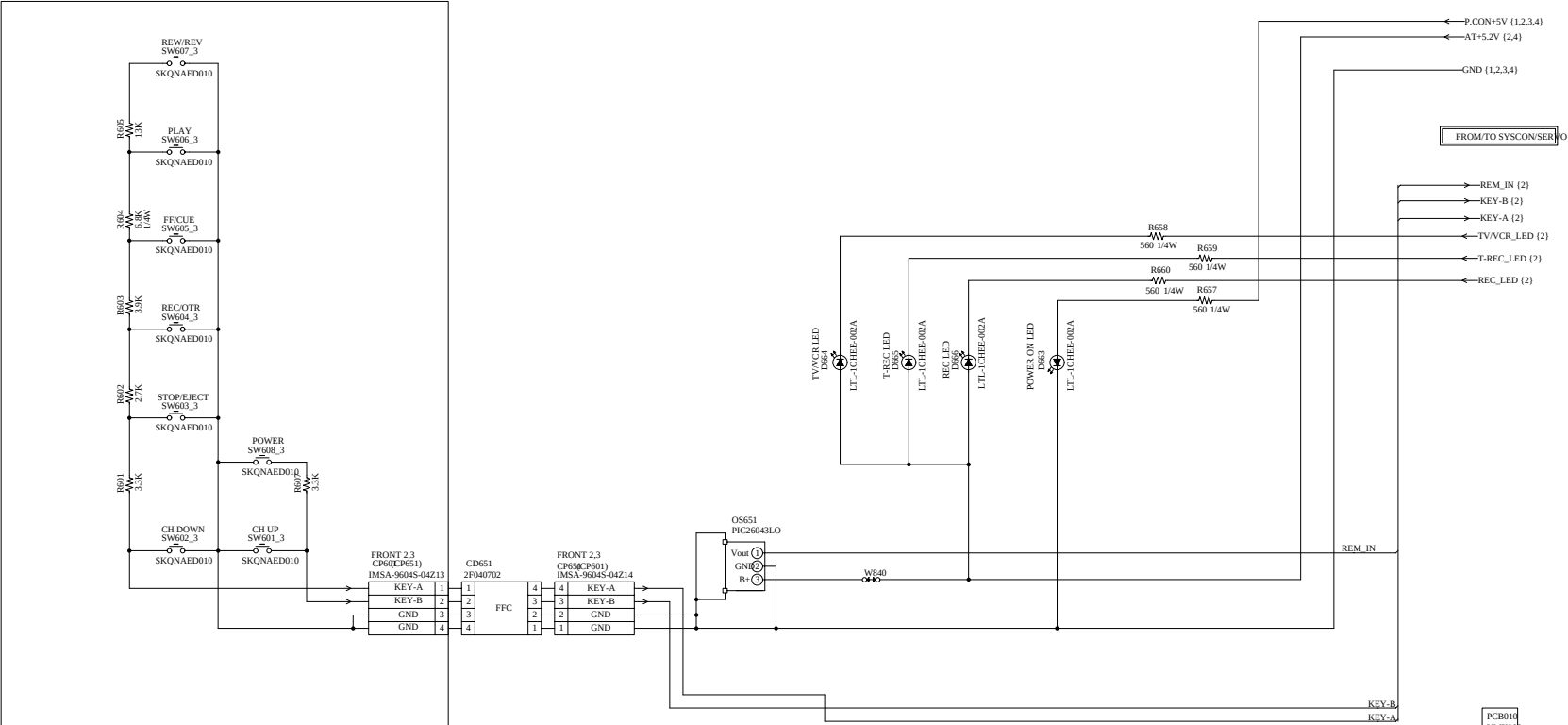
RECORD LUMINANCE SIGNAL (Y)
 RECORD COLOR SIGNAL (C)
 AUDIO SIGNAL (REC)
 AUDIO SIGNAL (PB)
 TUNER VIDEO SIGNAL
 PLAYBACK LUMINANCE SIGNAL (Y)
 PLAYBACK COLOR SIGNAL (C)

DET.CHKD BY K.MURATA	DESCRIPTION NTSC	FUNCTION TUNER	PCB NAME VMX209A
CHKD BY W.PHON	STANDARD UL/FCC	DWG.NO. 135	Rev.No3.0 ISSU: A
DESIGNED BY C.CHAIPUIT	MODEL M4E2-04B	ORION ELECTRIC CO.LTD SECTION: TEC-HQ DATA1/24/200	
DRAWN BY C.SAPAPHAN	DISTRIBUTE: PUBLICATION: TEC-HQ 13 SEC		

POWER SCHEMATIC DIAGRAM (SYSCON PCB)



OPERATION/DISPLAY SCHEMATIC DIAGRAM
(SYSCON PCB)



NOTE: THE DC VOLTAGE AT EACH PART WAS
MEASURED WITH THE DIGITAL TESTER
DURING PLAYBACK.

NOTE: THIS SCHEMATIC DIAGRAM IS THE LATEST AT THE TIME
OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE

PCB010 VMX20	DET. CHKD BY K. MURATA	DESCRIPTION NTSC	FUNCTION OPERATION/DISPLAY	P.C.B NAME AX-209A_B
	CHKD BY W. PHON	STANDARD UL FCC	DWG. NO. 1/2	Rev. No. 3.0
	DESIGN BY C. CHAIPIIT	MODEL M4E2-04B	ORION ELECTRIC CO., LTD.	ISSU: A
	DRAWN BY C. SUPAPHAN	SECTION: TEC-HQ1/24/2000	DISTRIBUTE:	
			PUBLICATION: TEC-HQ1 SEC.	