

## **“Housed” Tag-it Reader System**

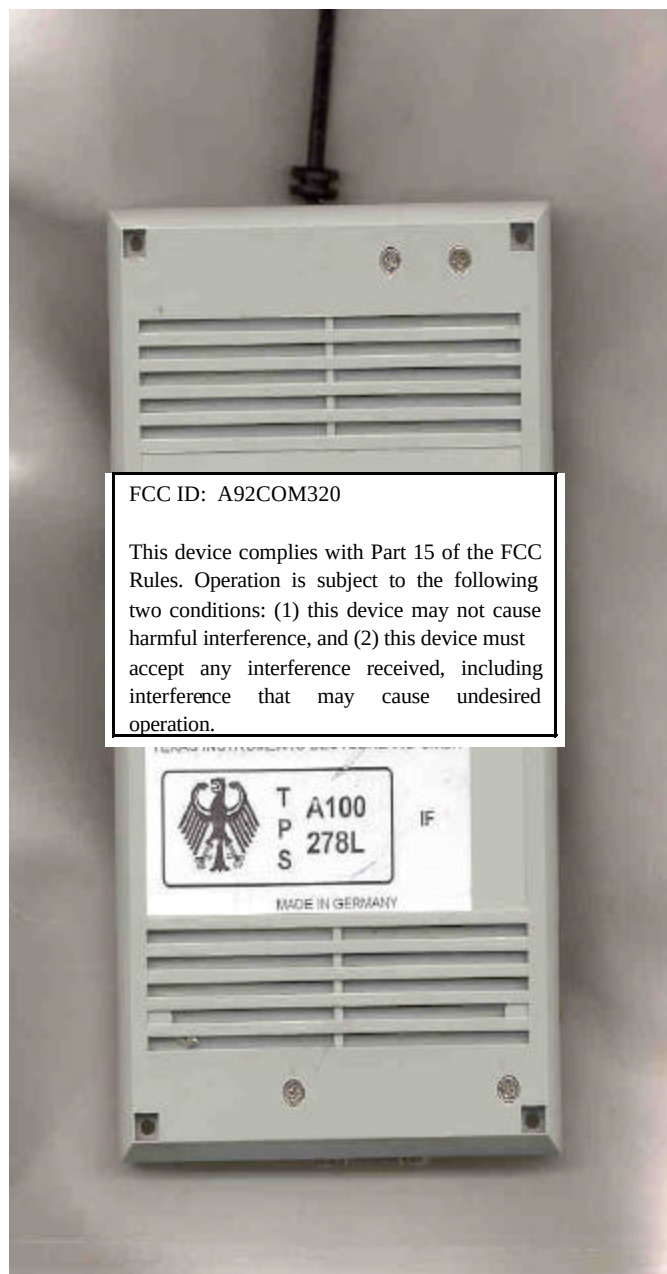
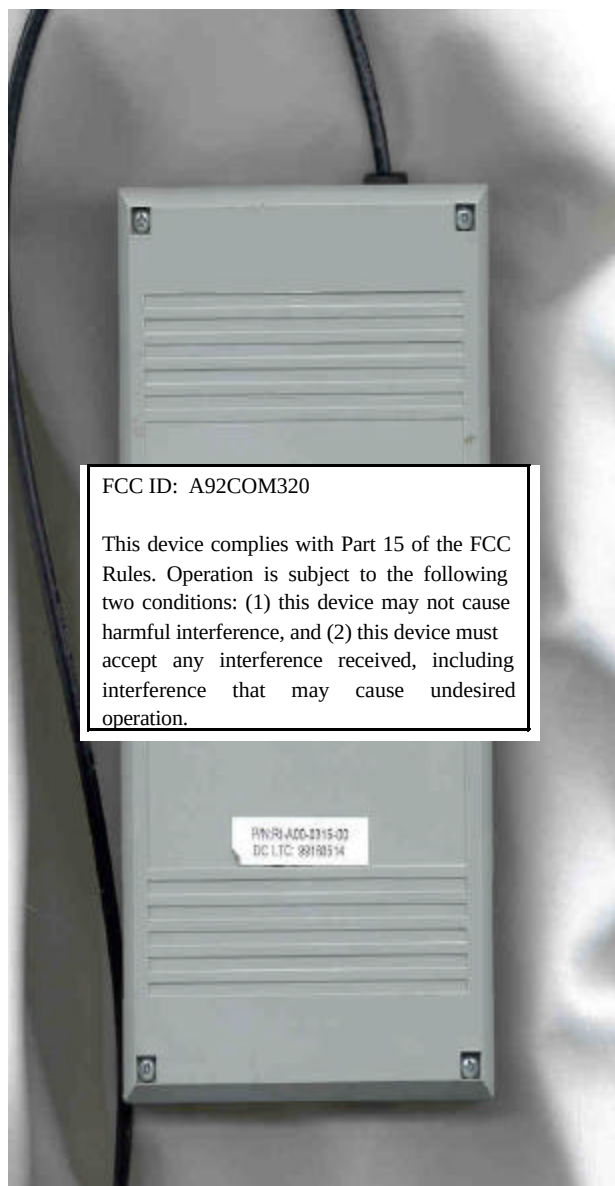
(front side shown, includes Reader & Antenna)

(Note: Firmware 2.0.0 & FPGA 1.2.0 revisions were tested but the revision labeling in all pictures (Firmware 1.98 & FPGA 1.1.0) was not updated.)

**RI-K01-320A**



## FCC Labeling of “House” Tag-it Reader System (back side shown) RI-K01-320A



## Inside Antenna Enclosure RI-ANT-320A (Including Antenna Matching Network RI-ANT-M50A)

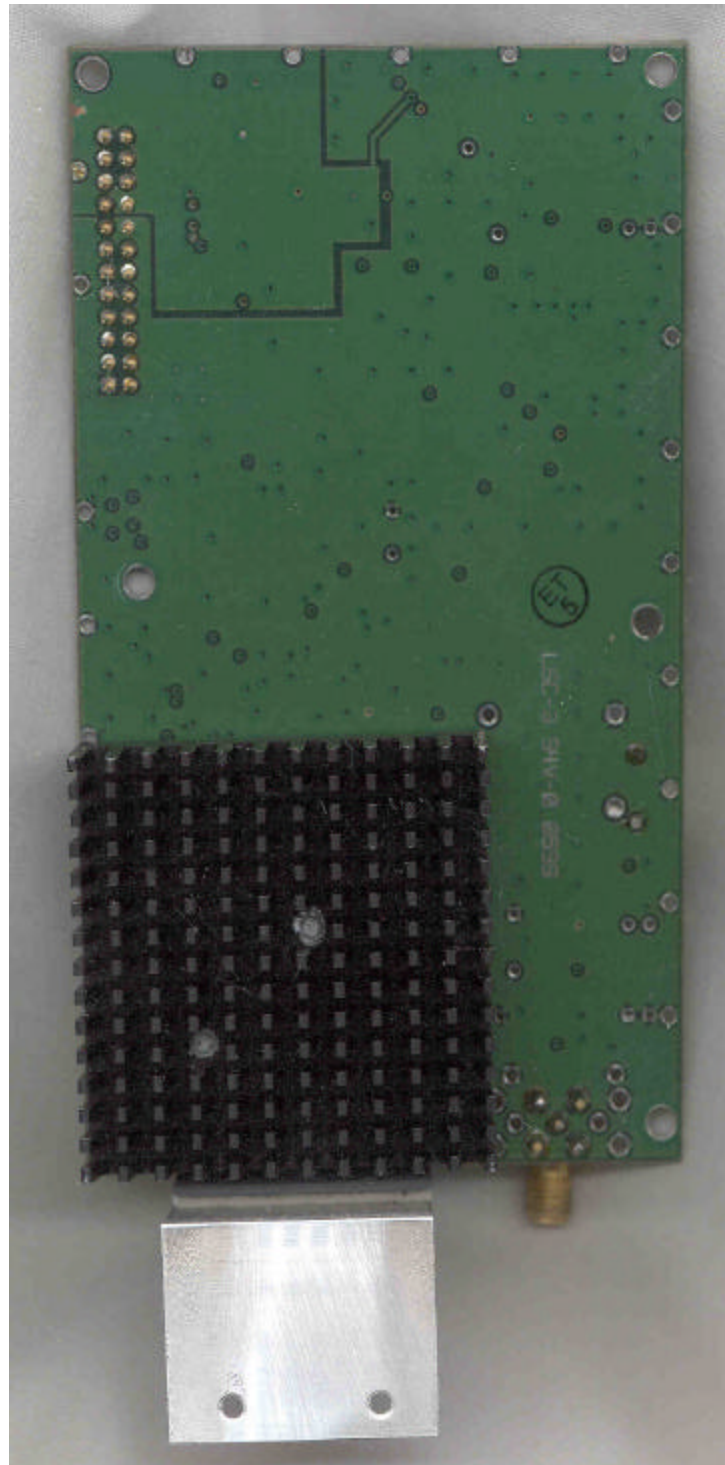


**Tag-it Reader System Inside its Housing**  
(Analog RFM – RI-RFM-320A, shown face up with heat sink )

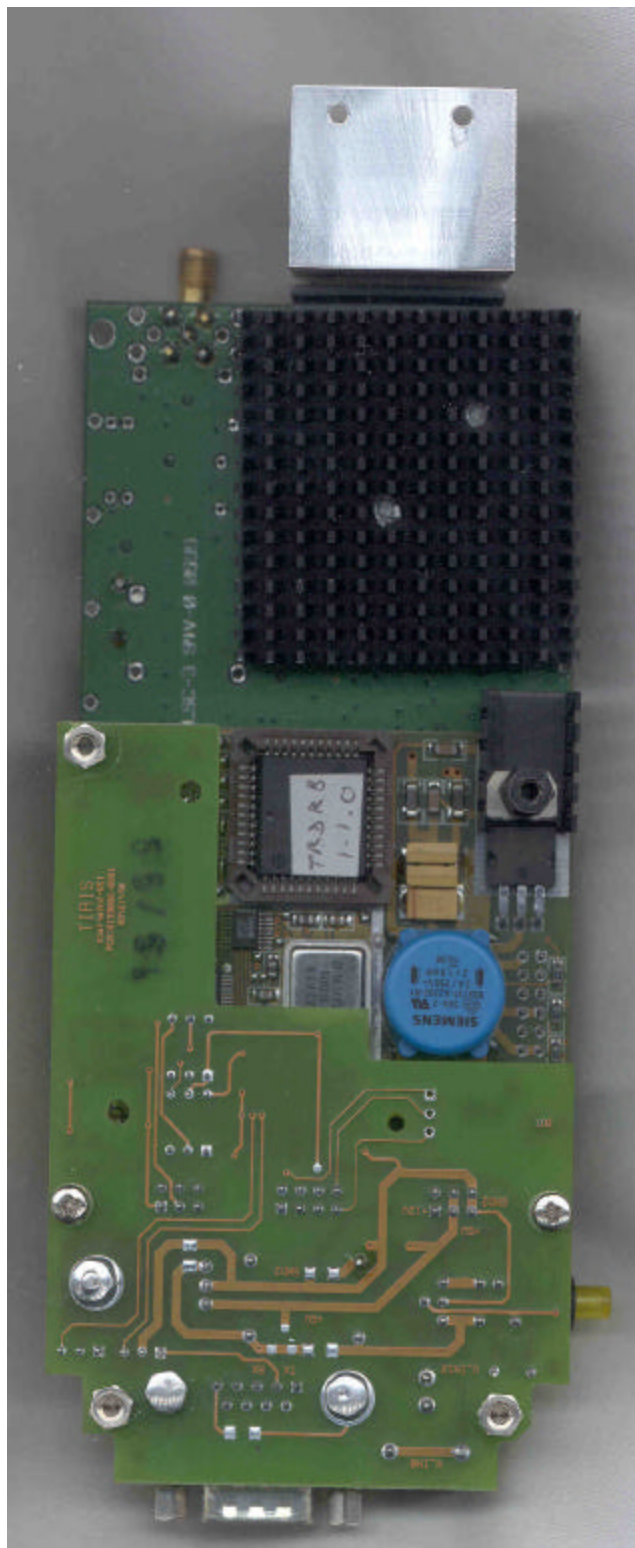
**RI-K01-320A**



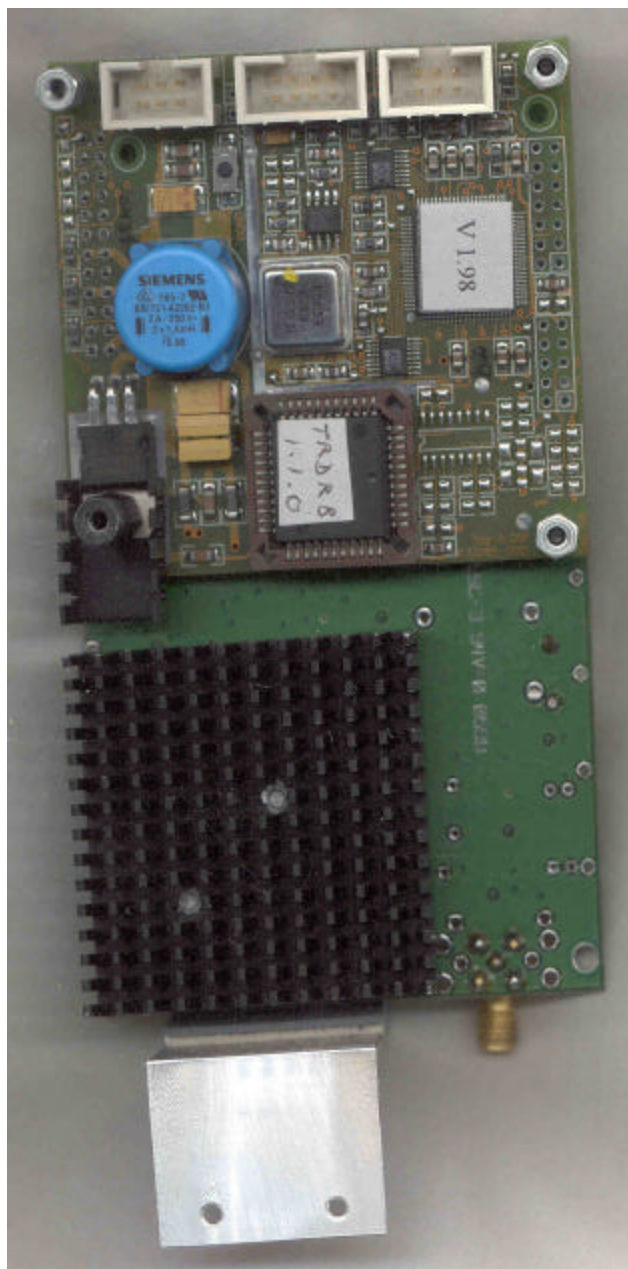
**Analog RFM – RI-RFM-320A with heatsink  
(back side shown )**



**Reader Assembly RI-R00-321 with heat sink** (back side shown)  
(includes Analog RFM - RI-RFM-320A (back side),  
RS-232 Interface RI-R00-232A (back side)  
& Partial of Digital CPU RI-CTL-0320)



**Reader Assembly RI-R00-320A with heat sink** (front side shown)  
(includes Digital CPU RI-CTL-0320 (front side)  
& Partial of Analog RFM - RI-RFM-320A (back side),  
RS-232 Interface RI-R00-232A removed)



**Digital CPU Module RI-CTL-0320-00 (back side shown)  
& Partial of RS-232 Interface RI-R00-232A**

