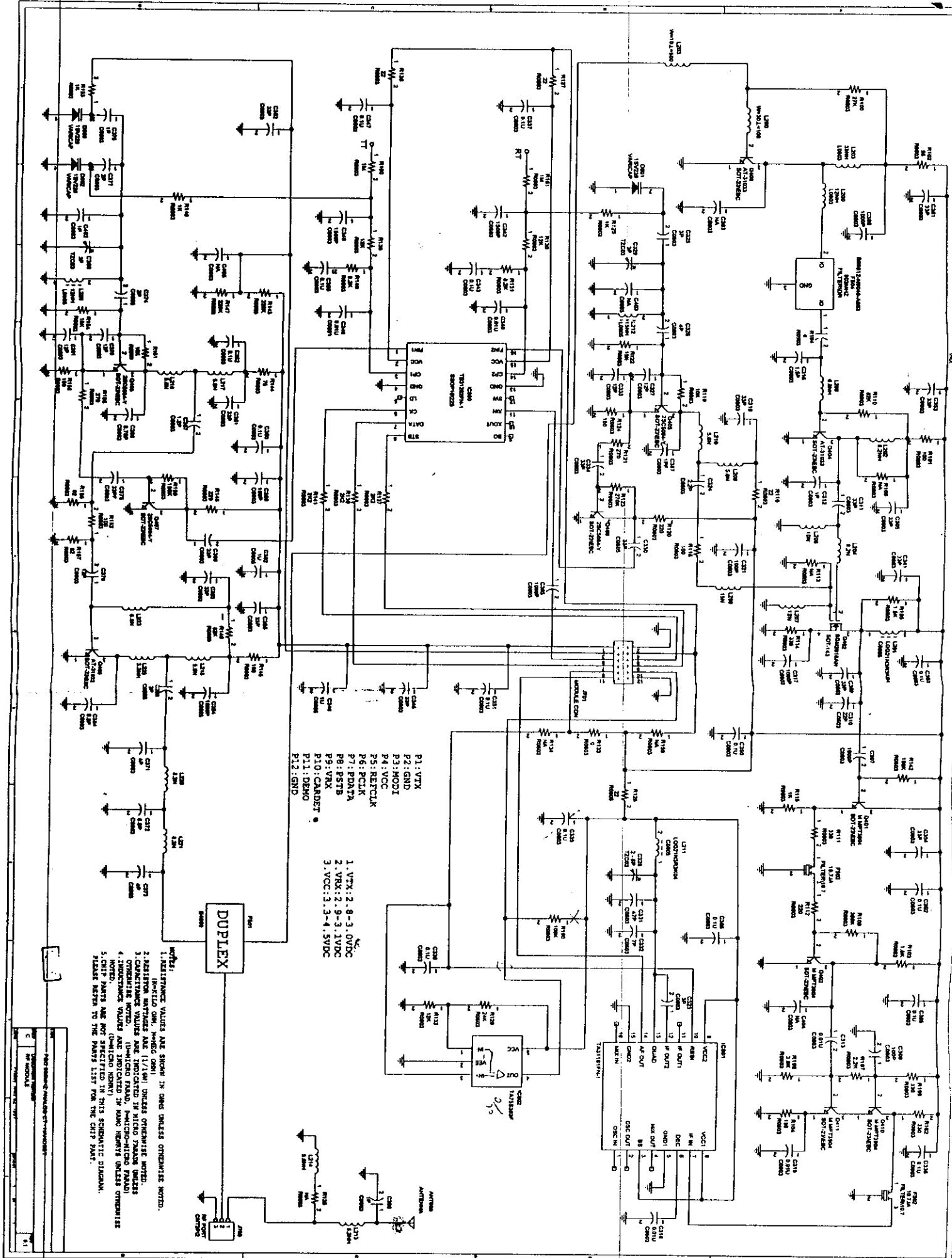


EXHIBIT D

Circuit Diagram



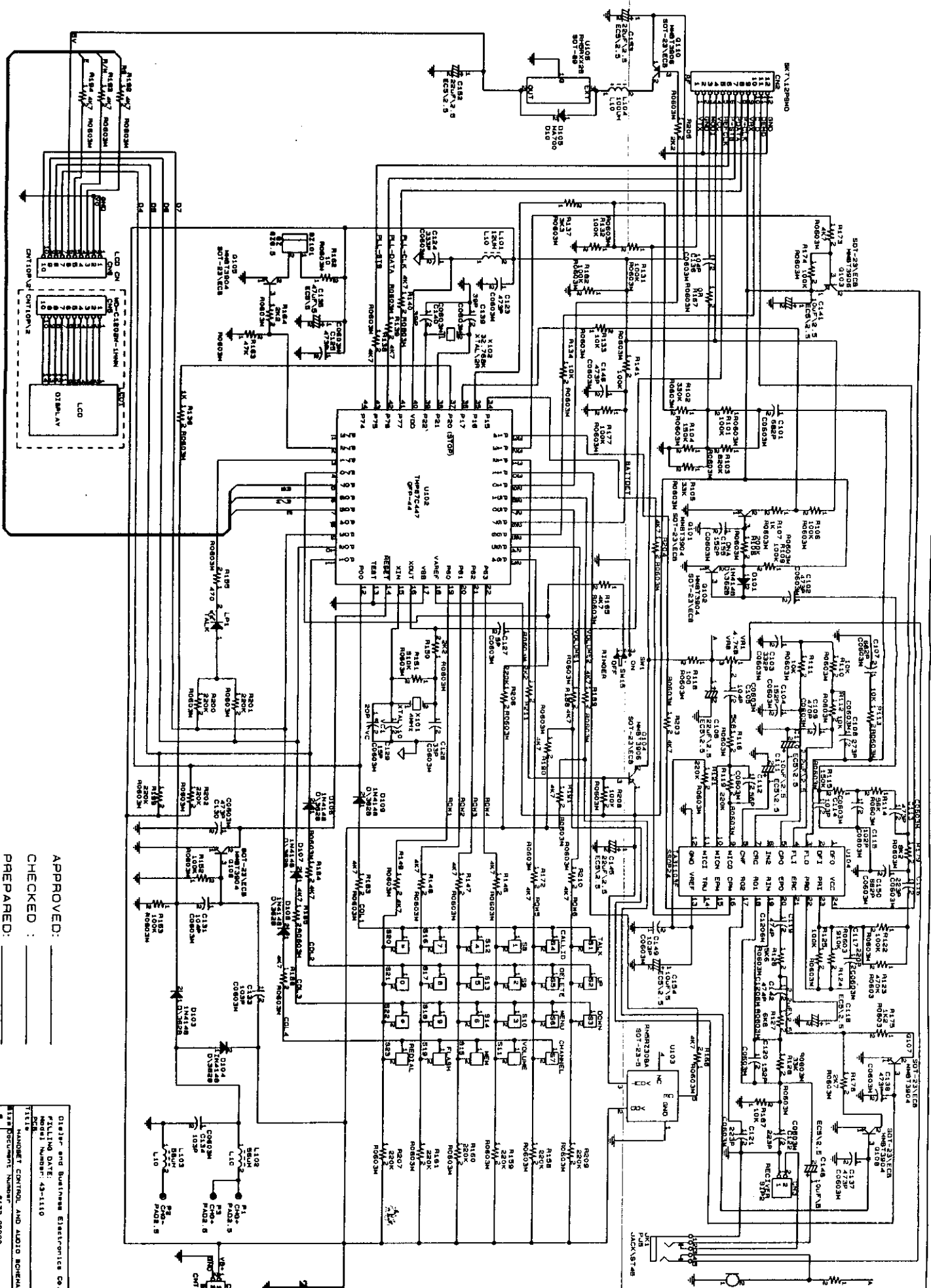
- P1: VTX
- P2: GND
- P3: MODI
- P4: VCC
- P5: REFCLK
- P6: PCLK
- P7: PDATA
- P8: PSTB
- P9: VRX
- P10: CARDET
- P11: DEMO
- P12: GND

- 1. VTX: 2.8-3.0VDC
- 2. VRX: 2.9-3.1VDC
- 3. VCC: 3.3-4.5VDC

DUPLX

NOTES:
 1. RESISTANCE VALUES ARE SHOWN IN OHMS UNLESS OTHERWISE NOTED.
 2. INDUCTOR VALUES ARE (1/100) UNLESS OTHERWISE NOTED.
 3. CAPACITOR VALUES ARE INDICATED IN NANO FARAD (NANO FARAD)
 4. CAPACITOR VALUES INDICATED IN MICRO FARAD (MICRO FARAD)
 5. CHIP PARTS ARE NOT SPECIFIED IN THIS SCHEMATIC DIAGRAM.
 PLEASE REFER TO THE PARTS LIST FOR THE CHIP PART.

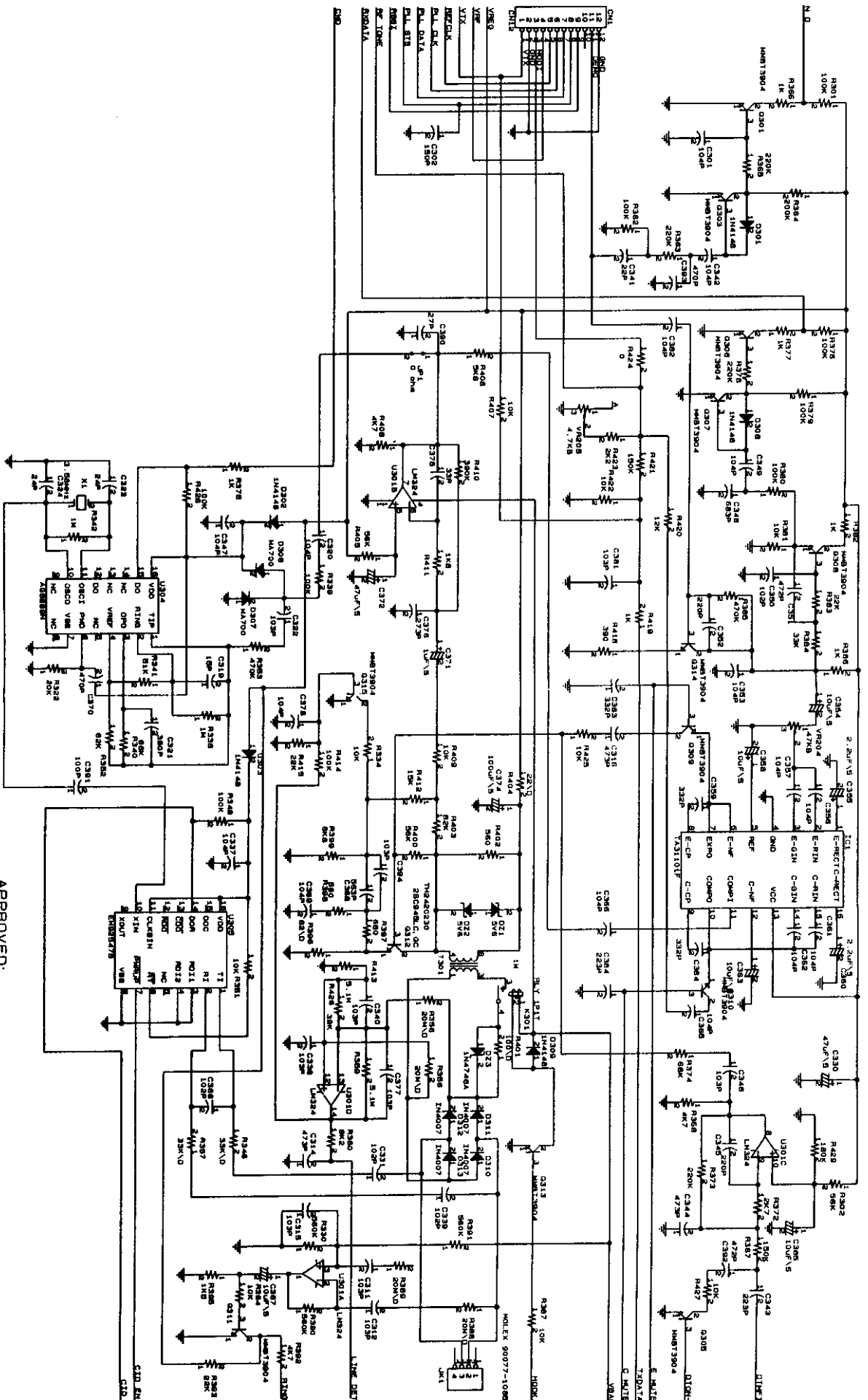
NO.	PART NUMBER	QUANTITY	REMARKS
1	TA1179A-1	1	IC
2	TA1179A-1	1	IC
3	TA1179A-1	1	IC
4	TA1179A-1	1	IC
5	TA1179A-1	1	IC
6	TA1179A-1	1	IC
7	TA1179A-1	1	IC
8	TA1179A-1	1	IC
9	TA1179A-1	1	IC
10	TA1179A-1	1	IC
11	TA1179A-1	1	IC
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98	TA1179A-1	1	IC
99	TA1179A-1	1	IC
100	TA1179A-1	1	IC



APPROVED: _____
CHECKED: _____
PREPARED: _____

Design and Business Electronics Co.
FILLING DATE: _____
Model Number: 43-1110
HANDSET CONTROL AND AUDIO SCHEMATIC
SHEET NUMBER: 6133-0000
DATE: JANUARY 21, 1958

DB-8132 BASE UNIT AUDIO & LINE INTERFACE SCHEMATIC



APPROVED:

CHECKED:

PREPARED:

Order and Business Electronics Co.	
FILLING DATE: 10-SEP-80	
Model Number: DB-8110	
Revision: 1.0	
BASE UNIT LINE INTERFACE	
SHEET NUMBER: 1 OF 1	
DATE: 10-SEP-80	

PREPARED:

EXHIBIT E

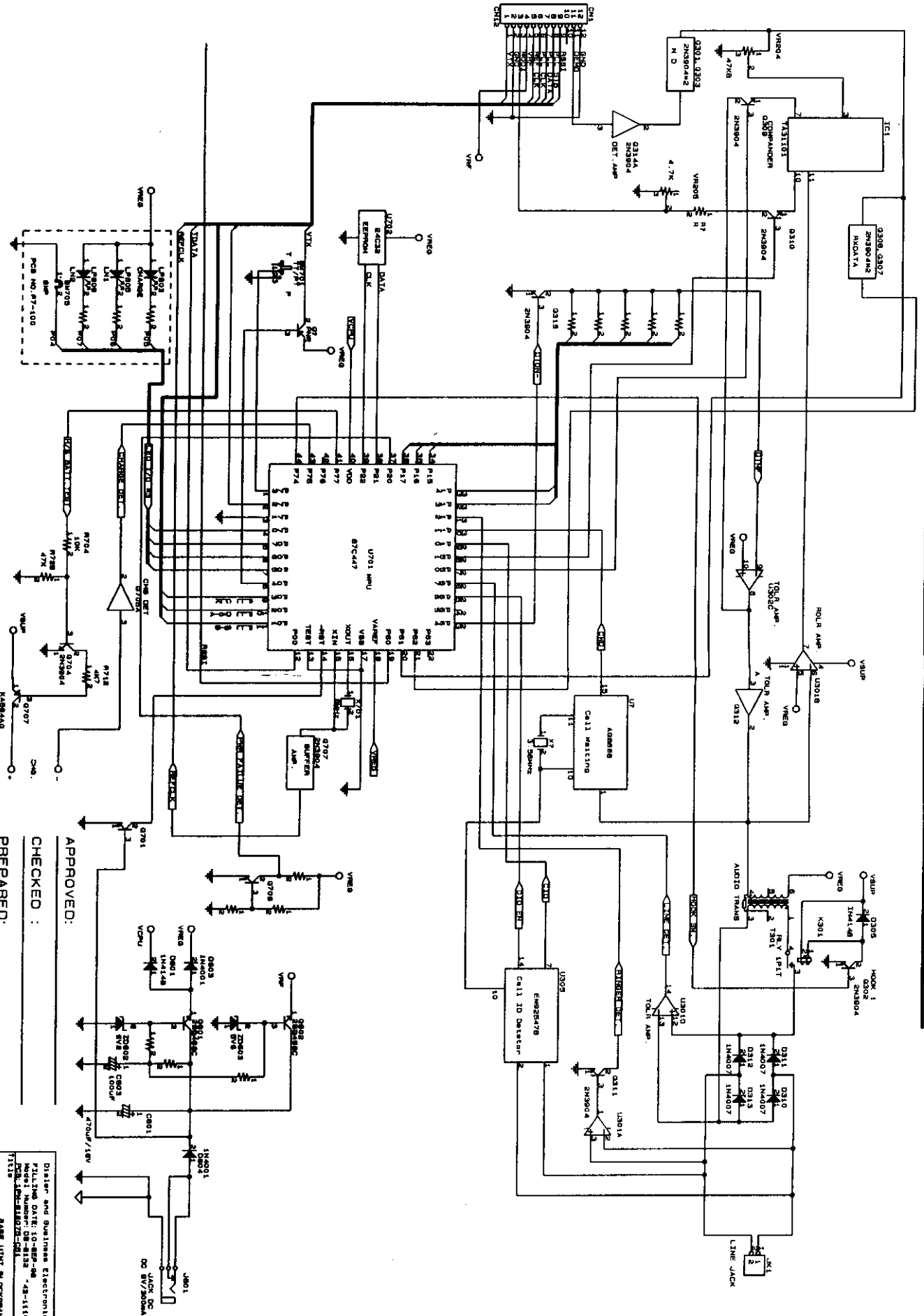
Block Diagram



1 N	17 PND	35 R-MOTE
2 BUI	18 VAREE	34 C-MATE
3 TR-LED	19 COL	33 TR-DATA
4 RTR-SM	20 COL2	36 R-DATA
5 LCO-SM	21 COL3	37 CHD-DCT
6 LCO-PHM	22 COL4	38 XTIN
7 LCO-E	23 COL5	39 XTOUT
8	24 COL8	40 VNO
9	25 COL1	41 PUL-CLK
10 COL4	26 BAT-DCT	42 PUL-DATA
11 COL2	27 VOLUME1	43 PUL-STB
12 COL1	28 VOLUME0	44 N
13 TEST	29 LCO-PHM	
14 RESET	30 VAP	
15 TEST	31 VTX	
16 ADUT	32 VNR	

KEY PAD

DB-8132 BASE UNIT AUDIO BLOCK DAIGRAM



APPROVED:

CHECKED :

PREPARED: