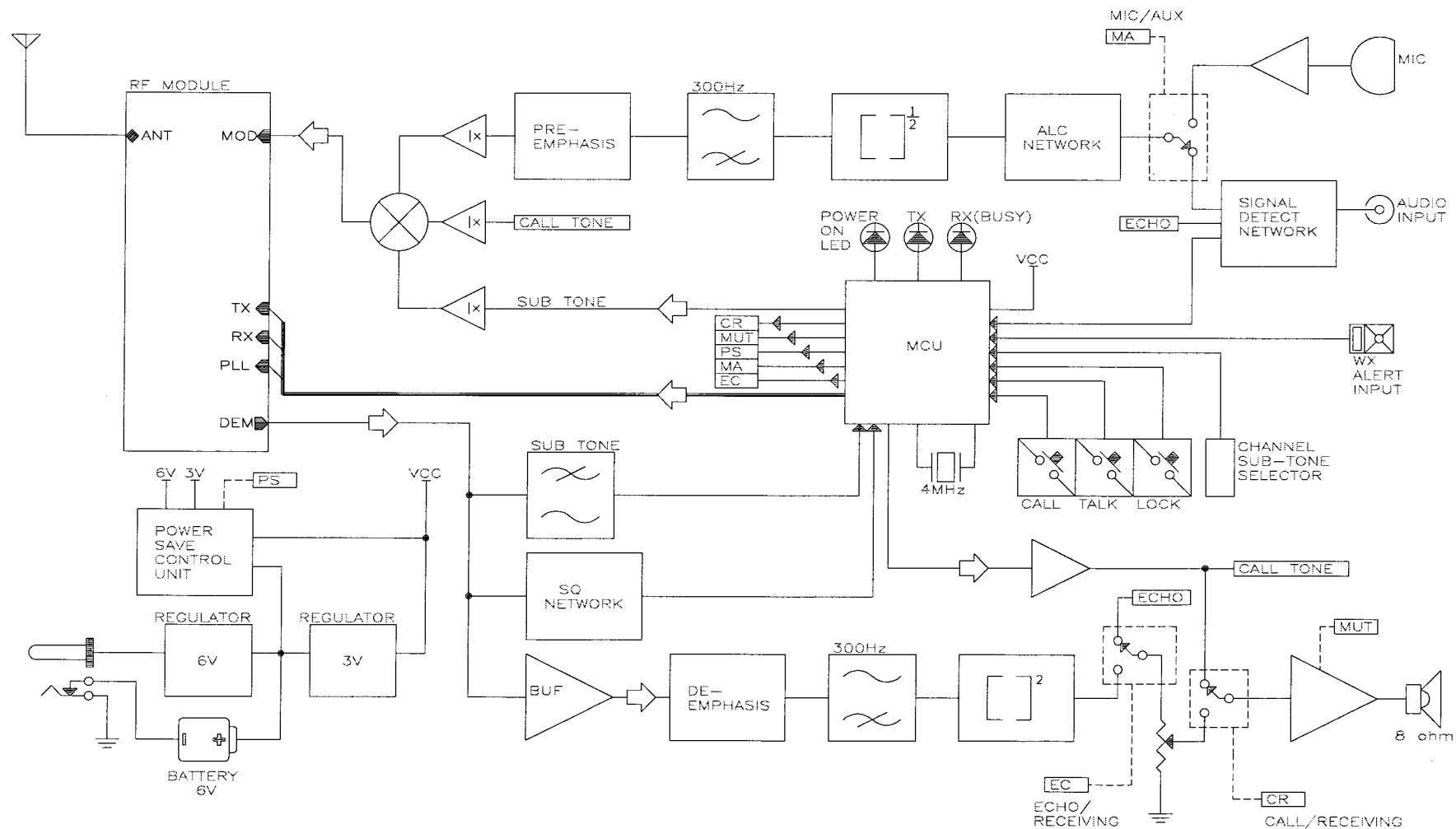


BLOCK DIAGRAM OF 43-3102 900MHz INTERCOM SYSTEM

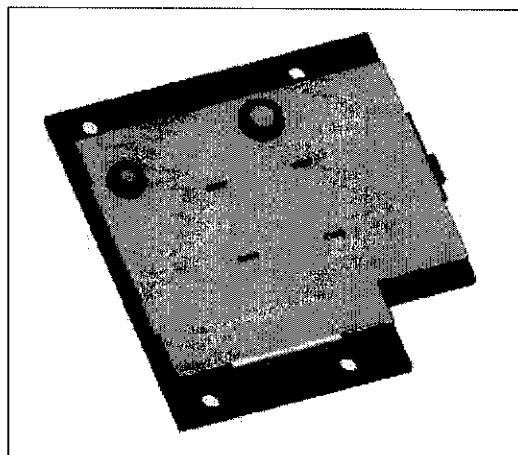


Description

The F05926A is a 900MHz RF module designed for transceiver for the simplex telecommunication.

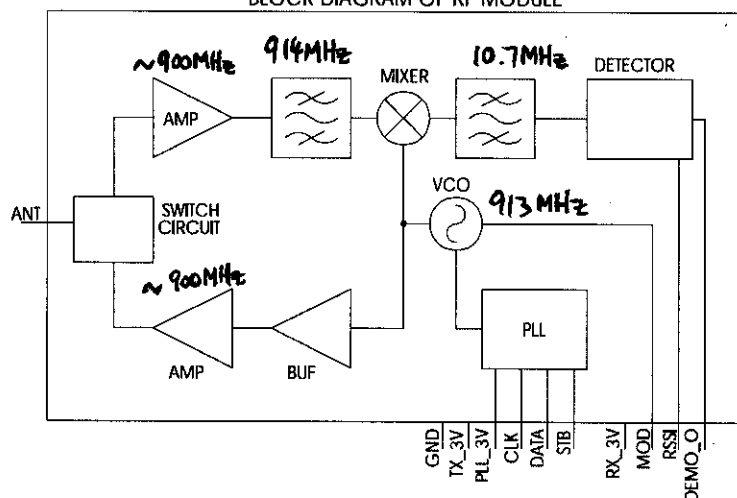
Features

- Built-in LNA
- Built-in MIXER and detector
- Built-in VCO
- Built-in power amplifier
- Low volt operation 2.7-5.5 volts
- High receiving sensitivity
- PLL control



Block Diagram

BLOCK DIAGRAM OF RF MODULE



PIN Assignment

Pin No.	Pin Name	Function
1	ANT	Antenna terminal
2	GND	Ground
3	TX_3V	Vcc terminal for Transmit
4	PLL_3V	Vcc terminal for Built-in PLL
5	CLK	PLL control pin clock
6	DATA	PLL control pin data
7	STB	PLL control pin signal enable
8	RX_3V	Vcc terminal for Receive
9	MOD	Audio input terminal for transmit
10	RSSI	RSSI output terminal
11	DEMO_O	Audio output terminal for receiving