



Integrated Display Technology Ltd.

萬 威 科 技 有 限 公 司

Technical descriptions of 63-1026

Receiver circuit

The receiver employs superregeneration technique. The LC circuitry, L2, C2, C3 and C4, provides channel selection and the resonant frequency is set to 433.92MHz. By extracting the emitter output of the transistor, Q2, a regenerated signal is obtained. The following circuitry, C8 and R10, acts as a low pass filter which extracts the envelope of the regenerated signal. Demodulated signal is obtained from the output port of the operational amplifier, pin 1 of TL062C. The other part of the TL062C composes of a Smith-trigger circuitry that converts the demodulated signal into pulses that can be read by computer.

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