

EXHIBIT F-1

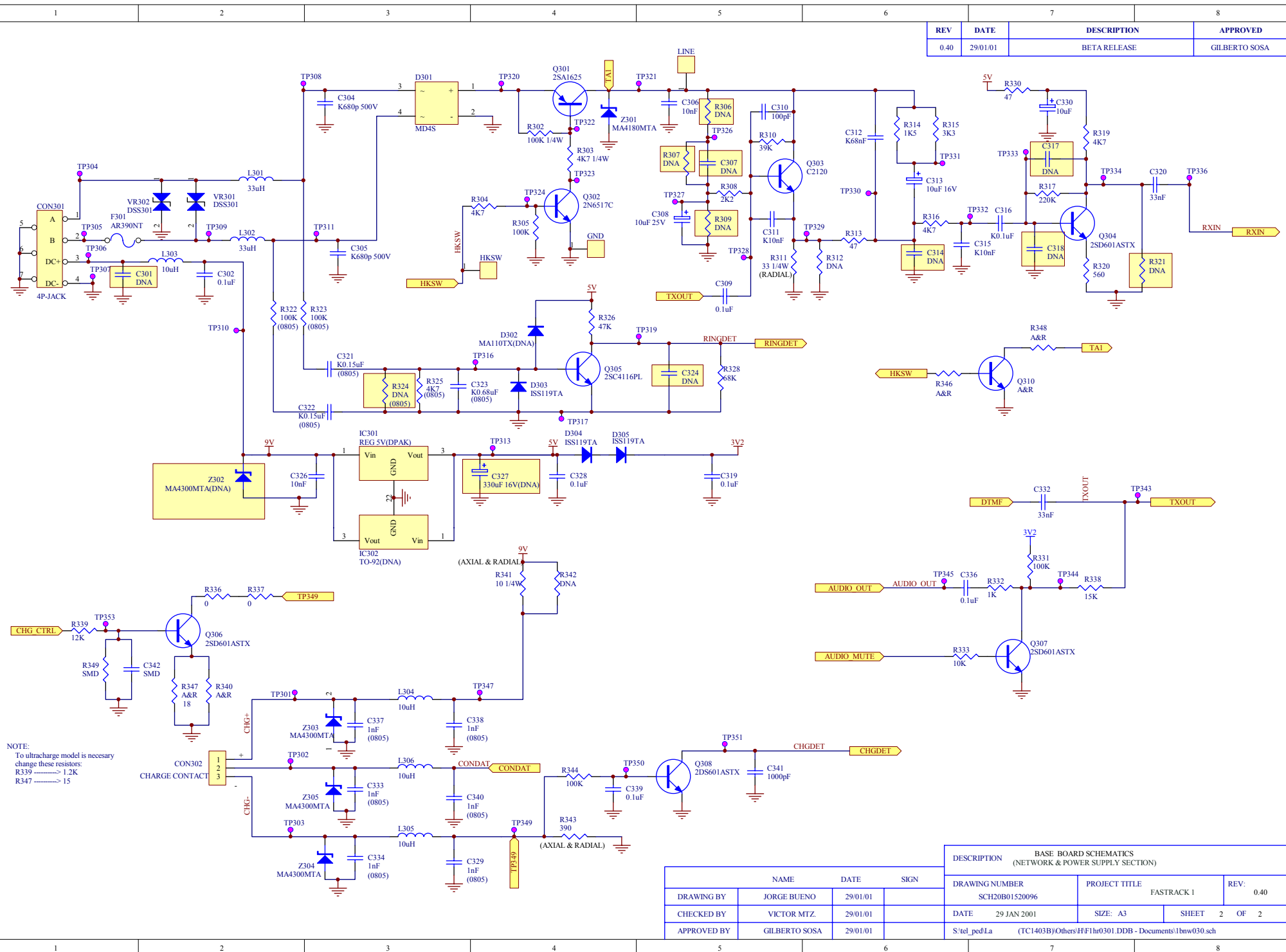
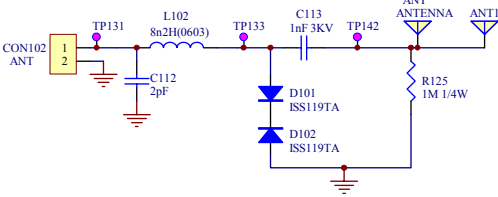
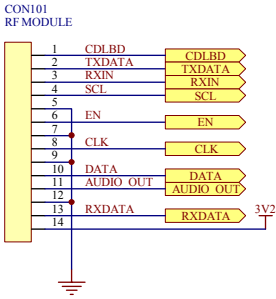
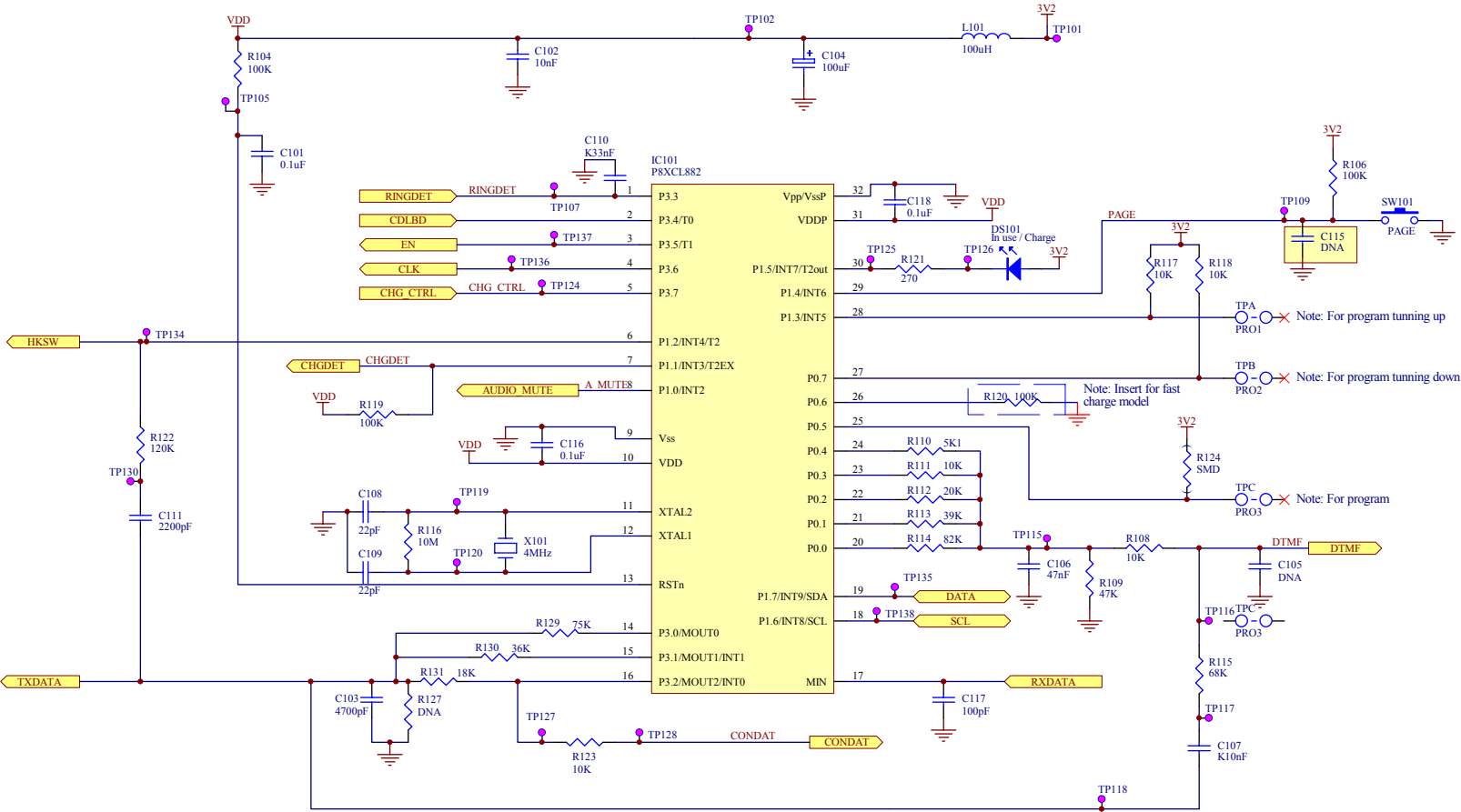


EXHIBIT F-2

REV	DATE	DESCRIPTION	APPROVED
0.40	29/01/01	BETA RELEASE	GILBERTO SOSA



NAME			DATE			SIGN			DESCRIPTION		
DRAWING BY			JORGE BUENO			29/01/01			BASE BOARD SCHEMATICS (MICROPROCESSOR SECTION)		
CHECKED BY			VICTOR MITZ			29/01/01			DRAWING NUMBER		
APPROVED BY			GILBERTO SOSA			29/01/01			SCH20B01520096		
									PROJECT TITLE		
									FASTRACK 1		
									REV: 0.40		
									DATE		
									29 JAN 2001		
									SIZE: A3		
									SHEET 1 OF 2		
									S:\tel_ped\La		
									(TC1403B)\Others\HF1hr0301.DDB - Documents\1bup030.sch		

