

The diagram illustrates the system architecture of a mobile phone, showing the interconnection of various components:

- Power Supply:** Includes a DC JACK, BATT. 2.6V, and multiple DC-DC CONVERTORS (2.5V, 4.8V, 3.5V) and regulators (3.5V, 2.65V) to provide power to different parts of the system.
- Input/Output:** Features a HEAD SET JACK, SPEAKER, MIC, and HANDSET for user interaction.
- Central Processing:** The 8BIT μC (IC1) is the core, connected to a KEY MATRIX, FLASH ROM (IC2), EEPROM (IC3), LCD, LED, and an RF-UNIT.
- RF-UNIT:** Contains a BURST MODE CONTROLLER, RF INTERFACE, ADPCM, PCM, SIDETONE, PCM CODEC, and TONE GENERATOR, responsible for radio communication.
- AFE (Analog Front End):** Connects the 8BIT μC to the RF-UNIT.
- SP-PHONE IC (IC9):** A specialized IC for phone functions, connected to the 3.5V DC-DC CONVERTOR and the RF-UNIT.
- Control Signals:** TXDA, RXDA, and PLL Serial control signals are shown for data and control flow.
- Crystal:** A 10.368MHZ crystal provides the clock signal for the system.

KYUSYU MATSUSHITA ELECTRIC CO., LTD ENGINEERING DEPARTMENT. TELECOM DIV.				Model	KX-TGA420B		
				Name	BLOCK DIAGRAM		
Designed	Drawn	Checked	Approved	Rev. 0REV	Name	@PCB	
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