

MODEL□DCR-IP220

FCC ID□AK8DCRIP220

This document intends to explain electrical design change on Bluetooth for Model DCR-IP220 as follows;

1. Version change of LSI on Bluetooth module in BT-012 board

Changes from Ver A and Ver B

In order to enhance production yield, the following optimization was attempt to Ver B.

(a) Resonant frequencies centerized in the LO_IQ and LNA blocks.

Inductor and capacitor has been adjusted to center of Bluetooth band because VerA chip is slightly out of center frequency.

(b) Capacitance redistributed in the TX Filter block.

Feed back line of DAC(I/Q base band signal out) to monitor port is disconnected because this circuit generated parasitic capacitance which causes distribution of transmitter performance

(c) Miscellaneous clock re- routing and screening.

clock distributing line has been re-routed away from VCO in order to decrease clock noise influence to Local oscillator

2. Value of two capacitors on BT-012 board changes as follows;

This change intends to match the specification of Bluetooth module.

(1) C9904 1uF → 0.1uF

(2) C9906 0.1uF → 2.2uF

Please note that in the transition period of production, original and new status is alternatively used.