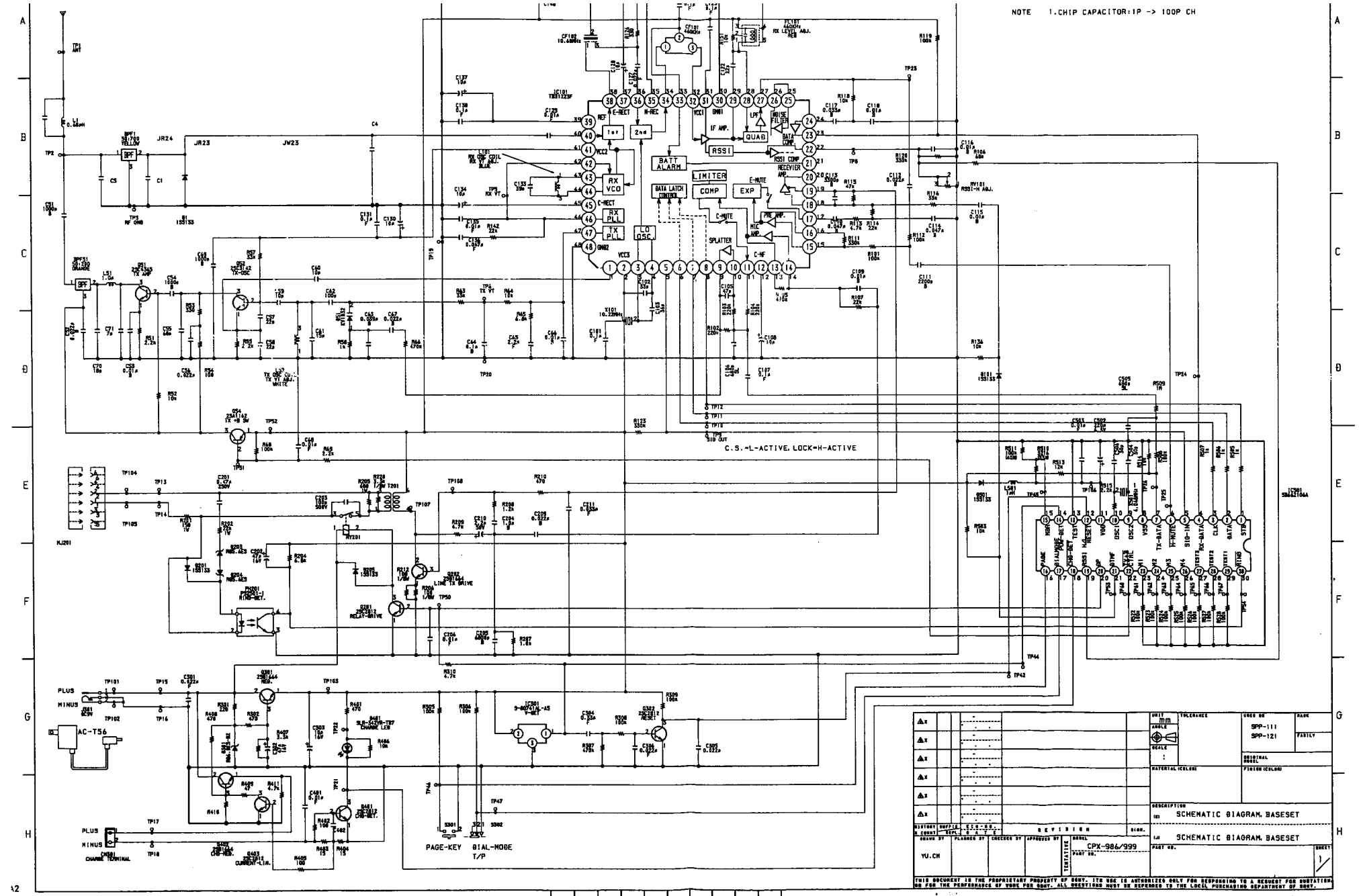
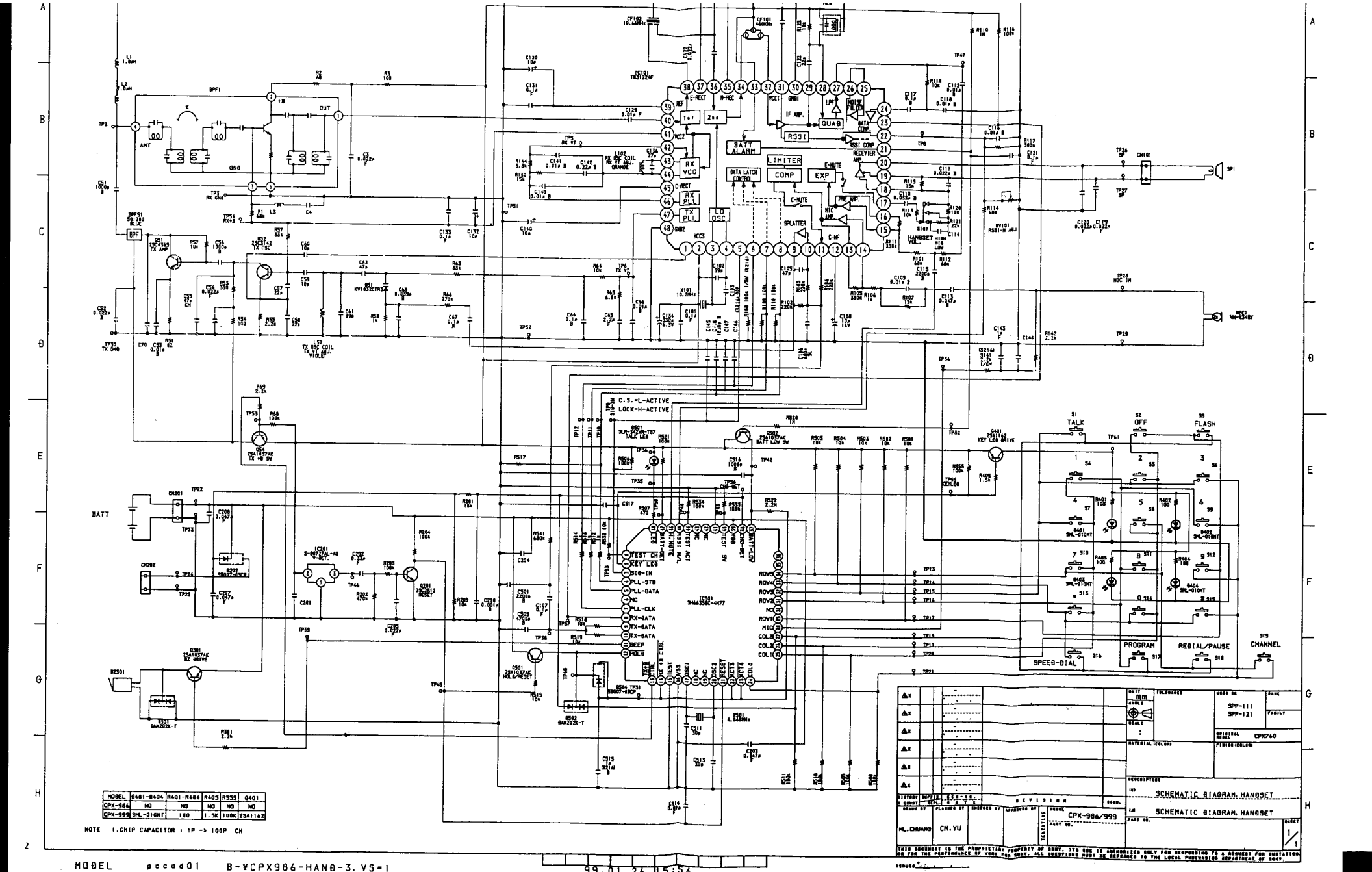

NOTE 1.CHIP CAPACITOR:1P -> 100P CH
C.S.-L-ACTIVE, LOCK-H-ACTIVE
PAGE-KEY DIAL-MODE T/P
AC-T56
CHARGE TERMINAL
SCHEMATIC DIAGRAM, BASESET
SCHEMATIC DIAGRAM, BASESET
CPX-986/999
SPP-111
SPP-121
YU.CH
99.02.06 17:40
MODEL pccad02 B-¥CPX986-BASE-2, VS=1



MODEL	R401-R404	R405	R555	Q401
CPX-986	NO	NO	NO	NO
CPX-999	SML-010MT	1.5K	100K	2SA1162

NOTE 1.CHIP CAPACITOR : 1P -> 100P CH

MODEL pccad01 B-¥CPX986-HAND-3.VS-1

99.01.26 15:54