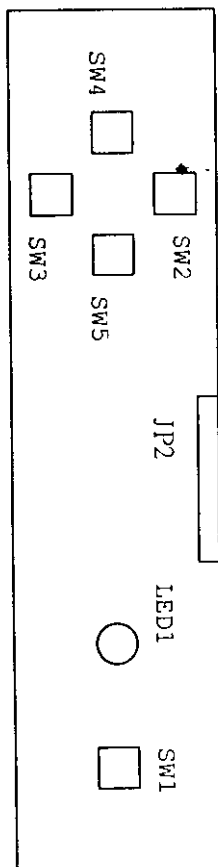
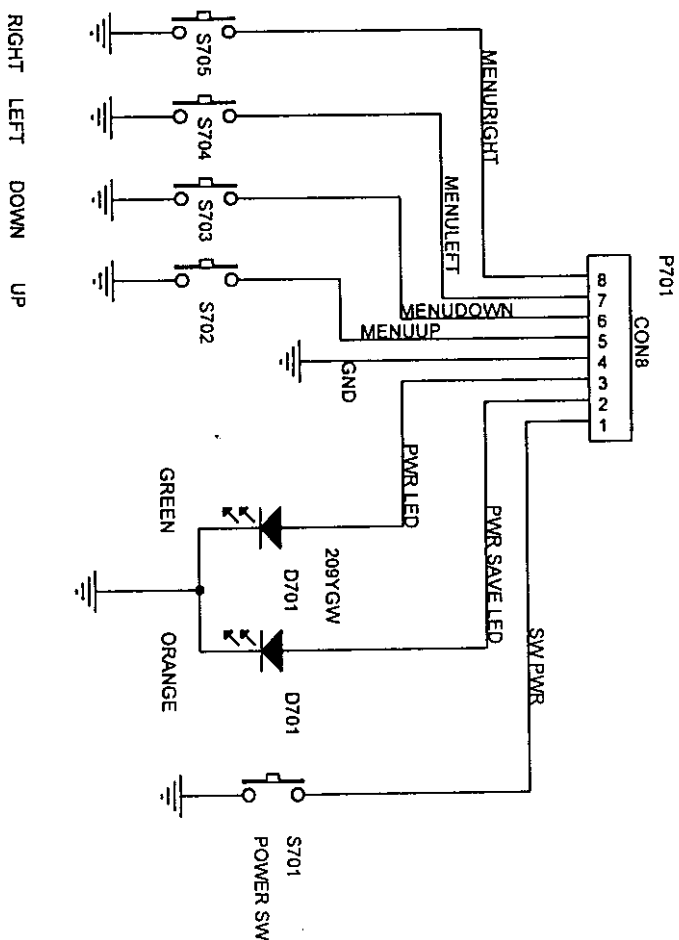


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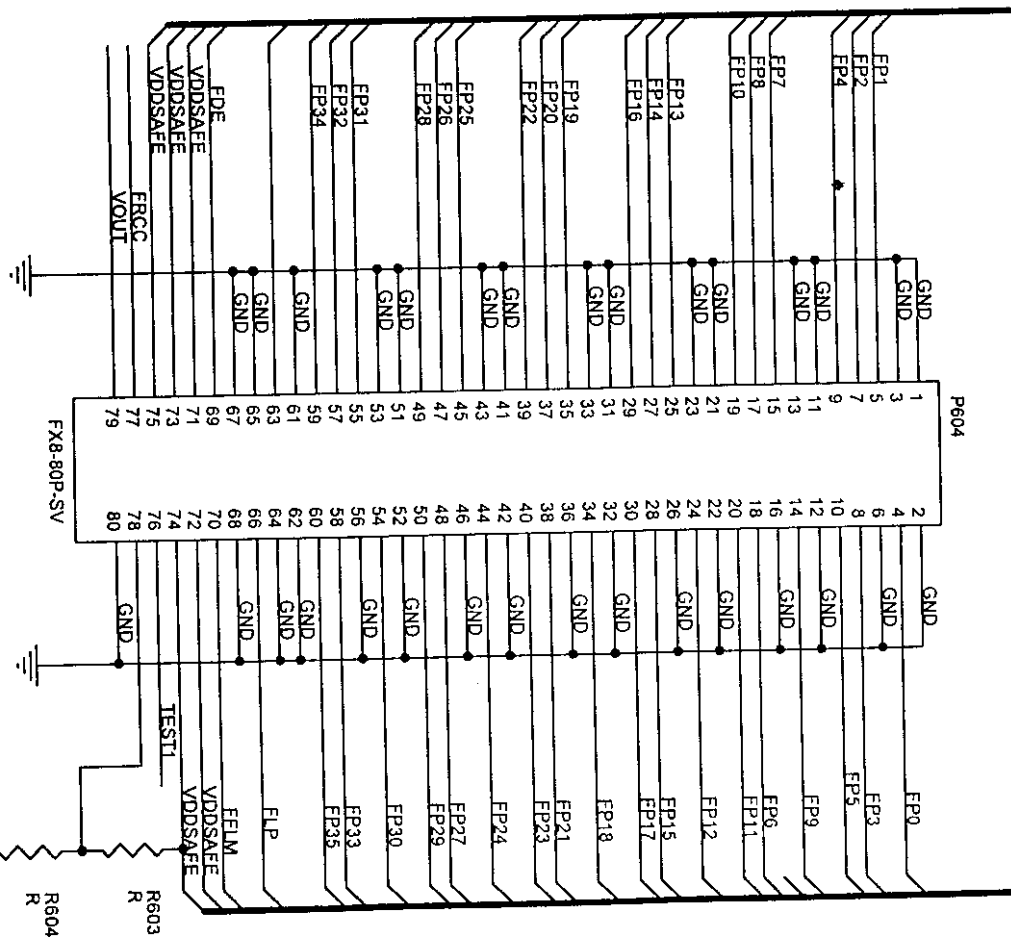
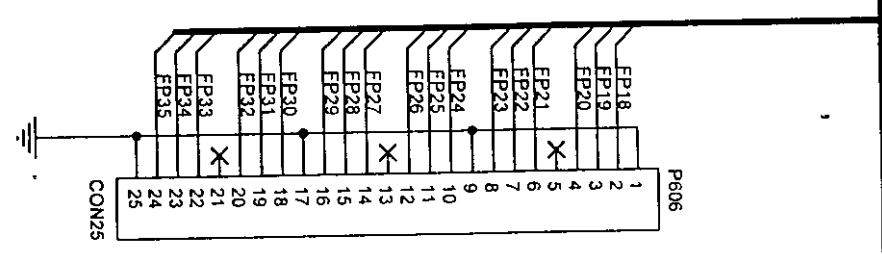
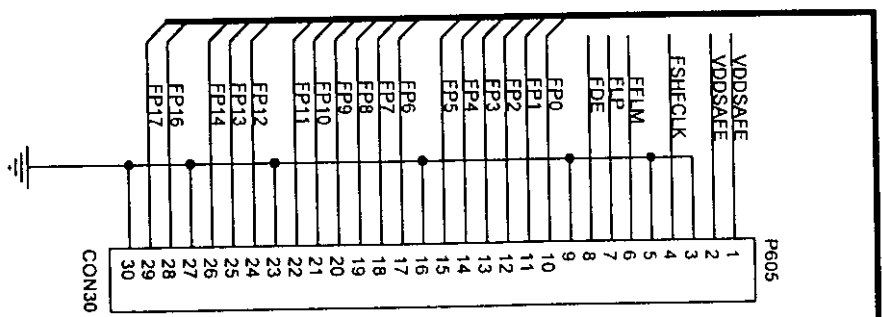


注意! 1. 尺寸同舊
2. LAYOUT 成單面板



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Rev		(RevCode)

JEAN

CHEETAH BASED FLAT PANEL MONITOR CONTROLLER PHILIPS TDA8752 VIDEO FRONT END

REFRESH RATE : 75Hz(Max) DCLK: 80MHz(Max)

REV 0.3

29th MARCH 1998

ROAD MAP

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REVISION HISTORY

REV.	DESCRIPTION	BY	LAST UPDATE
0.1	JEANS AUDIO & DC DC CONVERTOR ADDED TO BASE DRAGON SCHEMATIC.	PRAMOD RAVI	18th march
0.2	R46 & D10 ADDED IN AUDIO/MIC CNT. EX-OR GATE ADDED FOR HSYNC & FOR AUDIO VOLUME CONTROL LOGIC BUS SERIES RESISTORS REPLACED WITH 8 PIN SMT RESISTOR PACKS OTHER CHANGES AS DISCUSSED WITH JEAN	PRAMOD RAVI	20th march
0.3	Capacitors are added in flat panel interface. FOR CLK SIGNALS CAPCITOR IS ADDED FERRITE BEADS ADDED FOR panel interface.	PRAMOD RAVI	29th march

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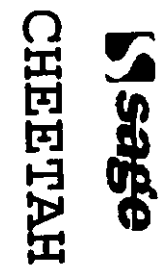
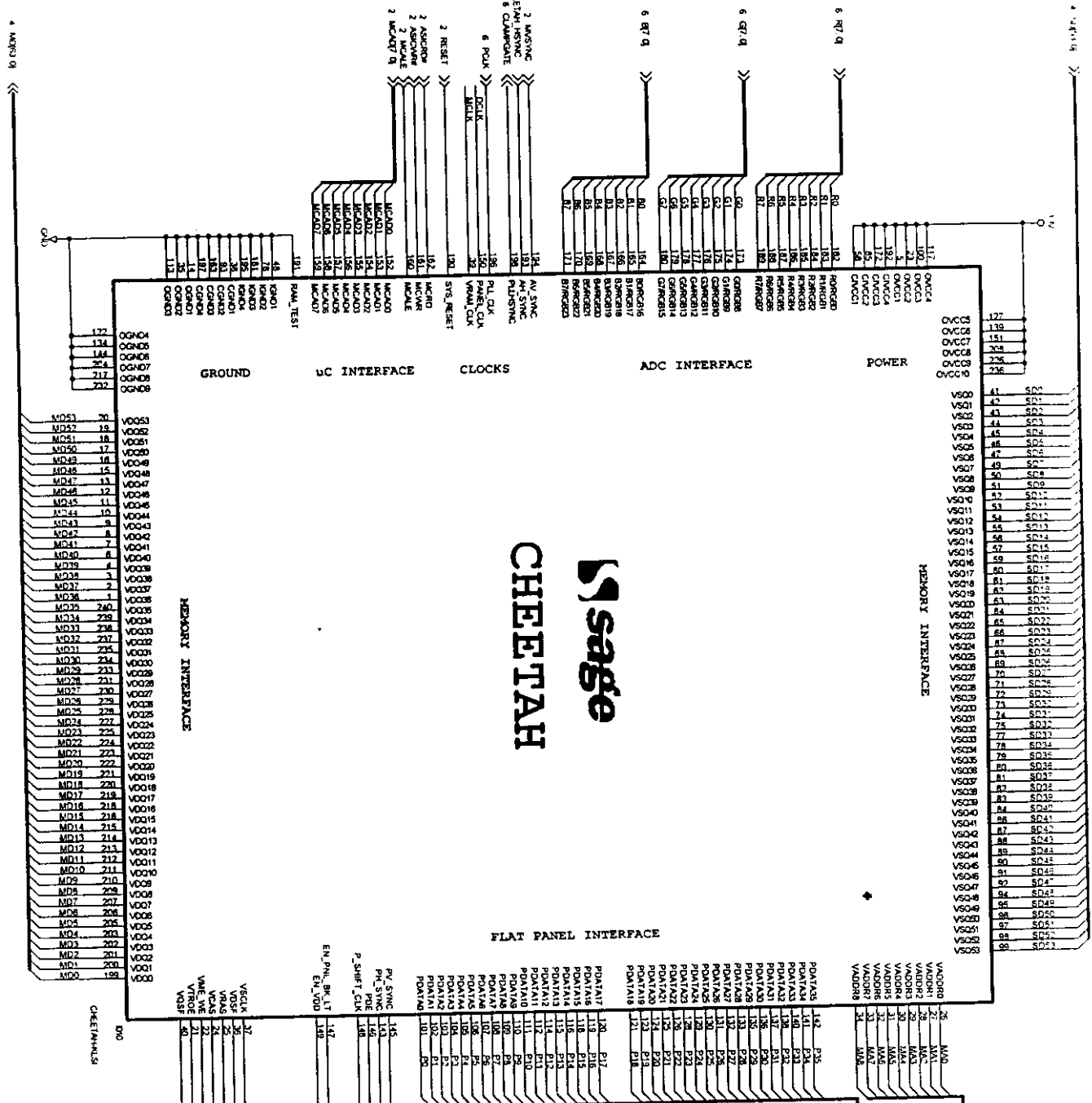
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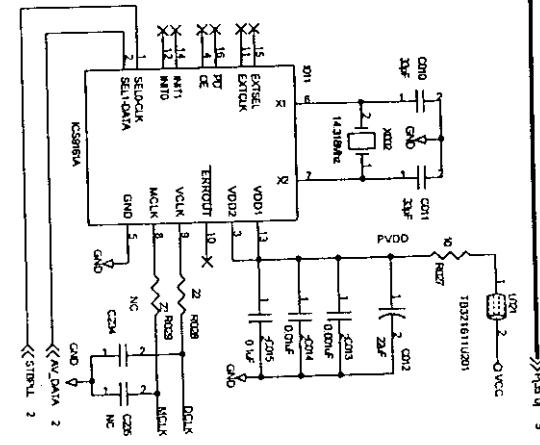
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FLAT PANEL INTERFACE



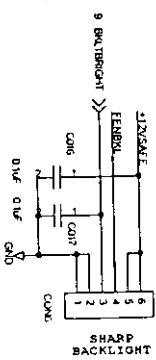
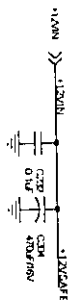
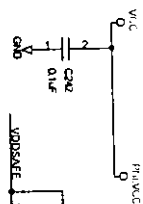
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JEAN - CHEETAH ASIC INTERFACE

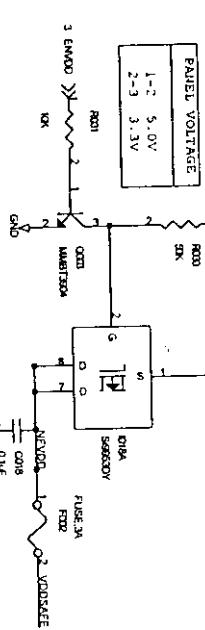
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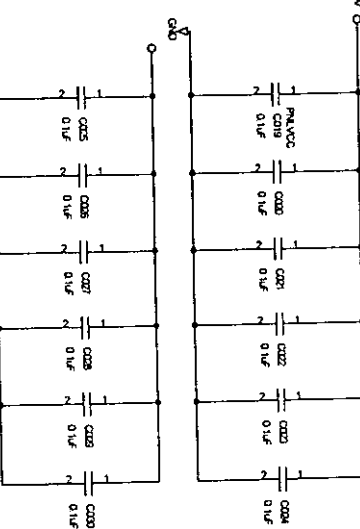
PANEL BACKLIGHT ENABLE



FLAT PANEL, VDD ENABLE



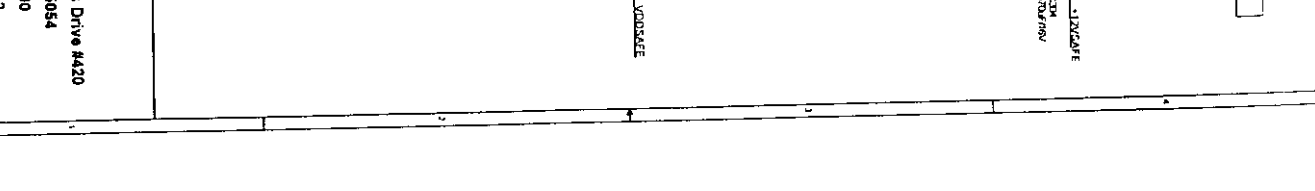
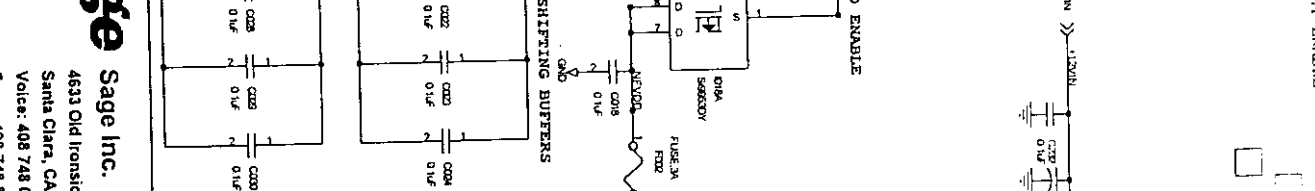
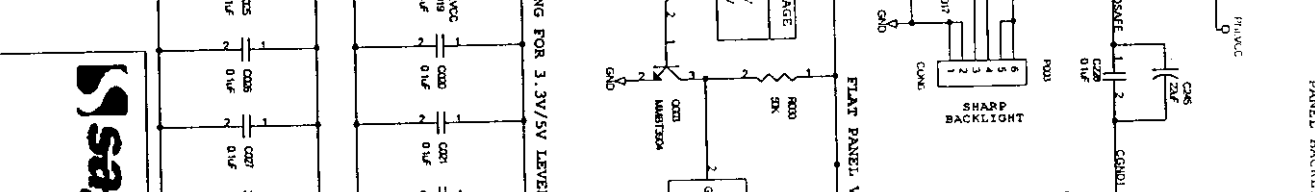
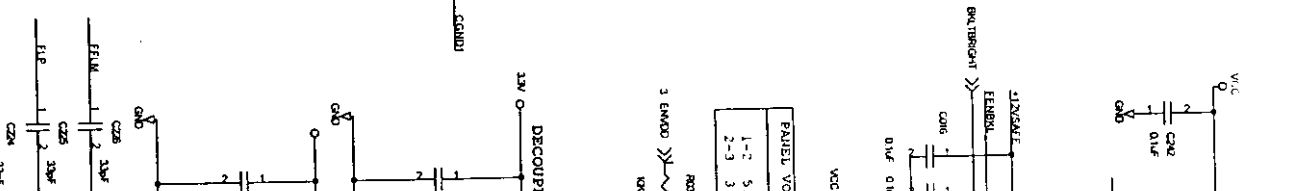
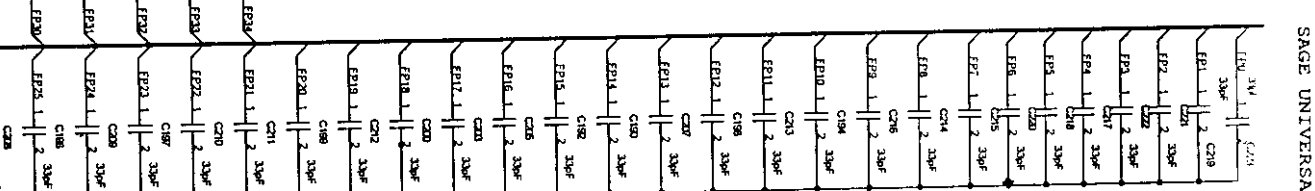
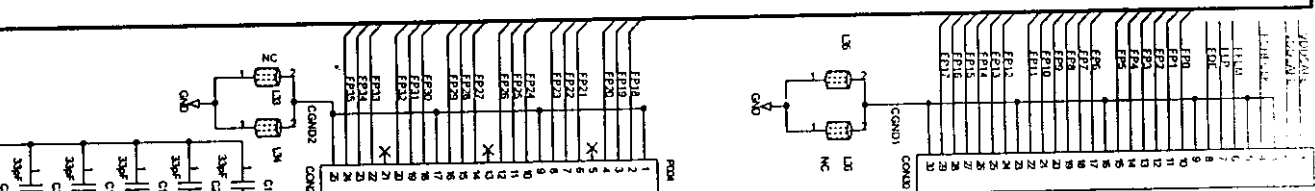
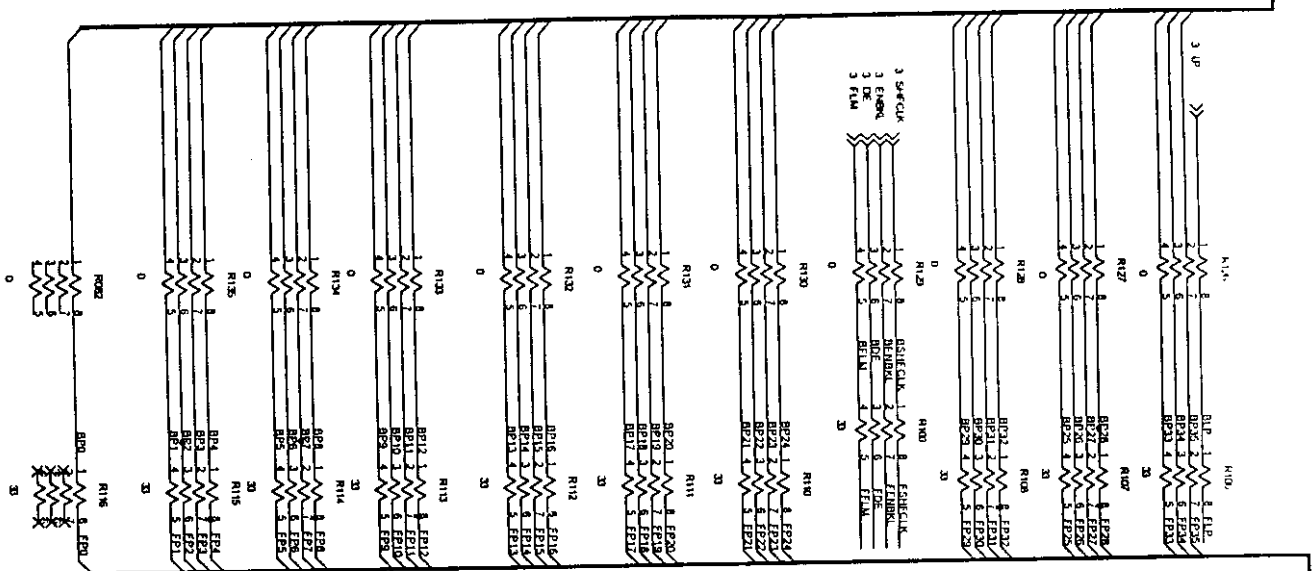
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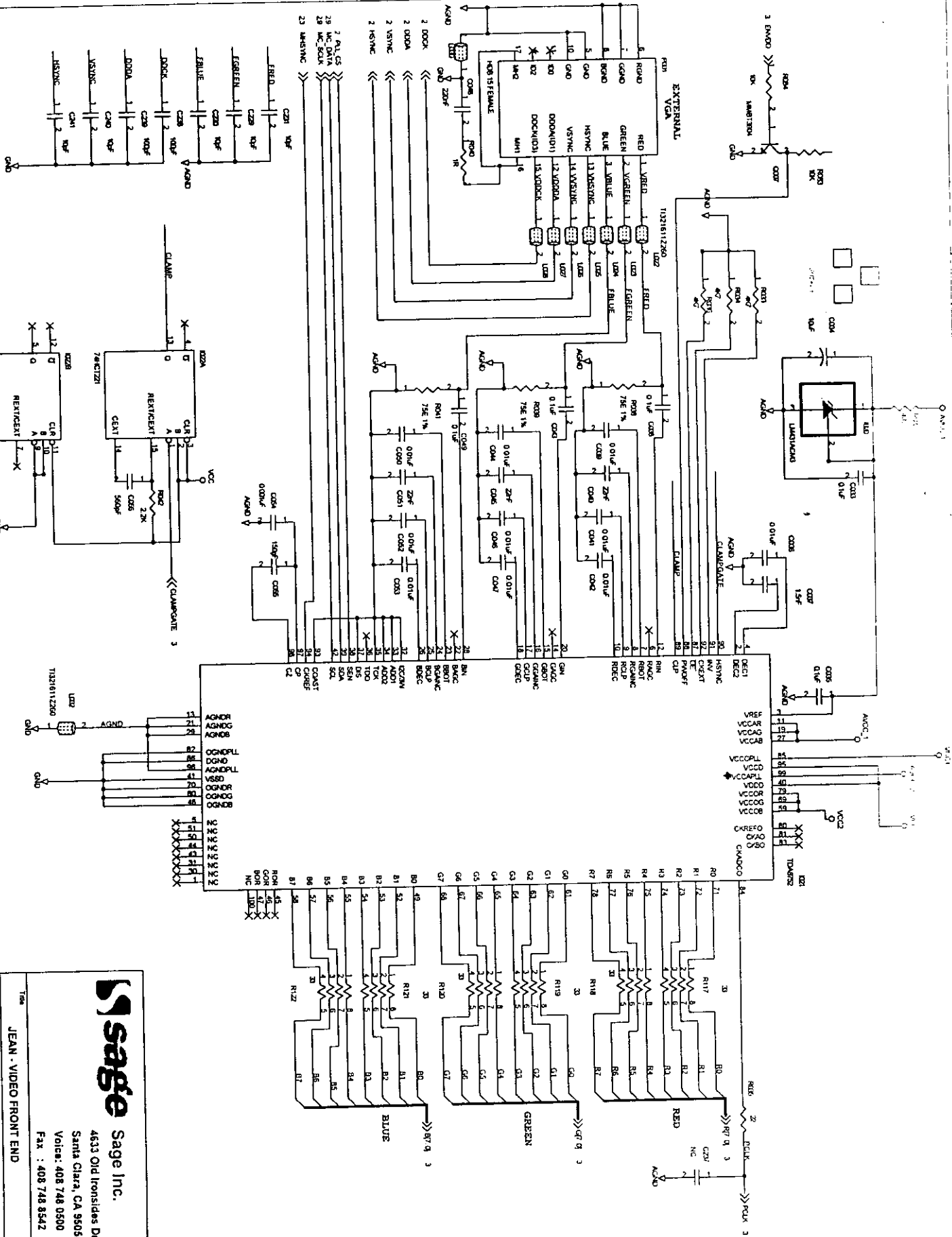


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JEAN - PANEL INTERFACE

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JEAN - VIDEO FRONT END

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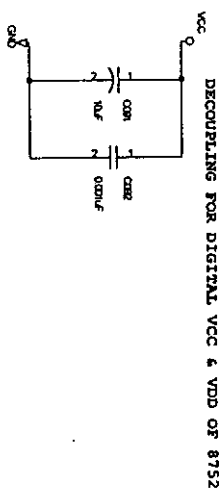
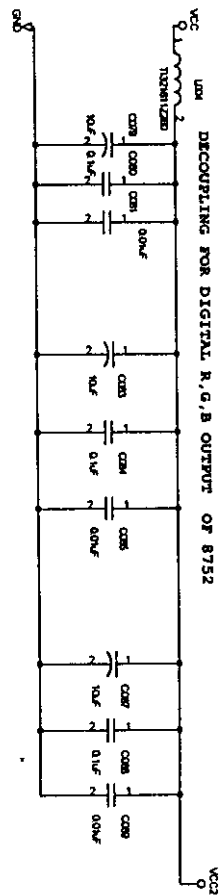
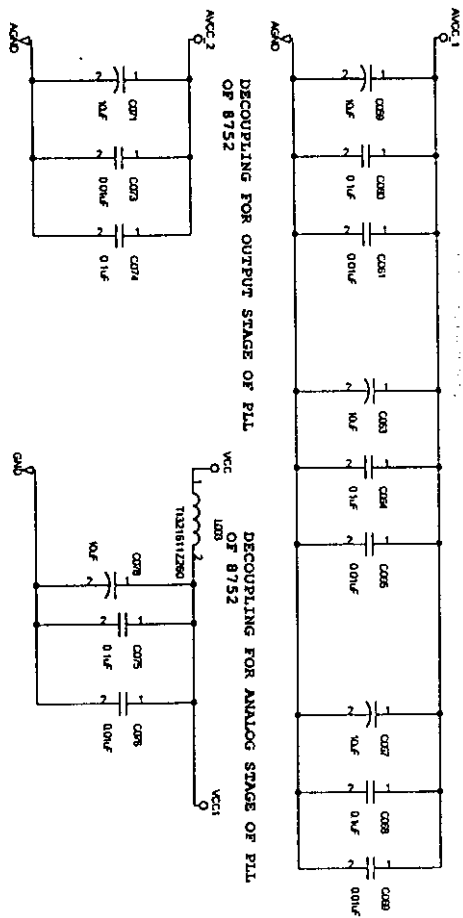
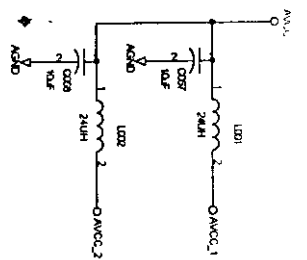
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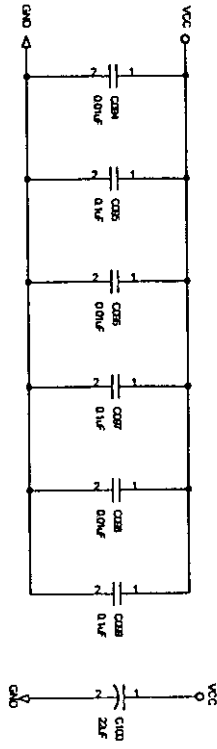


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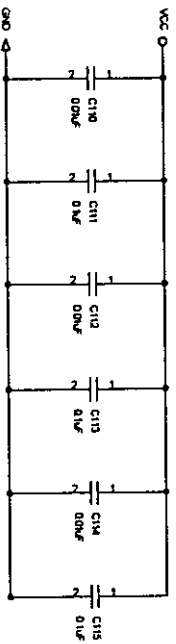
JEAN-POWER AND DECAP FOR 8752

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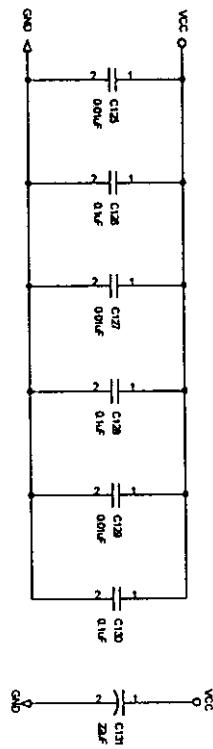
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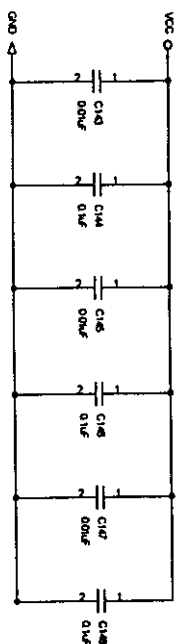
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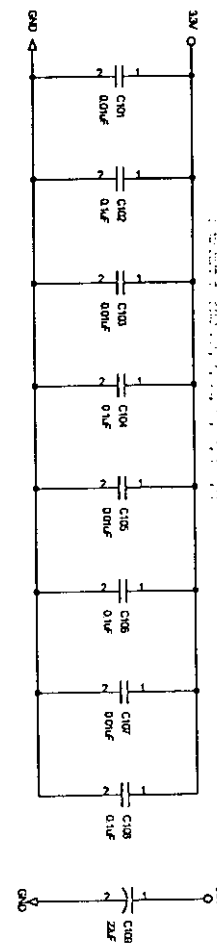
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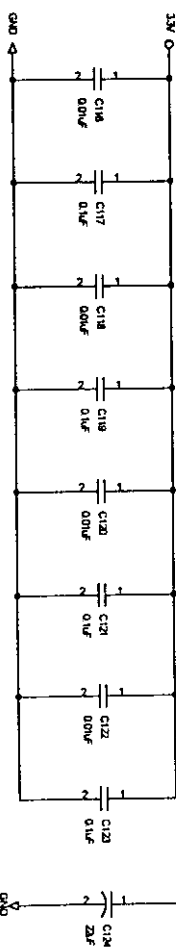
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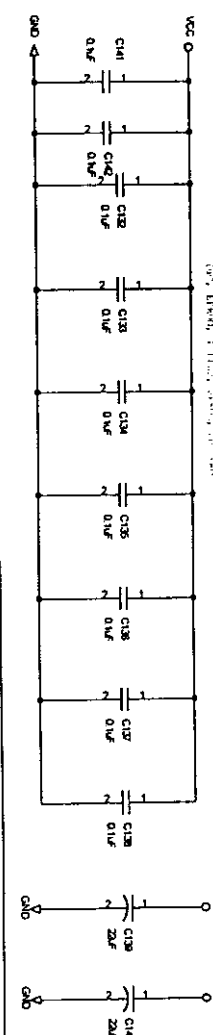
DECOUPLING FOR CHEETAH ASIC



DECOUPLING FOR CHEETAH ASIC



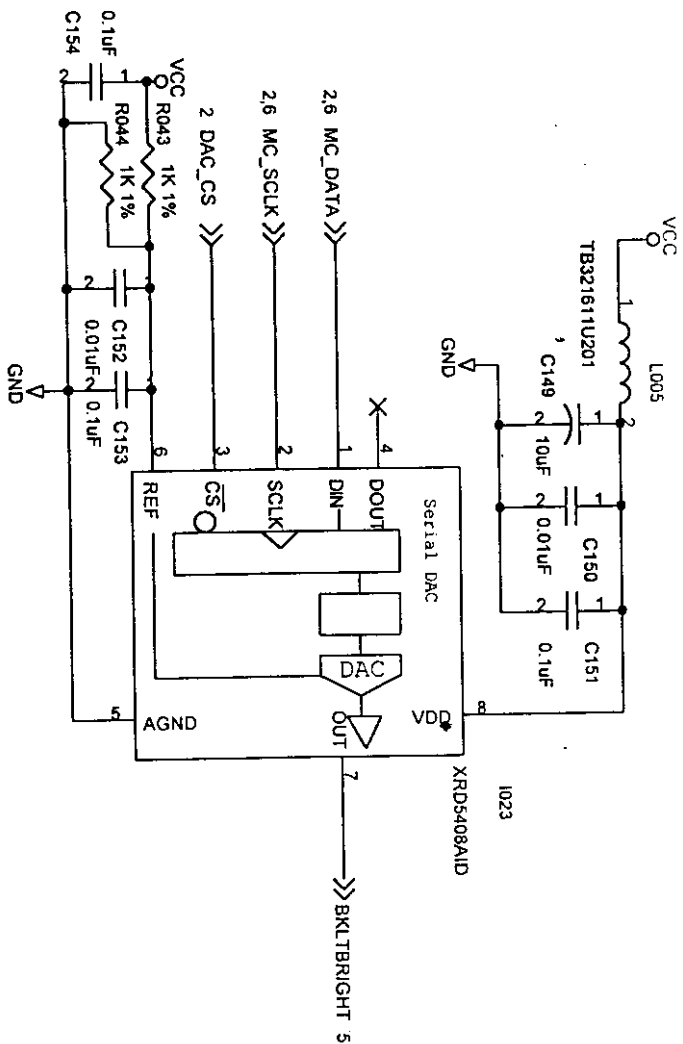
DECOUPLING FOR MISC ICs



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JEAN - POWER AND DECOUPLING CAPS

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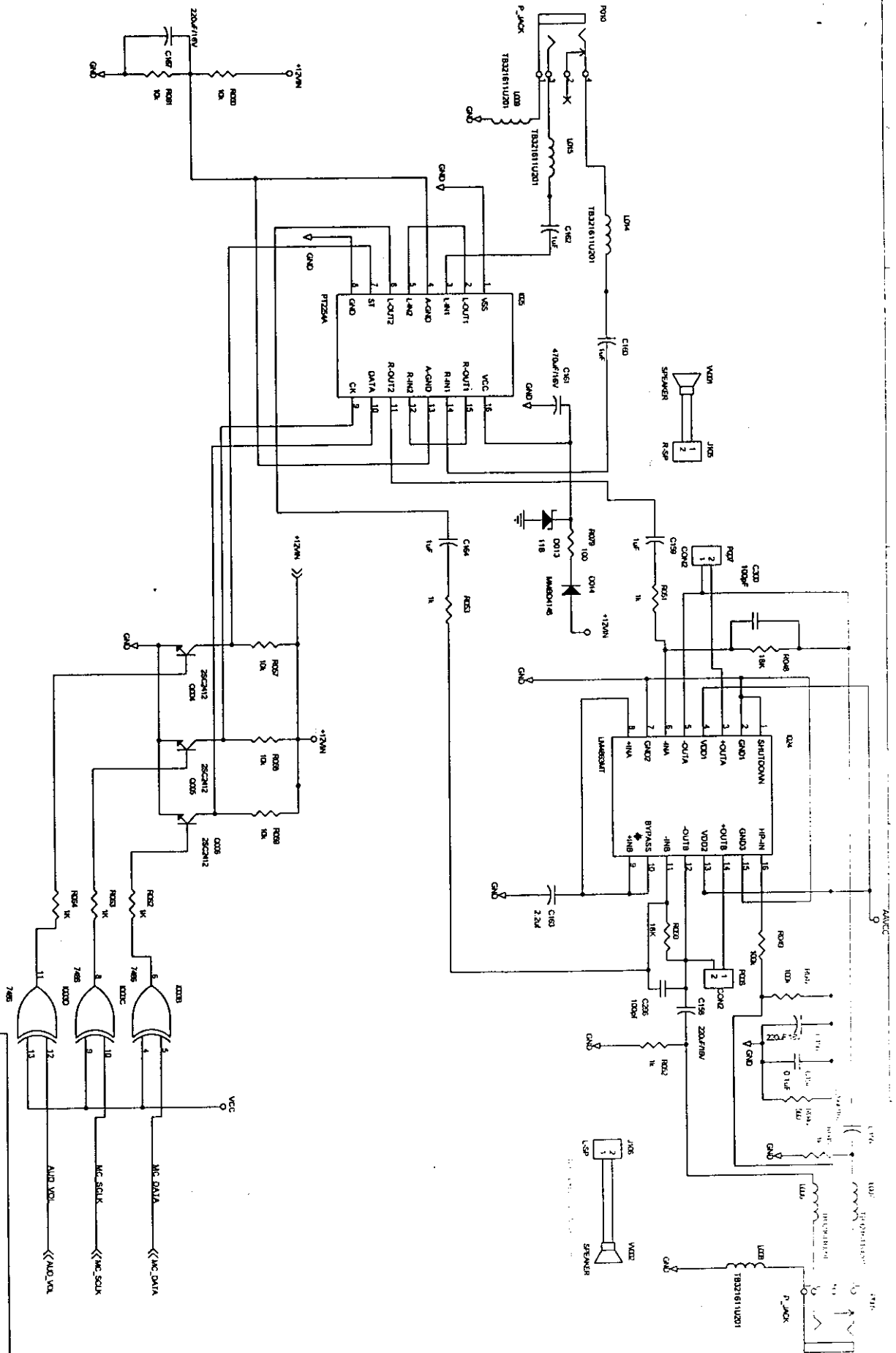
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JEAN - AUDIO/MIC CKT

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