

Legend Eagle 17" (gm2121) LCD CONTROLLER BOARD SCHEMATICS

CONTENTS

SCHEMATIC	SHEET
Contents	1
Top Level	2
Signal Input	3
GM2121	4
I/O Connectors	5
POWER	6

REVISION HISTORY

Date	Author	Ver	Comments
5/6/03	Rick	A	A. 依聯想要求, 修正Connector B. 由於有共用問題, 將另建料號
5/19/03	Rick	B	A. Inverter控制訊號反向 B. LED控制訊號反向

Approval	Organization	Signature	Date

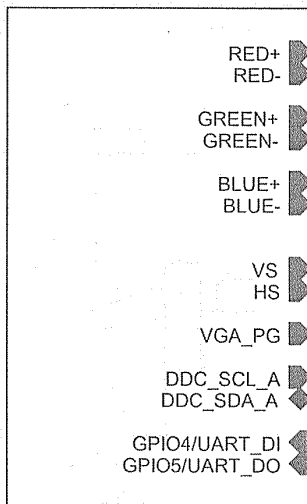


Title		0. Contents	
Size	Document Number		Rev
A	EAGLE17-715L1130-B-LEG.DSN		B
Date:	Monday, May 19, 2003	Sheet	1 of 6

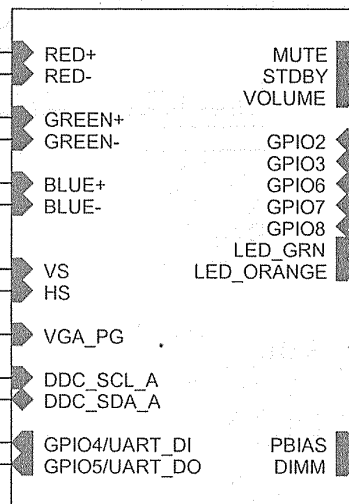
SHEET 2

SHEET 3

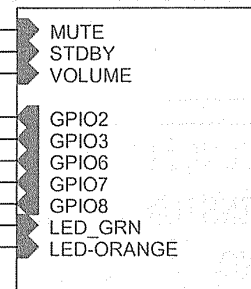
SHEET 4



2.Signal Input

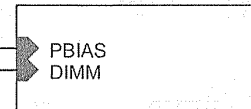


3.GM2121



4.I/O Connectors

SHEET 5



5.Power



Title

1.Top Level

Size

Document Number

Custom

EAGLE17-715L1130-B-LEG.DSN

Rev

B

Date:

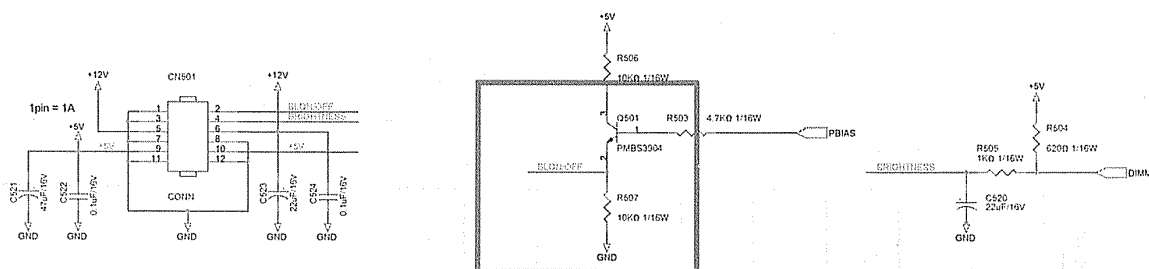
Monday, May 19, 2003

Sheet

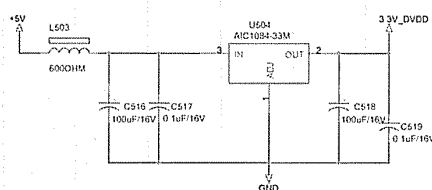
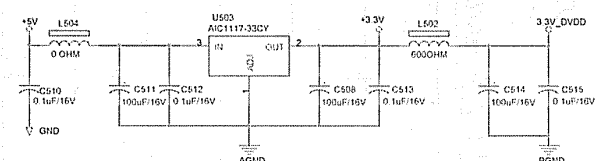
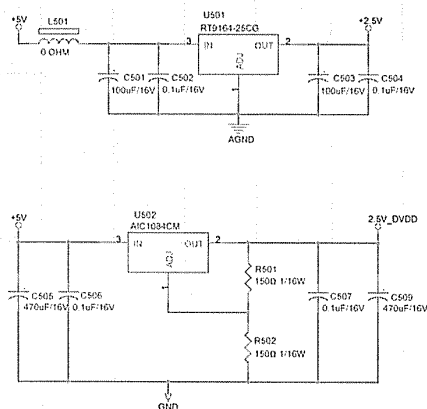
2

of

6



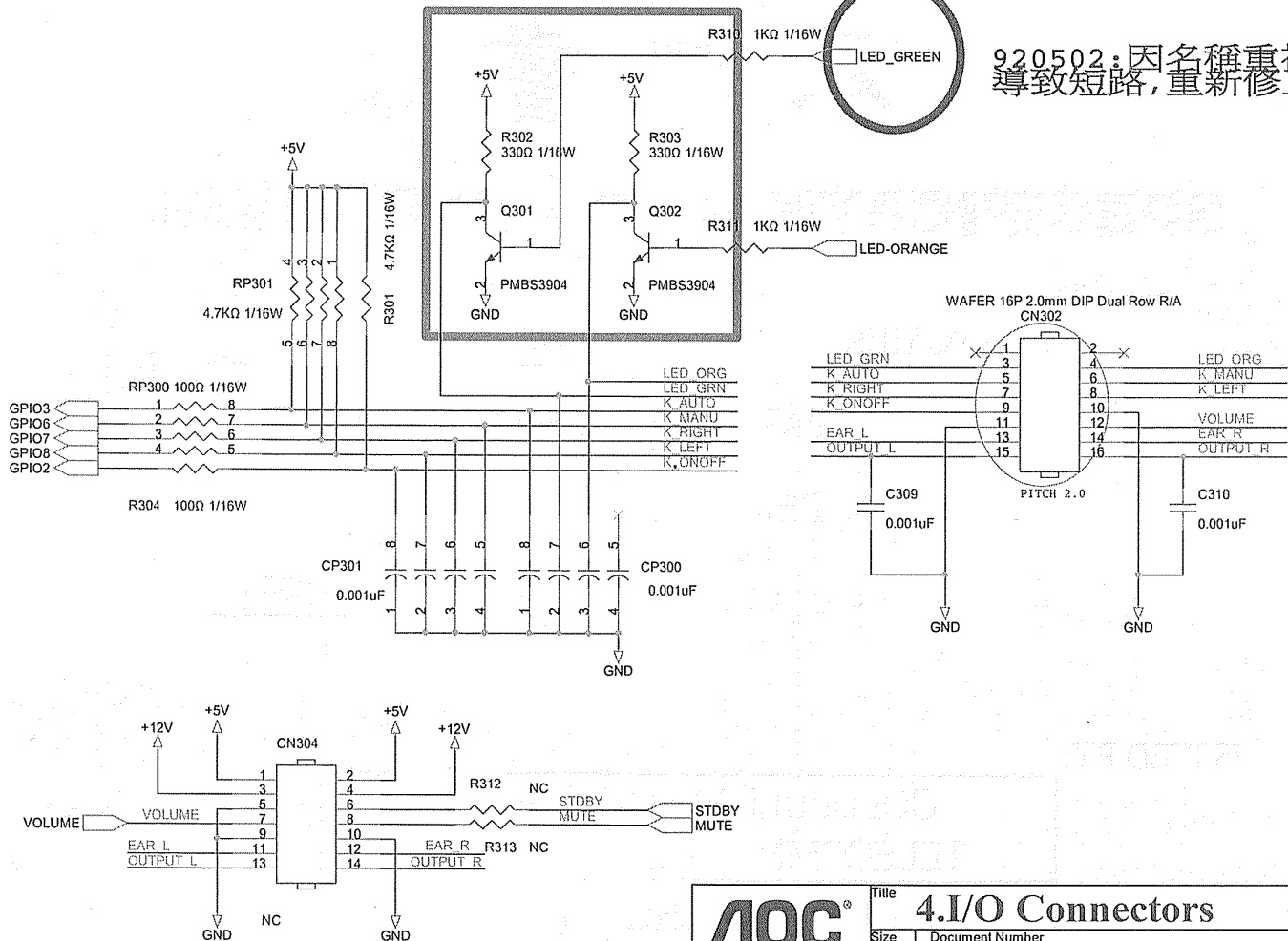
920519: Inverter 訊號腳反向

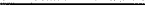


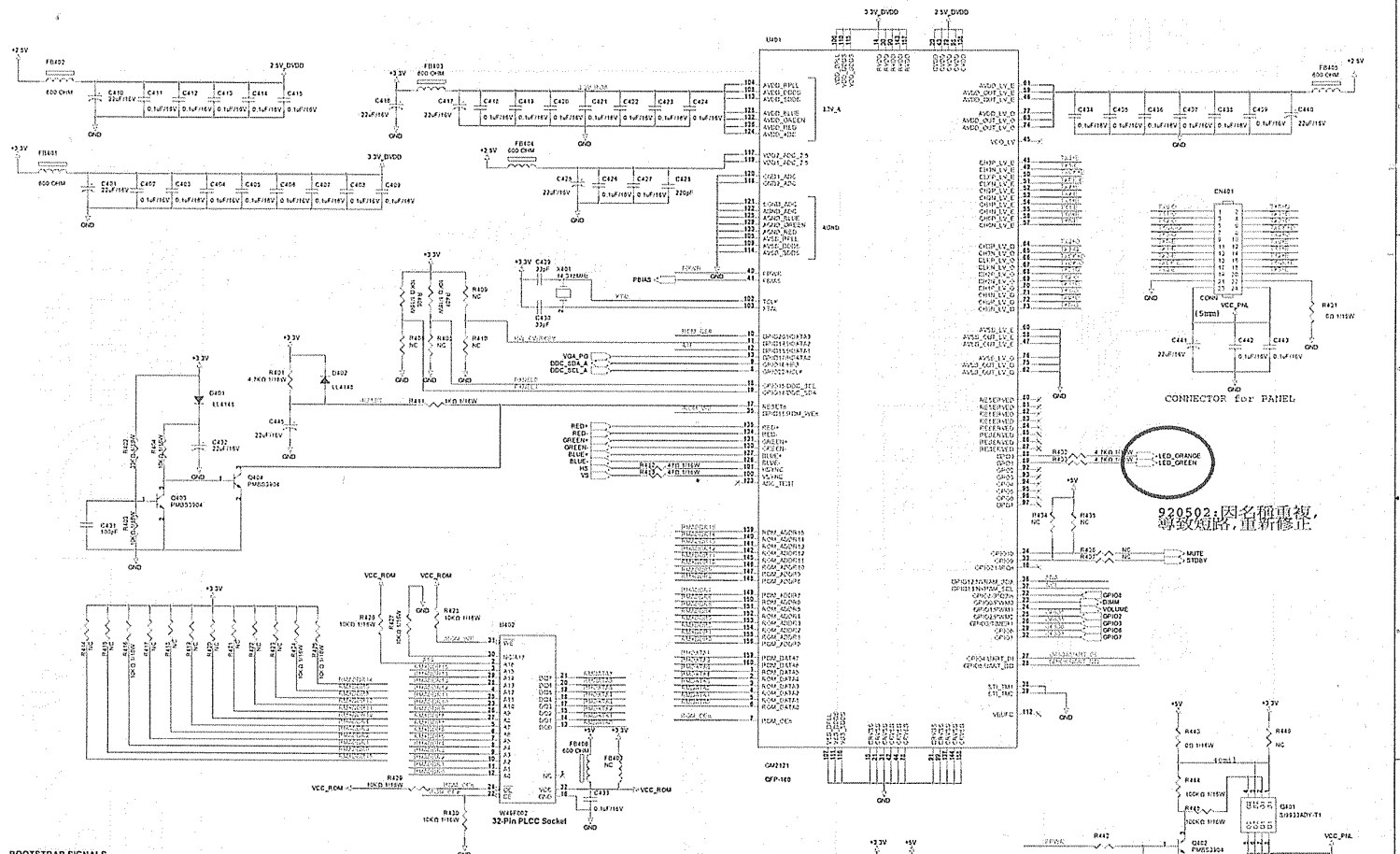
 EYES VALUE	Title			
	5.Power			
	Size	Document Number		Rev
	B	EAGLE17-71SL1130-B-LEG.DSN		B
	Date	Monday, May 26, 2003		
	Sheet	6	of	6

920519:LED訊號腳反向

920502:因名稱重複,
導致短路,重新修正



	Title			4.I/O Connectors		
	Size	Document Number			Rev	
	A	EAGLE17-715L1130-B-LEG.DSN			B	
	Date:	Monday, May 26, 2003		Sheet	5	of 6



BOOTSTRAP SIGNALS

ADDRESS	NAME	SET	DESCRIPTION
ROM_ADDR(4)	USER_BIT(4)	X	Available for reading from a status register
ROM_ADDR5	Reserved	X	If using Green host protocol, program the bit to 0
ROM_ADDR6	SCROL	X	Disables polarity of HCLK output
ROM_ADDR7	HOST_PROTOCOL	0	If using Green host protocol, program this bit to 1
ROM_ADDR8	HOST_PORT_EN	1	GPIO22: 10 is an "Host Port" pin
ROM_ADDR9	OCM_START	1	1 = OCM becomes active after OCM_CLK is stable
ROM_ADDR(12-13)	USER_BIT(2)	X	Available for reading from a status register
ROM_ADDR13	OSC_SEL	0	0 = XTAL and TCLK pins are connected
ROM_ADDR14	OCM_ROM_Clock	1	1 = At 40K of ROM in memory ROM

AOC 3.GM2121

Doc# Eagle17-715L1110-B-LEG.DSN

Rev B

