

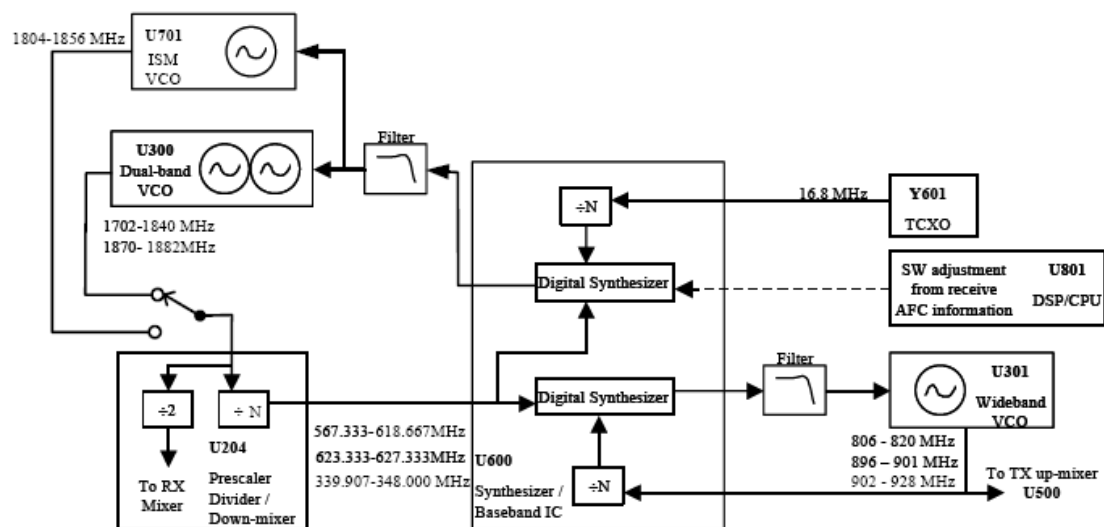


Figure 4-1: Frequency Generation Block Diagram