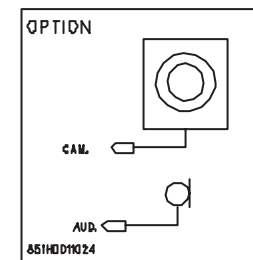


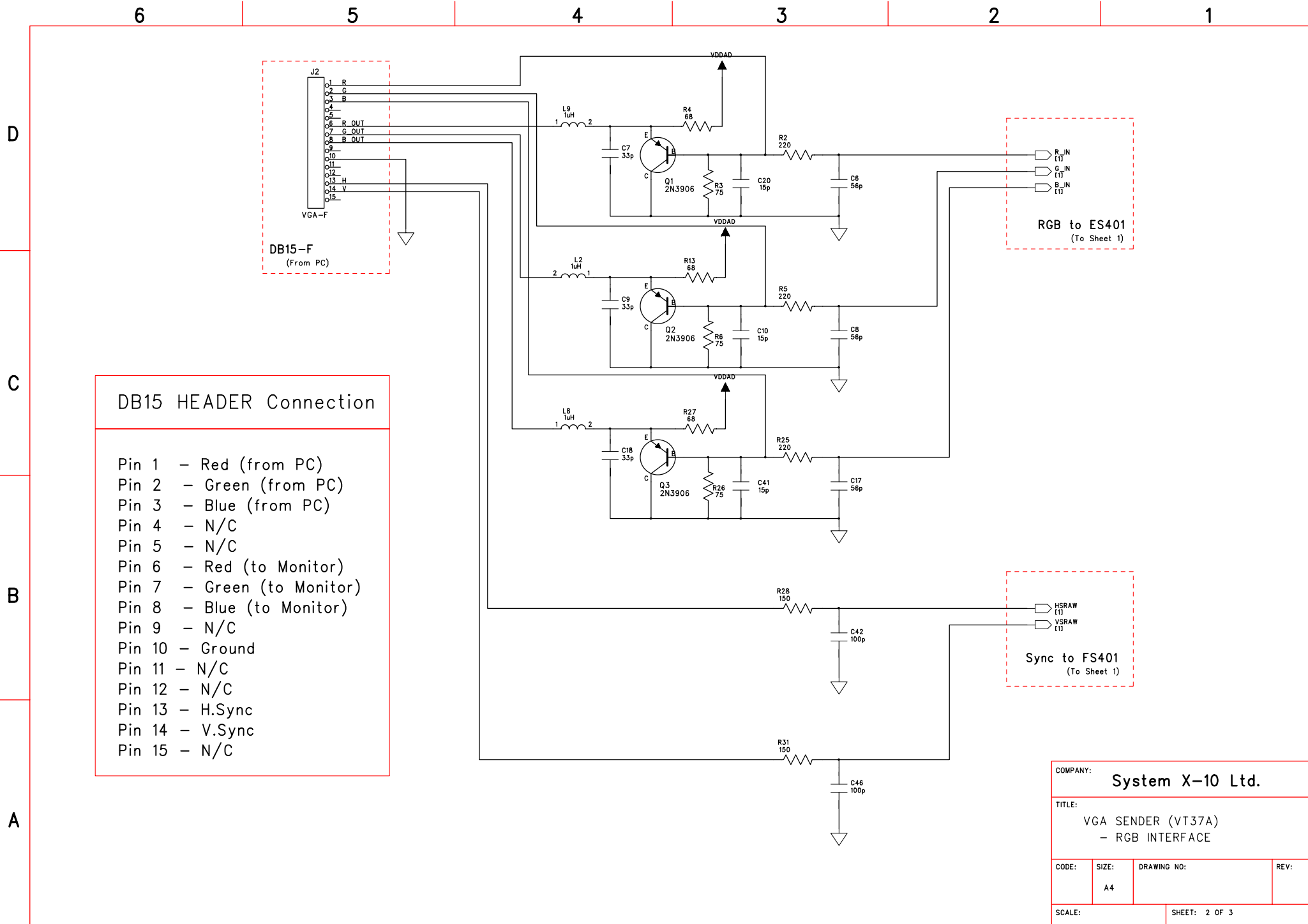


The schematic diagram illustrates a 3-wire SPI interface circuit. It includes a 5V power supply, a 10k pull-up resistor, and three capacitors (100nF, 10nF, and 100nF) connected to the VCC, GND, and MISO lines respectively. The MISO line is also connected to an 'ANTI' pin on a component.



ECN	C18-09 C12-16 C17-C23-038	IMPROVING BPT BW DUE TO PCS TRACK WITH ERROR
ECN	103 DGE - FOR ETSI APPROVAL	
ECN	DESCRIPTION	

COMPANY		SYSTEM X-10 LIMITED	
TITLE:		VT37A	
ASSEMBY		851001H11119	
DATE	SIZE	DRAWING NO:	REV
90	A4	SCH.CAMTX1H2	03
SCALE: 1/8"		SHEET: 1 OF 1	



6

5

4

3

2

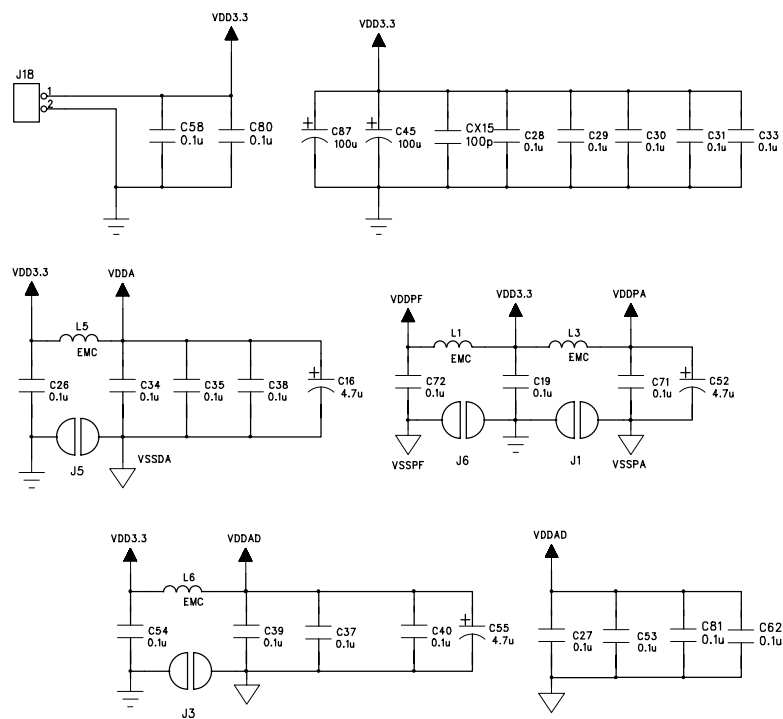
1

D

C

B

A



COMPANY: System X-10 Ltd.			
TITLE: VGA SENDER (VT37A) - Power			
CODE:	SIZE: A4	DRAWING NO:	REV:
SCALE:		SHEET: 3 OF 3	

