

LOCAL CONTROL SECTION 1

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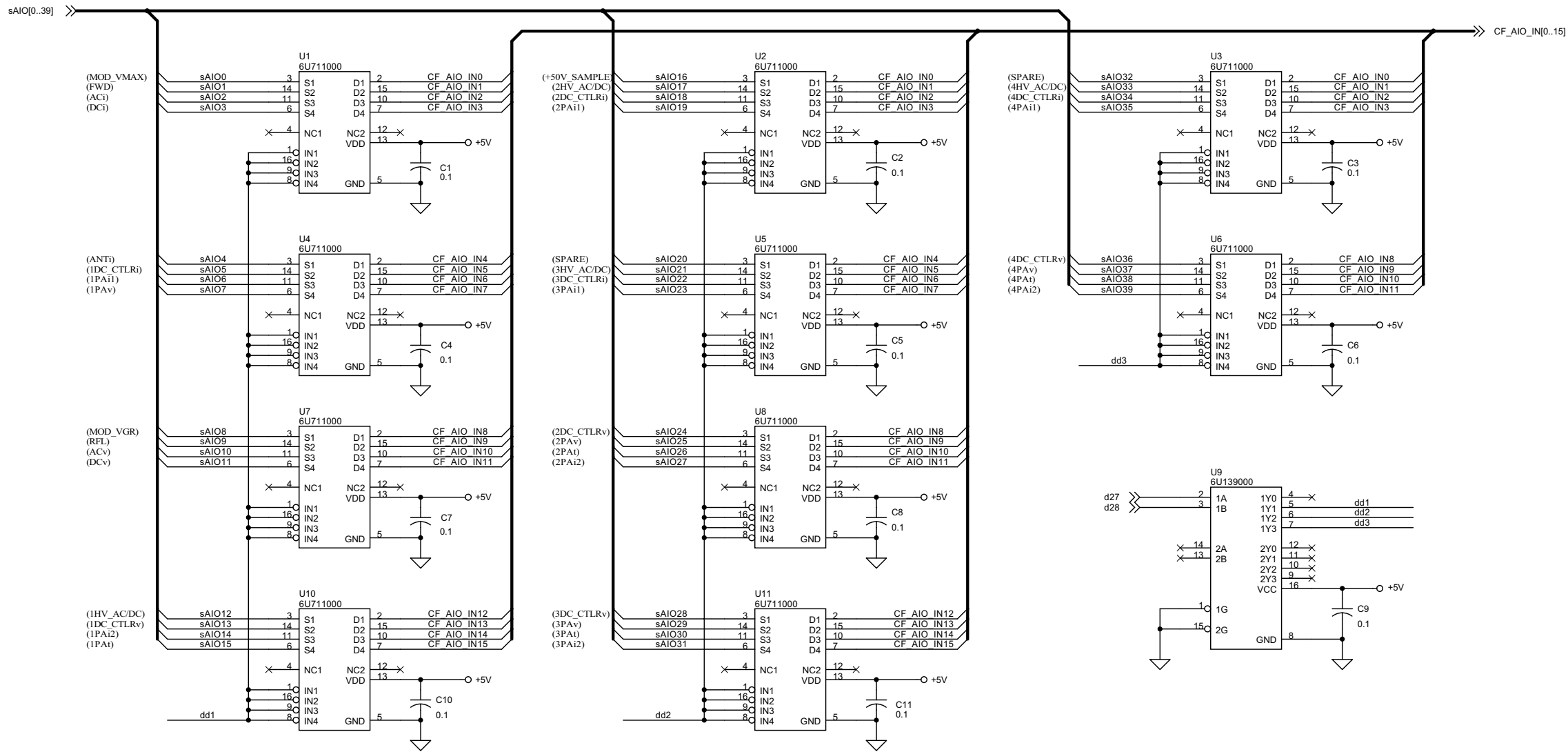
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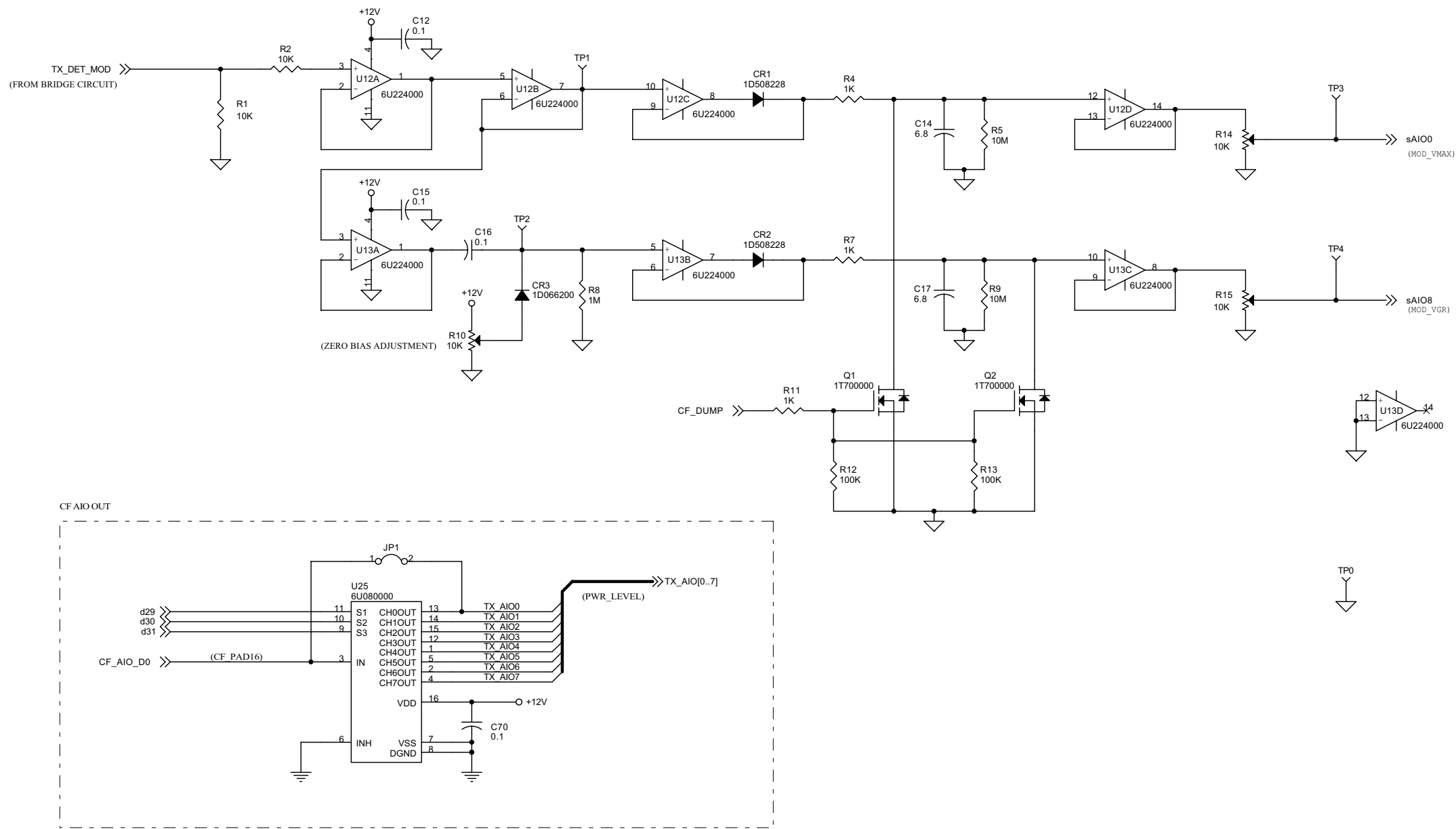
SEP10006 - SCHEMATIC, USER INTERFACE PCB 47



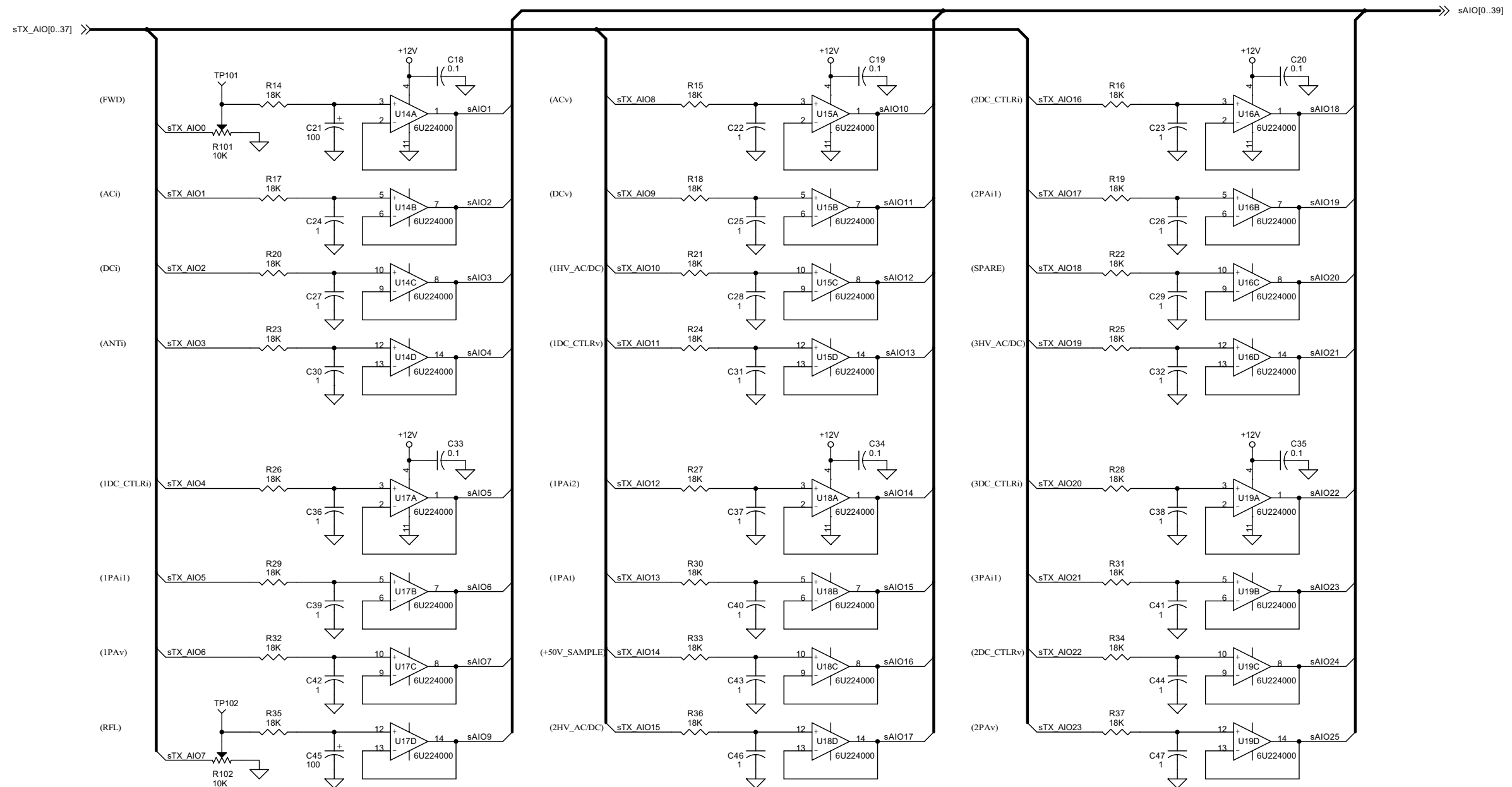
NOTES
1. THIS DRAWING NO. SEP10005 REV. 2.
2. TITLE: COLD FIRE ANALOG I/O PCB SCHEMATIC.
3. ASSEMBLY DWG. NO. SAP10005.
4. ALL RESISTORS 1/4W, UNLESS NOTED.
5. ALL CAPACITORS IN uF, UNLESS NOTED.
6. " << " INDICATES OFF-PAGE CONNECTION.

SEP10005 - SCHEMATIC, ANALOG I/O PCB
SHEET 1 OF 5

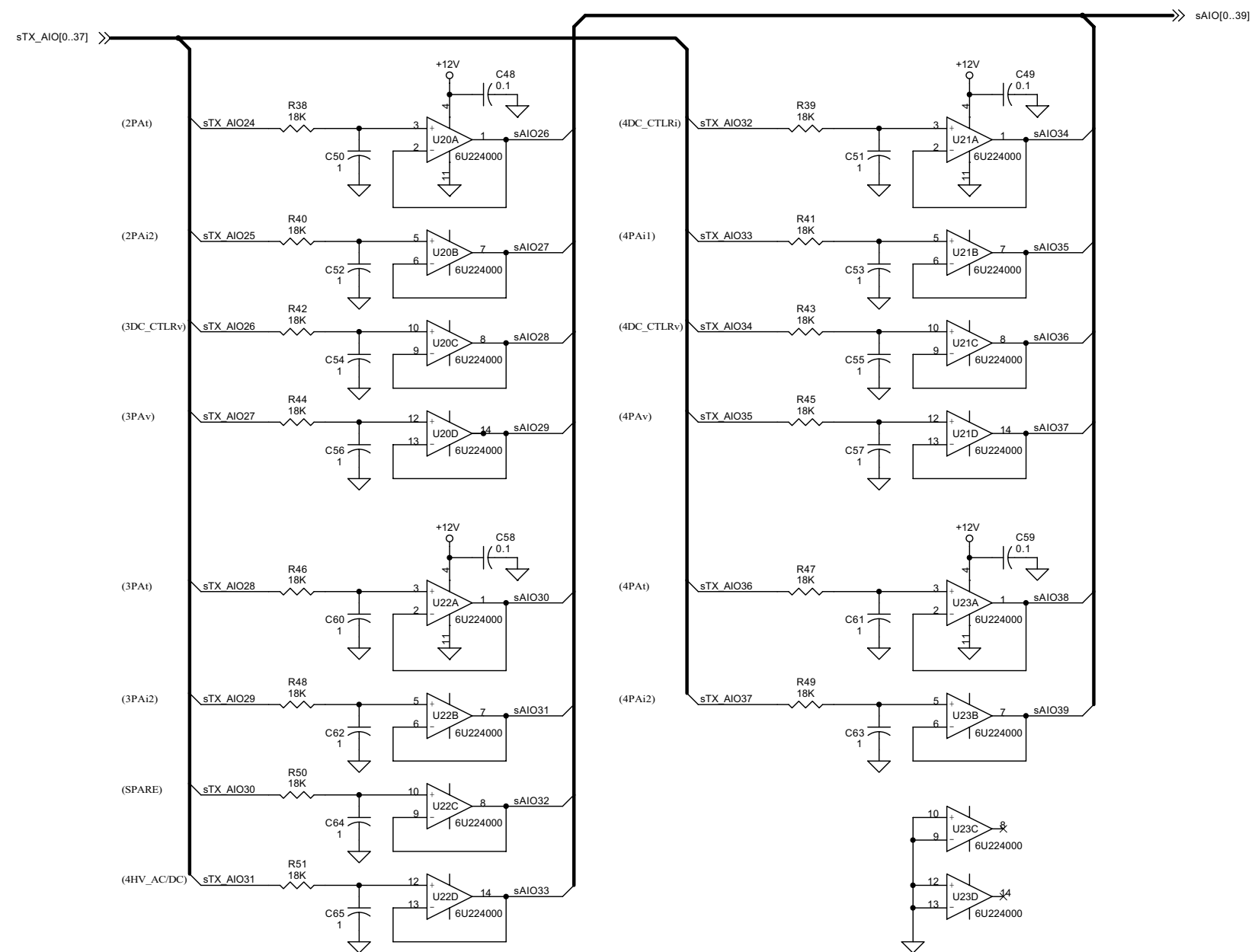
MODULATION DETECT



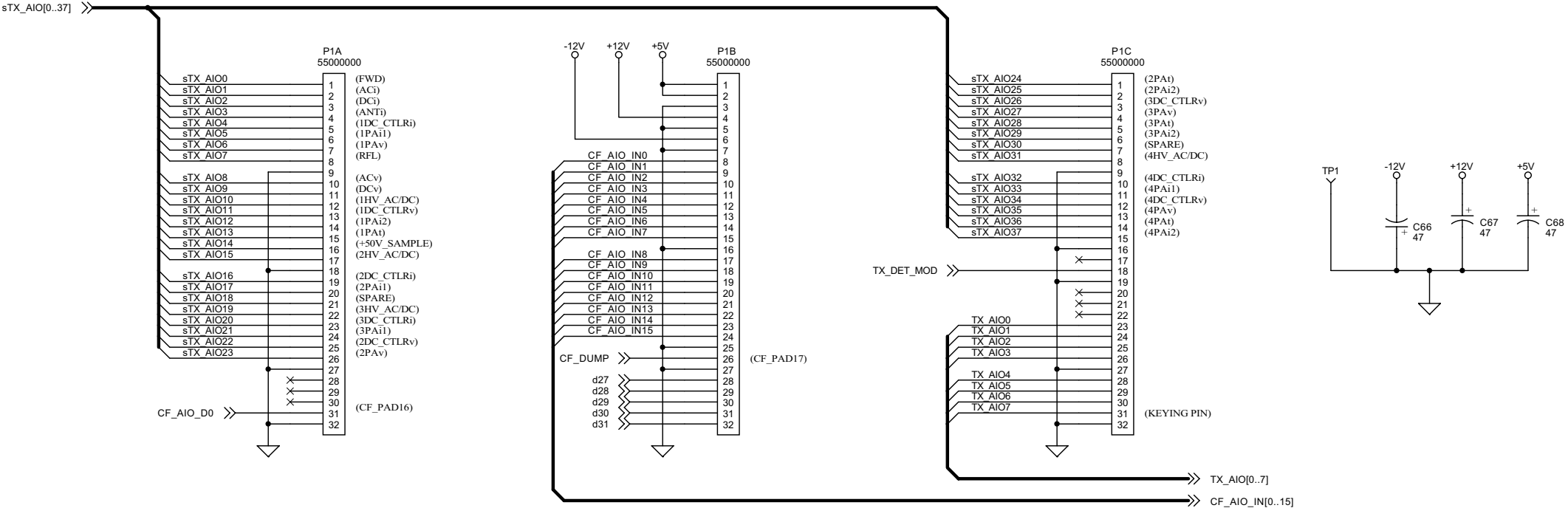
SEP10005 - SCHEMATIC, ANALOG I/O PCB
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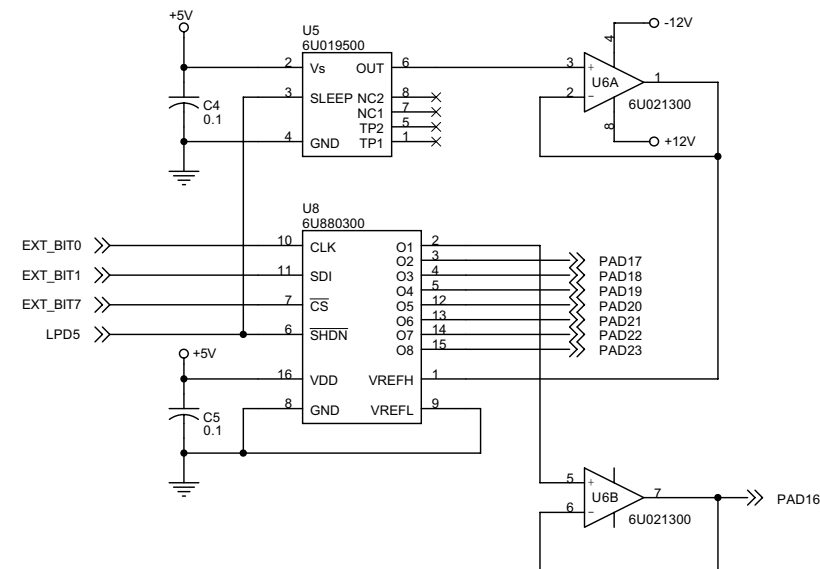
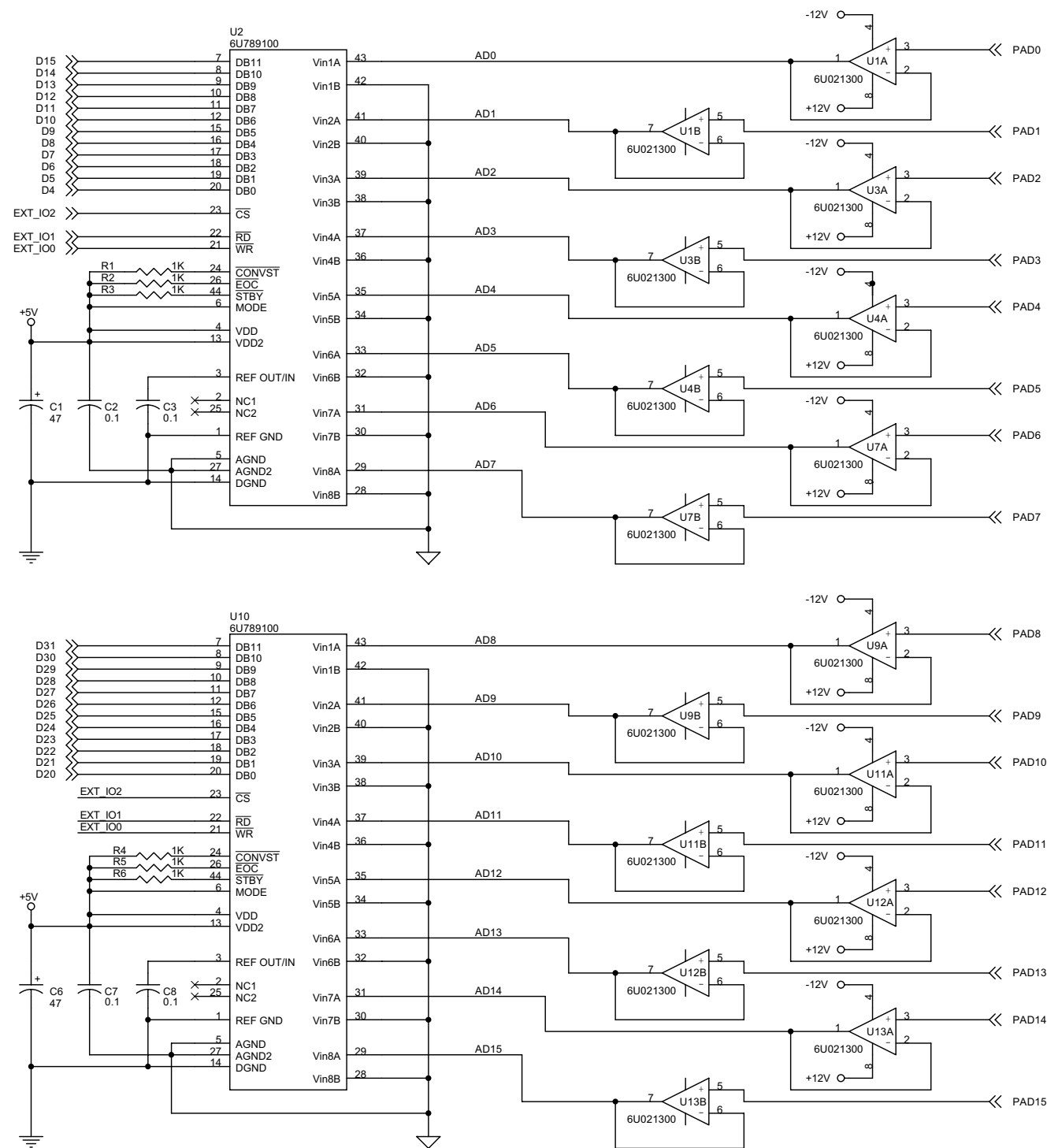
SEP10005 - SCHEMATIC, ANALOG I/O PCB
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SEP10005 - SCHEMATIC, ANALOG I/O PCB
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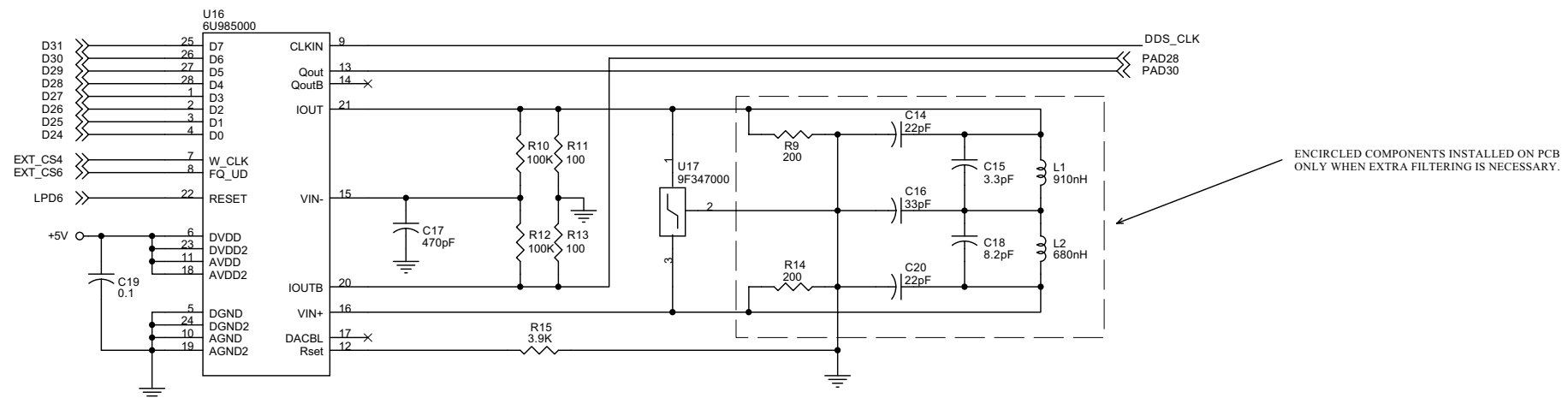
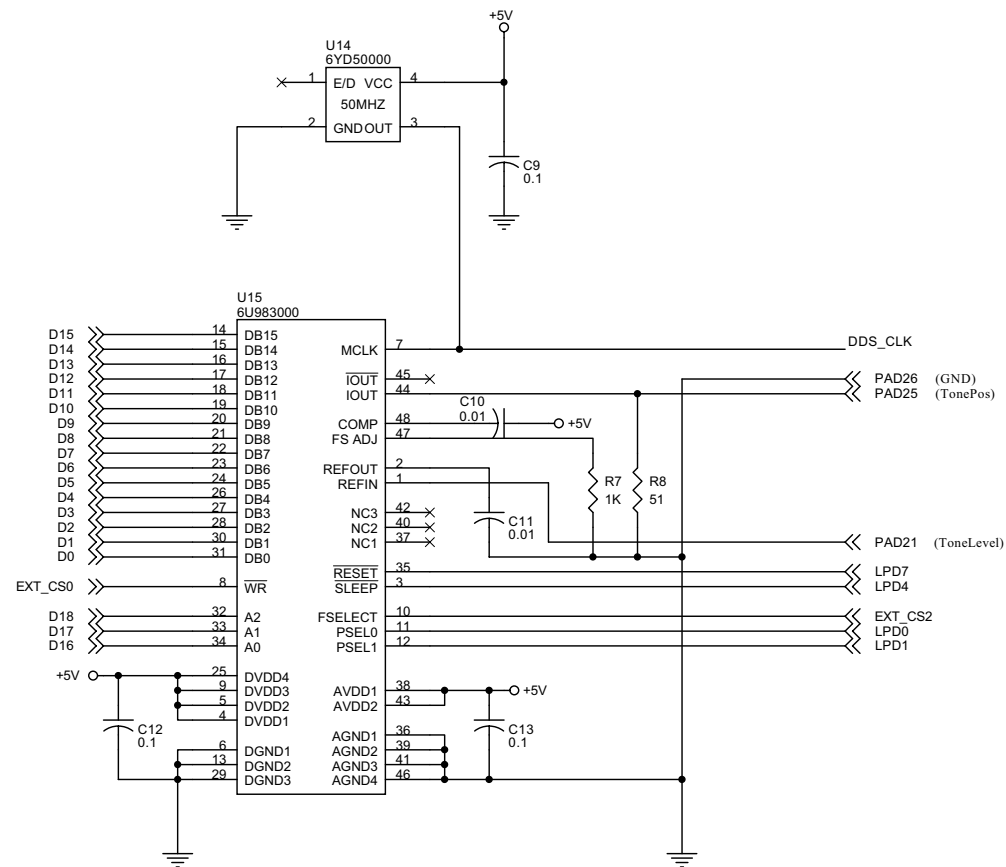


SEP10005 - SCHEMATIC, ANALOG I/O PCB
SHEET 5 OF 5

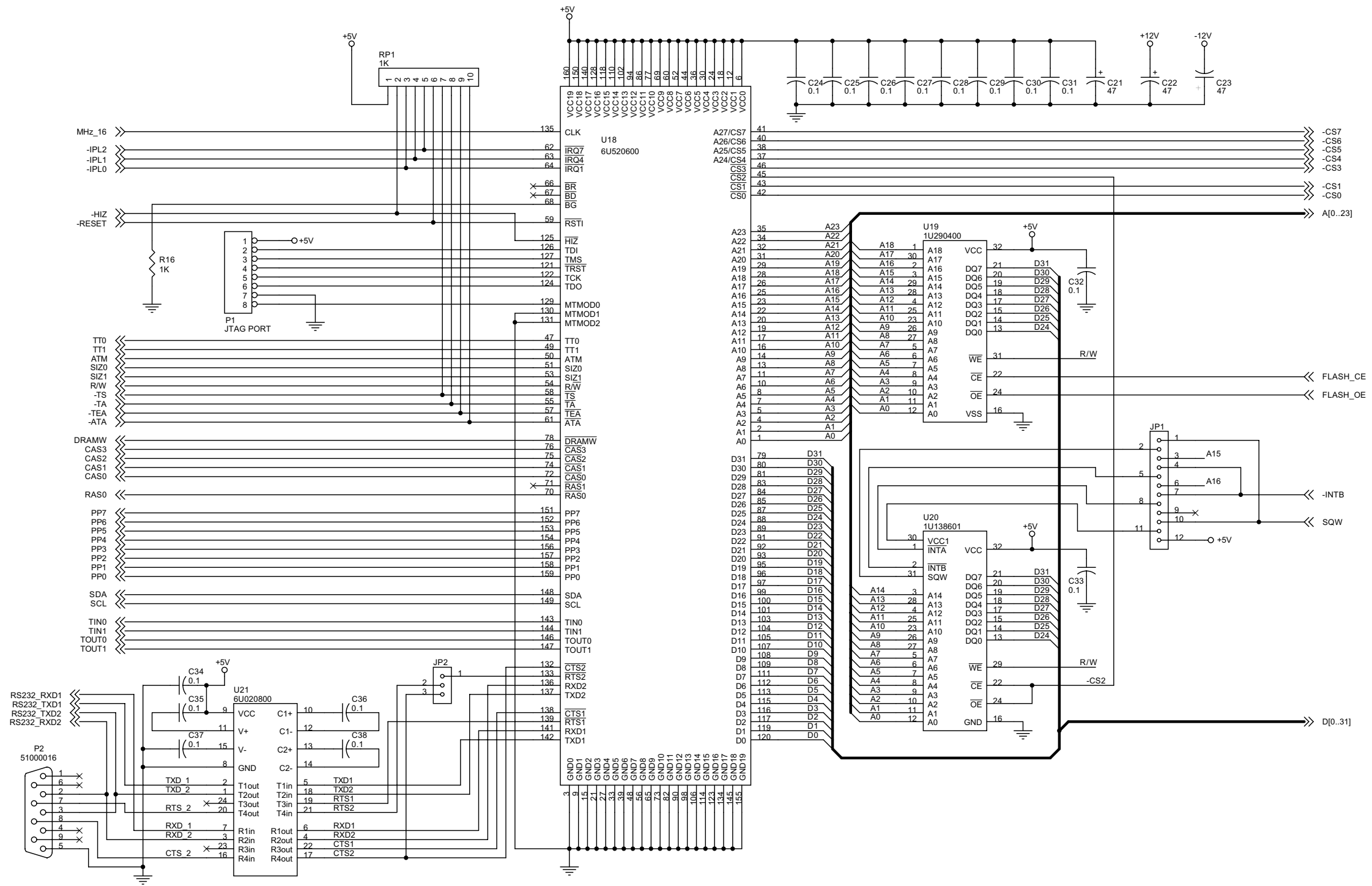


- NOTES
1. THIS DRAWING NO. SEP10007 REV. 3.
 2. TITLE: COLD FIRE CONTROLLER PCB SCHEMATIC.
 3. ASSEMBLY DWG. NO. SAP10007.
 4. ALL RESISTORS 1/4W, UNLESS NOTED.
 5. ALL CAPACITORS IN uF, UNLESS NOTED.
 6. * << * INDICATES OFF-PAGE CONNECTION.

SEP10007 - SCHEMATIC, SD SERIES CONTROLLER / MONITOR PCB
SHEET 1 OF 6

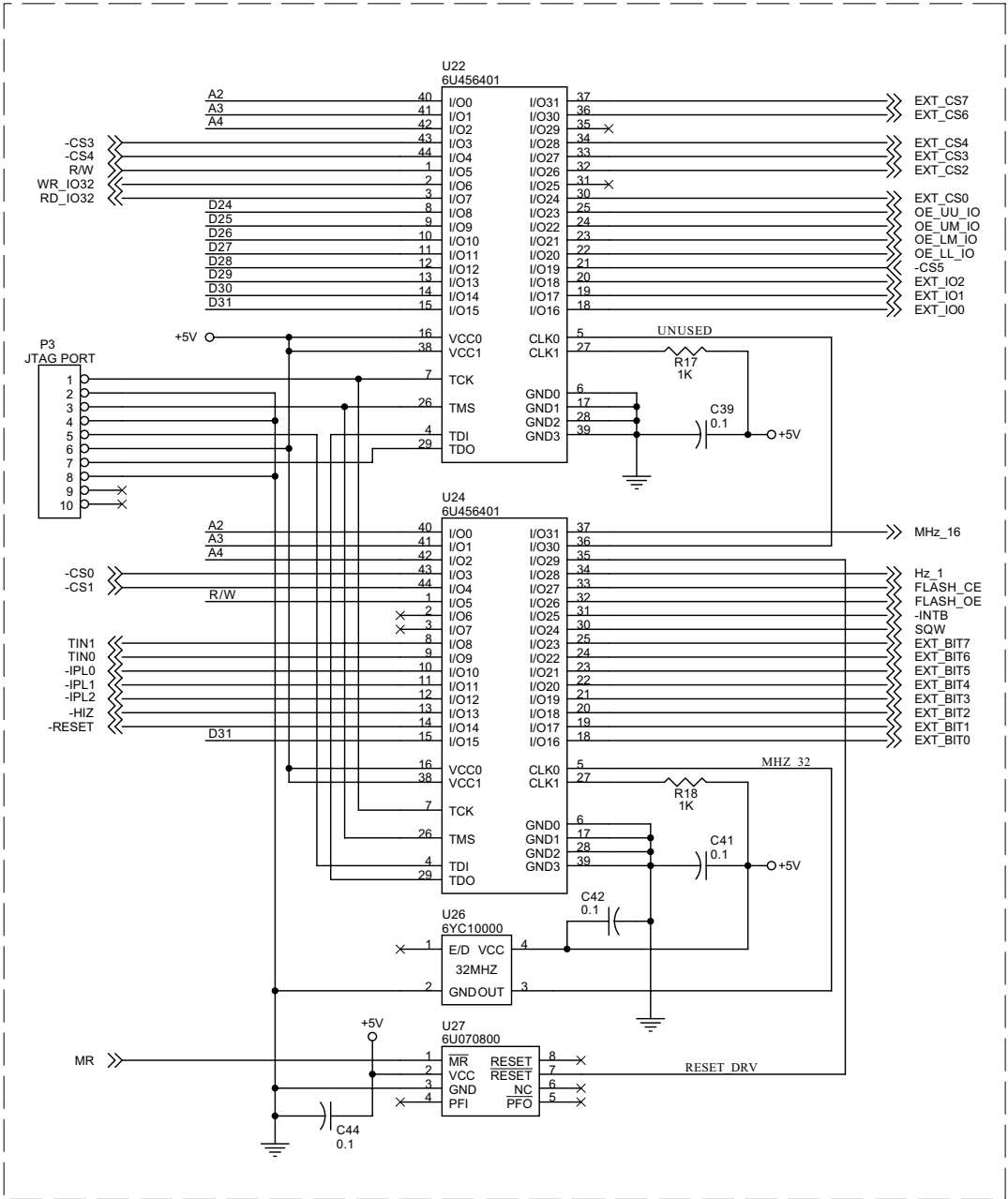


SEP10007 - SCHEMATIC, SD SERIES CONTROLLER / MONITOR PCB
SHEET 2 OF 6

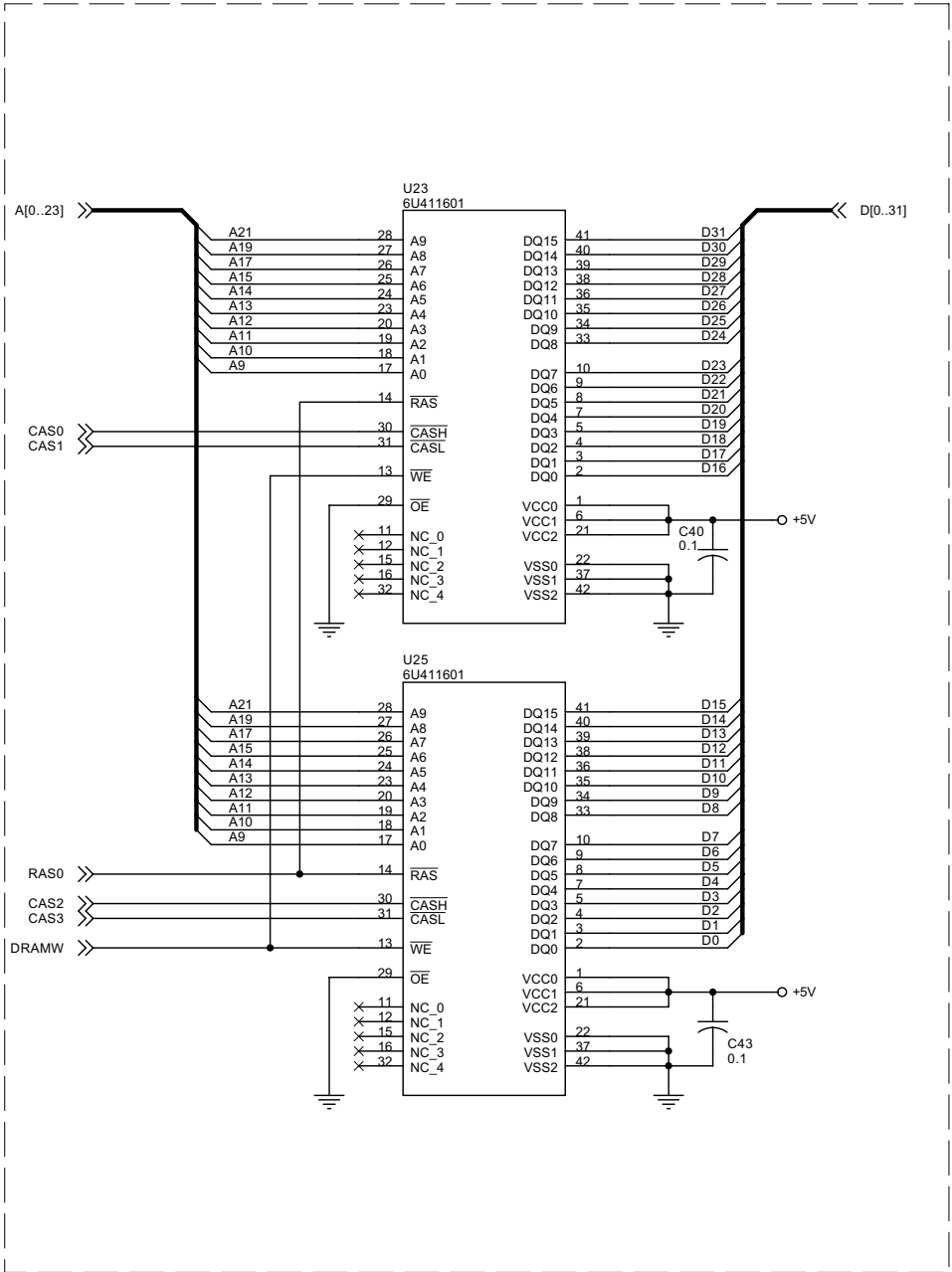


SEP10007 - SCHEMATIC, SD SERIES CONTROLLER / MONITOR PCB
SHEET 3 OF 6

LOGIC SECTION

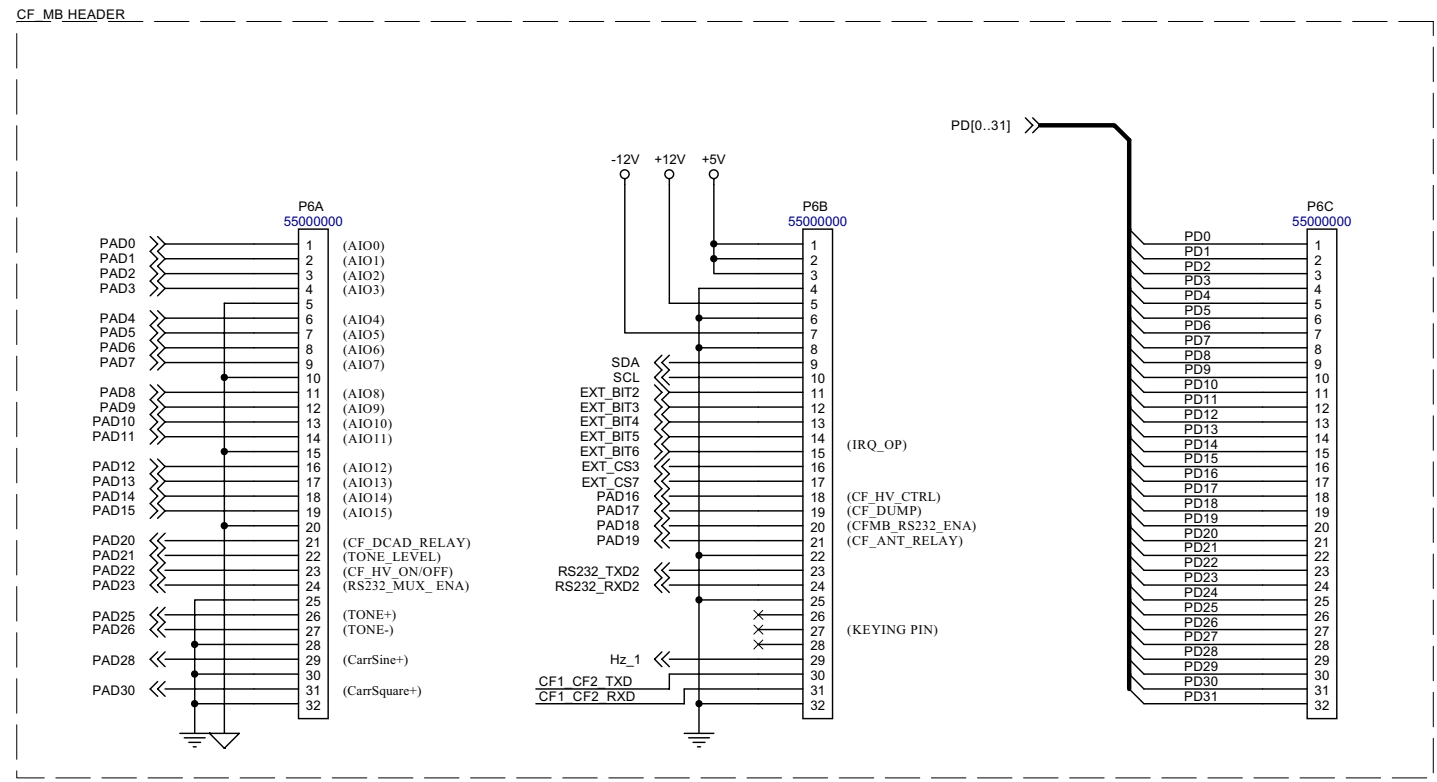
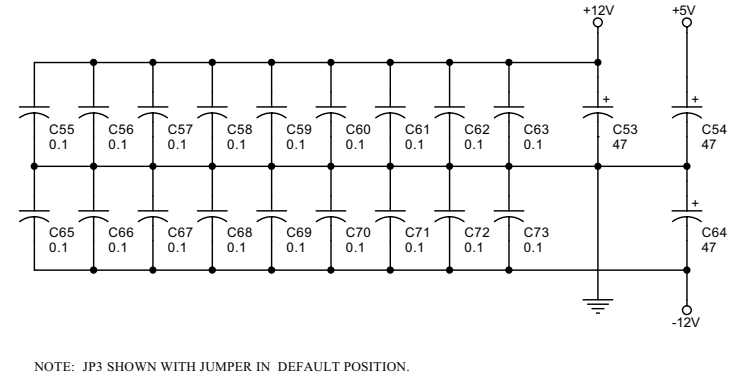
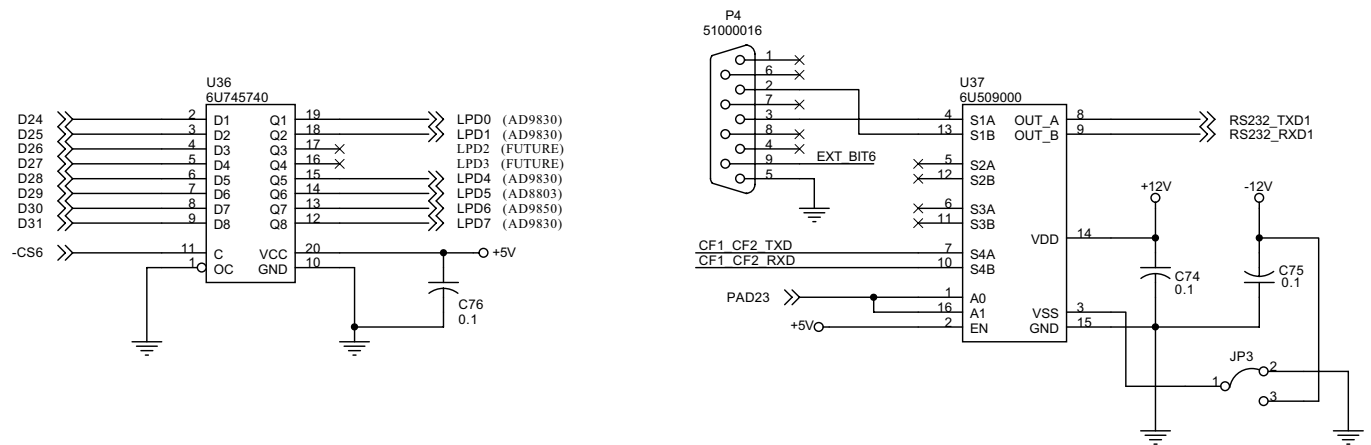


DRAM SECTION

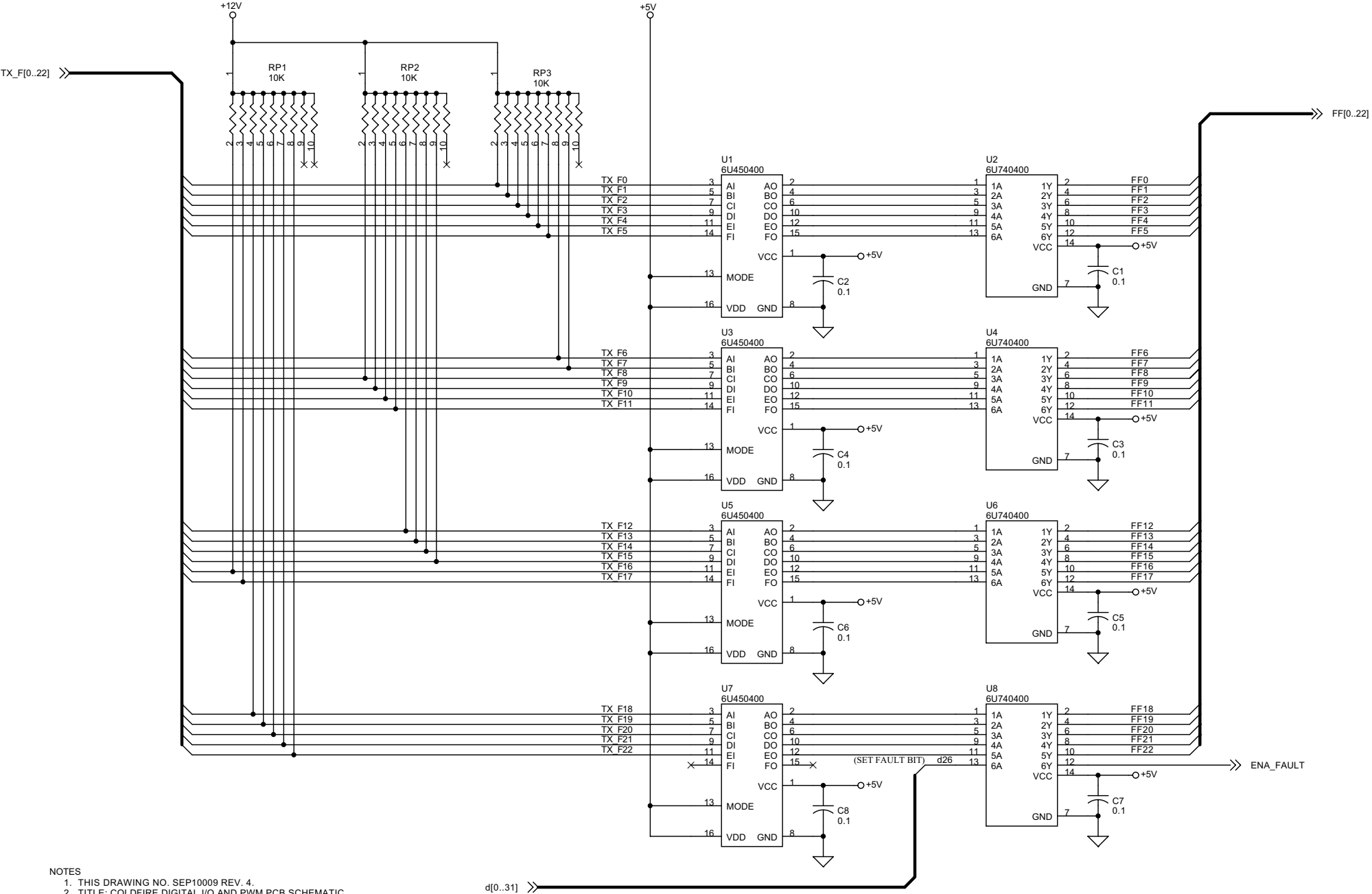


SEP10007 - SCHEMATIC, SD SERIES CONTROLLER / MONITOR PCB
SHEET 4 OF 6





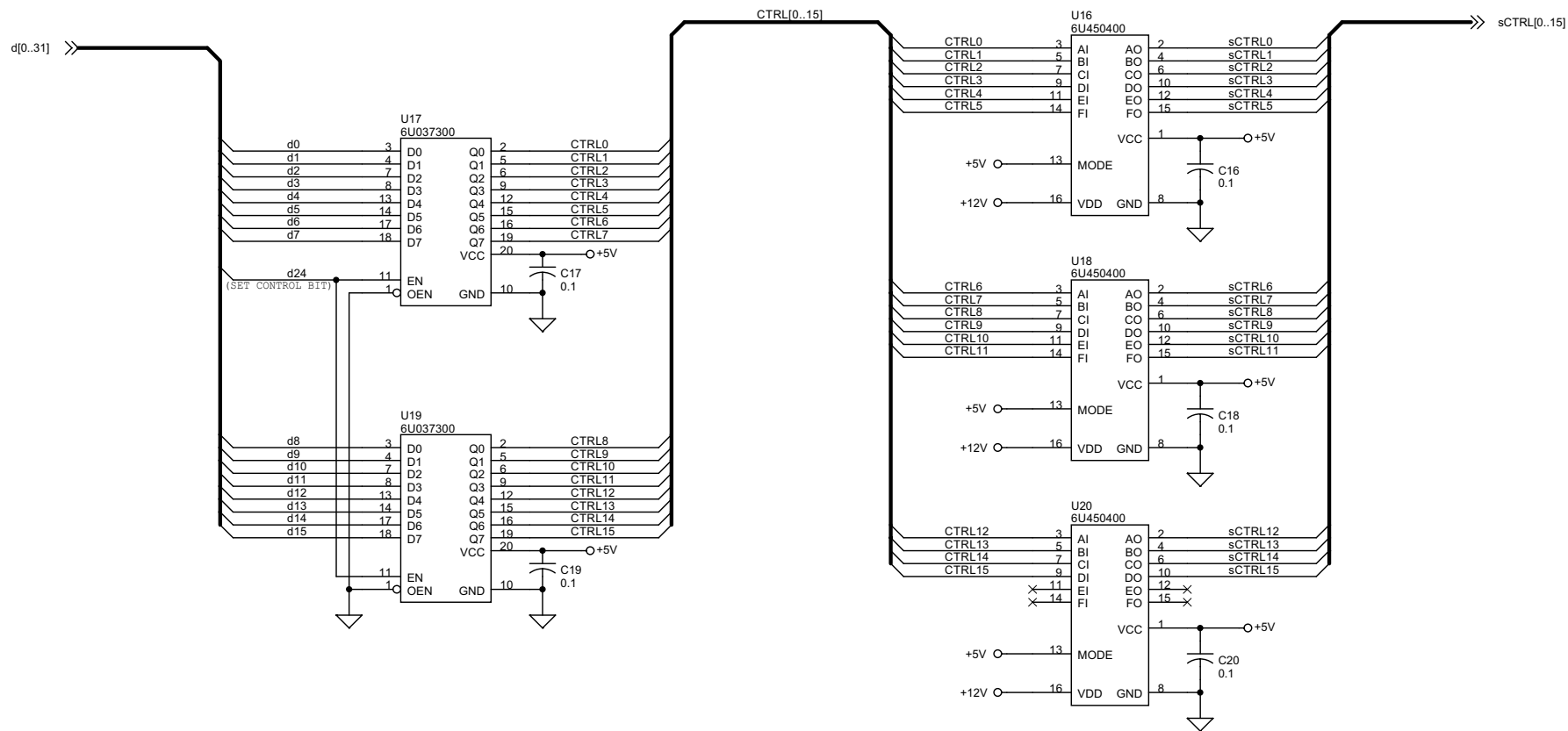
SEP10007 - SCHEMATIC, SD SERIES CONTROLLER / MONITOR PCB
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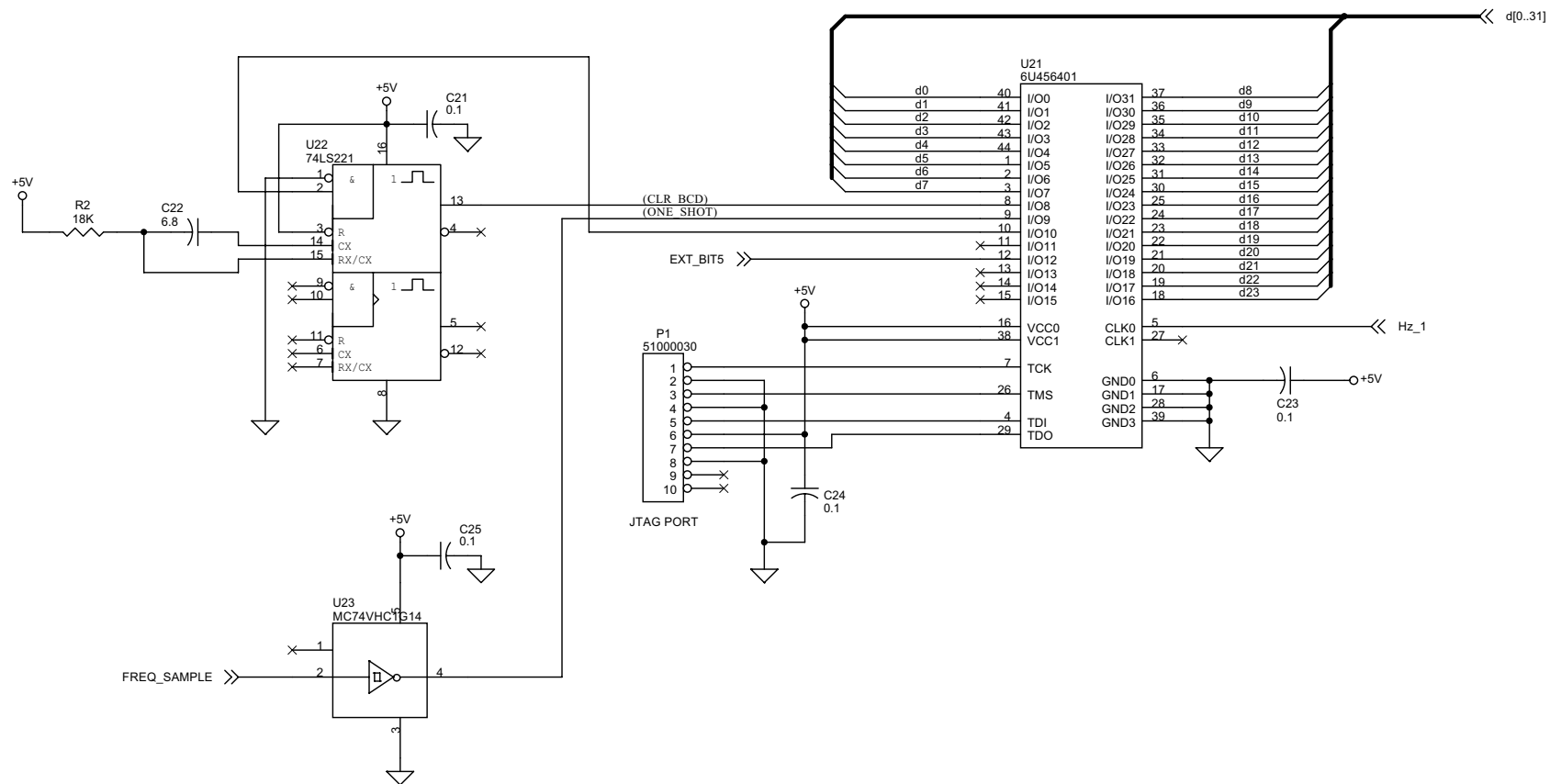
- NOTES
- 1. THIS DRAWING NO. SEP10009 REV. 4.
 - 2. TITLE: COLD FIRE DIGITAL I/O AND PWM PCB SCHEMATIC.
 - 3. ASSEMBLY DWG. NO. SAP10009.
 - 4. ALL RESISTORS 1/4W, UNLESS NOTED.
 - 5. ALL CAPACITORS IN μ F, UNLESS NOTED.
 - 6. " << " INDICATES OFF-PAGE CONNECTION.

SEP10009 - SCHEMATIC, DIGITAL I-O AND PWM PCB
SHEET 1 OF 6

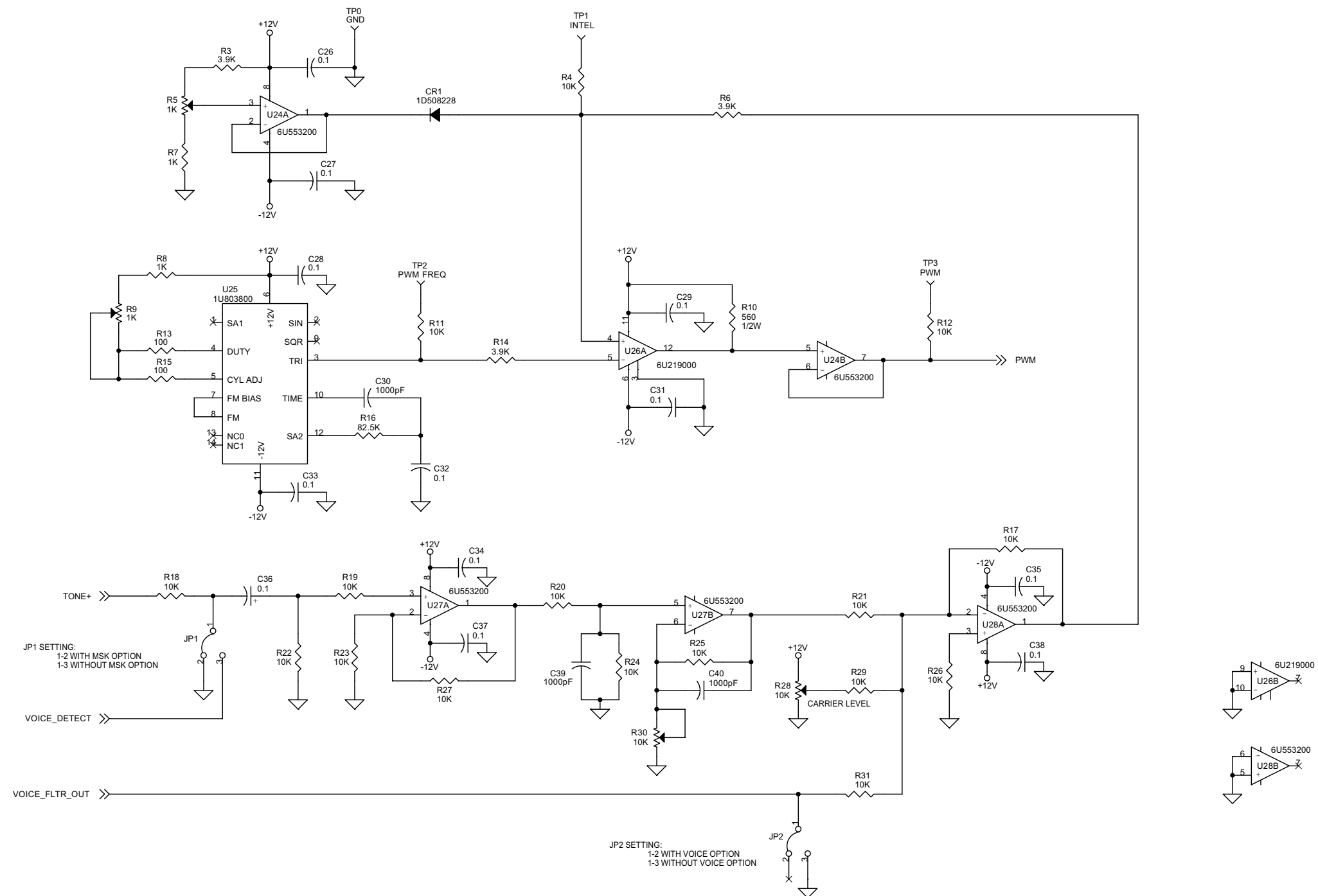




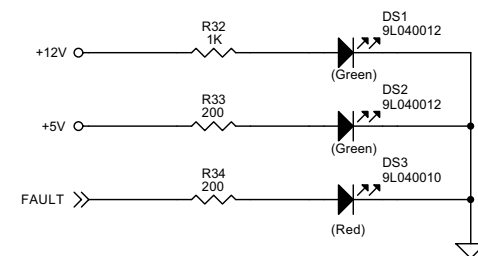
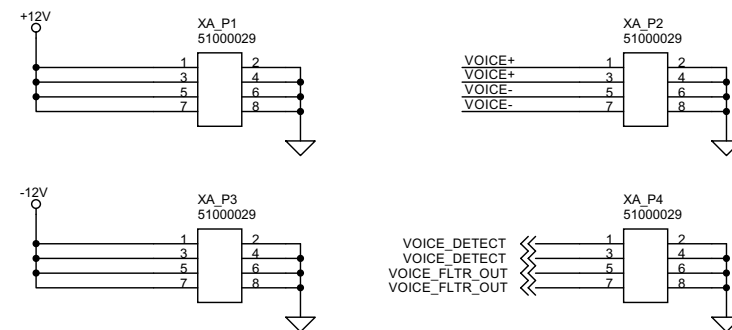
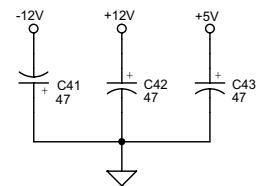
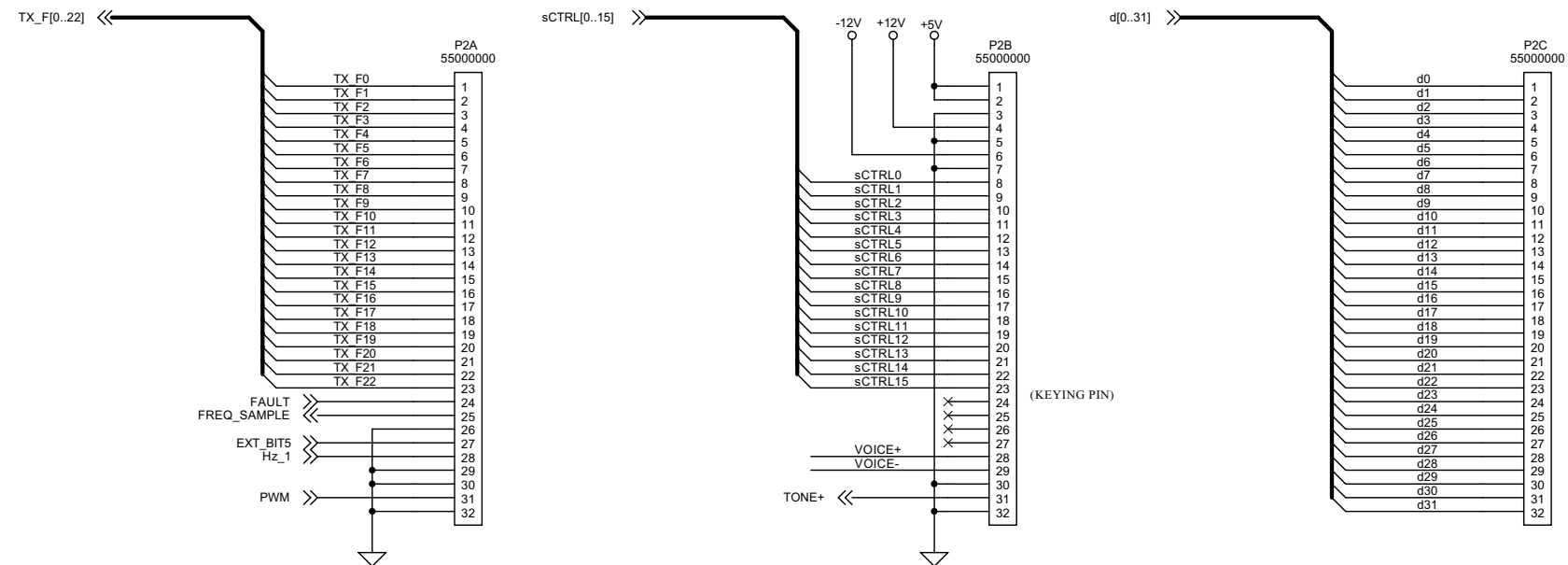
SEP10009 - SCHEMATIC, DIGITAL I-O AND PWM PCB
SHEET 3 OF 6



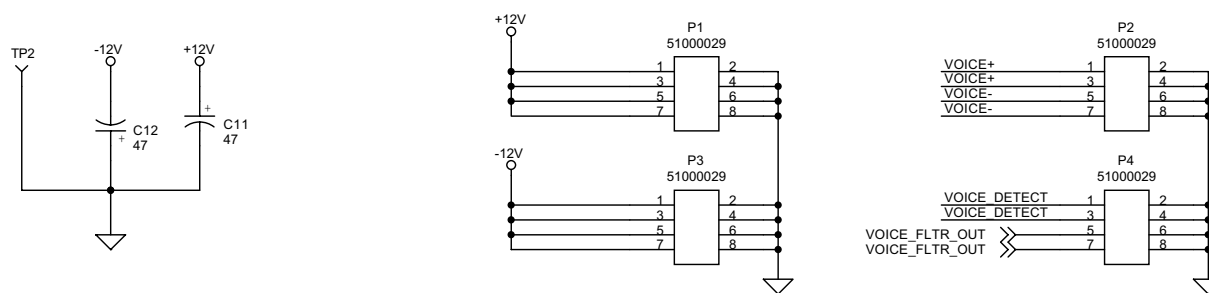
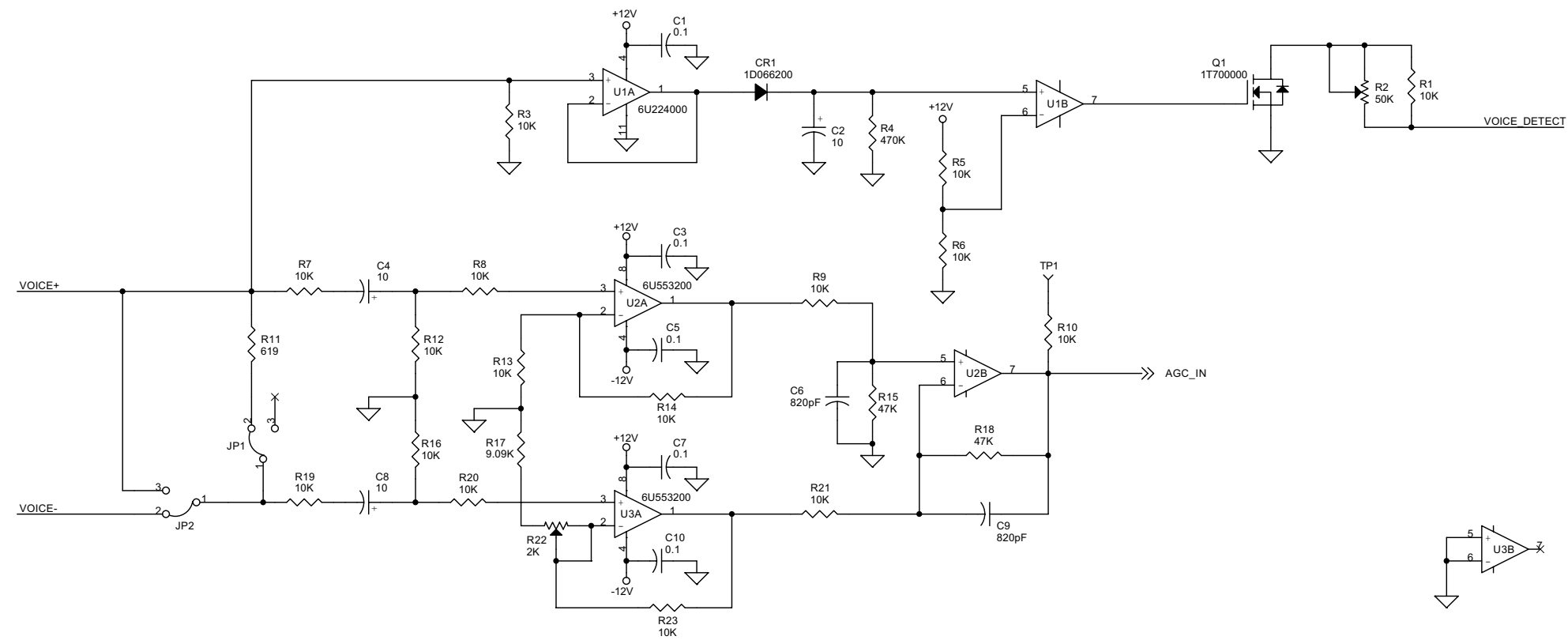
SEP10009 - SCHEMATIC, DIGITAL I-O AND PWM PCB
SHEET 4 OF 6



SEP10009 - SCHEMATIC, DIGITAL I-O AND PWM PCB
SHEET 5 OF 6

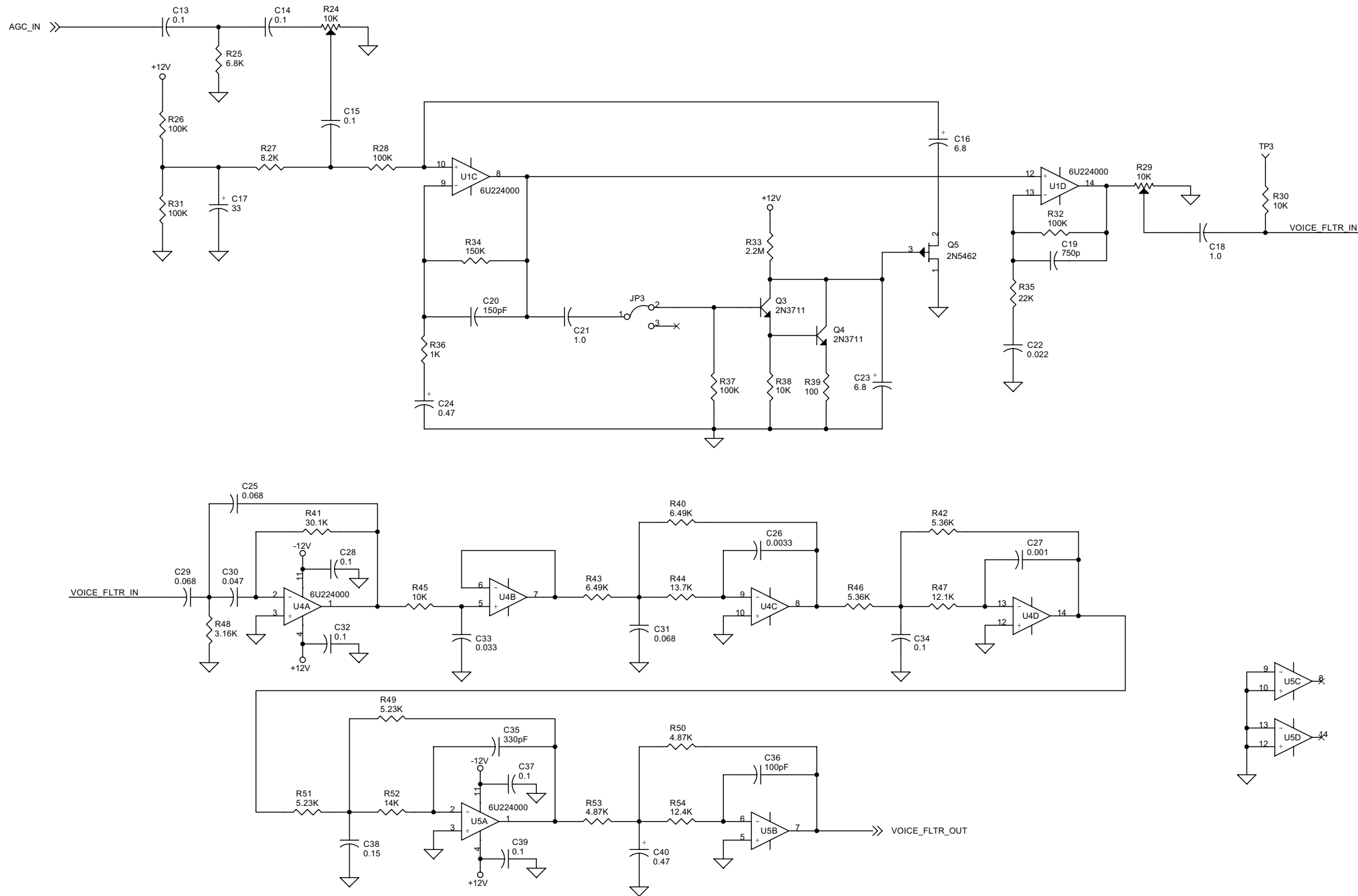


SEP10009 - SCHEMATIC, DIGITAL I-O AND PWM PCB
SHEET 6 OF 6

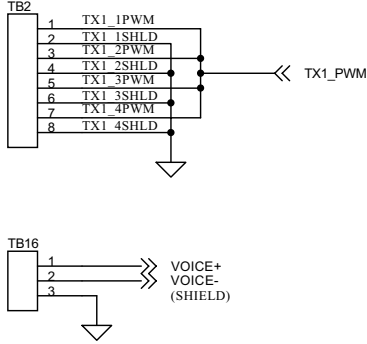
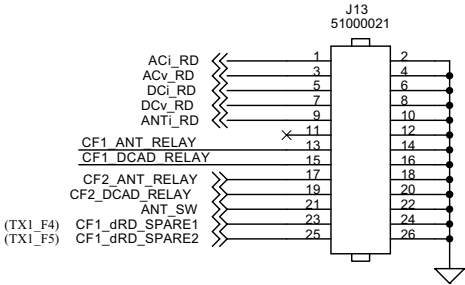
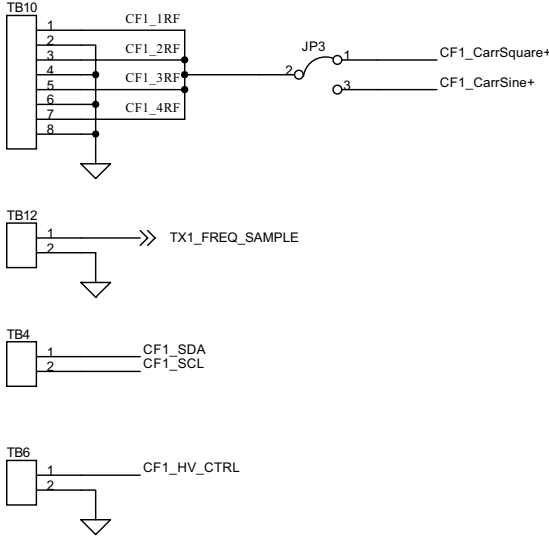
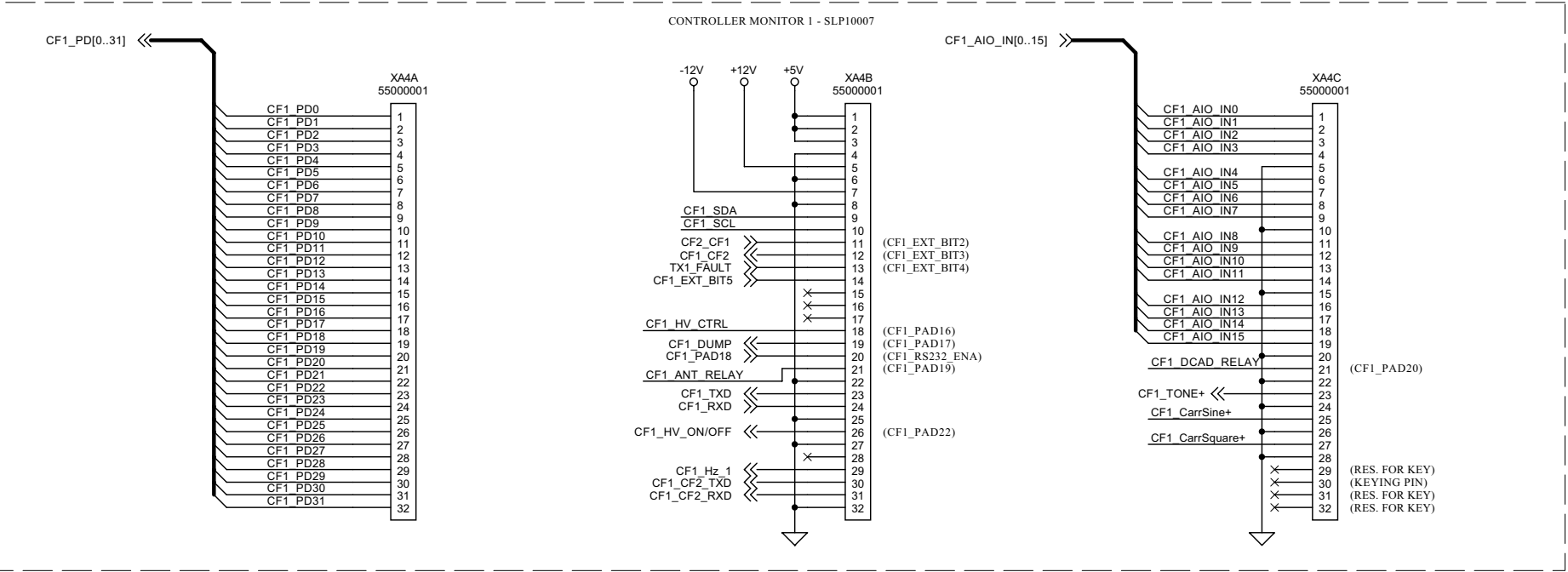


- NOTES
1. THIS DRAWING NO. SEP10020 REV. 1.
 2. TITLE: COLD FIRE AUDIO PCB SCHEMATIC.
 3. ASSEMBLY DWG. NO. SAP10020.
 4. ALL RESISTORS 1/4W, UNLESS NOTED.
 5. ALL CAPACITORS IN μ F, UNLESS NOTED.
 6. " << " INDICATES OFF-PAGE CONNECTION.

SEP10020 - SCHEMATIC, AUDIO PCB
SHEET 1 OF 2



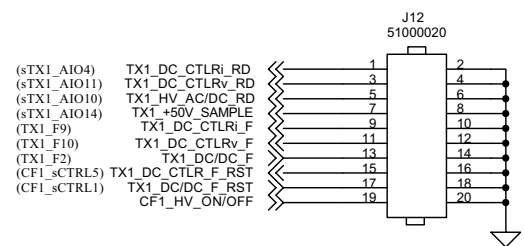
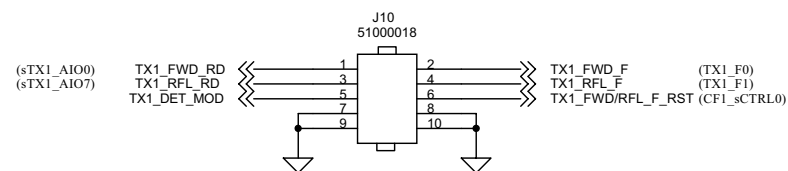
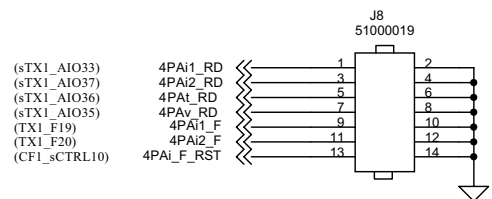
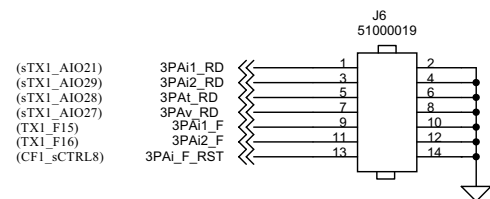
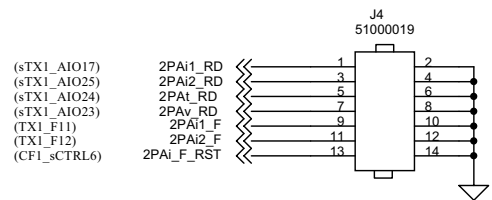
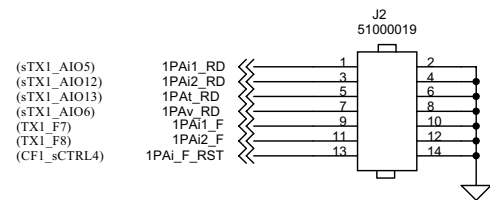
SEP10020 - SCHEMATIC, AUDIO PCB
SHEET 2 OF 2



NOTES
1. THIS DRAWING NO. SEP10012 REV. A.
2. TITLE: CONTROLLER MOTHERBOARD PCB SCHEMATIC.
3. ASSEMBLY DWG. NO. SAP10012.
4. ALL CAPACITORS IN μ F, UNLESS NOTED.
5. * << * INDICATES OFF-PAGE CONNECTION.

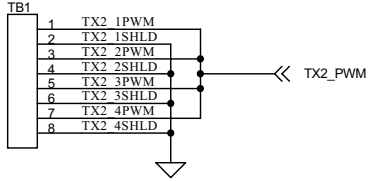
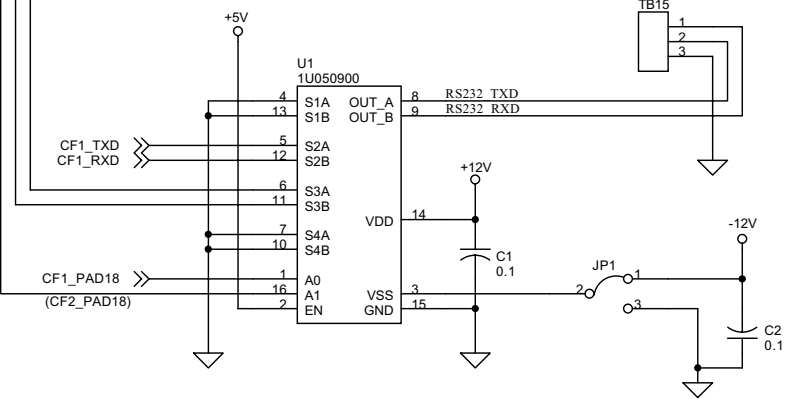
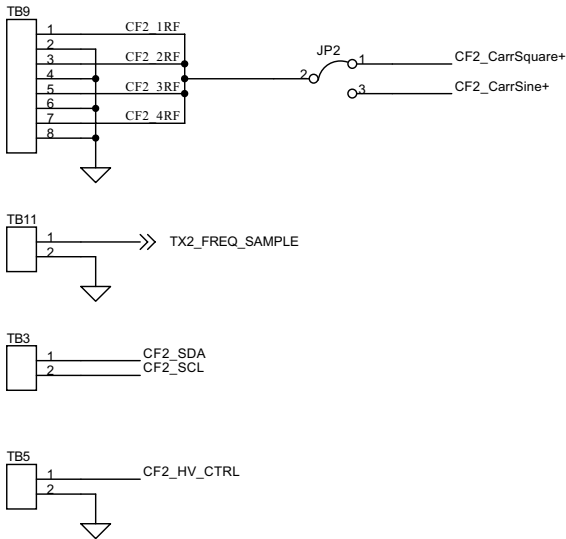
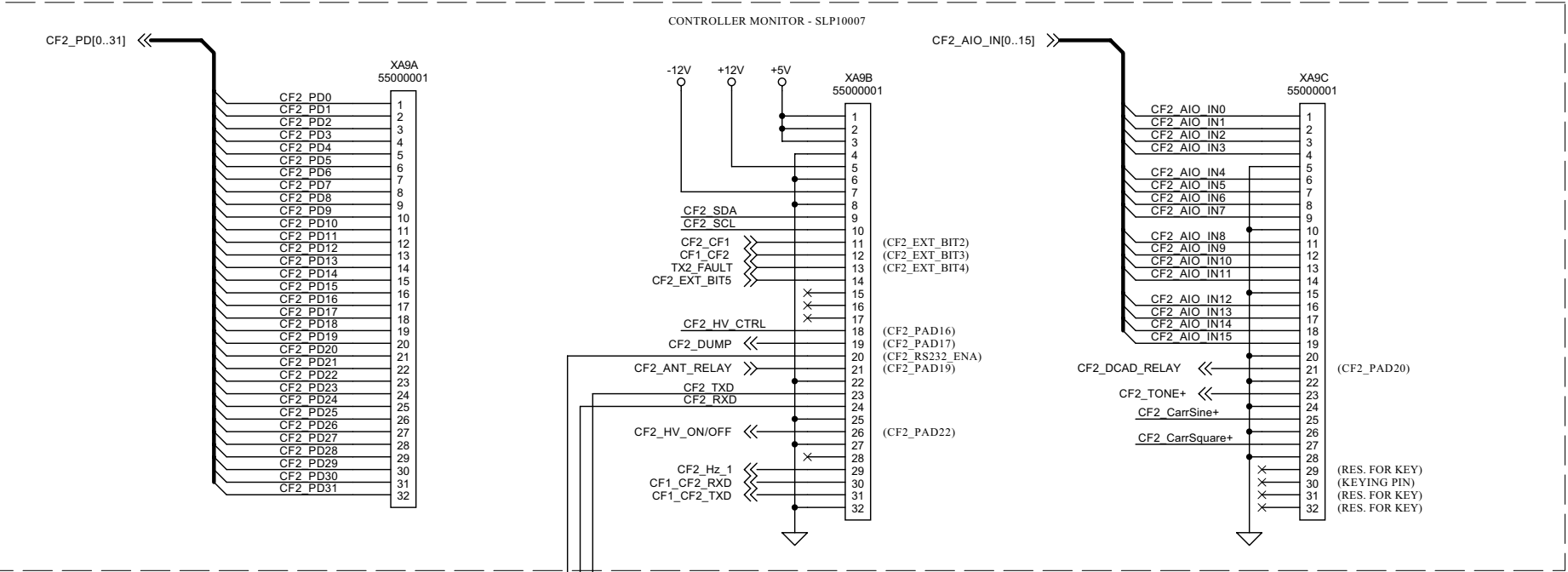
CONTROLLER

SEP10012 - SCHEMATIC, CONTROLLER MOTHERBOARD PCB
SHEET 1 OF 7



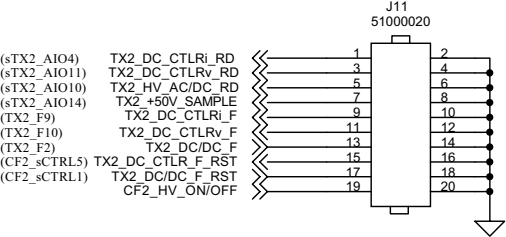
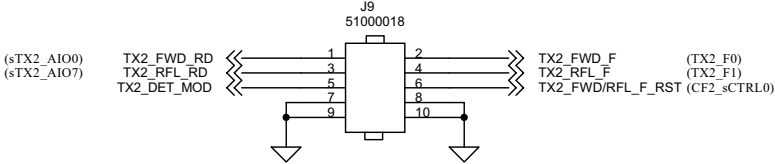
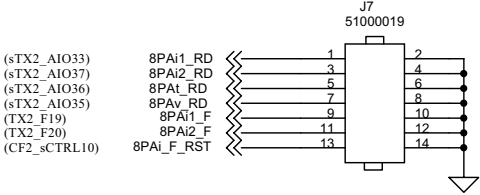
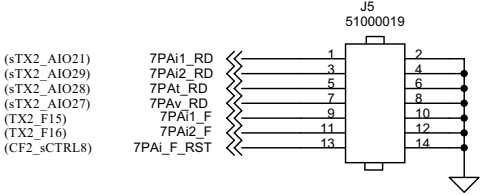
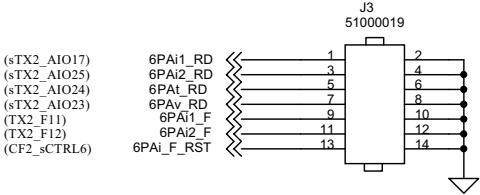
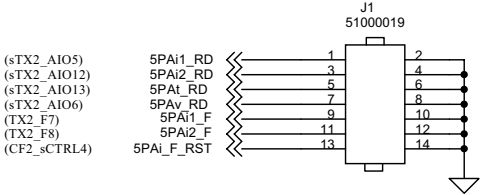
CONTROLLER

SEP10012 - SCHEMATIC, CONTROLLER MOTHERBOARD PCB SHEET 2 OF 7



CONTROLLER

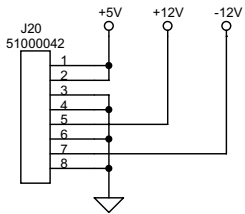
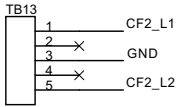
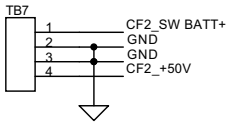
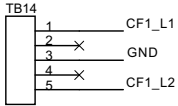
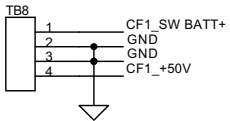
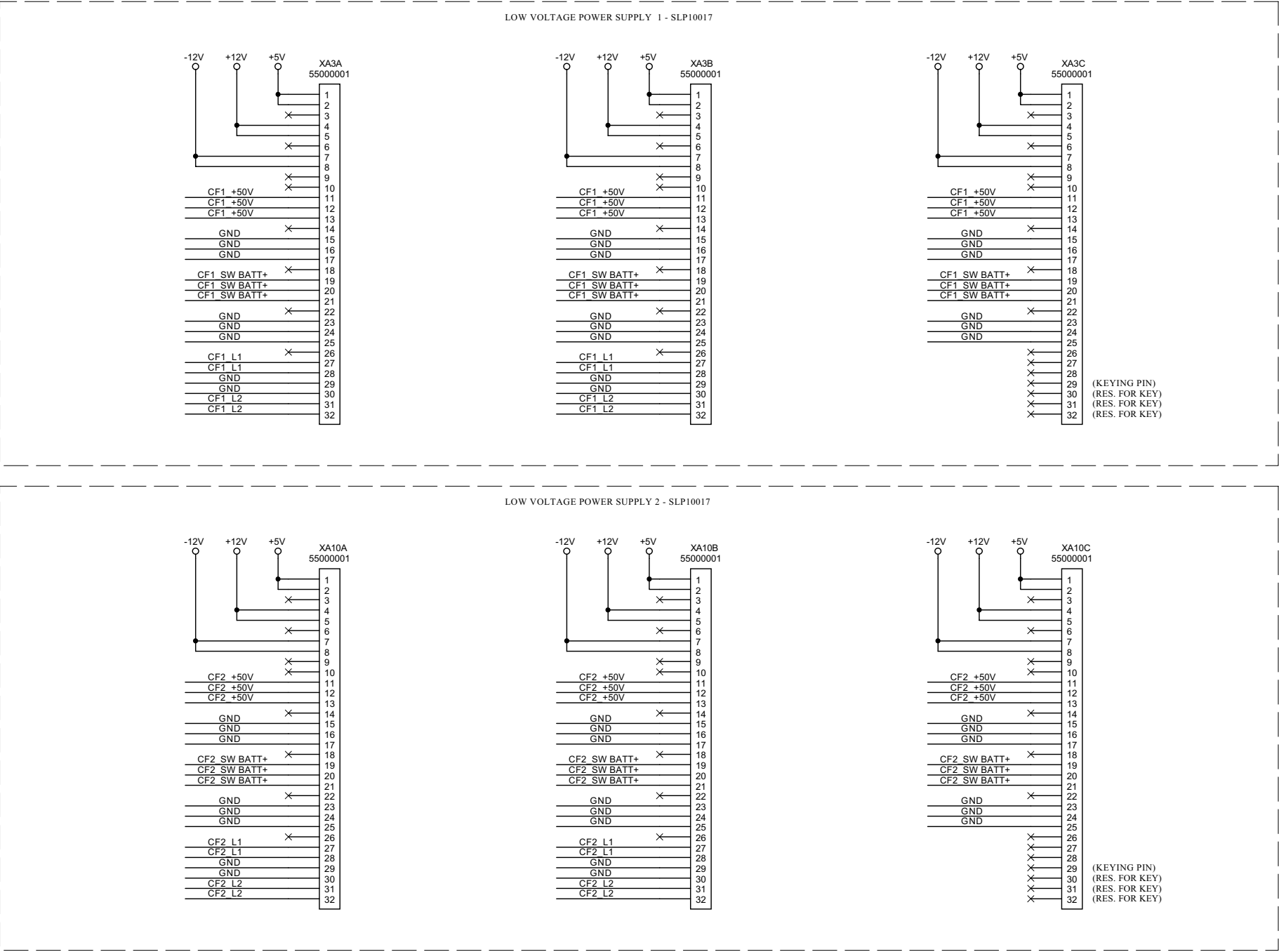
SEP10012 - SCHEMATIC, CONTROLLER MOTHERBOARD PCB
SHEET 4 OF 7



CONTROLLER

SEP10012 - SCHEMATIC, CONTROLLER MOTHERBOARD PCB
SHEET 5 OF 7

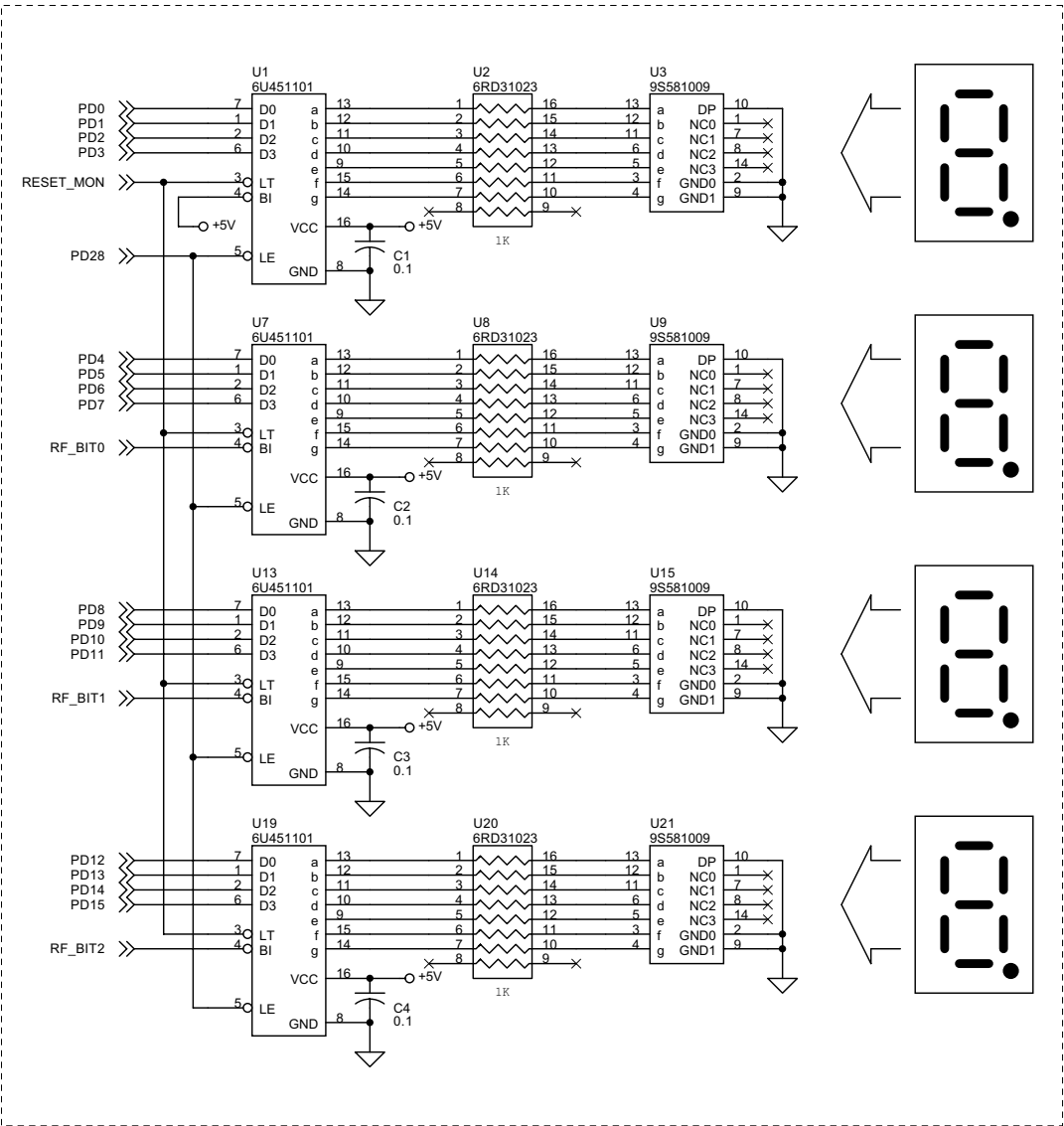




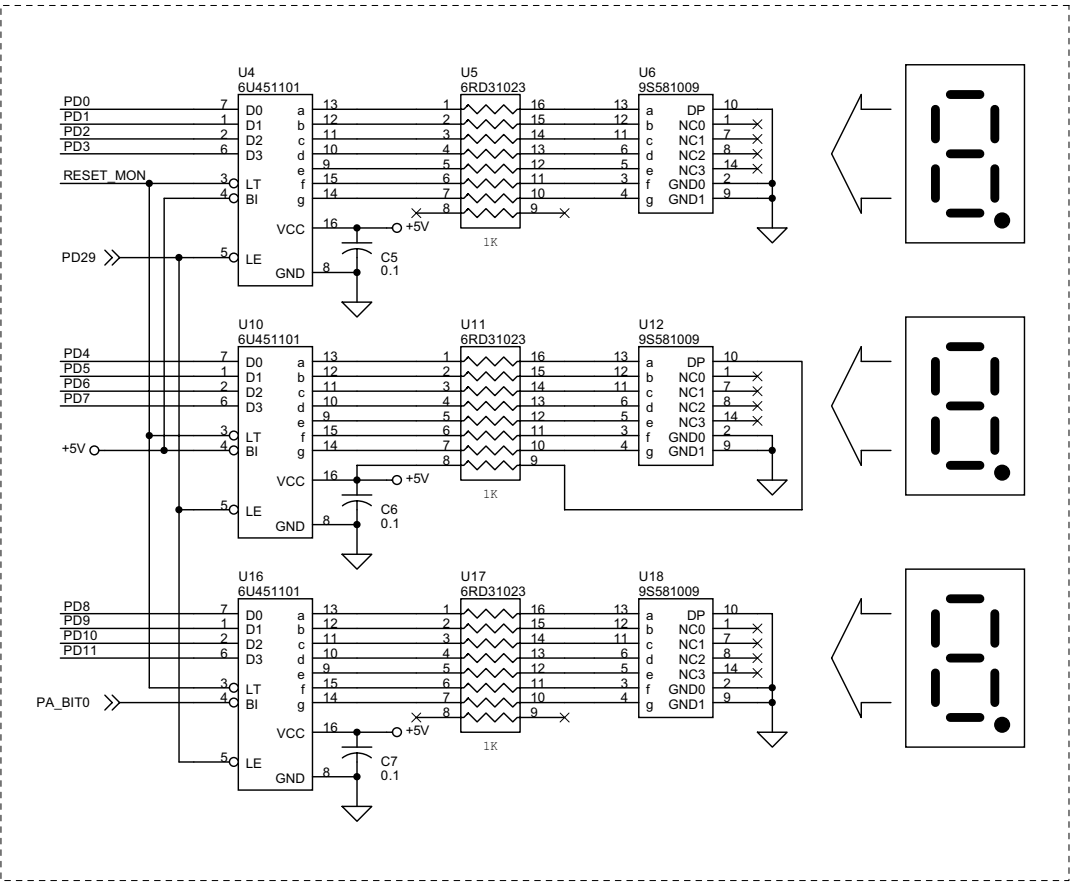
CONTROLLER

SEP10012 - SCHEMATIC, CONTROLLER MOTHERBOARD PCB
SHEET 7 OF 7

RF: 7-SEGMENT DISPLAYS



POWER AMPLIFIER: 7-SEGMENT DISPLAYS

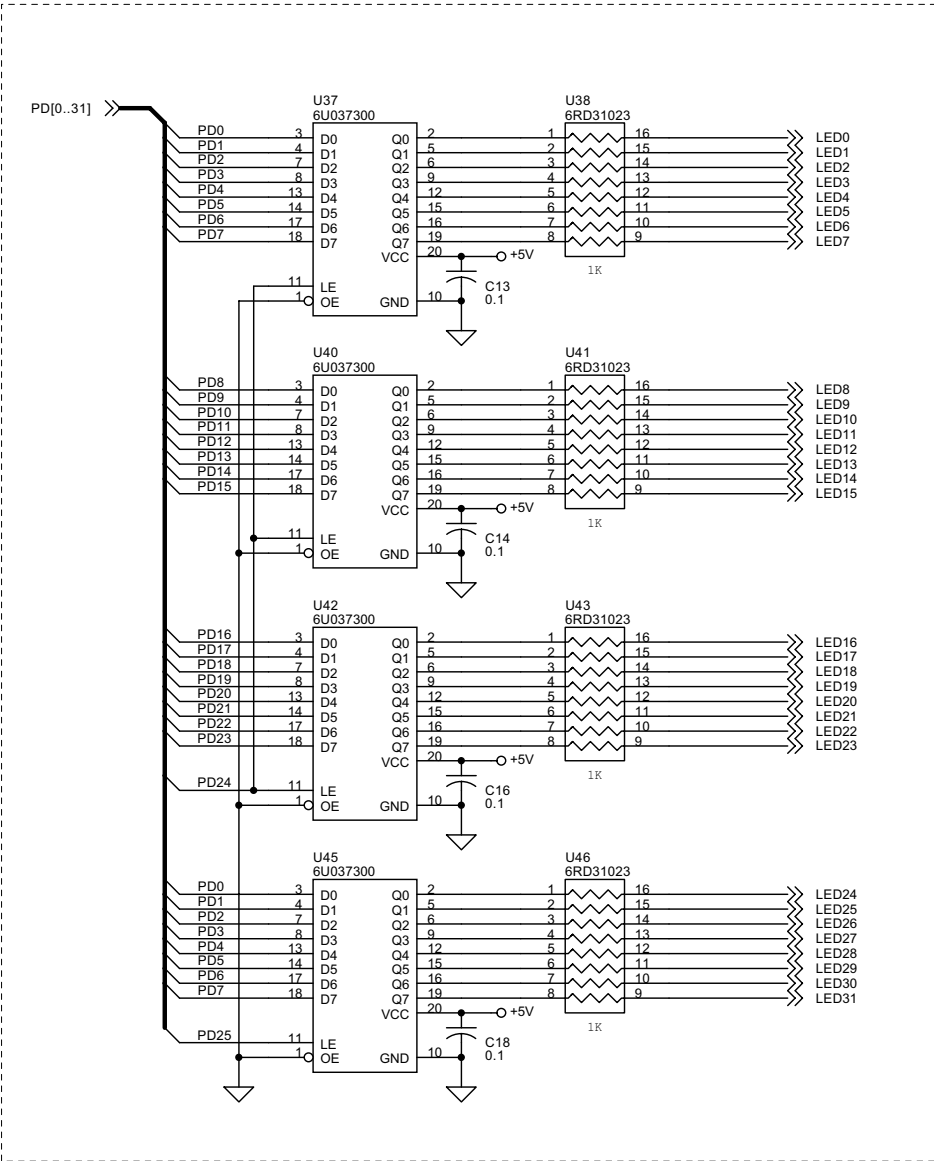


- NOTES
1. THIS DRAWING NO. SEP10016 REV. 4.
 2. TITLE: LOCAL/REMOTE CONTROL MONITOR PCB SCHEMATIC.
 3. ASSEMBLY DWG. NO. SAP10016.
 4. ALL RESISTORS 1/4W, UNLESS NOTED.
 5. ALL CAPACITORS IN uF, UNLESS NOTED.
 6. " << " INDICATES OFF-PAGE CONNECTION.

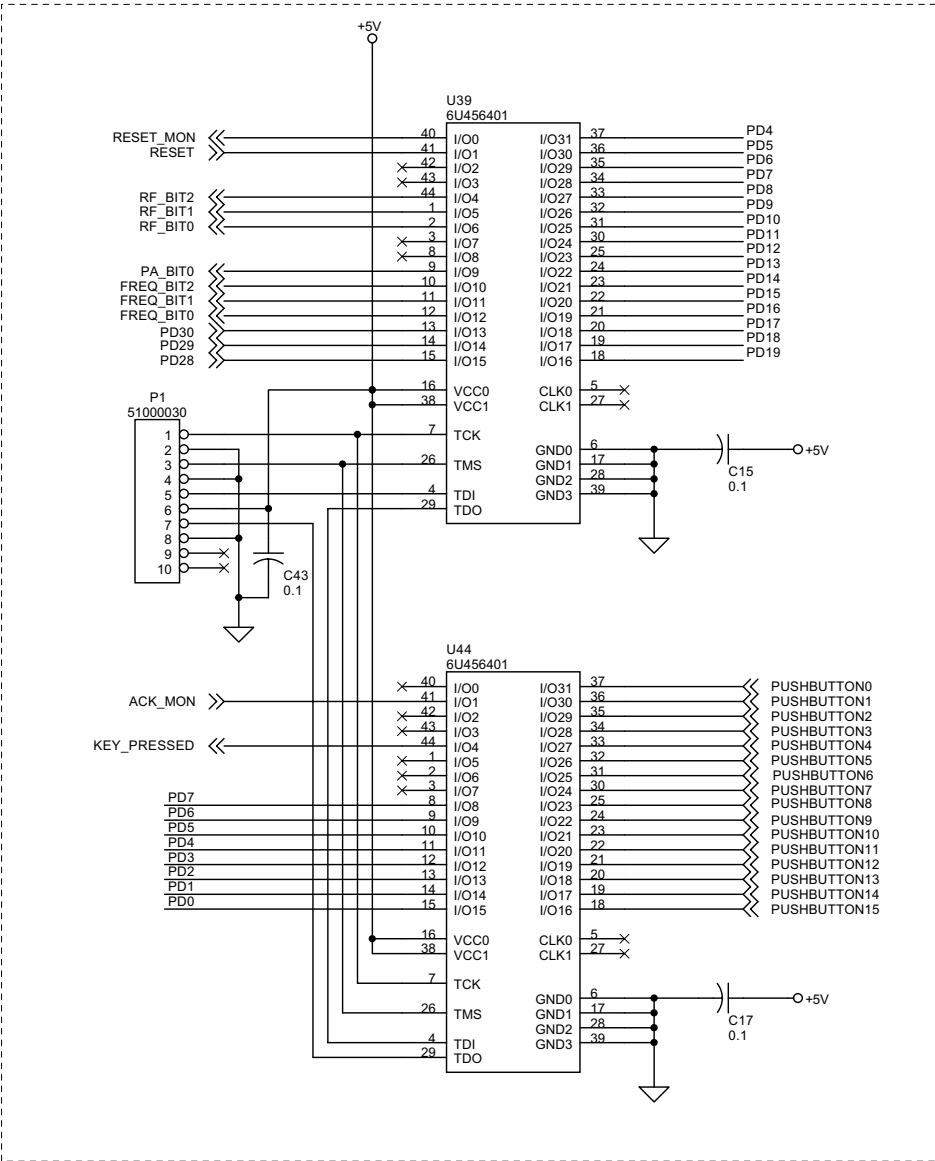
SEP10016 - SCHEMATIC, LOCAL CONTROL PCB
SHEET 1 OF 6

SEP10016 - SCHEMATIC, LOCAL CONTROL PCB
SHEET 2 OF 6

LATCHES - LED INDICATORS

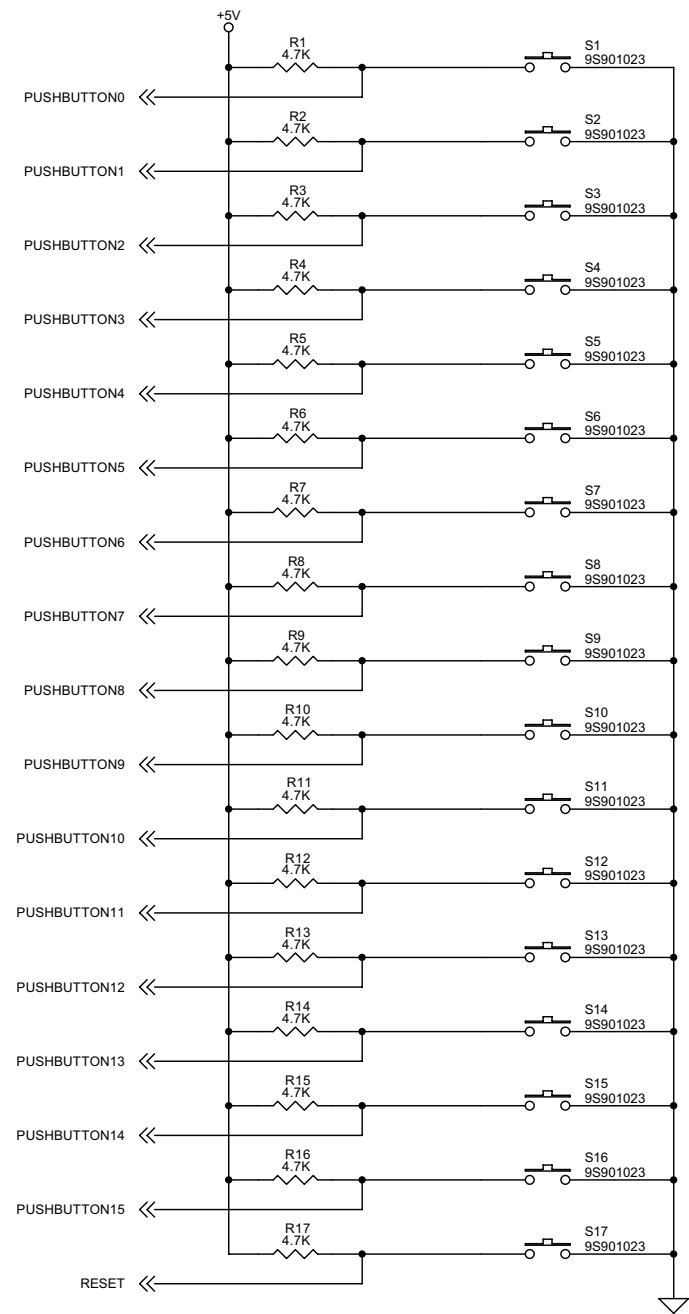


LOGIC

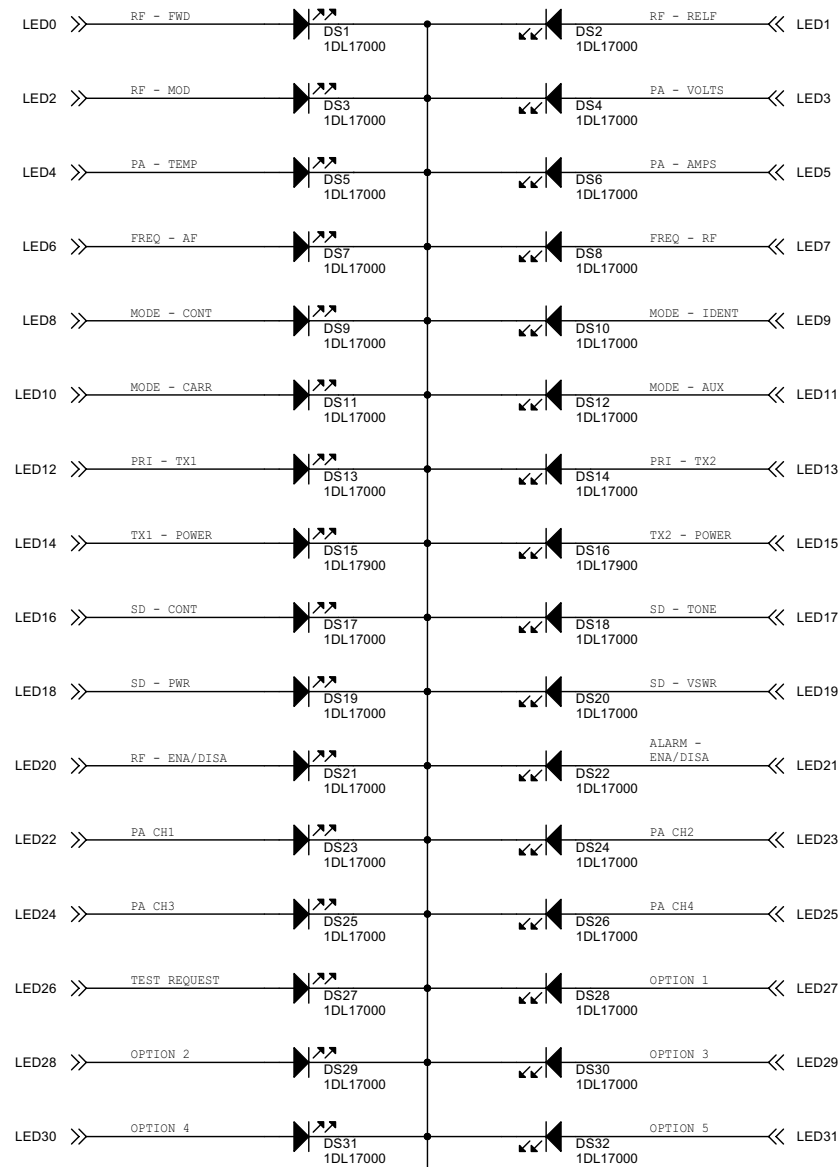


SEP10016 - SCHEMATIC, LOCAL CONTROL PCB
SHEET 3 OF 6

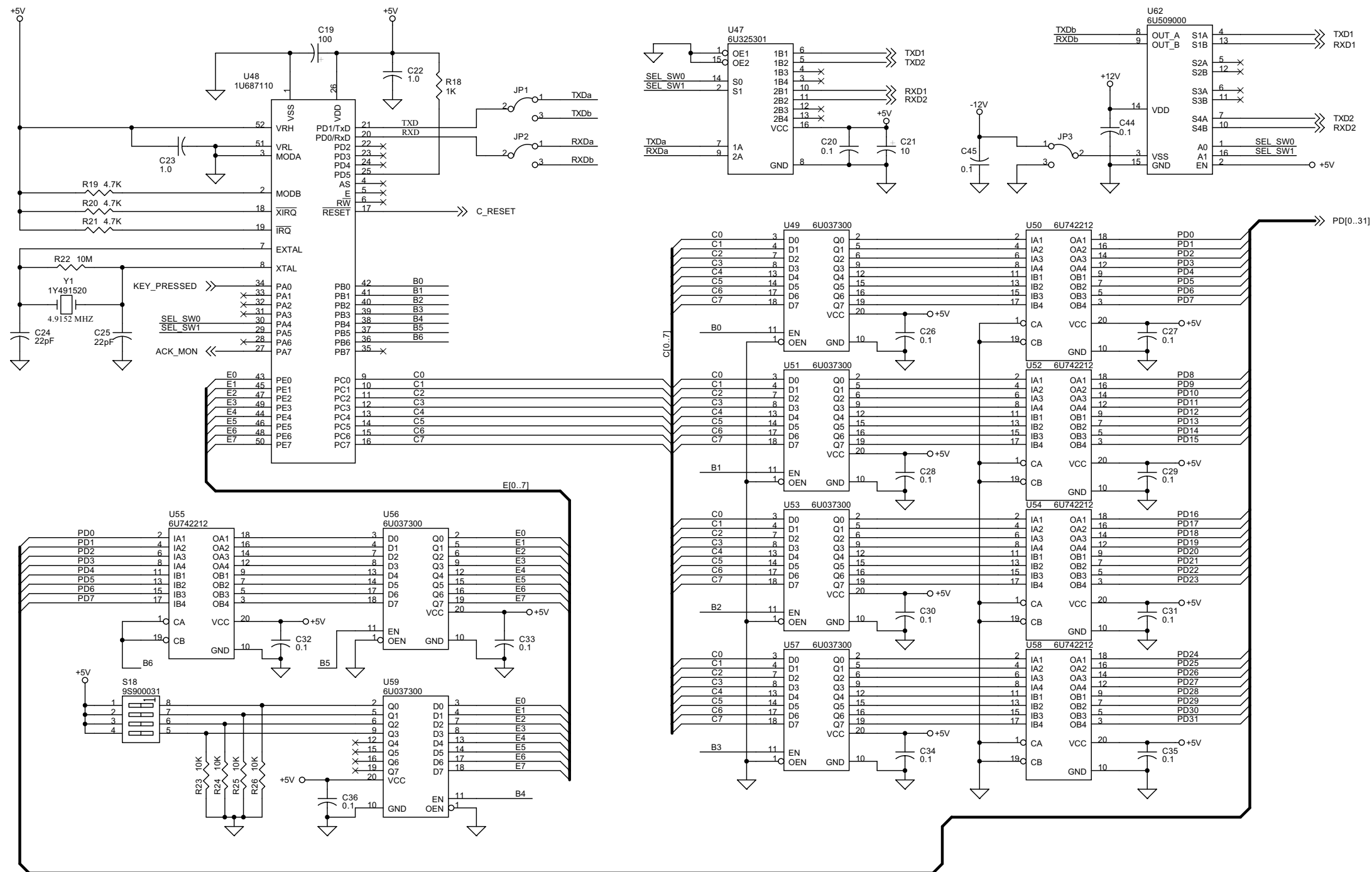
PUSHBUTTONS



STATUS - LED INDICATORS

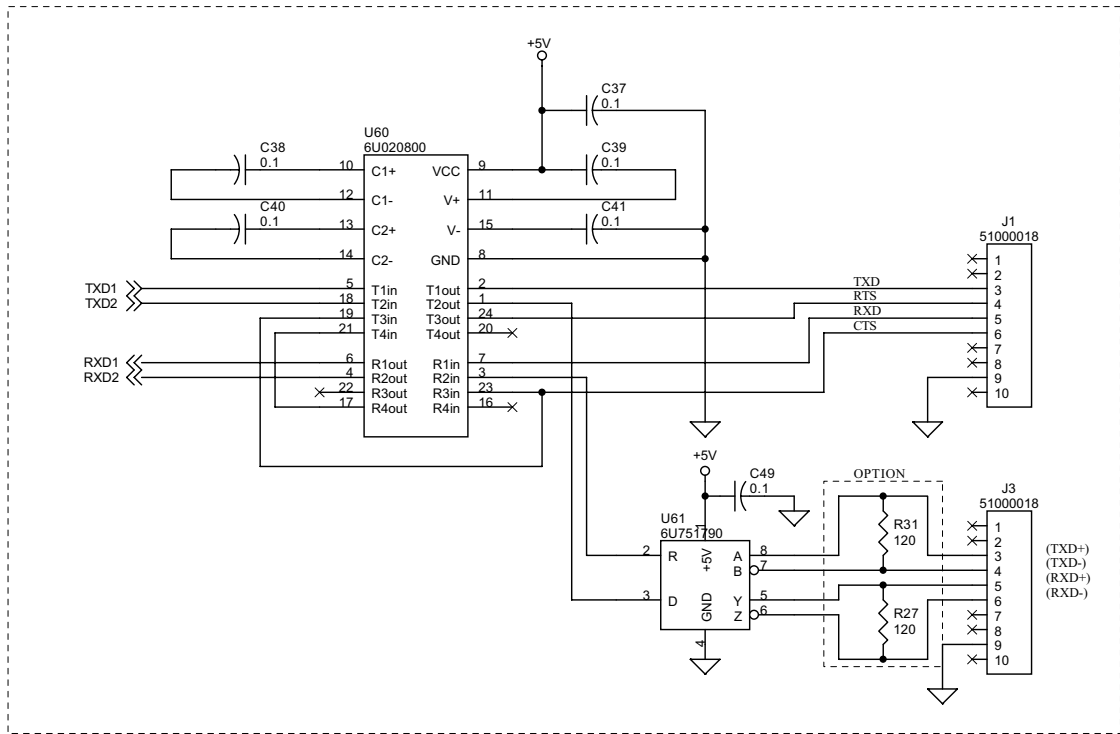


SEP10016 - SCHEMATIC, LOCAL CONTROL PCB
SHEET 4 OF 6

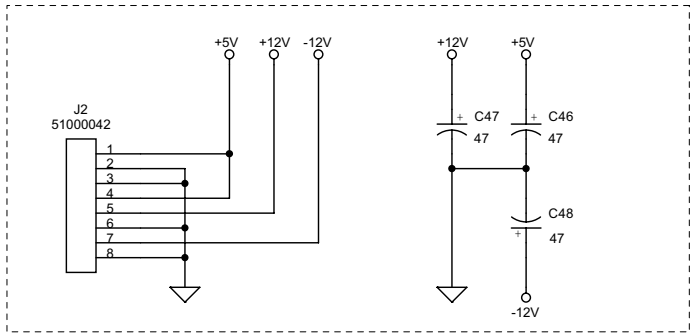


SEP10016 - SCHEMATIC, LOCAL CONTROL PCB
SHEET 5 OF 6

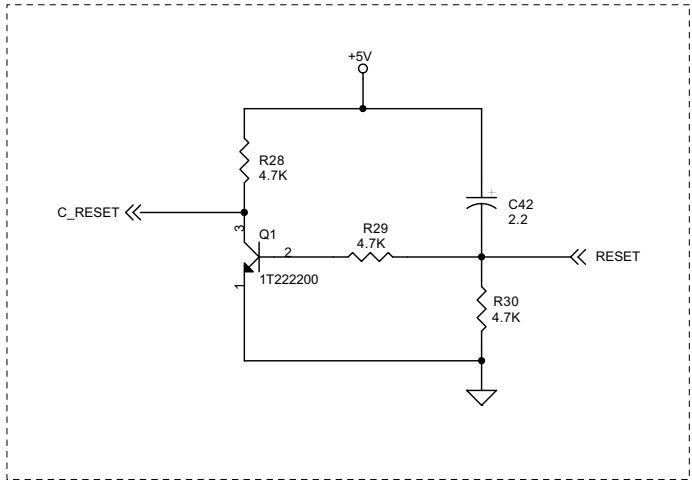
RS-232 / 485



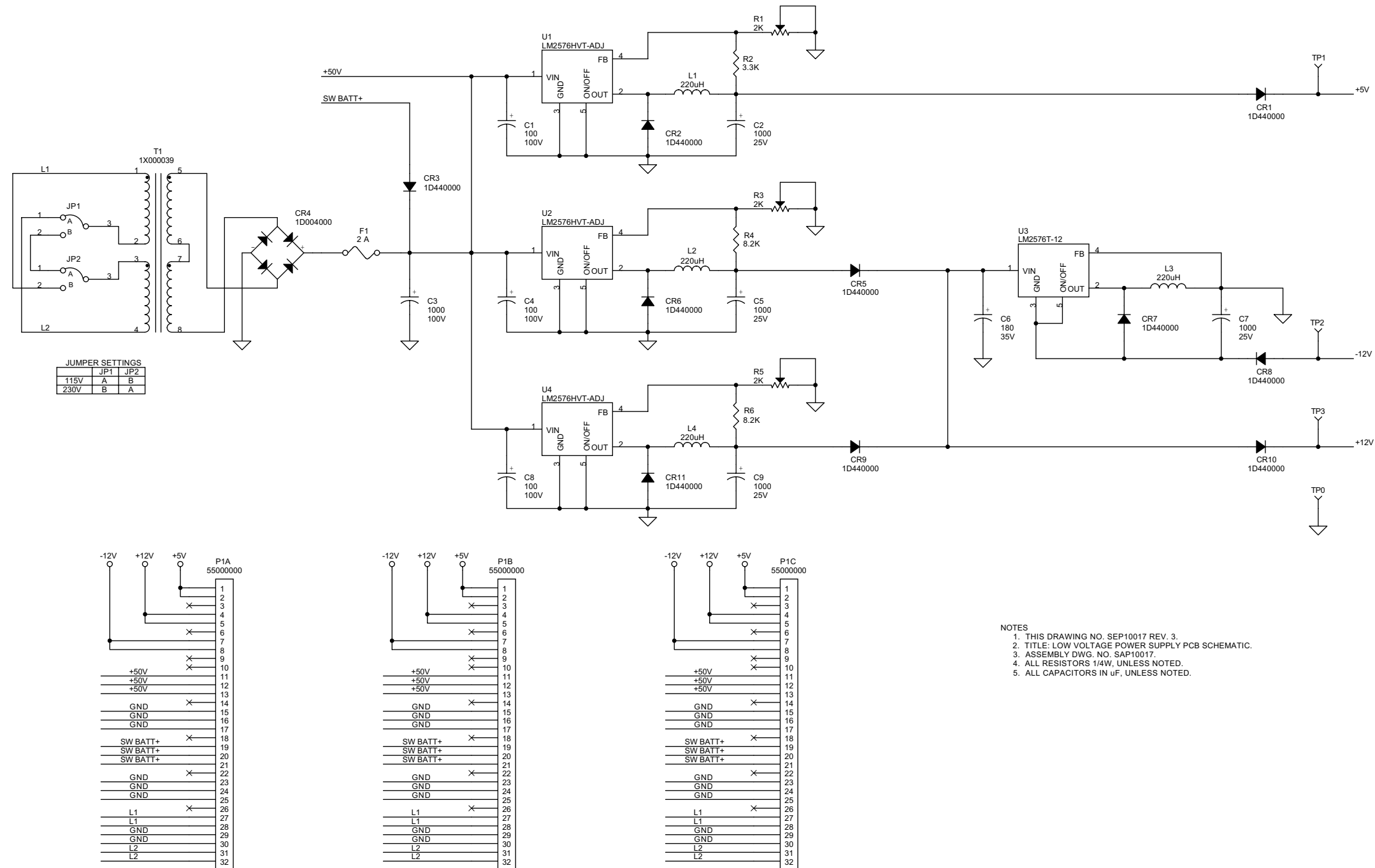
FROM POWER SUPPLY (NOTE: +12V & -12V ARE OPTIONS)



RESET

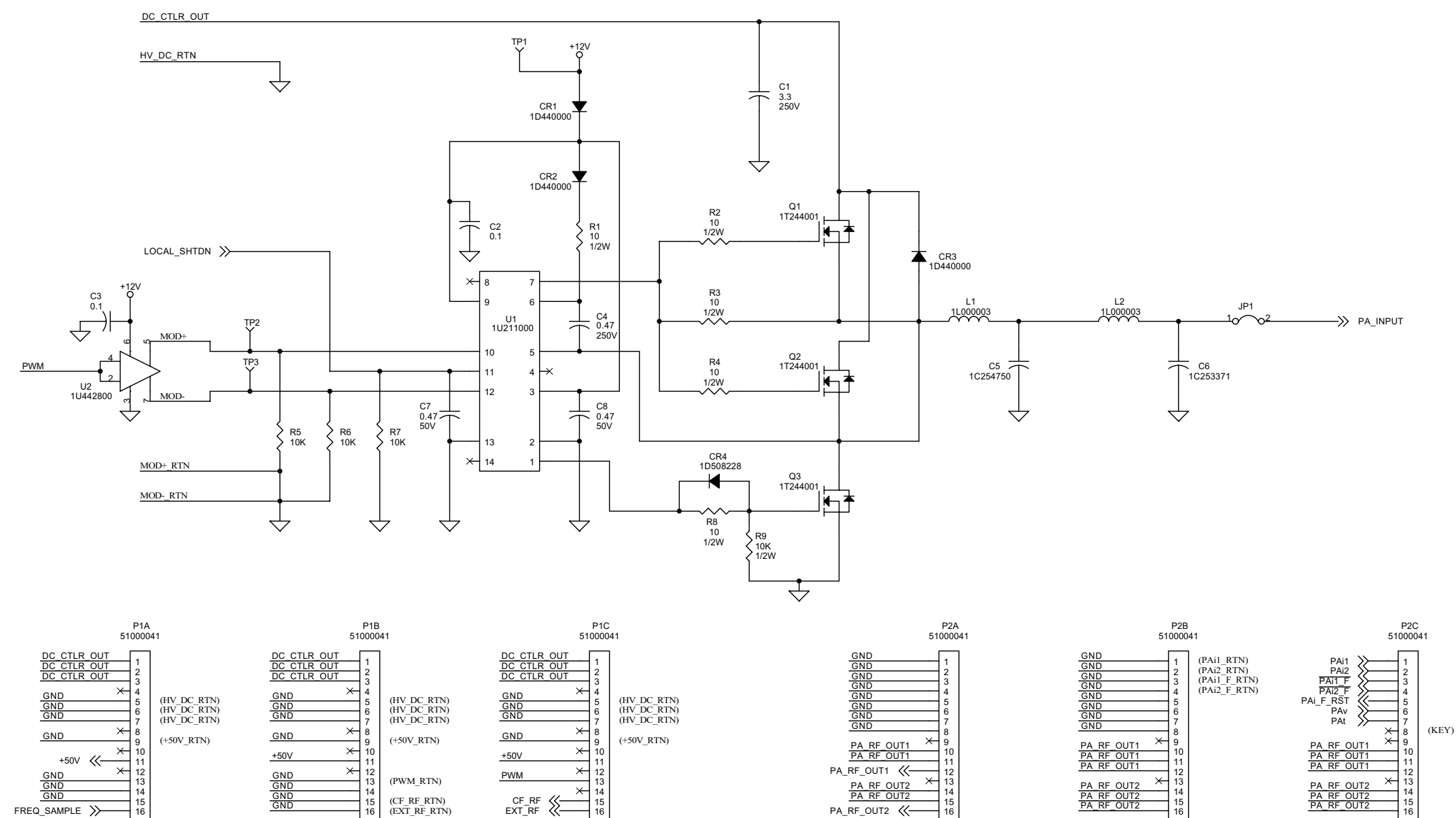


SEP10016 - SCHEMATIC, LOCAL CONTROL PCB
SHEET 6 OF 6



- NOTES
1. THIS DRAWING NO. SEP10017 REV. 3.
 2. TITLE: LOW VOLTAGE POWER SUPPLY PCB SCHEMATIC.
 3. ASSEMBLY DWG. NO. SAP10017.
 4. ALL RESISTORS 1/4W, UNLESS NOTED.
 5. ALL CAPACITORS IN μ F, UNLESS NOTED.

SEP10017 - SCHEMATIC, LOW VOLTAGE POWER SUPPLY PCB

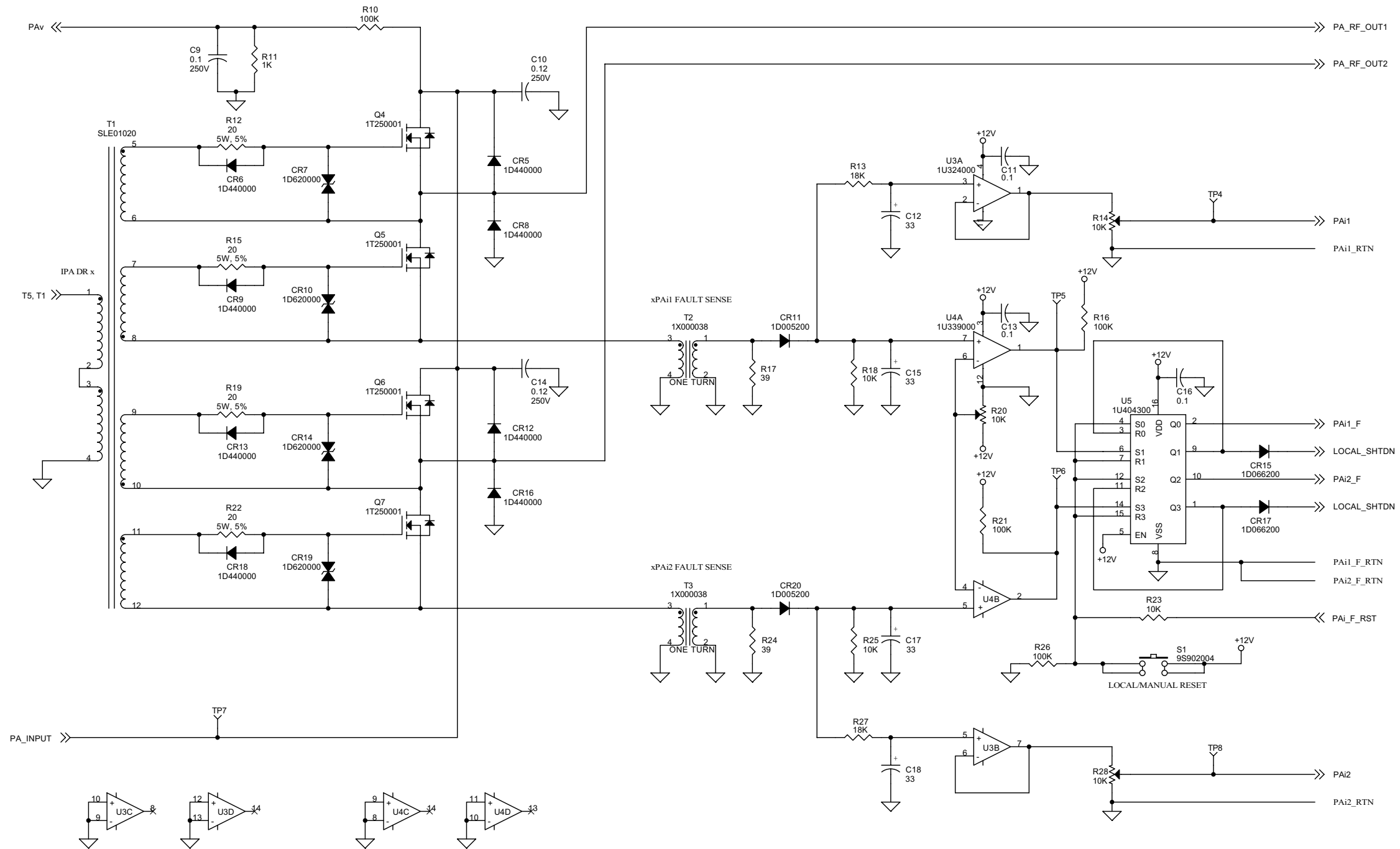


- NOTES
1. THIS DRAWING NO. SEP10001 REV. A.
 2. TITLE: RF POWER AMPLIFIER PCB SCHEMATIC.
 3. ASSEMBLY DWG. NO. SAP10001.
 4. ALL RESISTORS 1/4W, 1%, UNLESS NOTED.
 5. ALL CAPACITORS IN uF, UNLESS NOTED.
 6. ">>" INDICATES OFF-PAGE CONNECTION.

SEP10001 - SCHEMATIC, POWER AMPLIFIER PCB

SHEET 1 OF 3

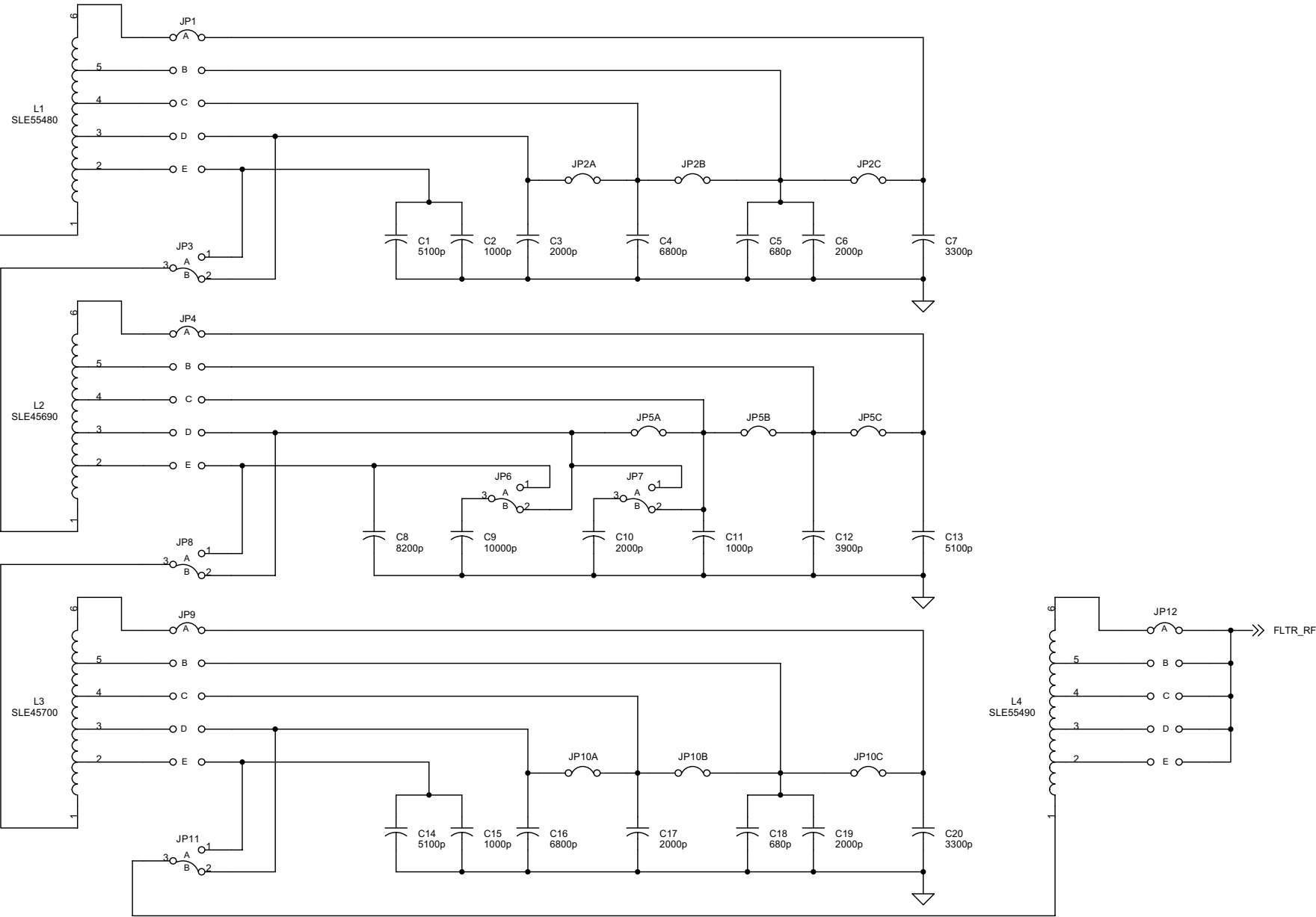
TRANSMITTER SECTION



SEP10001 - SCHEMATIC, POWER AMPLIFIER PCB
SHEET 2 OF 3

FILTER

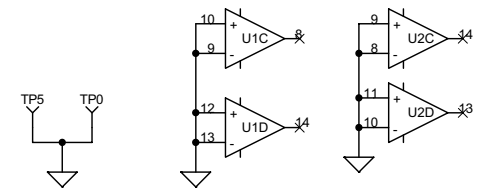
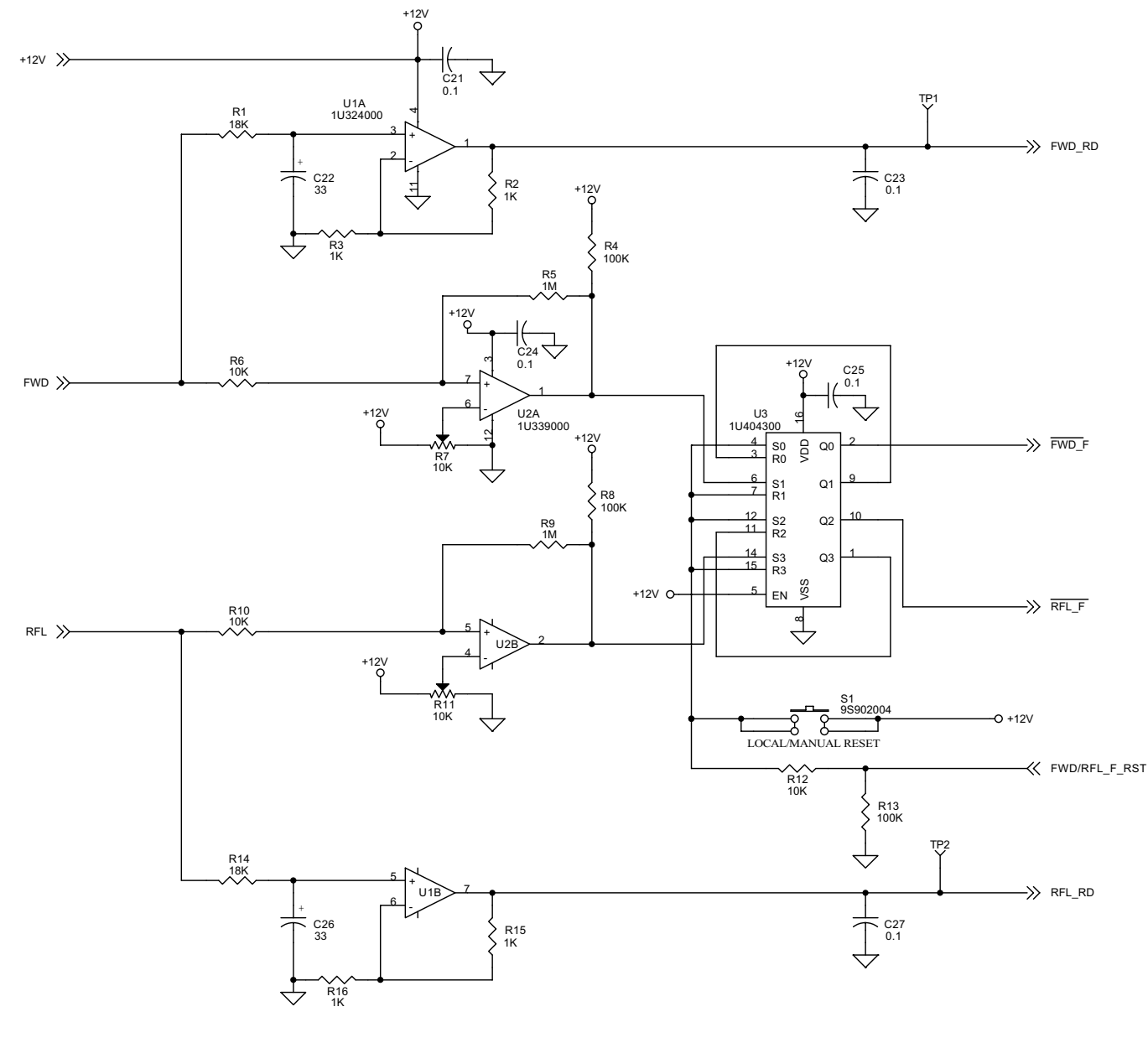
	FREQUENCY BAND (KHz)				
	190-220	220-280	280-360	360-440	440-535
JP1	A	B	C	D	E
JP2	A,B,C	A,B	A	OPEN	OPEN
JP3	B	B	B	B	A
JP4	A	B	C	D	E
JP5	A,B,C	A,B	A	OPEN	OPEN
JP6	B	B	B	B	OPEN
JP7	B	B	B	OPEN	OPEN
JP8	B	B	B	B	A
JP9	A	B	C	D	E
JP10	A,B,C	A,B	A	OPEN	OPEN
JP11	B	B	B	B	A
JP12	A	B	C	D	E



- NOTES
- 1. THIS DRAWING NO. SEP10002/00 REV A.
 - 2. TITLE: FILTER/BRIDGE PCB SCHEMATIC.
 - 3. ASSEMBLY DWG. NO. SAP10002.
 - 4. ALL RESISTORS 1/4W, 1%, UNLESS NOTED.
 - 5. ALL CAPACITORS IN uF, UNLESS NOTED.
 - 6. FILTER SHOWN JUMPED FOR BAND 1.
 - 7. ">>" INDICATES OFF-PAGE CONNECTION.

SEP10002 - SCHEMATIC, FILTER/BRIDGE PCB
SHEET 1 OF 3

FAULT SENSE



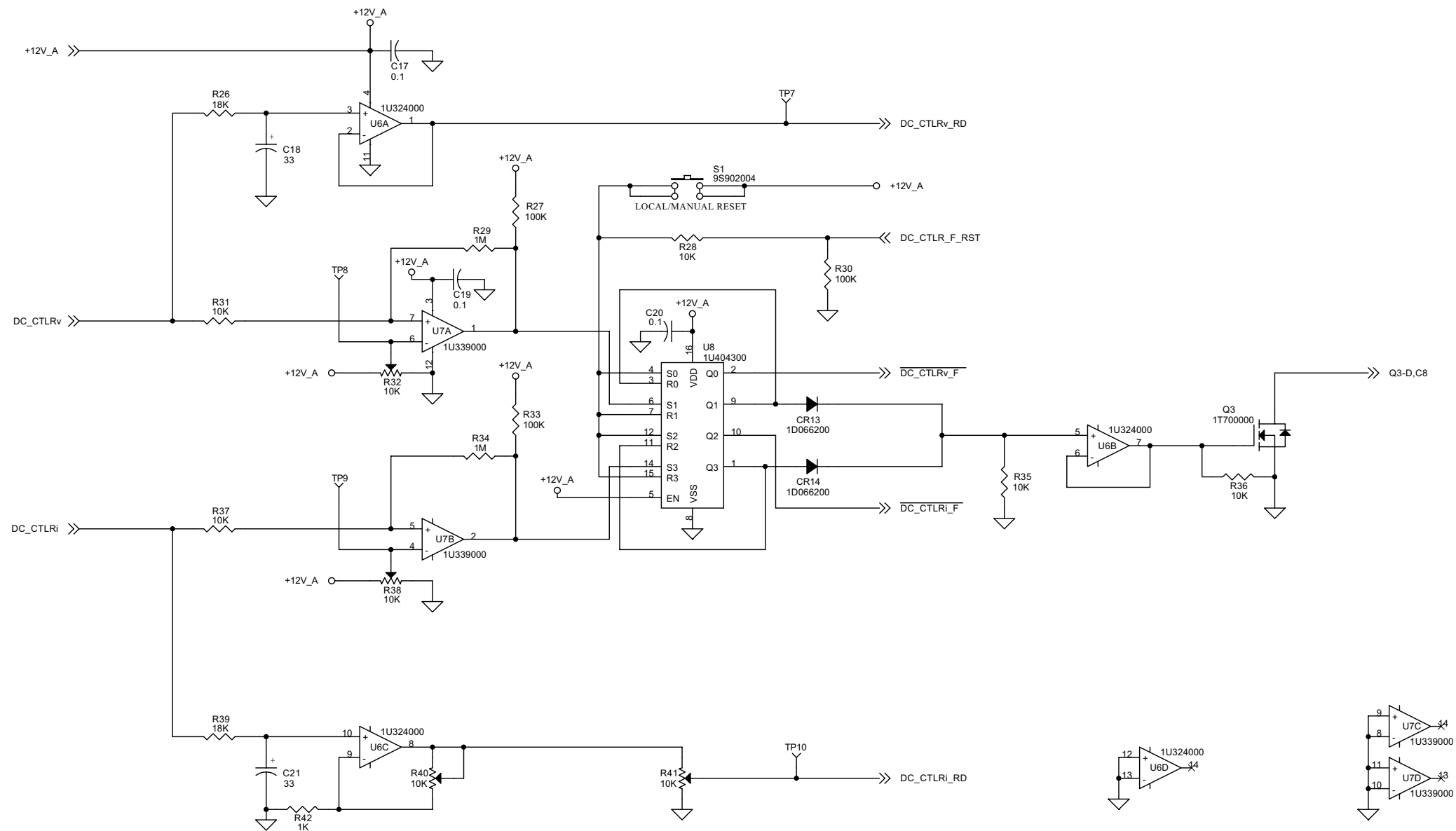
SEP10002 - SCHEMATIC, FILTER/BRIDGE PCB
SHEET 2 OF 3



SD SERIES TRANSMITTER

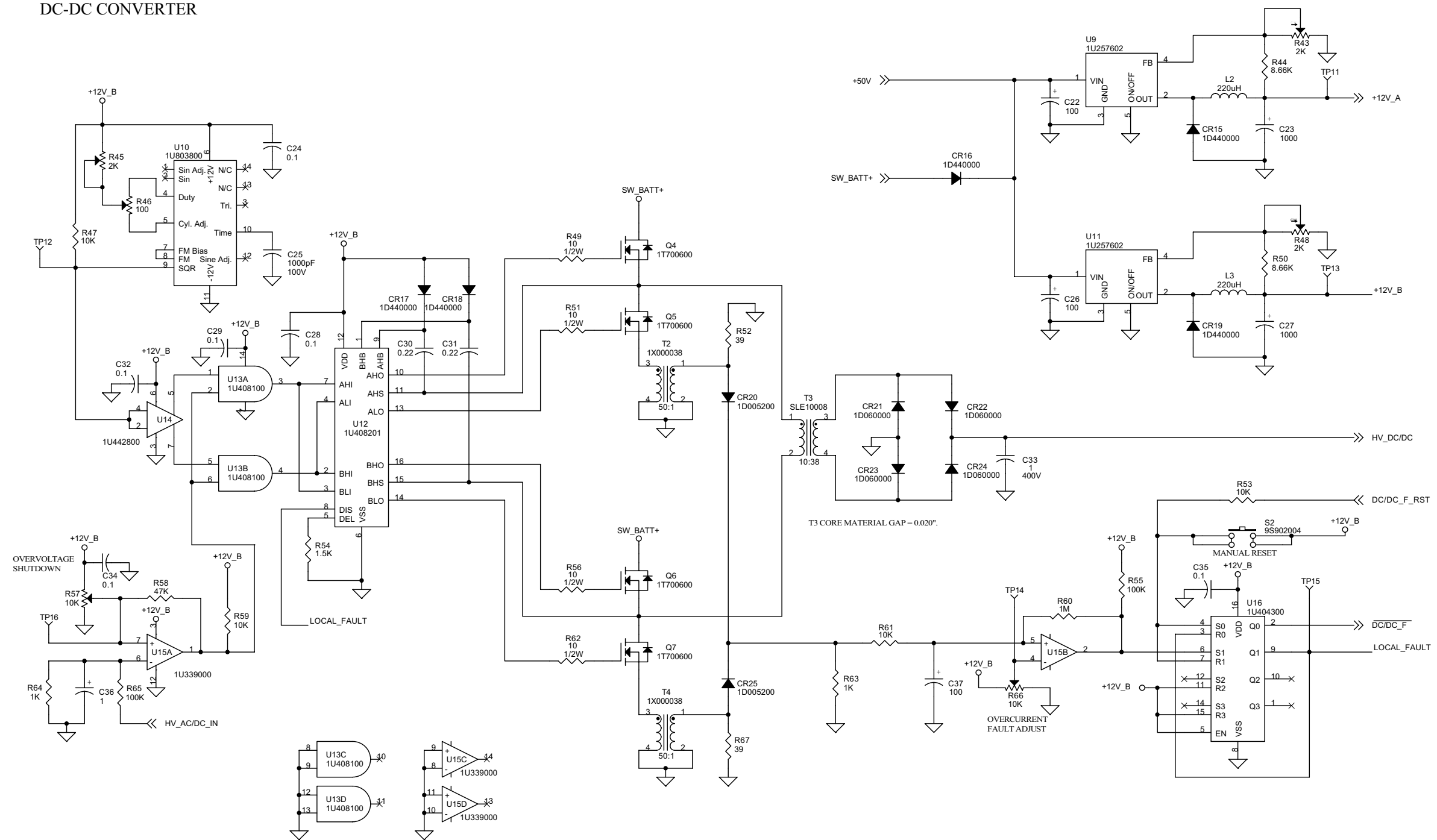


FAULT SENSE



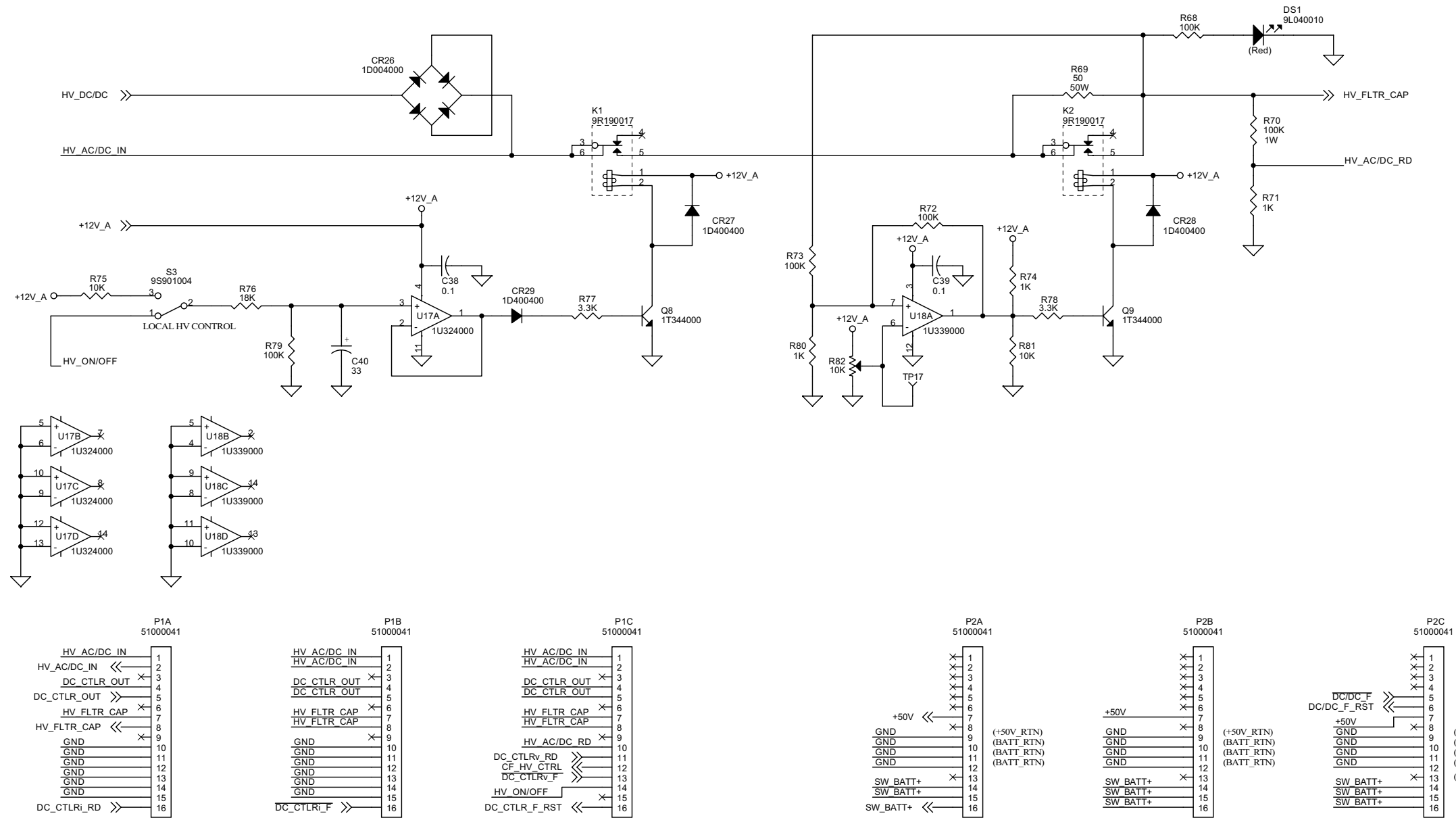
SEP10003 - SCHEMATIC, RF PA POWER SUPPLY PCB
SHEET 2 OF 4

DC-DC CONVERTER

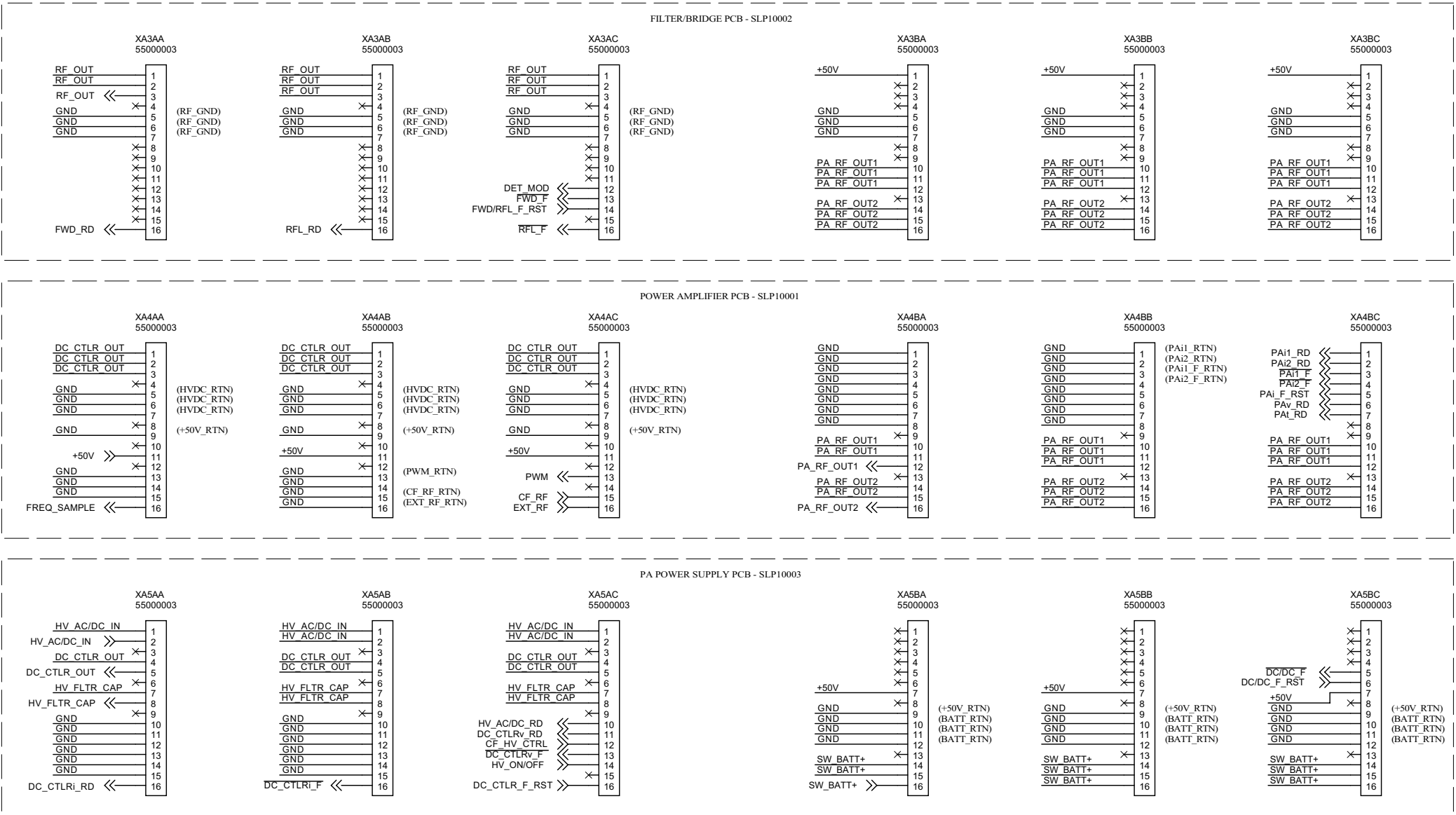


SEP10003 - SCHEMATIC, RF PA POWER SUPPLY PCB
SHEET 3 OF 4

INRUSH CURRENT LIMIT

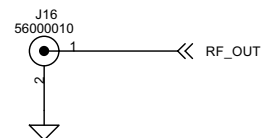
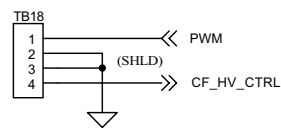
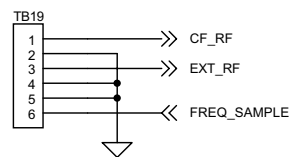
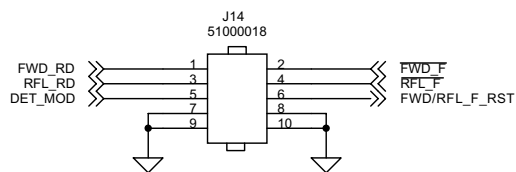
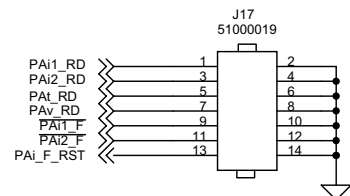
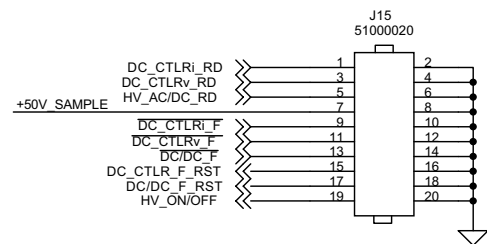


SEP10003 - SCHEMATIC, RF PA POWER SUPPLY PCB
SHEET 4 OF 4

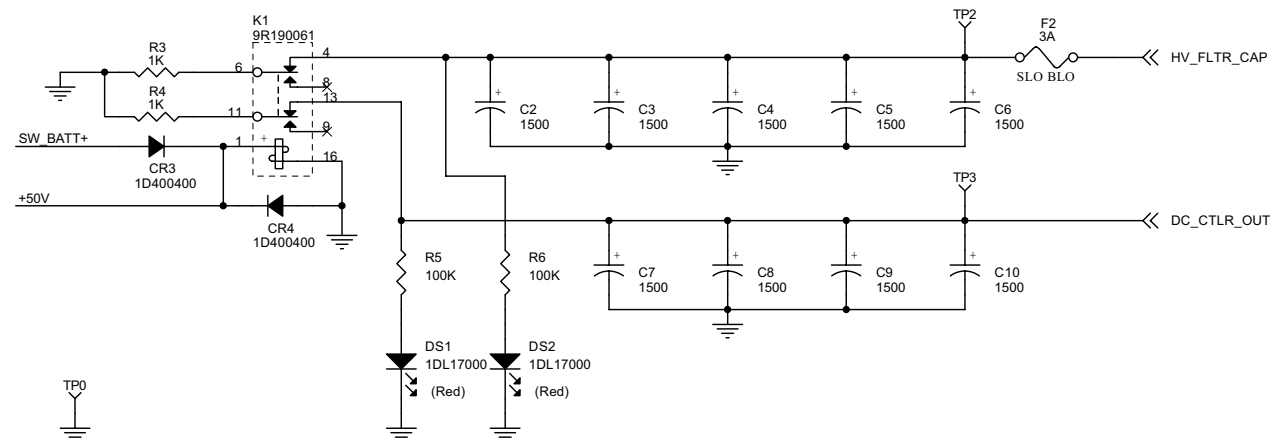
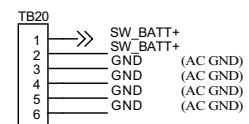
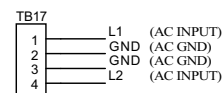
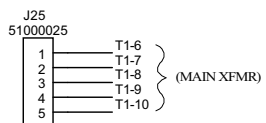
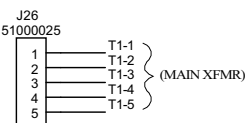
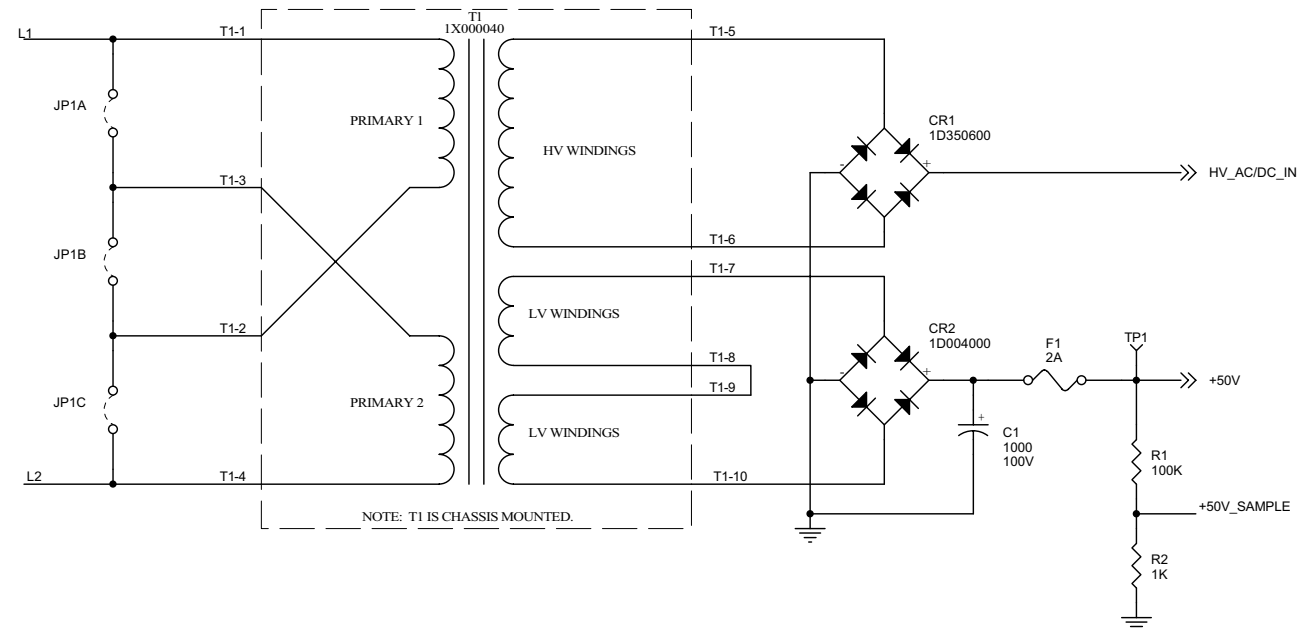


SEP10004 - SCHEMATIC, RF MOTHERBOARD PCB

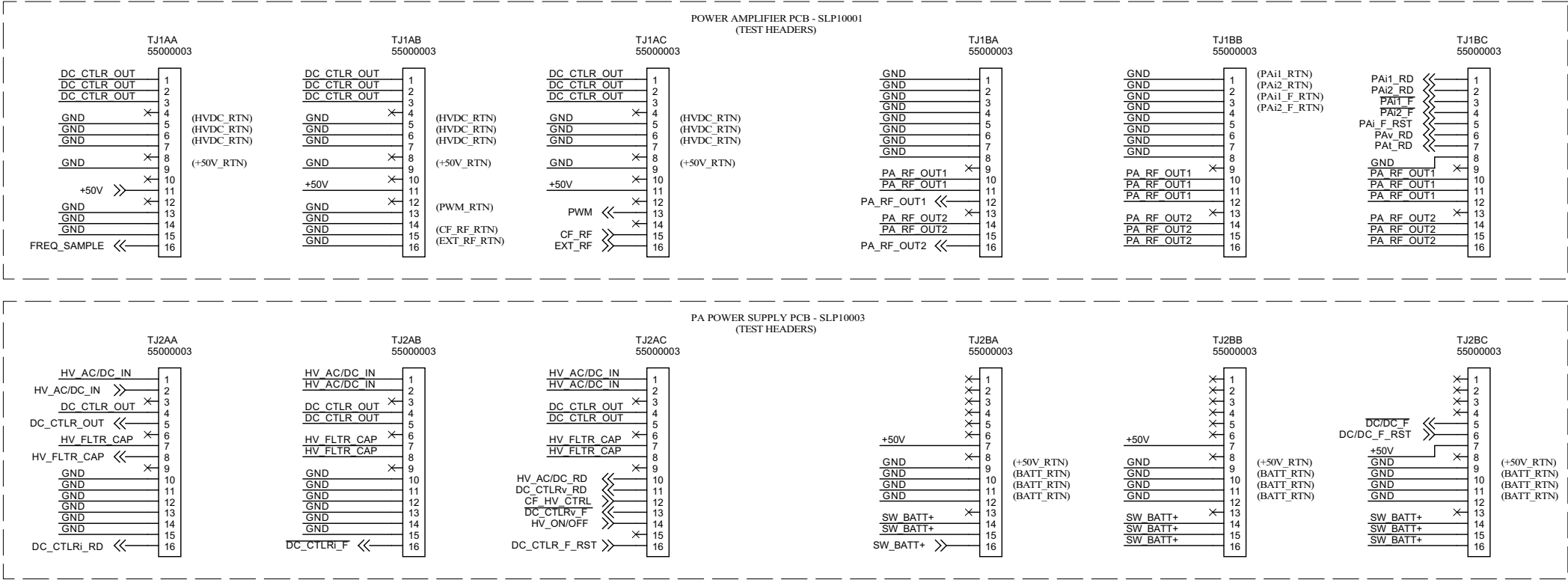
SHEET 1 OF 3



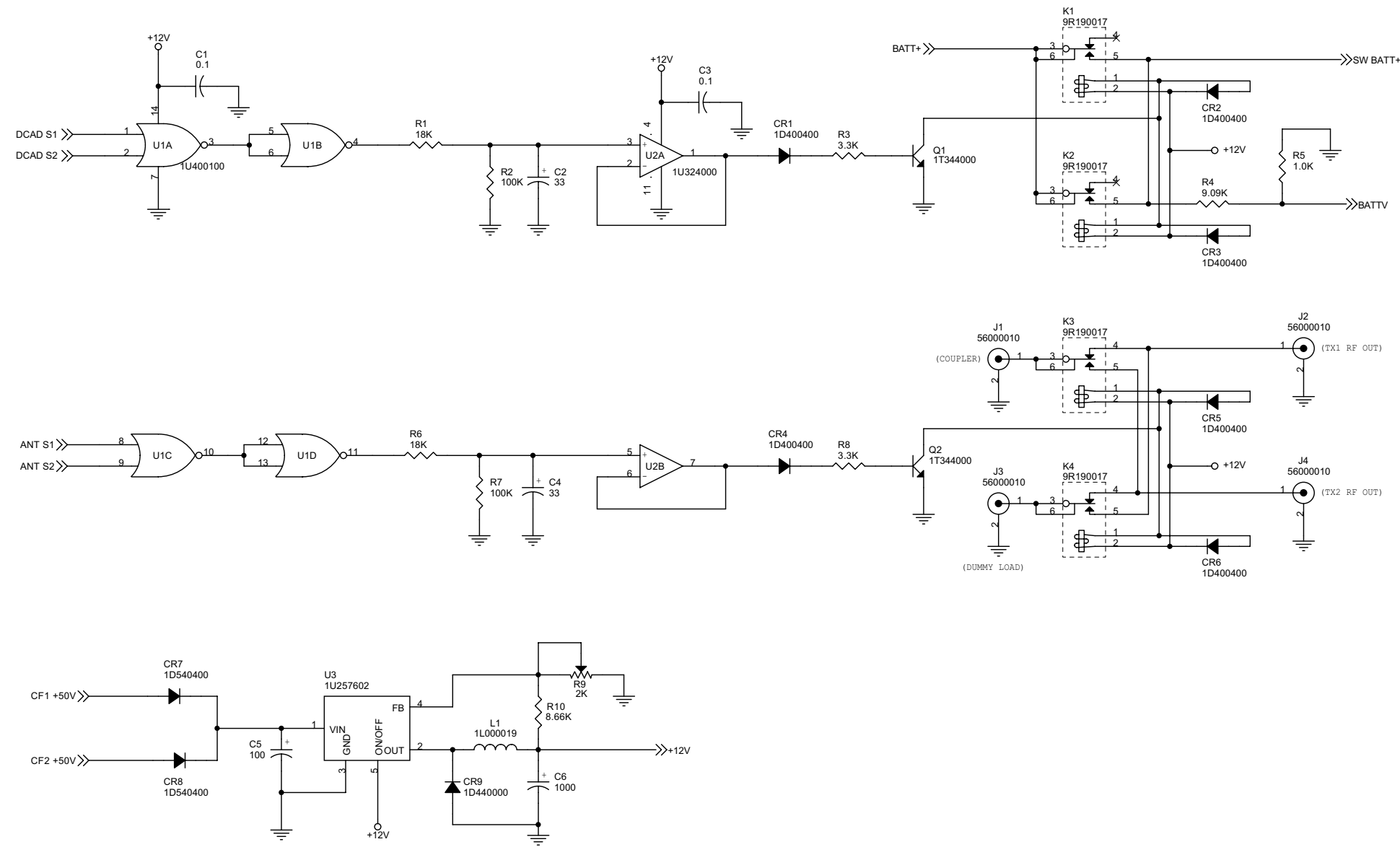
LINE VOLTAGE JUMPER SETTINGS	
115VAC	JP1A AND JP1C
230VAC	JP1B ONLY



SEP10004 - SCHEMATIC, RF MOTHERBOARD PCB SHEET 2 OF 3

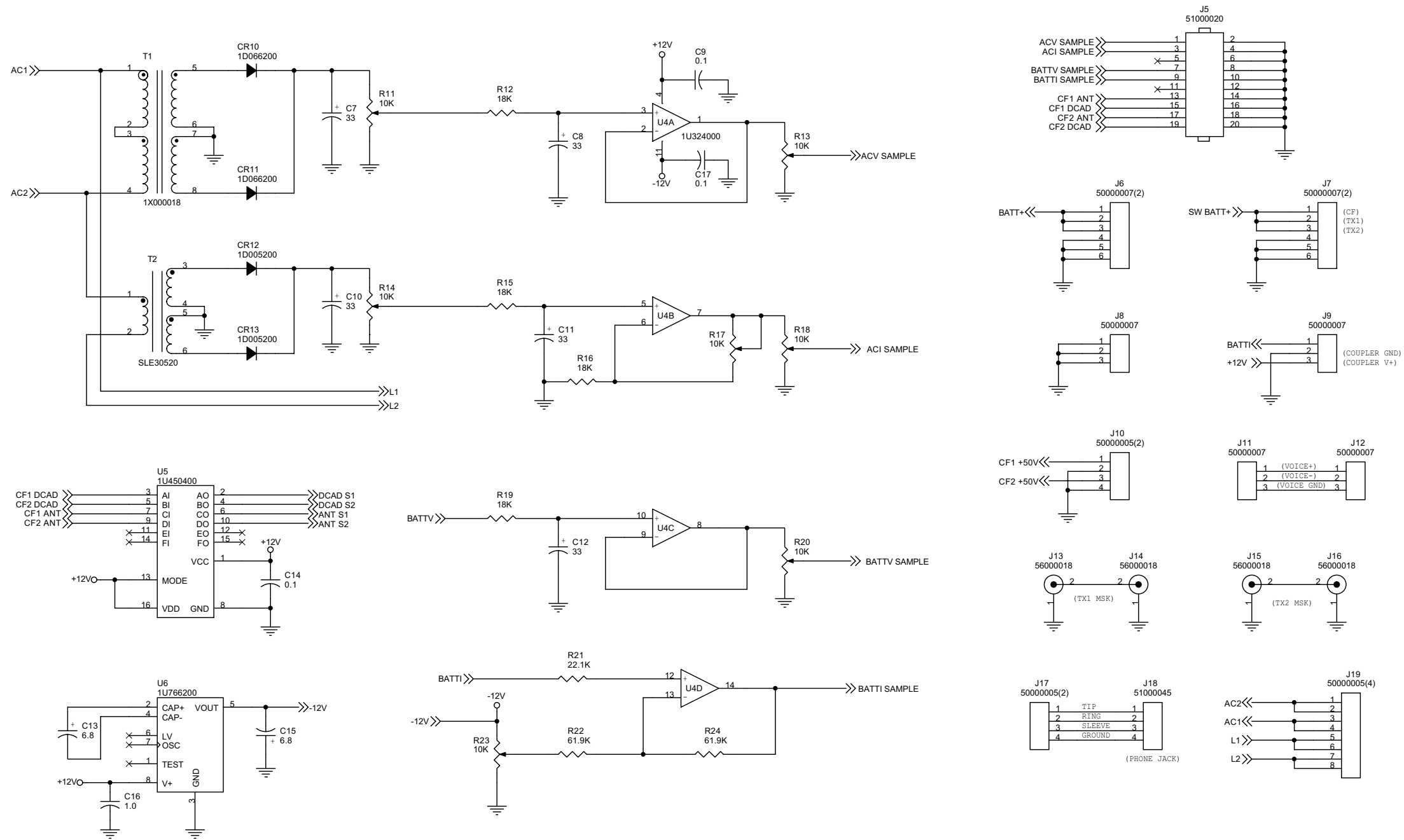


SEP10004 - SCHEMATIC, RF MOTHERBOARD PCB
SHEET 3 OF 3



- NOTES
1. THIS DRAWING NO. SEP10006 REV. 1.
 2. TITLE: USER INTERFACE PCB SCHEMATIC.
 3. ASSEMBLY DWG. NO. SAPI0006.
 4. ALL RESISTORS 1/4W, UNLESS NOTED.
 5. ALL CAPACITORS IN uF, UNLESS NOTED.
 6. " << " INDICATES OFF-PAGE CONNECTION.

SEP10006 - SCHEMATIC, USER INTERFACE PCB
SHEET 1 OF 2



SEP10006 - SCHEMATIC, USER INTERFACE PCB
SHEET 2 OF 2