

TEST SETUP PHOTOGRAPHS

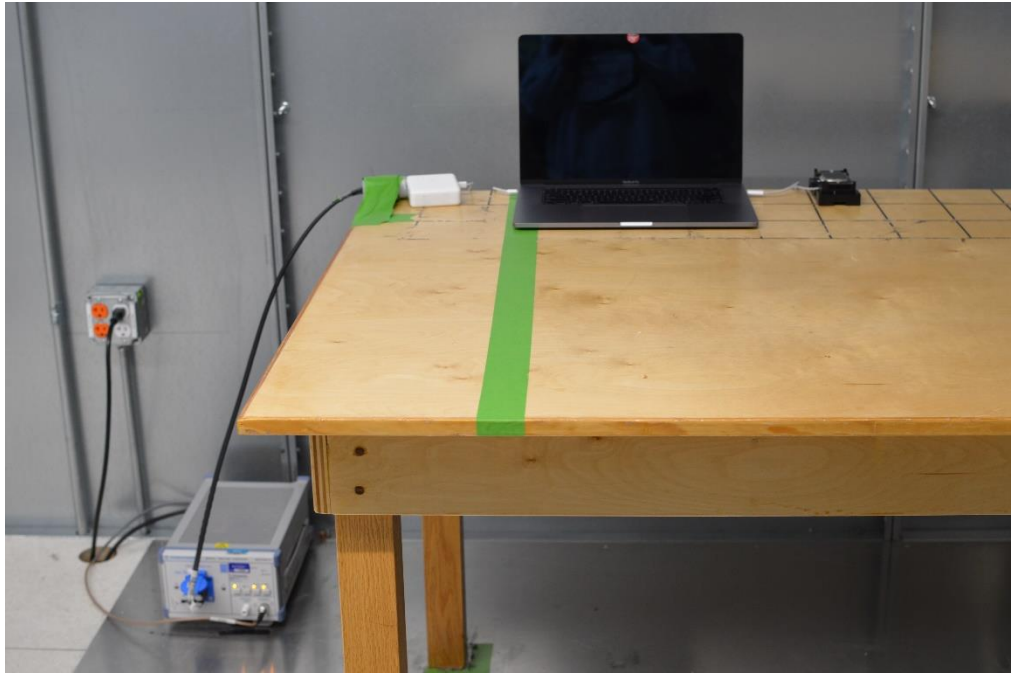


Figure 1: AC Line Conducted Emissions (Front View)



Figure 2: AC Line Conducted Emissions (Side View)

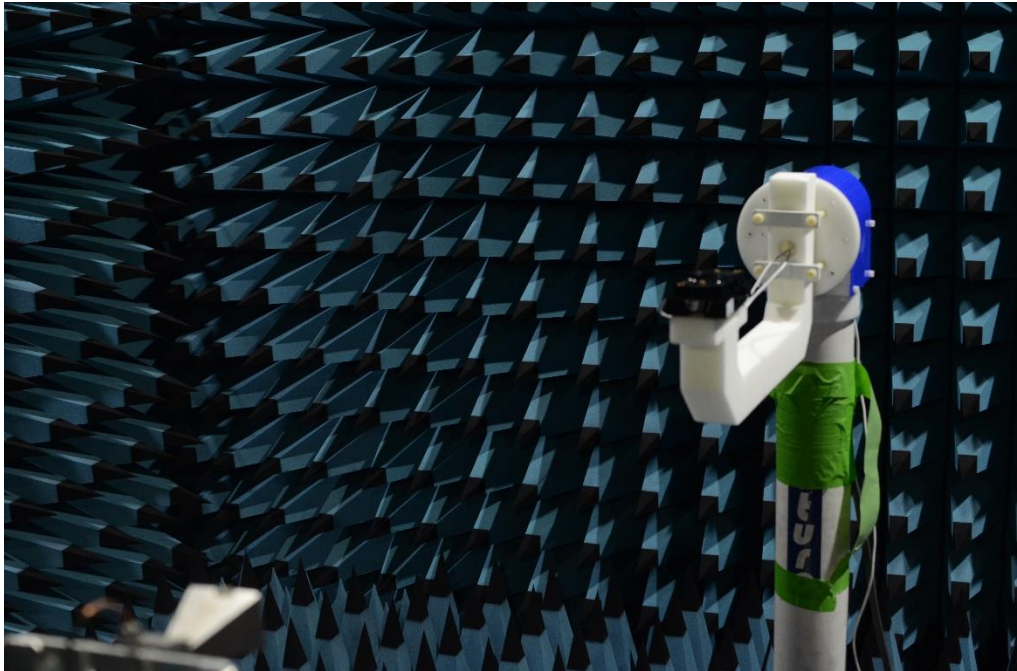


Figure 3: 18-40GHz Test Setup

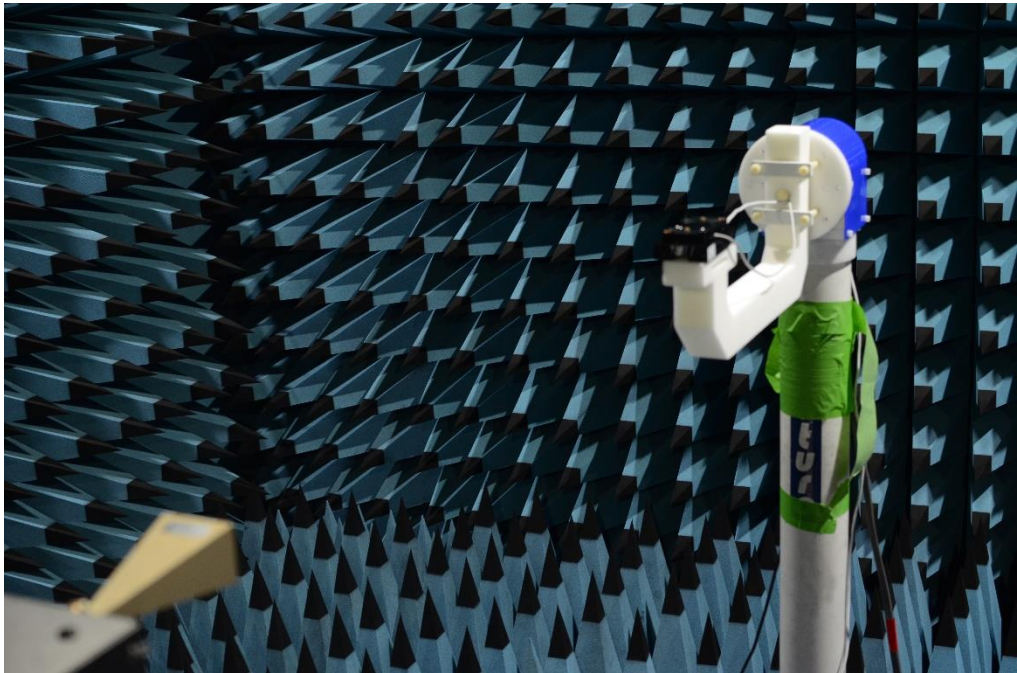


Figure 4: 40-60GHz Test Setup



Figure 5: 60-90GHz Test Setup

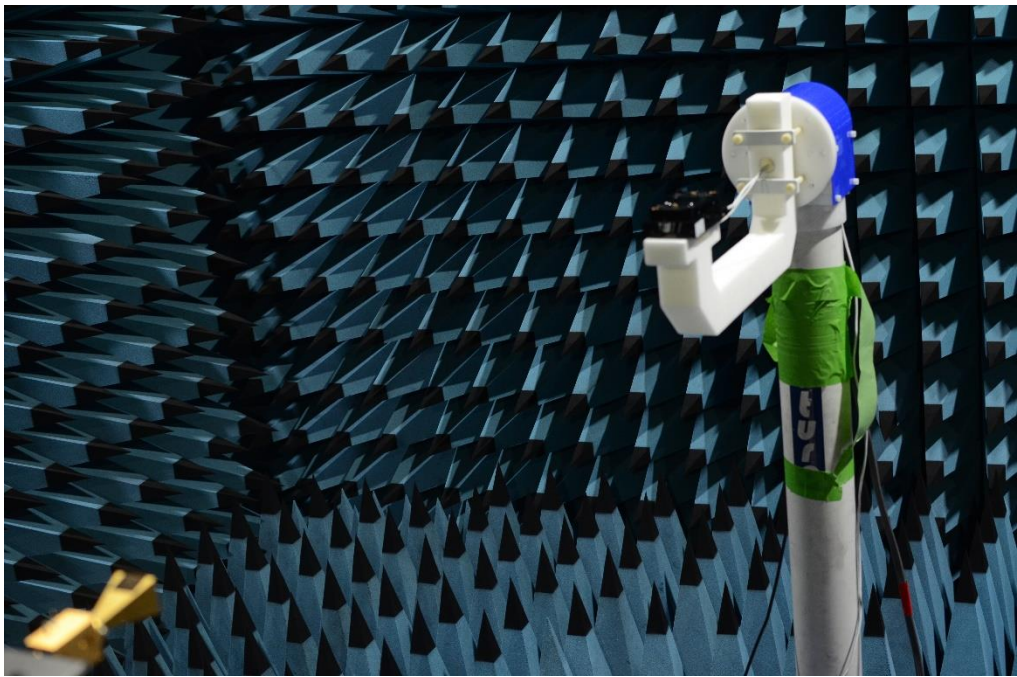


Figure 6: 90-140GHz Test Setup

 element	Apple Inc. FCC ID: BCG-A3335, IC: 579C-A3335	Model/HVIN: A3335, A3452
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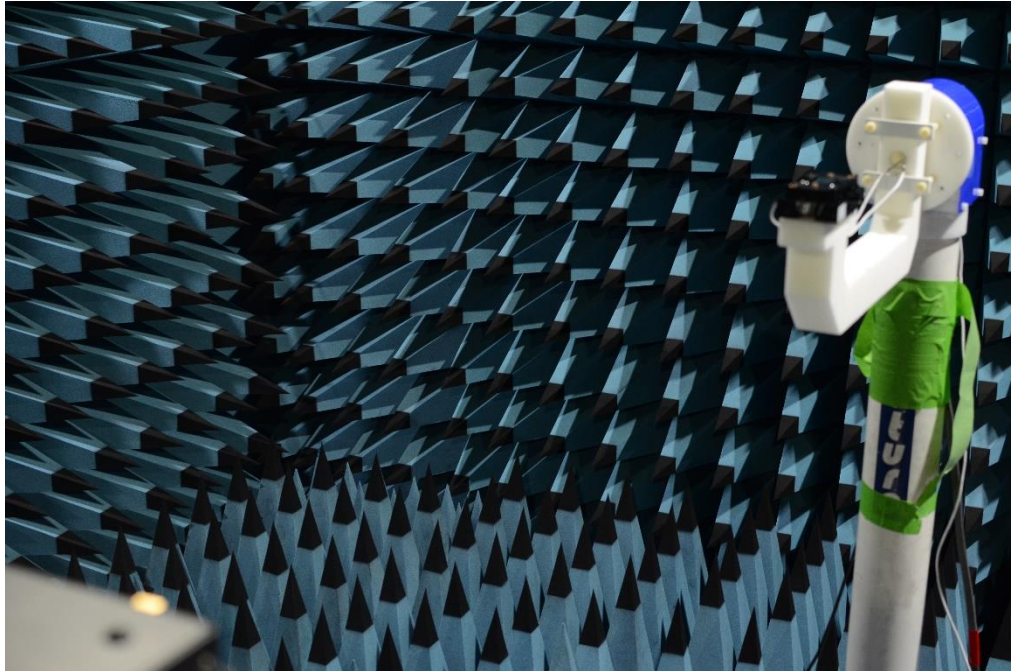


Figure 7: 140-200GHz Test Setup

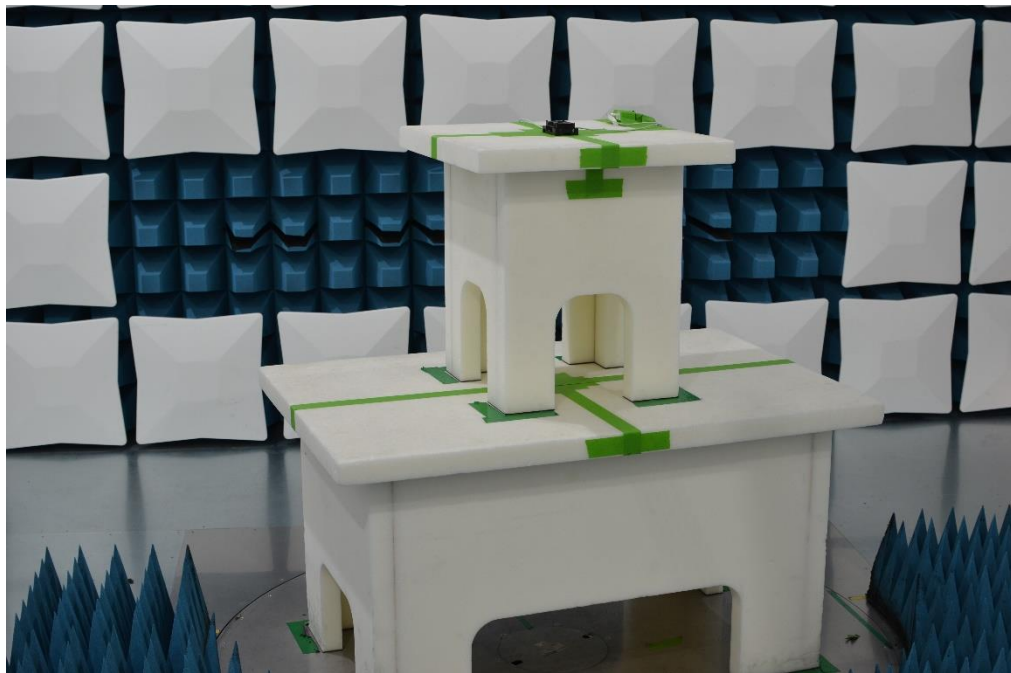


Figure 8: 1-18GHz Test Setup (Front View)

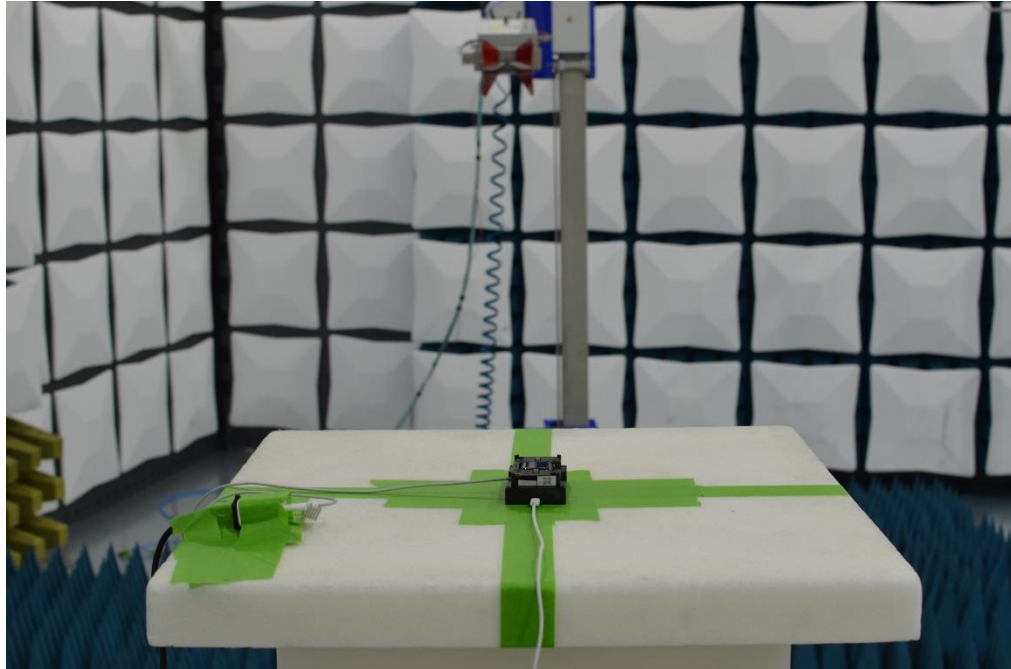


Figure 9: 1-18GHz Test Setup (Rear View)

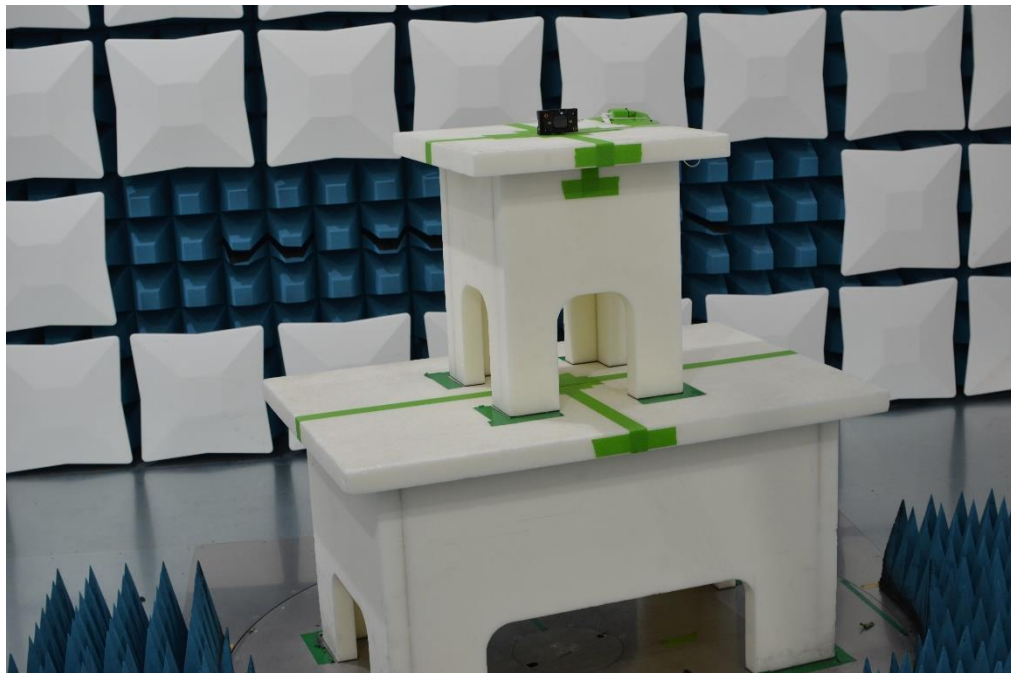


Figure 10: 1-18GHz Test Setup (Front View)

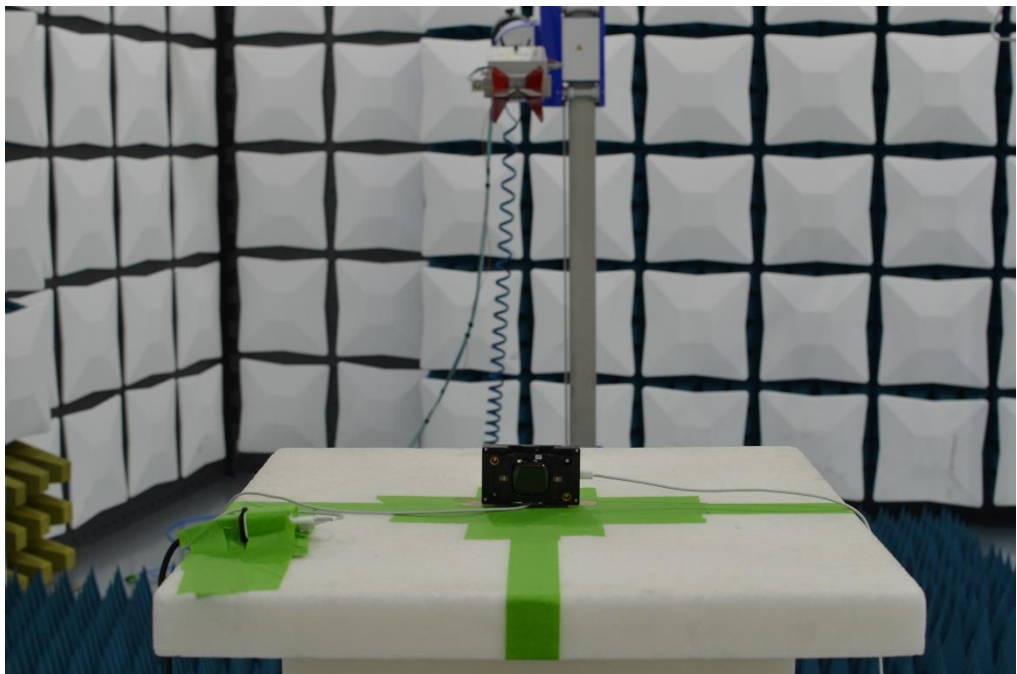


Figure 11: 1-18GHz Test Setup (Rear View)

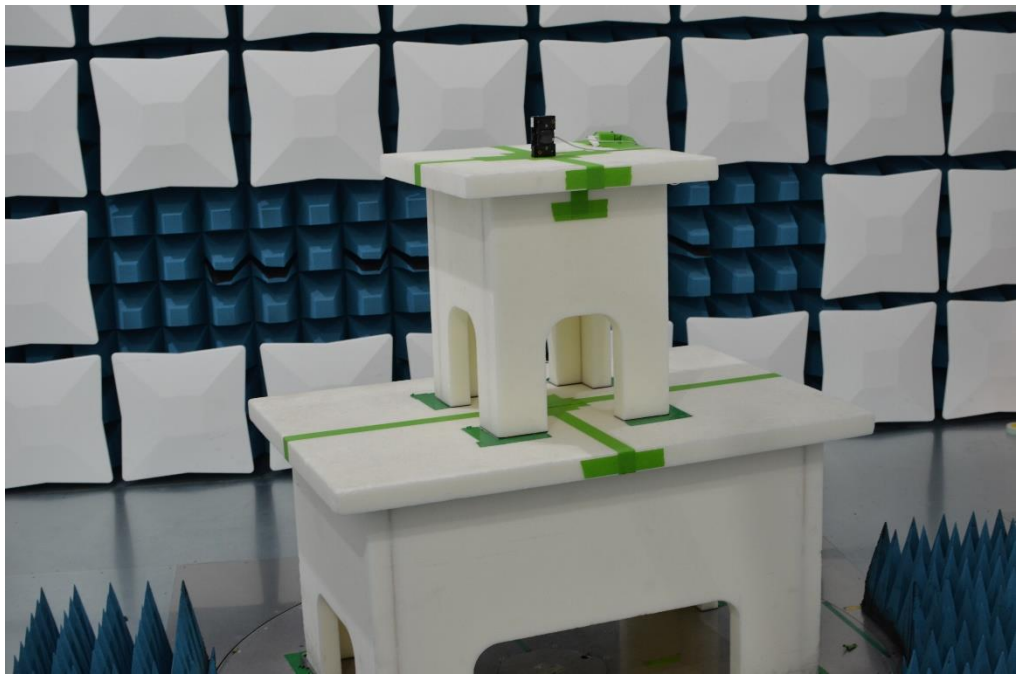


Figure 12: 1-18GHz Test Setup (Front View)

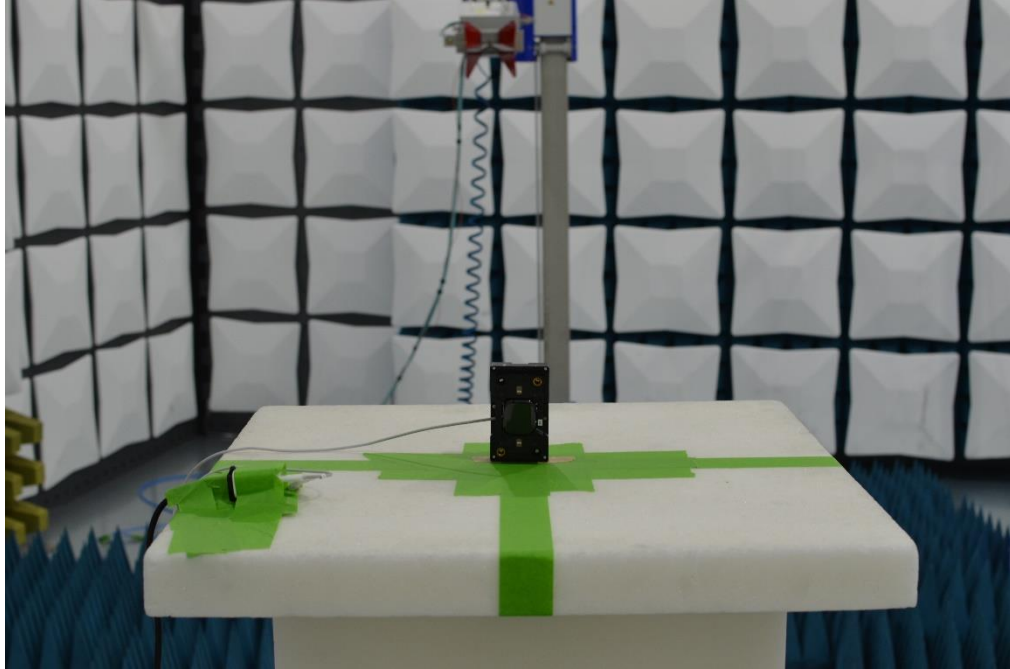


Figure 13: 1-18GHz Test Setup (Rear View)



Figure 14: 30MHz-1GHz Test Setup (Front View)

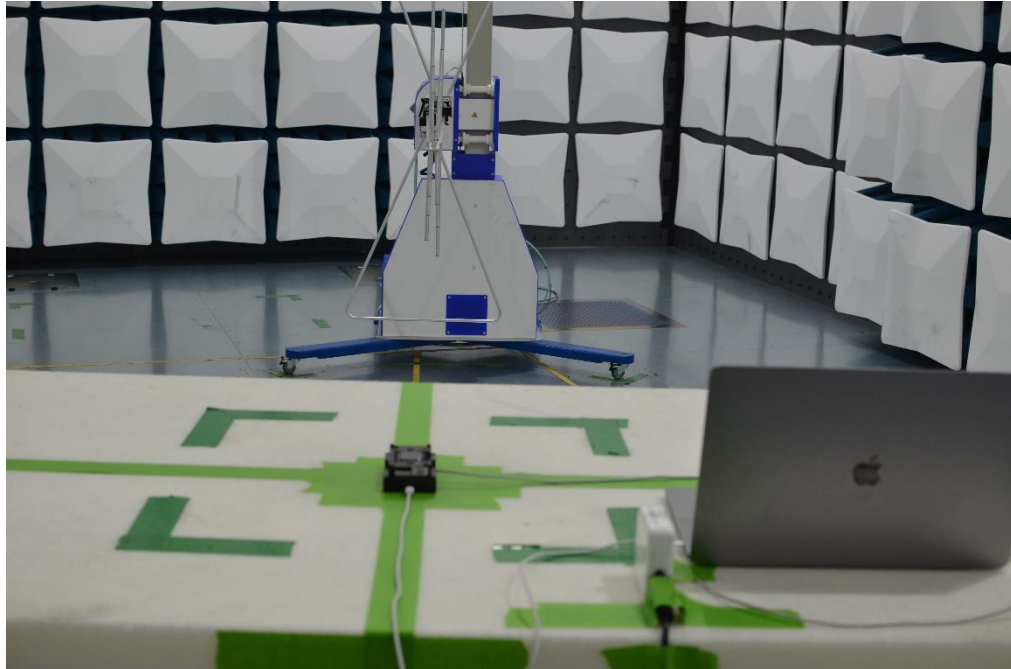


Figure 15: 30MHz-1GHz Test Setup (Rear View)

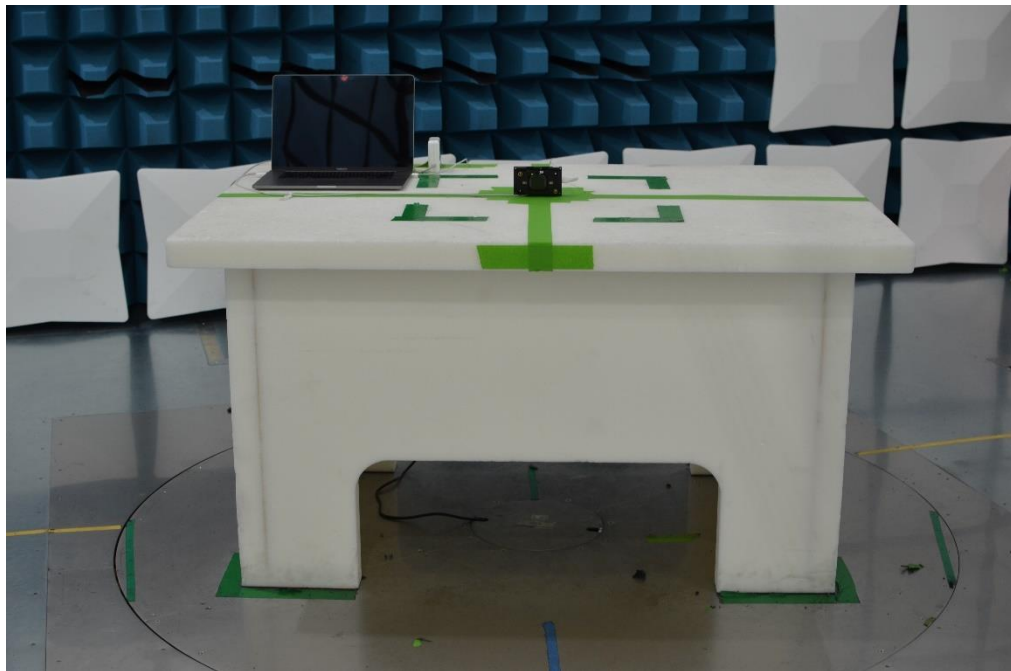


Figure 16: 30MHz-1GHz Test Setup (Front View)

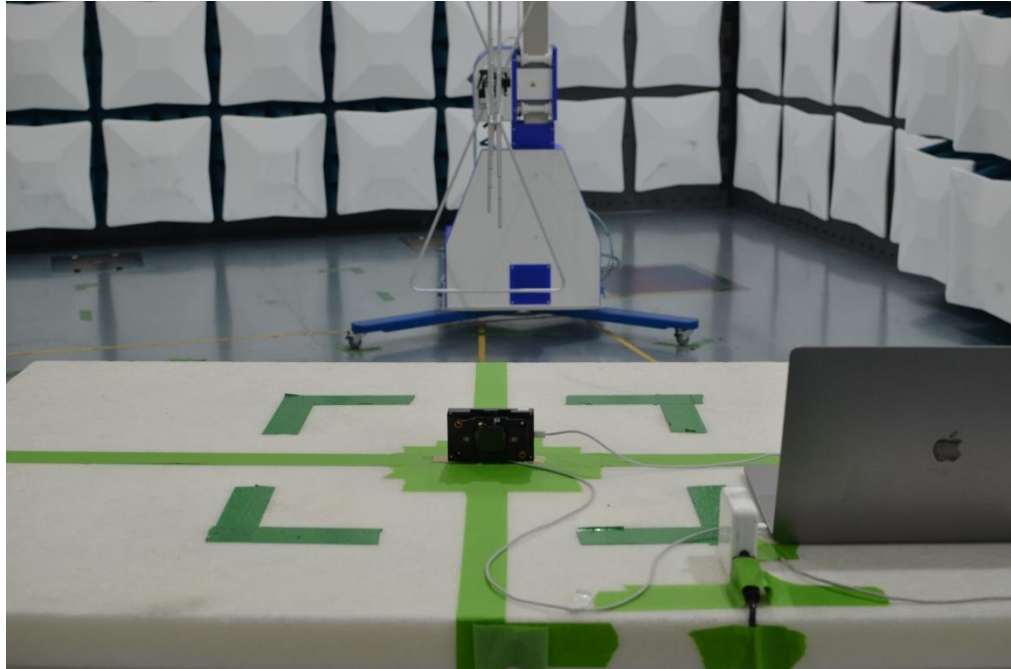


Figure 17: 30MHz-1GHz Test Setup (Rear View)



Figure 18: 30MHz-1GHz Test Setup (Front View)

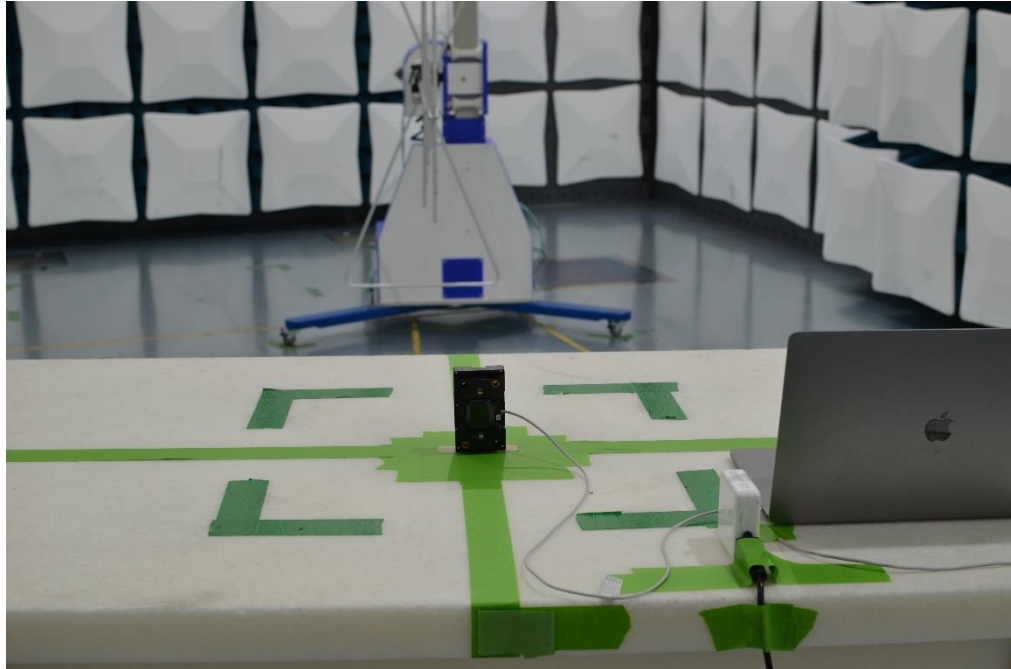


Figure 19: 30MHz-1GHz Test Setup (Rear View)

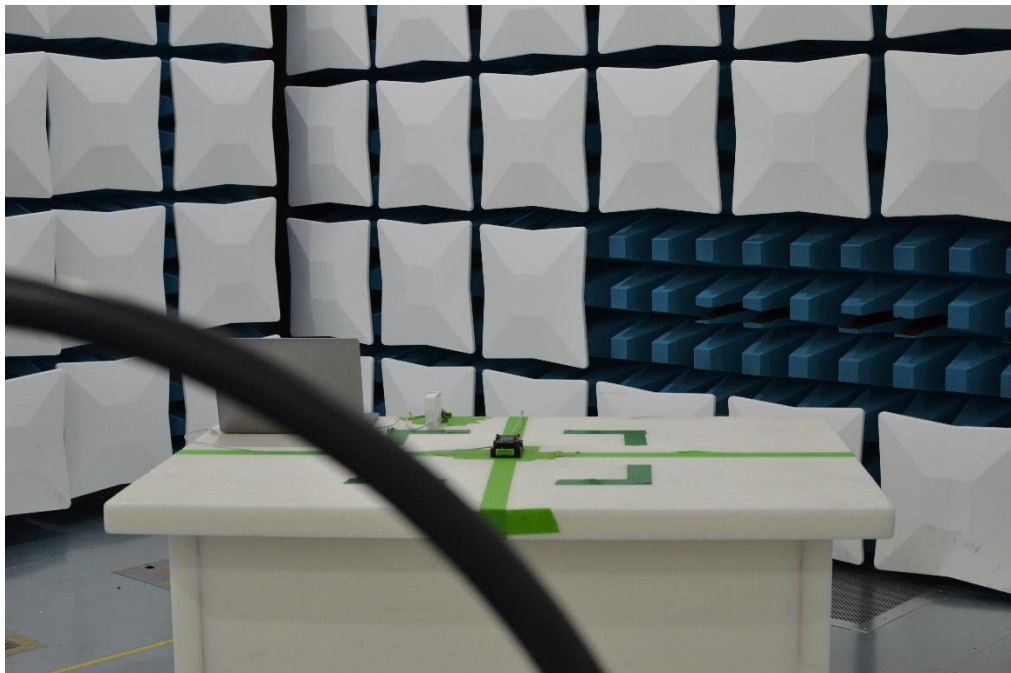


Figure 20: 9kHz-30MHz Test Setup (Front View)

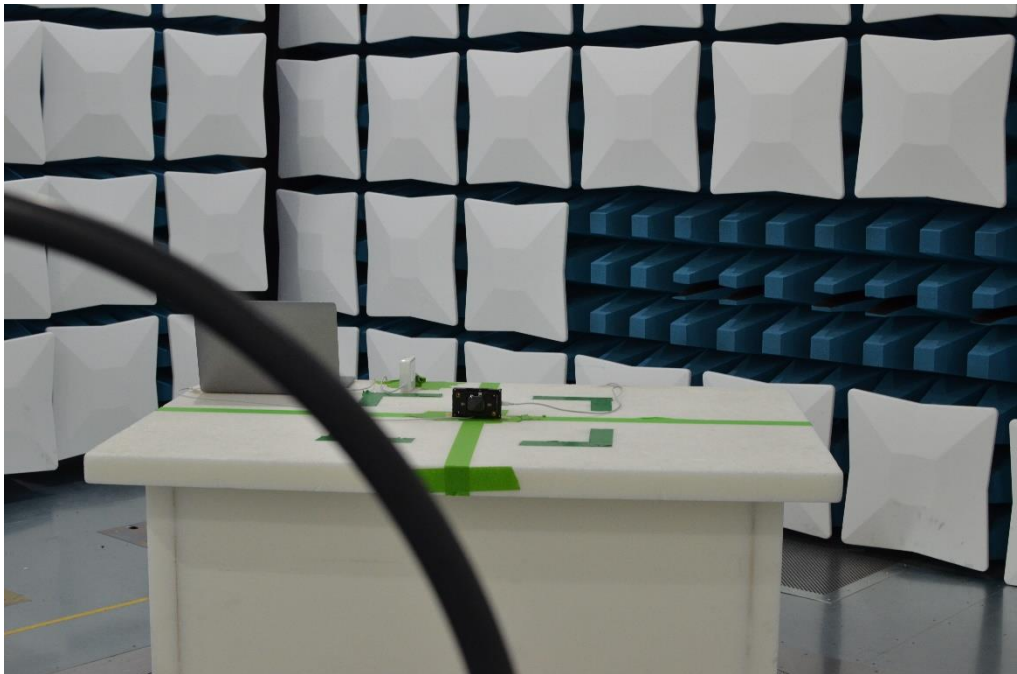


Figure 19: 9kHz-30MHz Test Setup (Front View)

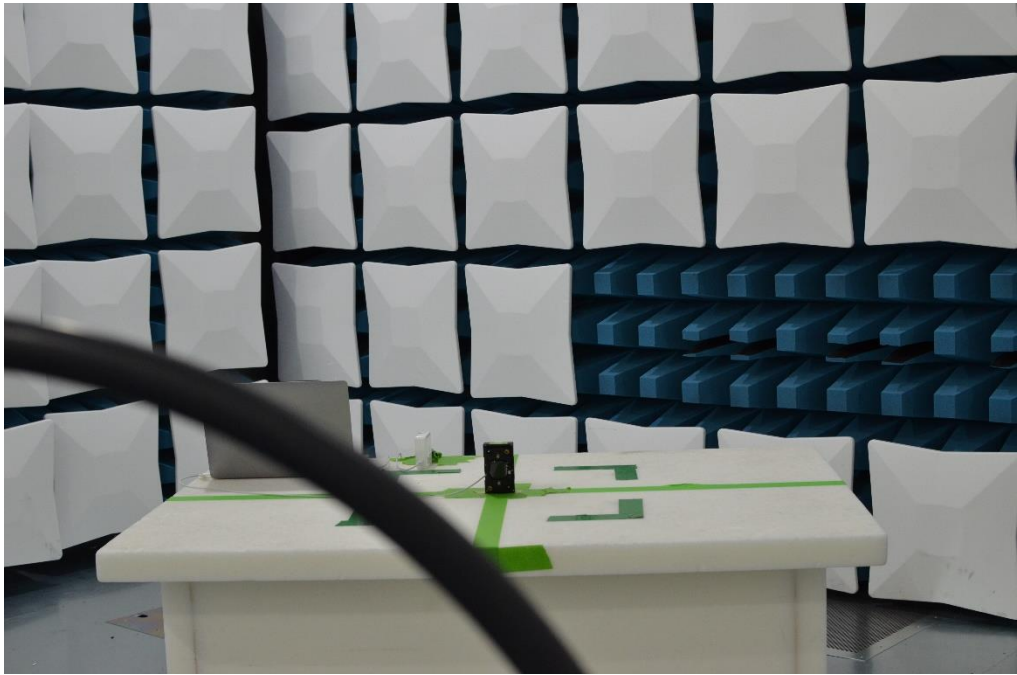


Figure 20: 9kHz-30MHz Test Setup (Front View)