

TAS Validation Test Report

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1. Test Specifications, Methods/Procedures, and Facilities

The tests documented in this report were performed following the OEM's pre-approval guidance KDB 388624 D02 Pre-Approval Guidance List v18r06, PWRCNG).

The test sites and measurement facilities used to collect data are located at:

47173 Benicia Street	47266 Benicia Street
SAR Labs A to Q	SAR Labs 1 to 30

The Test Lab Conformity Assessment Body Identifier (CABID)

Location	CABID	Company Number
47173 Benicia Street, Fremont, CA, 94538 UNITED STATES	US0104	2324A
47266 Benicia Street, Fremont, CA, 94538 UNITED STATES		

2. Introduction

The modem is enabled with WWAN/WLAN/Bluetooth/NB U-NII/802.15.4/MSS TAS technology feature to control and manage transmitting power, in real time.

This DUT supports a time-average method of transmit power control for the aforementioned technologies. Consumed SAR from active radios is tracked and stored for the duration of the applicable regulatory averaging window. Rolling time-averaged SAR values are calculated from the consumed SAR reported from active radios. The time-averaged SAR is always maintained less than the applicable $SAR_{Reported}$.

The purpose of this document is to demonstrate the validity of the TAS feature(s) implemented within this product with a technology agnostic approach.

Table 2-1 defines the applicable time-averaging durations per regulatory body:

Table 2-1: Time-averaging Duration

FCC						
Frequency (GHz)	WWAN (s)	MSS (s)	WLAN (s)	Bluetooth (s)	NB U-NII (s)	802.15.4 (s)
< 3	100	99.84	100	100	N/A	100
3 - 6	60	N/A	60	N/A	60	N/A
6 - 10	N/A	N/A	30	N/A	30	N/A
24 - 42	4	N/A	N/A	N/A	N/A	N/A

3. Key TAS Parameters

This section defines the relevant power and RF Exposure parameters related to the TAS mechanism for this DUT.

3.1 Operating State Summary

Table 3-1 summarizes the operating states supported by the DUT:

Table 3-1: DUT Supported Operating States

RF Exposure	Power/Index State	Description
Head	Mode A	Max permissible power relative to P_{Limit} when the device is used against the user's head
Body-worn	Mode B	Max permissible power relative to P_{Limit} when the device is used in a Body-worn configuration
Hotspot	Mode B	Max permissible power relative to P_{Limit} when the device is used in a Hotspot configuration
Extremity	Mode B	Max permissible power relative to P_{Limit} when the device is used in an Extremity configuration

3.2 TAS Parameter Definitions

- P_{Max} : The maximum possible instantaneous power
- P_{Limit} : The maximum possible time-averaged power
- $P_{\text{Limit, nom}}$: The listed time-averaged target power, excluding total device uncertainty
- $P_{\text{Max, nom}}$: The maximum possible instantaneous power, excluding total device uncertainty
- SAR_{Measured} : The maximum spatial peak averaged SAR value corresponding to $P_{\text{Limit, nom}}$
- SAR_{Reported} : The maximum spatial peak averaged SAR value inclusive of total device uncertainty, corresponding to P_{Limit}
- PD_{Reported} : The maximum 4 cm² Square averaged PD value inclusive of total device uncertainty, corresponding to P_{Limit}

$P_{\text{Limit, nom}}$, P_{Limit} , $P_{\text{Max, nom}}$, and P_{Max} values are defined for all antennas, technologies, bands, power states, and operating states combinations supporting TAS on the DUT. Instantaneous power for the specified antenna/technology/band/operating state combinations is capped at P_{Max} .

3.3 Device Characterizations

The device characterizations can be found within the SAR Report 15982168-S1, Appendix G.

4. Test Sequences

4.1 Conducted

The TAS algorithm shall be validated when the base station requests different power levels to manage the link budget.

Conducted power measurements shall be performed for at least one band per technology. The P_{Limit} value associated with the chosen band should be several dB lower than the corresponding value of P_{Max} , e.g. 2 to 4 dB lower. Whenever possible, the same band should not be repeated for different technologies.

4.1.1 Start-up Test Sequences

4.1.1.1 Start-up Sequence A

- Upon start-up, request a power level of $P_{Max, nom}$ for a period of at least 1.5x of the Time-averaging Duration, followed by $0.5 \cdot P_{Limit, nom}$ for a period of at least 1.5x of the Time-averaging Duration.

4.1.1.2 Start-up Sequence B

- Upon start-up, request a power level of 1 mW (0 dBm) for a period of at least 1.5x of the Time-averaging Duration, followed by $P_{Max, nom}$ for a period of at least 1.5x of the Time-averaging Duration.

4.1.2 Pseudo-random Sequence

A pseudo-random sequence of power requests shall be applied to validate the dynamic behavior of the TAS algorithm. Each test shall be performed with a unique sequence of 150 independent power requests for WWAN and 40 independent power requests for WLAN. These power levels are calculated as follows (Equation 1):

Equation 1: Power Level Equation

$$P_{req} = P_{Max, nom} \left(\frac{P_{Limit, nom}}{P_{Max, nom}} \right)^x$$

where P_{req} is the requested power in watts and x is a random value drawn from the Weibull distribution with shape and scale parameters of 2.0 and 0.8, and 1.0 and 0.4, respectively, for WWAN and WLAN. These values were chosen to ensure that P_{req} exceeds $P_{Limit, nom}$ on average, while maintaining a reasonable likelihood that some P_{req} values will sometimes fall well below $P_{Limit, nom}$.

The corresponding request durations are given by (Equation 2):

Equation 2: T_{req} Durations

$$T_{req} = d * 2(1 + 2y)$$

where T_{req} is the duration of the power request in seconds, d is the extended duration (1 for WWAN and 4 for WLAN), and y is a uniformly distributed random value between 0 and 1.

4.1.3 Antenna Switching

Conducted power measurements shall be performed to validate the TAS algorithm in antenna switching scenarios. Maximum power shall be requested from the DUT throughout the test. The switch between antennas and duplexes shall occur once the TAS algorithm has reached steady state for the first antenna and duplex, and the test shall conclude once the algorithm has reached steady state for the second antenna and duplex.

When different P_{Limit} and P_{Max} values are associated with each transmitting antenna and duplex, consideration shall be given to the combinations of antennas/duplexes and operating state(s) for which the P_{Limit} values are below the corresponding P_{Max} values and where one of the largest differences between P_{Max} and P_{Limit} occurs. Of these combinations, the performance of the TAS algorithm shall be validated when the DUT switches from the antenna with the highest P_{Limit} value to the lowest.

4.1.4 Change in Operating State

Conducted power measurements shall be performed to validate the TAS algorithm when the DUT changes between operating states with different P_{Limit} values, e.g., when sensors or other mechanisms are used to change operating states. Maximum power shall be requested from the DUT throughout the test. The change in operating state shall occur once the TAS algorithm has reached steady state for the first operating state, and the test shall conclude once the algorithm has reached steady state for the second operating state.

4.1.5 Frequency Band Hand-off or Redirect

Conducted power measurements shall be performed to validate the TAS algorithm when the DUT switches between frequency bands with different P_{Limit} values. When the frequency band hand-off or redirect logic of the algorithm is the same regardless of the technology, only one test is required. Maximum power shall be requested from the DUT throughout the test. The change in frequency band shall occur once the TAS algorithm has reached steady state, and the test shall also conclude once the algorithm has reached steady state.

4.1.6 Technology Hand-off

Conducted power measurements shall be performed to validate the TAS algorithm when the DUT switches between technologies with different P_{Limit} values. Maximum power shall be requested from the DUT throughout the test. The technology hand-off shall occur once the TAS algorithm has reached steady state for the first technology(ies), and the test shall conclude once the algorithm has reached steady state for the second technology(ies). When there are more than two technologies that are supported by the TAS algorithm or the final product implementation, more than two technologies can be tested within one test as long as the TAS algorithm has reached steady state prior to commencing the next technology hand-off.

4.1.6.1 Additional WWAN Test Coverage

Intra-technology hand-off shall include the following test(s) for WWAN:

- Enhanced New Radio Dual Connectivity (EN-DC) with LTE and NR transmitting simultaneously

Note: If non-standalone (NSA) is supported, when power is requested for NR, LTE shall be set at low power.

- Uplink Carrier Aggregation (ULCA) transmitting simultaneously for two tests, one with two LTE Bands and the other with two NR bands

These test cases shall be performed in the following sequence: Standalone technology with one of the highest P_{Limit} , simultaneous transmission of technologies, and standalone technology with one of the lowest P_{Limit} .

4.1.7 Switching Between Time Division Duplexing and Frequency Division Duplexing

Conducted power measurements shall be performed to validate the TAS algorithm when the DUT switches between TDD and FDD configurations with different P_{Limit} values, both of which should be lower than the corresponding P_{Max} values and where one of the largest differences between P_{Max} and P_{Limit} occurs. Maximum power shall be requested from the DUT

throughout the test. The switch between configurations shall occur once the TAS algorithm has reached steady state and the test shall also conclude once the algorithms has reached steady state.

Note: This test case can be combined with frequency band hand-off or redirect.

4.1.8 Change in Modulation Scheme within a Communication Technology

Conducted power measurements shall be performed to validate the TAS algorithm for approval and final product implementations for WWAN and WLAN when the DUT changes between modulation schemes with different P_{Limit} values. For example, measurements would be required to validate a change from a higher order, for example, 64 QAM (quadrature amplitude modulation), to a lower order, such as QPSK (quadrature phase shift keying) and vice versa.

Only one test is required with the configurations where both P_{Limit} values are lower than the corresponding P_{Max} values and where one of the largest differences between P_{Max} and P_{Limit} occurs. Maximum power shall be requested from the DUT throughout the test. The switch between modulation schemes shall occur once the TAS algorithm has reached steady state and the test shall also conclude once the algorithm has reached steady state.

Note: This test case may be waived if the same P_{Limit} applies to all modulations for the technology under test.

4.1.9 Dropped Connection

Conducted power measurements shall be performed to validate the TAS algorithm during dropped connections to ensure the algorithm is able to account for previous connection states. When the dropped connection logic of the algorithm is the same regardless of the technology, only one test is required. Only one test is required with one of the configurations for which P_{Limit} is below P_{Max} and where one of the largest differences between P_{Max} and P_{Limit} occurs. Maximum power shall be requested from the DUT throughout the test. The dropped connection shall occur once the TAS algorithm has reached steady state, and the test shall conclude once steady state has been regained after the dropped connection.

4.1.10 Test Sequence Validation

Validation plots are included for the applicable test cases for this DUT. These plots serve to validate the operation of this TAS algorithm by ensuring that the measured power averaged over the applicable regulatory limit ($P[n]$) is always maintained below the defined P_{Limit} .

Conducted power measurements (P_{Meas}) are used to validate all TAS test cases. In test cases where P_{Limit} remains constant, the equation, Equation 3, below is used to calculate the rolling time-averaged power for the n^{th} time step ($P[n]$).

Equation 3: Rolling Time-averaged Power (Constant P_{Limit})

$$P[n] = \frac{1}{M} \sum_{m=0}^{M-1} P_{Meas}[n-m]$$

Where M is the total number of reporting periods per time-averaging window and m is the index of the time-averaging window. The TAS algorithm is validated when $P[n]$ is maintained less than or equal to P_{Limit} .

In test cases where P_{Limit} is not constant, the equation, Equation 4, below is used to calculate the normalized rolling time-averaged power ($p[n]$).

Equation 4: Rolling Time-averaged Power (P_{Limit} not Constant)

$$p[n] = \frac{1}{M} \sum_{m=0}^{M-1} \frac{P_{Meas}[n-m]}{P_{Limit}[n-m]}$$

Where M is the total number of reporting periods per time-averaging window and m is the index of the time-averaging window. The TAS algorithm is validated when $p[n]$ is maintained less than or equal to 1.

4.2 Radiated

4.2.1 Test Procedure and Validation

To validate linearity, point SAR measurements are performed with TAS operation turned off. Per antenna, the mode with the largest difference between P_{Limit} and P_{Max} will be selected for measurement. When possible, different frequency bands will be assessed per antenna.

Point SAR measurements are performed in 2 dB steps from P_{Max} to at least P_{Limit} minus 4 dB.

Linearity is sufficiently validated when the SAR versus requested power is within the defined device uncertainties.

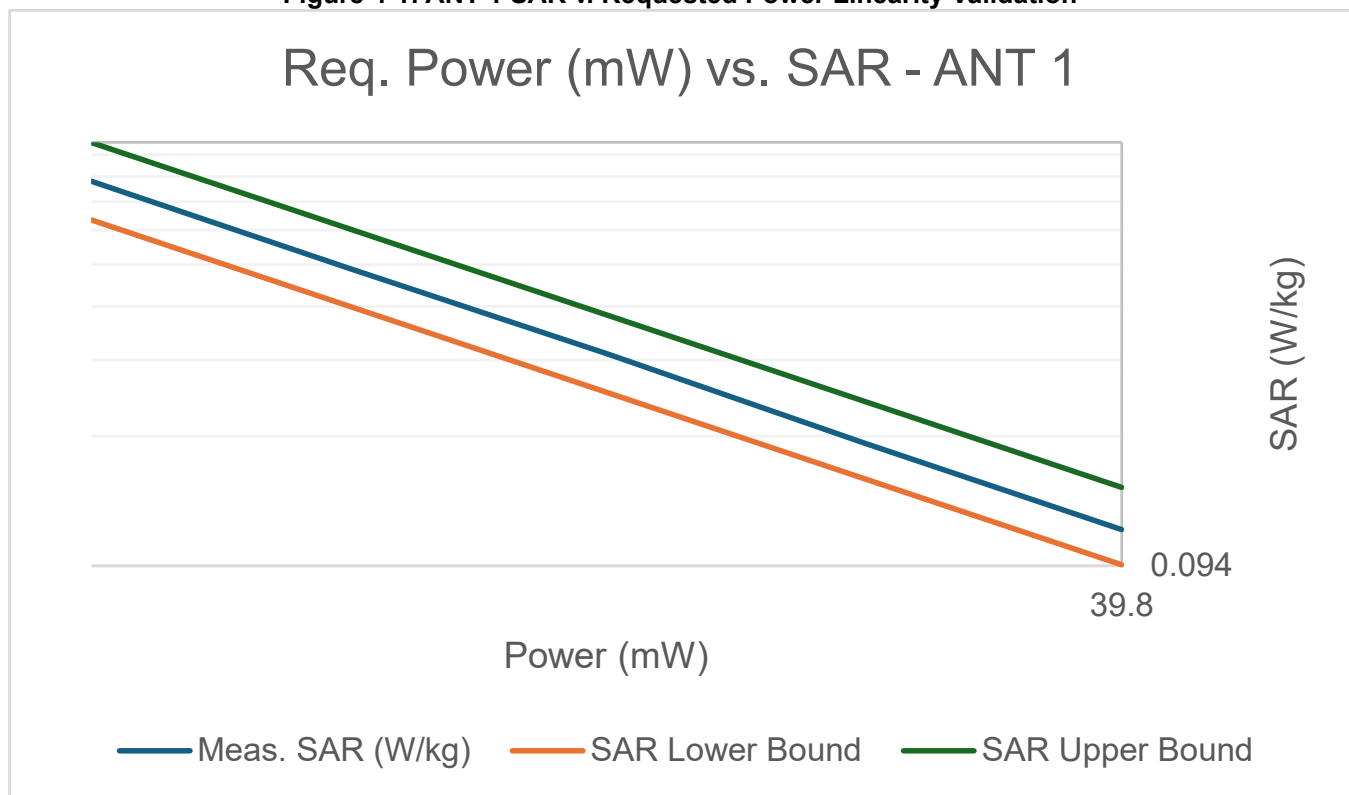
4.2.1.1 ANT 1

The table and figure below demonstrate that the linearity is sufficiently validated as the SAR versus requested power is within the expected uncertainties.

Table 4-1: ANT 1 SAR v. Requested Power

Transmitter	RF Exposure Condition	Technology	Uncert. ($\pm$)	P_{Max}	P_{Limit}	Req. Power (dBm)	Req. Power (mW)	Meas. SAR (W/kg)	SAR Lower Bound	SAR Upper Bound
ANT 1	Body	FR-1 TDD n41 PC3	0.9	25.7	21.9	24	251.2	0.733	0.596	0.902
ANT 1	Body	FR-1 TDD n41 PC3	0.9	25.7	21.9	22	158.5	0.461	0.376	0.569
ANT 1	Body	FR-1 TDD n41 PC3	0.9	25.7	21.9	20	100.0	0.292	0.237	0.359
ANT 1	Body	FR-1 TDD n41 PC3	0.9	25.7	21.9	18	63.1	0.181	0.150	0.227
ANT 1	Body	FR-1 TDD n41 PC3	0.9	25.7	21.9	16	39.8	0.114	0.094	0.143

Figure 4-1: ANT 1 SAR v. Requested Power Linearity Validation



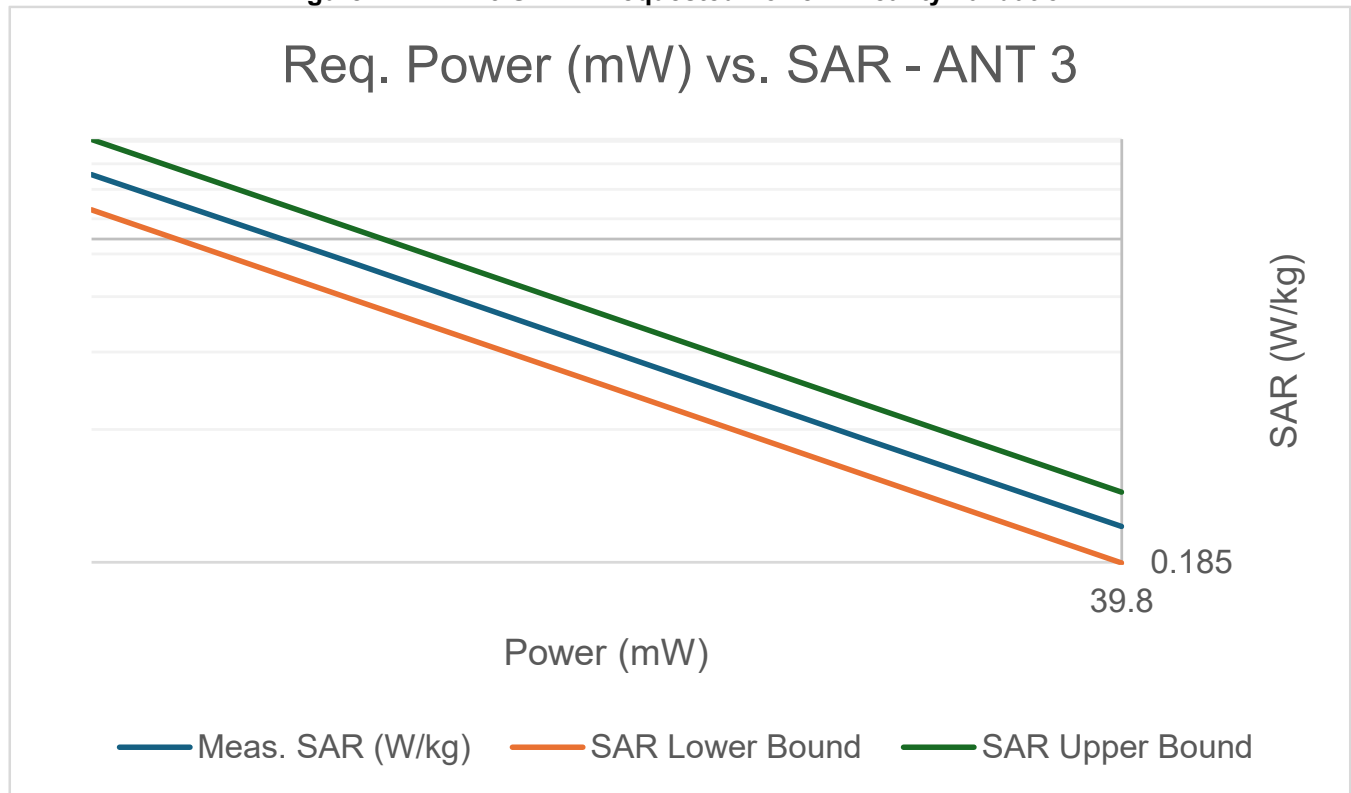
4.2.1.2 ANT 3

The table and figure below demonstrate that the linearity is sufficiently validated as the SAR versus requested power is within the expected uncertainties.

Table 4-2: ANT 3 SAR v. Requested Power

Transmitter	RF Exposure Condition	Technology	Uncert. ($\pm$)	PMax	PLimit	Req. Power (dBm)	Req. Power (mW)	Meas. SAR (W/kg)	SAR Lower Bound	SAR Upper Bound
ANT 3	Body	LTE FDD B7	0.8	25.7	20.6	24	251.2	1.400	1.164	1.683
ANT 3	Body	LTE FDD B7	0.8	25.7	20.6	22	158.5	0.889	0.735	1.062
ANT 3	Body	LTE FDD B7	0.8	25.7	20.6	20	100.0	0.559	0.464	0.670
ANT 3	Body	LTE FDD B7	0.8	25.7	20.6	18	63.1	0.352	0.293	0.423
ANT 3	Body	LTE FDD B7	0.8	25.7	20.6	16	39.8	0.223	0.185	0.267

Figure 4-2: ANT 3 SAR v. Requested Power Linearity Validation



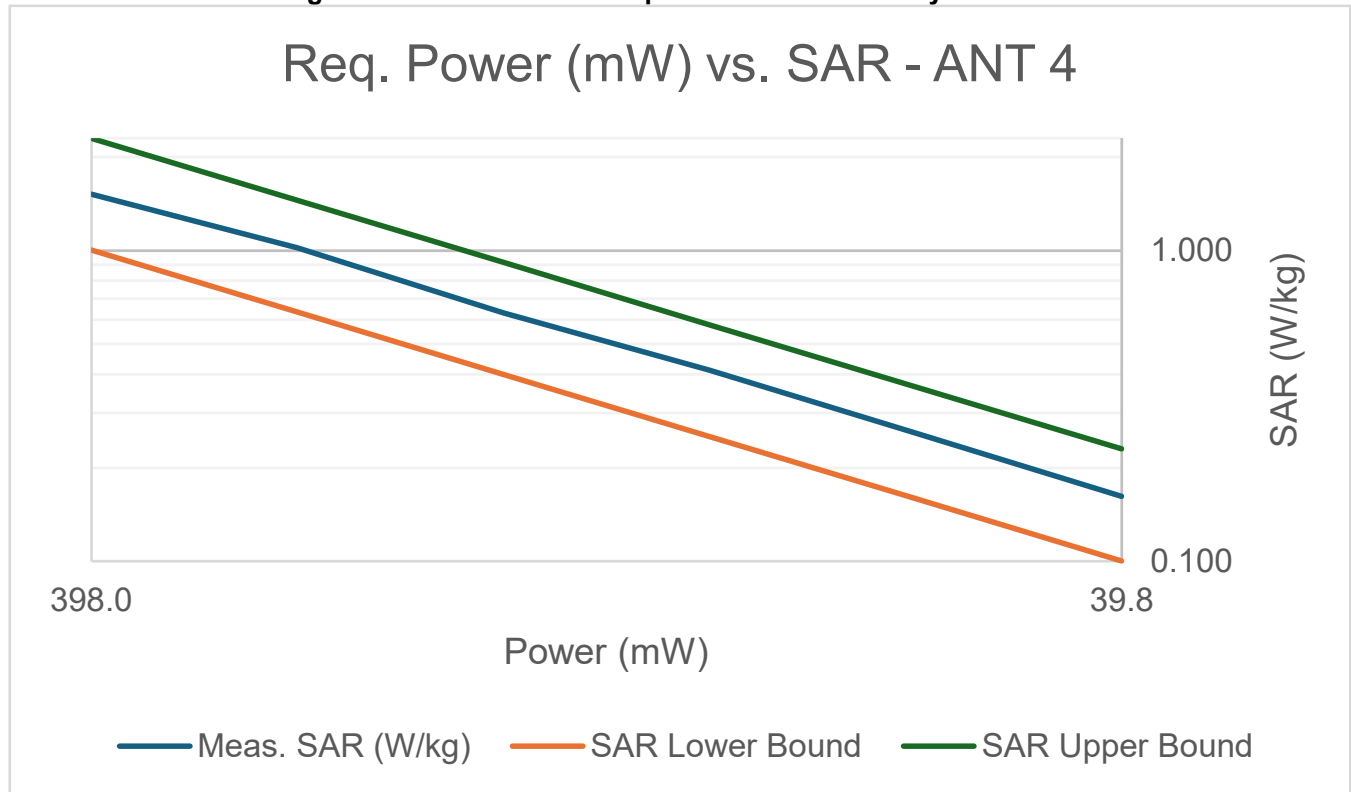
4.2.1.3 ANT 4

The table and figure below demonstrate that the linearity is sufficiently validated as the SAR versus requested power is within the expected uncertainties.

Table 4-3: ANT 4 SAR v. Requested Power

Transmitter	RF Exposure Condition	Technology	Uncert. ($\pm$)	PMax	PLimit	Req. Power (dBm)	Req. Power (mW)	Meas. SAR (W/kg)	SAR Lower Bound	SAR Upper Bound
ANT 4	Body	FR-1 n77 (Block C)	1.8	26.0	20.5	26	398.1	1.520	1.004	2.301
ANT 4	Body	FR-1 n77 (Block C)	1.8	26.0	20.5	24	251.2	1.020	0.634	1.452
ANT 4	Body	FR-1 n77 (Block C)	1.8	26.0	20.5	22	158.5	0.631	0.400	0.916
ANT 4	Body	FR-1 n77 (Block C)	1.8	26.0	20.5	20	100.0	0.412	0.252	0.578
ANT 4	Body	FR-1 n77 (Block C)	1.8	26.0	20.5	18	63.1	0.259	0.159	0.365
ANT 4	Body	FR-1 n77 (Block C)	1.8	26.0	20.5	16	39.8	0.162	0.100	0.230

Figure 4-3: ANT 4 SAR v. Requested Power Linearity Validation



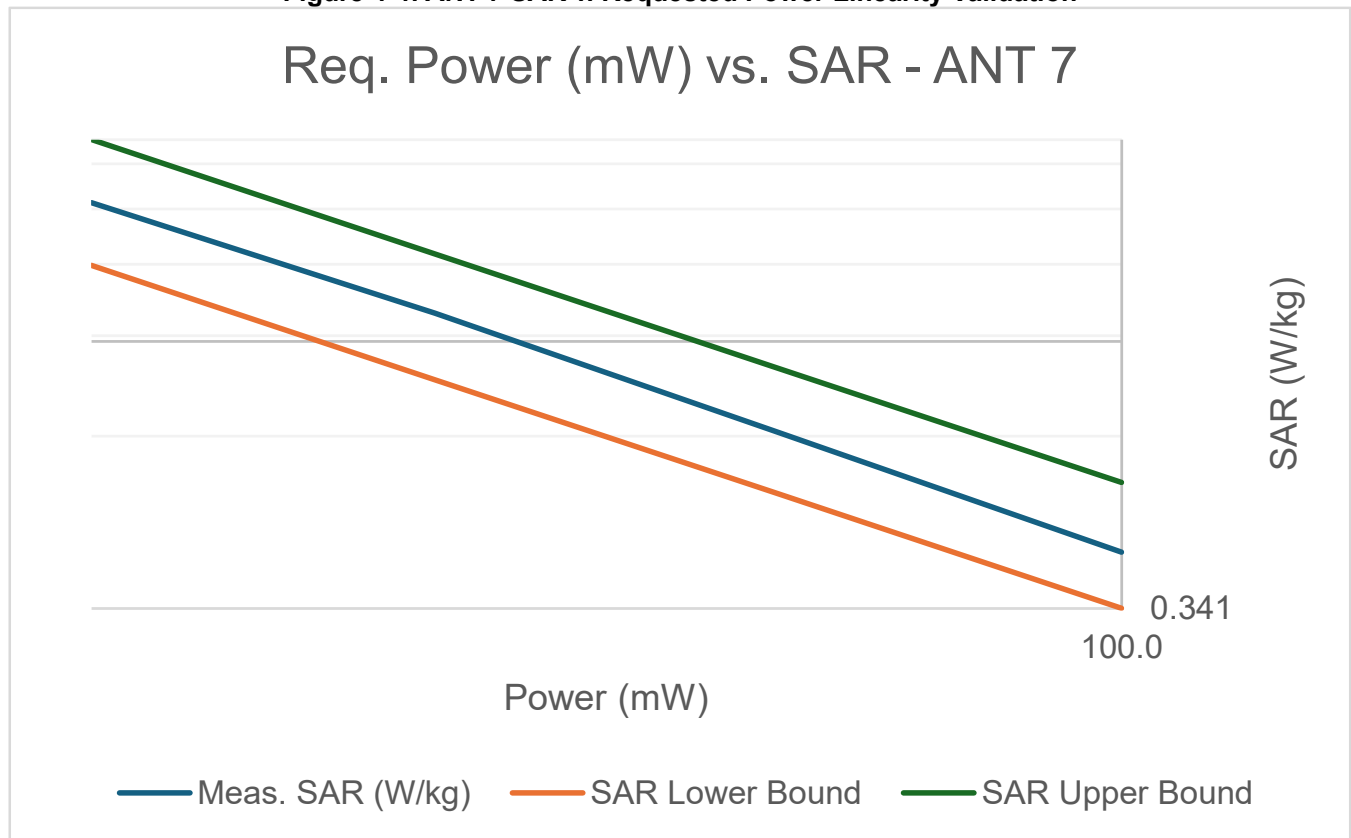
4.2.1.4 ANT 7

The table and figure below demonstrate that the linearity is sufficiently validated as the SAR versus requested power is within the expected uncertainties.

Table 4-4: ANT 7 SAR v. Requested Power

Transmitter	RF Exposure Condition	Technology	Uncert. (±)	PMax	PLimit	Req. Power (dBm)	Req. Power (mW)	Meas. SAR (W/kg)	SAR Lower Bound	SAR Upper Bound
ANT 7	Body	FR-1 n77 (Block A)	1.1	26.0	24.6	26	398.1	1.750	1.358	2.254
ANT 7	Body	FR-1 n77 (Block A)	1.1	26.0	24.6	24	251.2	1.120	0.857	1.422
ANT 7	Body	FR-1 n77 (Block A)	1.1	26.0	24.6	22	158.5	0.693	0.541	0.898
ANT 7	Body	FR-1 n77 (Block A)	1.1	26.0	24.6	20	100.0	0.428	0.341	0.566

Figure 4-4: ANT 7 SAR v. Requested Power Linearity Validation



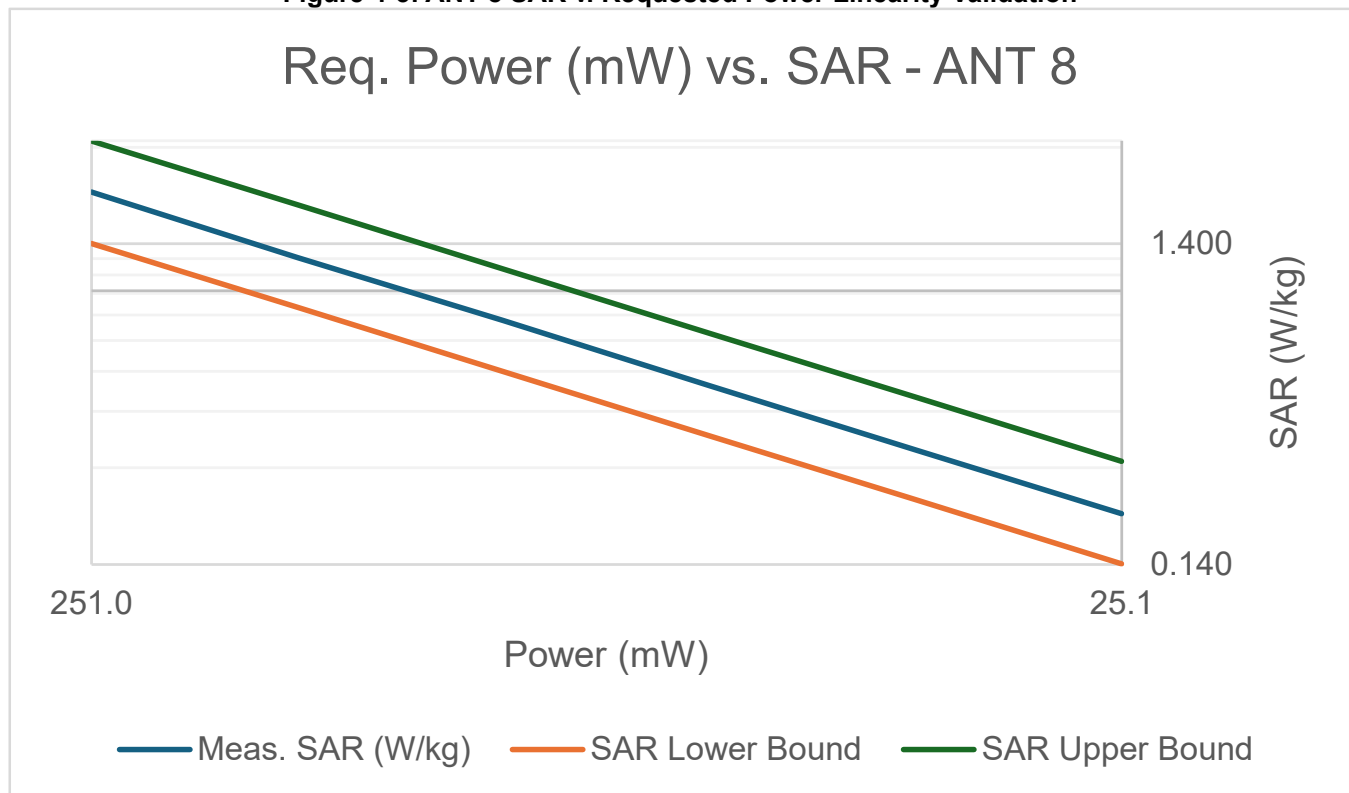
4.2.1.5 ANT 8

The table and figure below demonstrate that the linearity is sufficiently validated as the SAR versus requested power is within the expected uncertainties.

Table 4-5: ANT 8 SAR v. Requested Power

Transmitter	RF Exposure Condition	Technology	Uncert. ($\pm$)	PMax	PLimit	Req. Power (dBm)	Req. Power (mW)	Meas. SAR (W/kg)	SAR Lower Bound	SAR Upper Bound
ANT 8	Body	LTE TDD B48	1.6	25.3	19.7	24	251.2	2.030	1.404	2.934
ANT 8	Body	LTE TDD B48	1.6	25.3	19.7	22	158.5	1.270	0.886	1.851
ANT 8	Body	LTE TDD B48	1.6	25.3	19.7	20	100.0	0.806	0.559	1.168
ANT 8	Body	LTE TDD B48	1.6	25.3	19.7	18	63.1	0.506	0.353	0.737
ANT 8	Body	LTE TDD B48	1.6	25.3	19.7	16	39.8	0.319	0.223	0.465
ANT 8	Body	LTE TDD B48	1.6	25.3	19.7	14	25.1	0.201	0.140	0.293

Figure 4-5: ANT 8 SAR v. Requested Power Linearity Validation



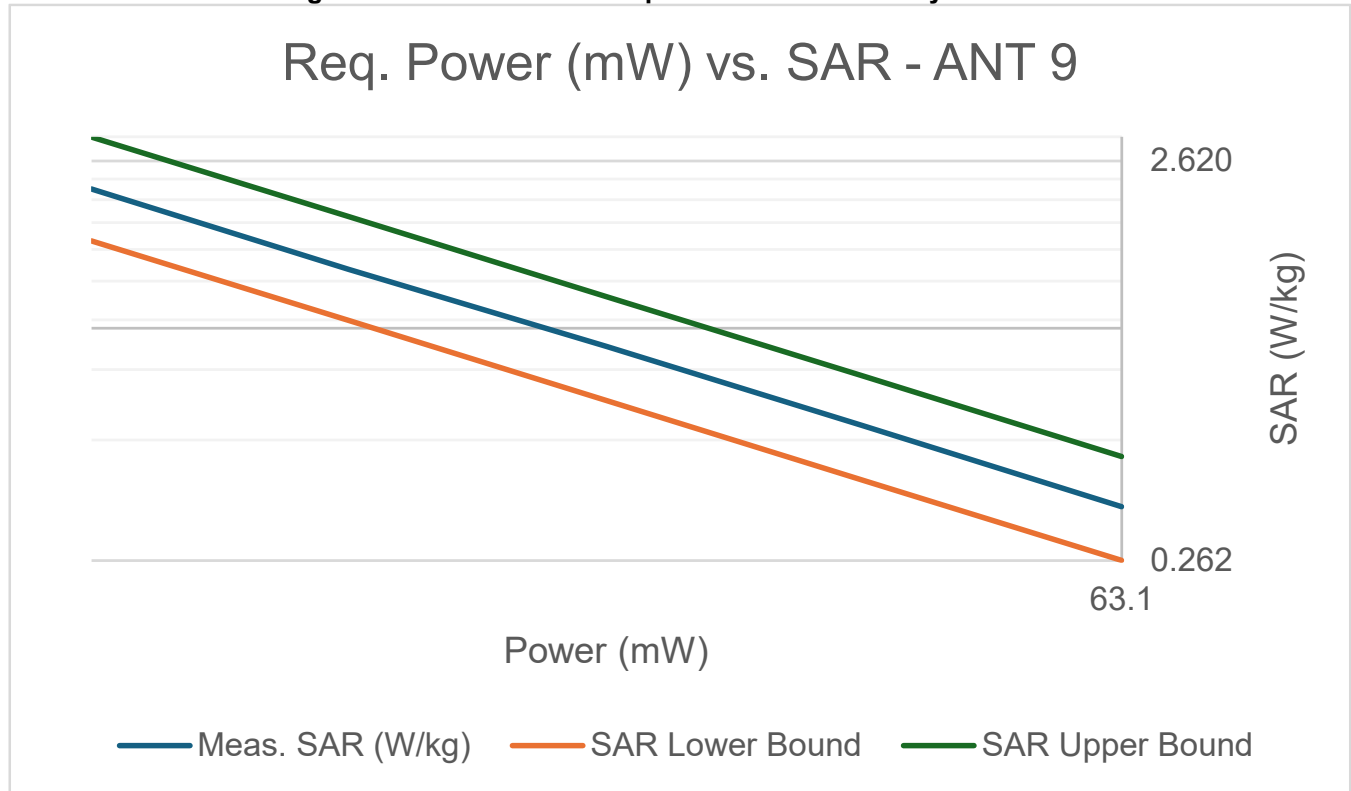
4.2.1.6 ANT 9

The table and figure below demonstrate that the linearity is sufficiently validated as the SAR versus requested power is within the expected uncertainties.

Table 4-6: ANT 9 SAR v. Requested Power

Transmitter	RF Exposure Condition	Technology	Uncert. (±)	PMax	PLimit	Req. Power (dBm)	Req. Power (mW)	Meas. SAR (W/kg)	SAR Lower Bound	SAR Upper Bound
ANT 9	Body	FR-1 n77 (Block C)	1.3	26.0	22.1	26	398.1	2.230	1.653	3.008
ANT 9	Body	FR-1 n77 (Block C)	1.3	26.0	22.1	24	251.2	1.400	1.043	1.898
ANT 9	Body	FR-1 n77 (Block C)	1.3	26.0	22.1	22	158.5	0.900	0.658	1.198
ANT 9	Body	FR-1 n77 (Block C)	1.3	26.0	22.1	20	100.0	0.568	0.415	0.756
ANT 9	Body	FR-1 n77 (Block C)	1.3	26.0	22.1	18	63.1	0.357	0.262	0.477

Figure 4-6: ANT 9 SAR v. Requested Power Linearity Validation



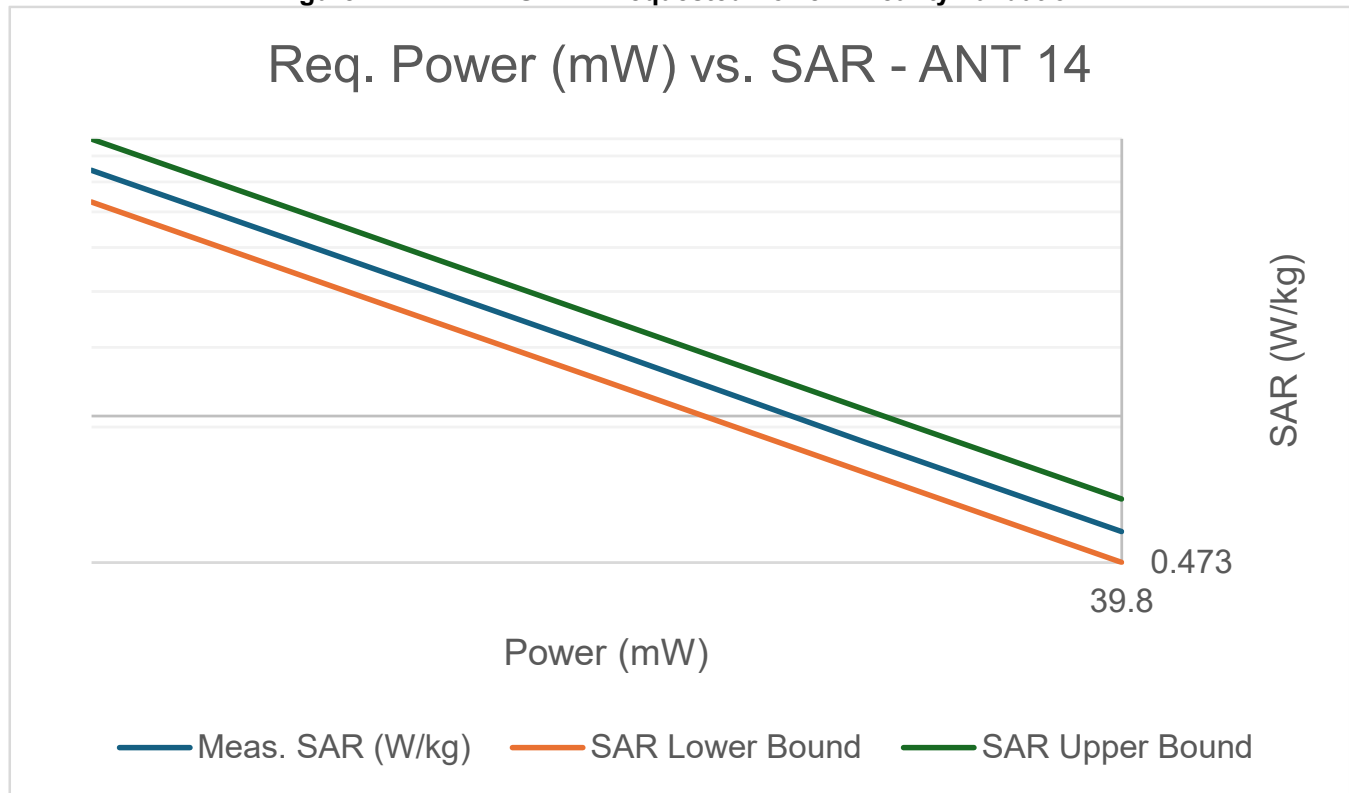
4.2.1.7 ANT 14

The table and figure below demonstrate that the linearity is sufficiently validated as the SAR versus requested power is within the expected uncertainties.

Table 4-7: ANT 14 SAR v. Requested Power

Transmitter	RF Exposure Condition	Technology	Uncert. (±)	PMax	PLimit	Req. Power (dBm)	Req. Power (mW)	Meas. SAR (W/kg)	SAR Lower Bound	SAR Upper Bound
ANT 14	Body	W-CDMA B2	0.7	25.7	20.1	24	251.2	3.510	2.987	4.124
ANT 14	Body	W-CDMA B2	0.7	25.7	20.1	22	158.5	2.220	1.885	2.602
ANT 14	Body	W-CDMA B2	0.7	25.7	20.1	20	100.0	1.400	1.189	1.642
ANT 14	Body	W-CDMA B2	0.7	25.7	20.1	18	63.1	0.877	0.750	1.036
ANT 14	Body	W-CDMA B2	0.7	25.7	20.1	16	39.8	0.554	0.473	0.654

Figure 4-7: ANT 14 SAR v. Requested Power Linearity Validation



5. Test Cases

5.1 Sub-7.2 GHz

The test cases are summarized in Table 5-1.

Table 5-1: Selected Test Cases for sub-7.2 GHz TAS Validation

Test Case No.	Test Case	Technology	Band	Modulation	Slot	Power Class	Bandwidth (MHz)	Channel	Duty Cycle (%)	Duty Cycle (dB)	Port	Transmitter	RF Exposure Condition	P _{Max} (dBm)	P _{Max} Corr. (dBm)	P _{1dB} (dBm)	P _{1dB} Corr. (dBm)	P _{Max} (mW)	P _{Max} Corr. (mW)	P _{1dB} (mW)	P _{1dB} Corr. (mW)	
1	Sequence A	LTE FDD	B30	QPSK	N/A	N/A	10	N/A	100.00%	0.00	Port C	ANT 3	Body	25.1	25.1	21.7	21.7	323.6	323.6	147.9	147.9	
2		GSM	1900	GMSK	2 Slots	N/A	N/A	N/A	25.00%	8.02	Port A	ANT 14	Body	31.0	31.0	27.2	27.2	158.9	158.9	534.8	534.8	
3		Wi-COMA	B4	QPSK	N/A	5	N/A	N/A	100.00%	0.00	Port C	ANT 3	Body	25.7	25.7	21.3	21.3	371.5	371.5	134.9	134.9	
4		LTE FDD	B25	QPSK	N/A	N/A	20	N/A	100.00%	0.00	Port D	ANT 4	Body	24.9	24.9	21.4	21.4	309.0	309.0	138.0	138.0	
5		LTE TDD	B48	QPSK	N/A	N/A	20	N/A	63.33%	1.98	Port D	ANT 8	Body	25.3	25.3	19.7	19.7	338.8	338.8	93.3	93.3	
6	Sequence B	FR-1 FDD	n7	DFT-s 1/2 BPSK	N/A	N/A	50	N/A	91.00%	0.41	Port A	ANT 14	Body	25.7	26.1	19.9	20.3	371.5	408.3	97.7	107.4	
7		FR-1 TDD	n77 (Block G)	DFT-s 1/2 BPSK	N/A	PC3	100	N/A	87.00%	1.74	Port B	ANT 4	Body	26.0	27.7	20.5	22.2	368.1	694.2	112.2	167.6	
8		Wi-Fi	UNII 3	BPSK	N/A	N/A	80	155	N/A	N/A	N/A	ANT 16	Body	20.0	20.0	15.8	15.8	100.0	100.0	37.6	37.6	
9		Wi-Fi	UNII 6	BPSK	N/A	N/A	160	111	N/A	N/A	N/A	ANT 6	Body	19.0	19.0	15.5	15.5	79.4	79.4	35.5	35.5	
10		Bluetooth	LE	LE	N/A	N/A	N/A	N/A	N/A	N/A	N/A	ANT 4	Head	21.0	21.0	19.5	19.5	125.9	125.9	89.1	89.1	
11		Pseudo-random	FR-1 TDD	n61	DFT-s 1/2 BPSK	N/A	PC3	100	N/A	87.00%	1.74	Port B	ANT 1	Body	25.7	27.4	21.9	23.6	371.5	654.5	154.0	231.2
17		Dropped Connection	LTE FDD	B7	QPSK	N/A	N/A	20	N/A	100.00%	0.00	Port B	ANT 1	Body	24.2	24.2	21.8	21.8	263.0	263.0	151.4	151.4
Test Case No.	Test Case	Technology	Band	Modulation	Slot	Power Class	Bandwidth (MHz)	Channel	Duty Cycle (%)	Duty Cycle (dB)	Port	Transmitter	RF Exposure Condition	P _{Max} (dBm)	P _{Max} Corr. (dBm)	P _{1dB} (dBm)	P _{1dB} Corr. (dBm)					
12	Antenna Switching & Change in Operating State	Wi-Fi	2.4 GHz	DSSS	N/A	N/A	20	6	N/A	N/A	N/A	ANT 3	Head	21.5	21.5	21.5	21.5	21.5	21.5	21.5	21.5	
		Wi-Fi	UNII 7	BPSK	N/A	N/A	160	143	N/A	N/A	N/A	ANT 16	Body	19.0	19.0	12.5	12.5	12.5	12.5	12.5	12.5	
15	Change in Technology	LTE FDD	B30	QPSK	N/A	N/A	10	N/A	100.00%	0.00	Port D	ANT 4	Body	23.8	23.8	21.8	21.8	21.8	21.8	21.8	21.8	
		Wi-COMA	B2	QPSK	N/A	N/A	N/A	N/A	100.00%	0.00	Port D	ANT 4	Body	24.9	24.9	20.8	20.8	20.8	20.8	20.8	20.8	
16	Switching Between Duplexes	LTE FDD	B71	QPSK	N/A	N/A	10	N/A	100.00%	0.00	Port B	ANT 2	Body	25.7	25.7	25.7	25.7	25.7	25.7	25.7	25.7	
		LTE TDD	B41	QPSK	N/A	PC3	20	N/A	63.33%	1.98	Port A	ANT 14	Body	25.7	25.7	21.4	21.4	21.4	21.4	21.4	21.4	
13	Technology Hand-off (ULCA)	LTE FDD	B12	QPSK	N/A	N/A	10	N/A	100.00%	0.00	Port B	ANT 2	Body	25.7	25.7	25.7	25.7	25.7	25.7	25.7	25.7	
		LTE FDD	B66	QPSK	N/A	N/A	20	N/A	100.00%	0.00	Port D	ANT 4	Head	25.7	25.7	21.3	21.3	21.3	21.3	21.3	21.3	
14	Technology Hand-off (ENDC)	LTE FDD	B25	QPSK	N/A	N/A	20	N/A	100.00%	0.00	Port B	ANT 1	Body	24.9	24.9	23.3	23.3	23.3	23.3	23.3	23.3	
		FR-1 TDD	n41	DFT-s 1/2 BPSK	N/A	PC3	100	N/A	87.00%	1.74	Port C	ANT 3	Body	25.7	27.4	20.6	22.3	20.6	22.3	20.6	22.3	
19	Technology Hand-off (MSS)	MSS	L-Band	N/A	N/A	N/A	N/A	N/A	N/A	N/A	N/A	Port D	ANT 4	Body	28.7	28.7	26.3	26.3	26.3	26.3	26.3	26.3
		LTE FDD	B66	QPSK	N/A	N/A	10	N/A	100.00%	0.00	Port A	ANT 14	Body	25.7	25.7	21.2	21.2	21.2	21.2	21.2	21.2	

The following figures outline the test setup for the applicable test cases:

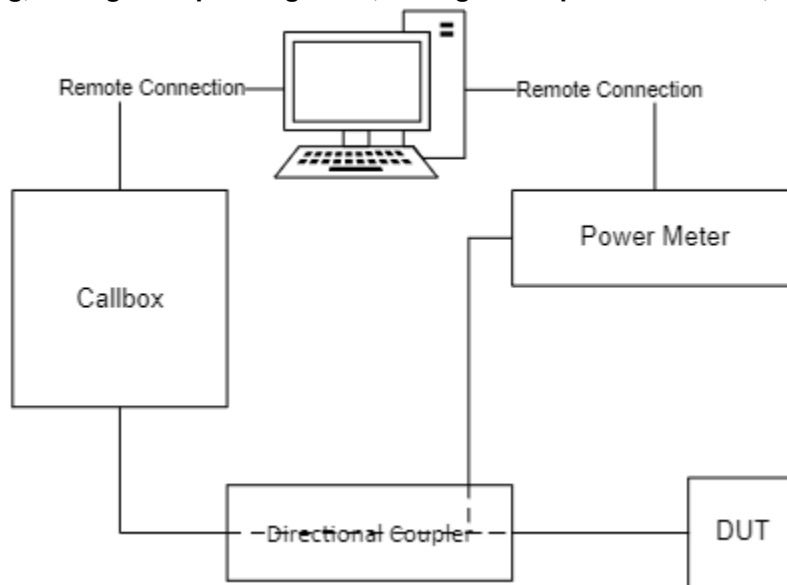
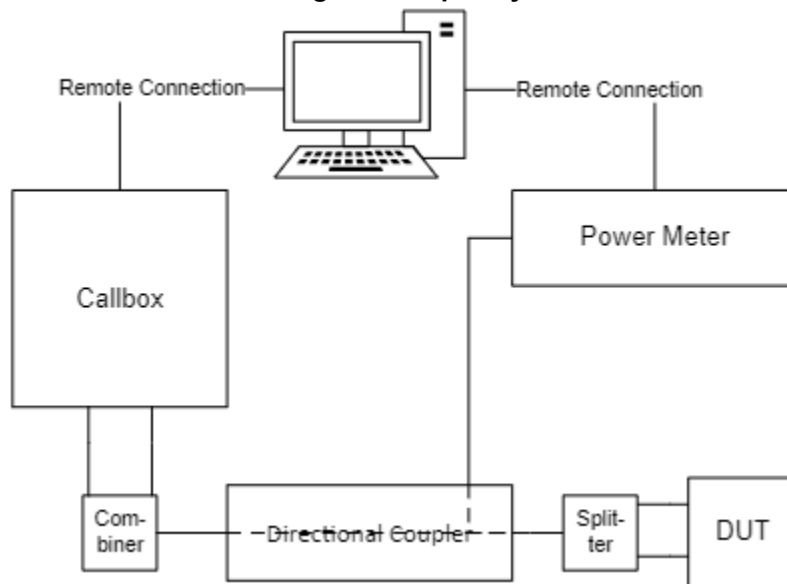
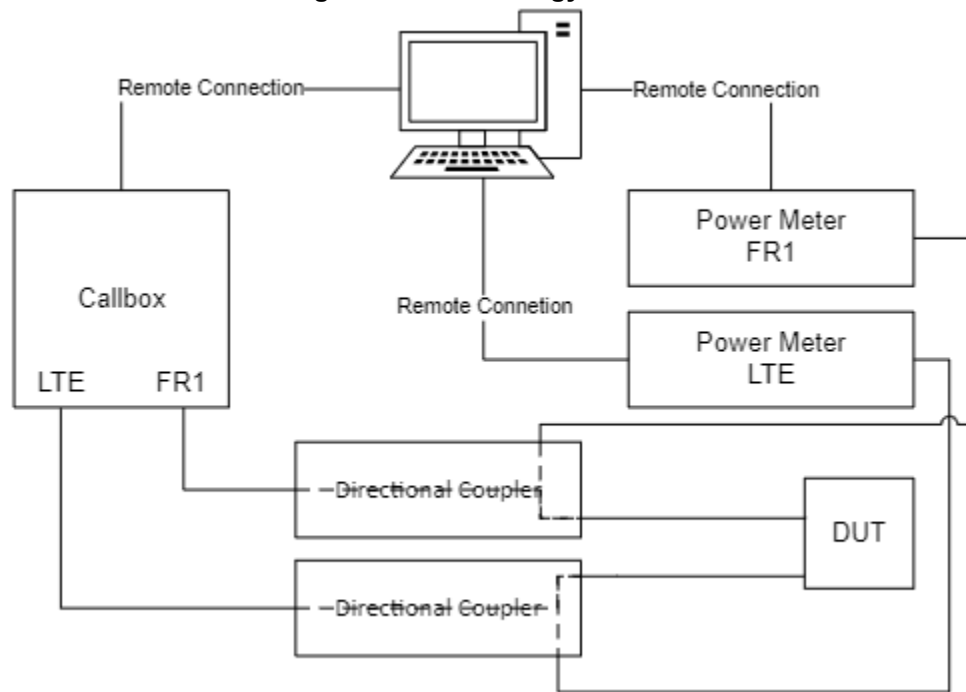
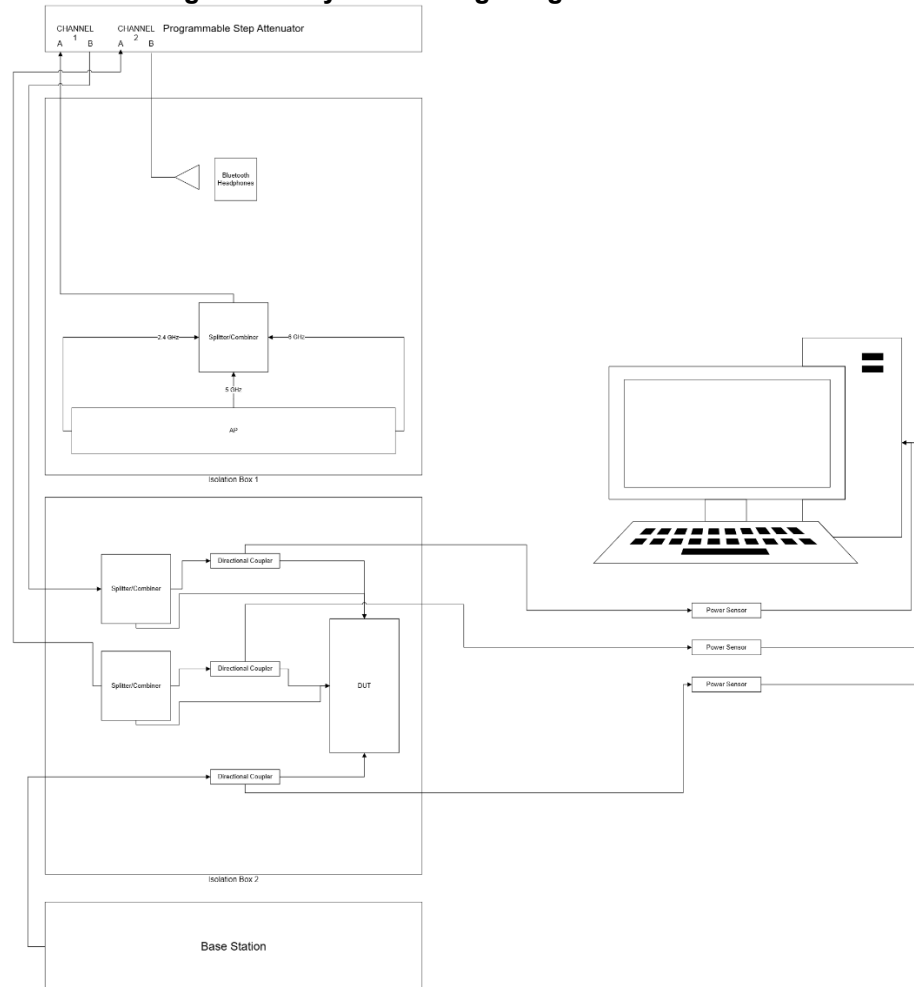
Figure 5-1: Time-varying, Change in Operating State, Change in Duplex/Modulation, and Dropped Connection**Figure 5-2: Antenna Switching and Frequency Band Hand-off or Redirect**

Figure 5-3: Technology Hand-off**Figure 5-4: Dynamic Budget Algorithm Validation**

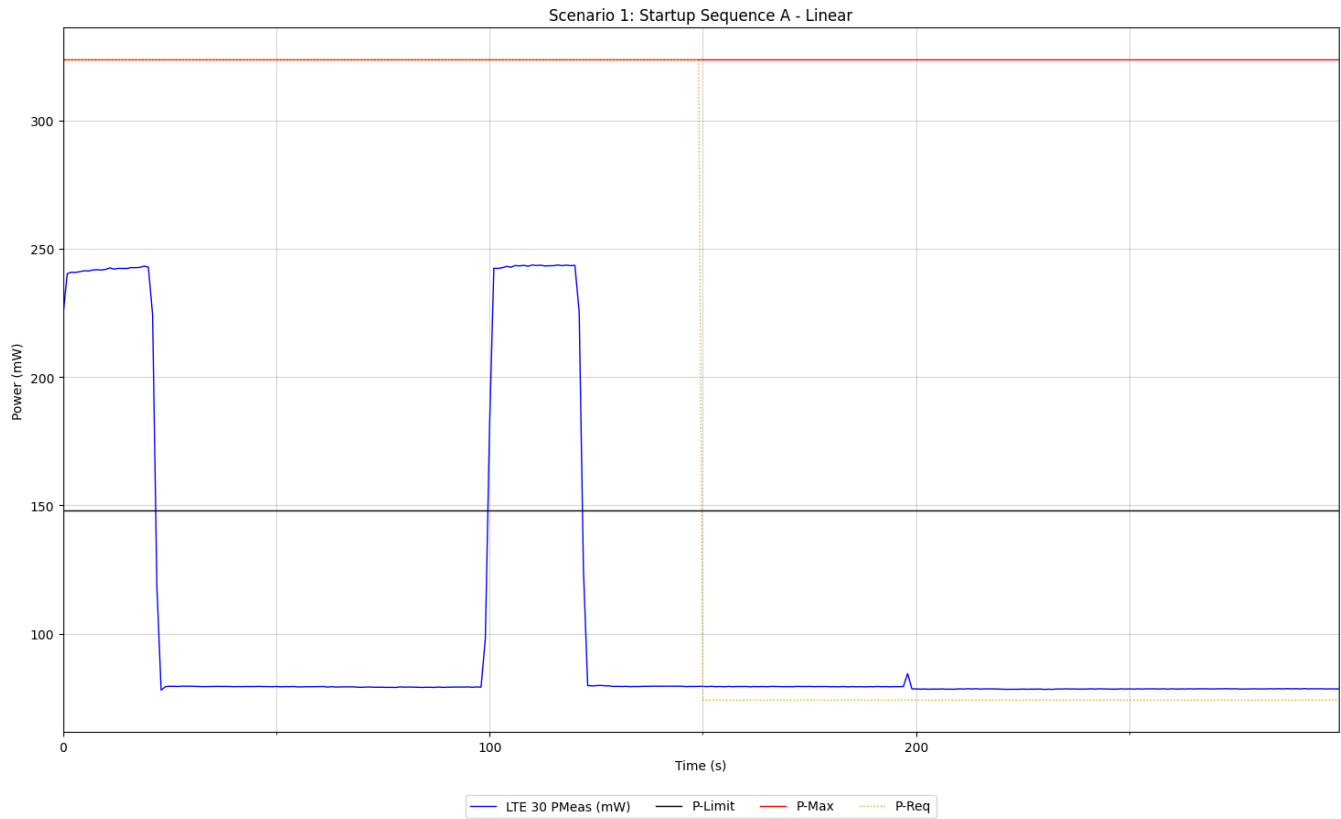
6. TAS Validation

6.1 Conducted

6.1.1 Start-up Sequence A

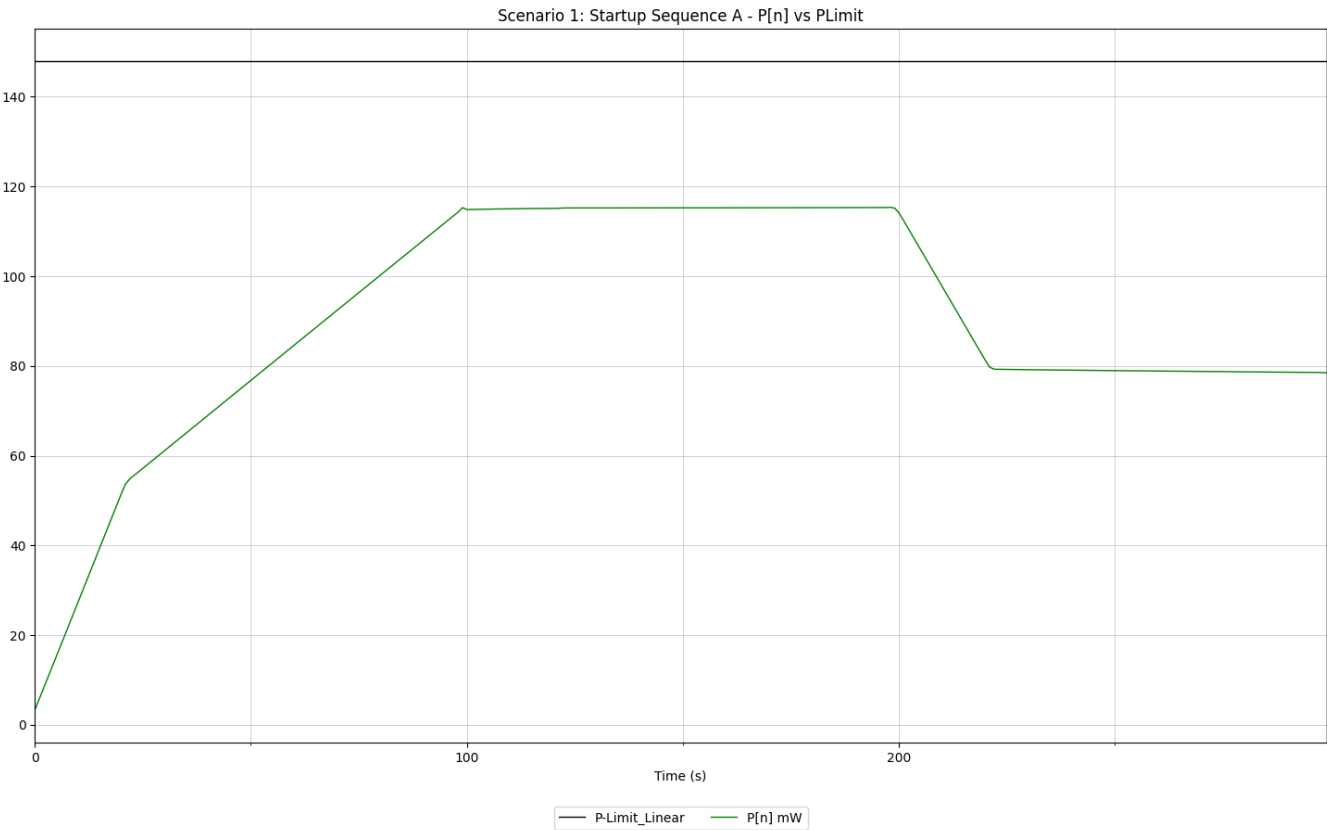
6.1.1.1 Test Case 1: LTE FDD Start-up Sequence A¹

Figure 6-1: LTE FDD Start-up Sequence A (Conducted)



¹ P_{Max} and P_{Limit} were adjusted to an operating 67% duty cycle.

Figure 6-2: LTE FDD P[n] vs. P_{Limit} Sequence A Validation (Conducted)



P[n] was calculated using the 100 second time-averaging duration.

6.1.2 Test Cases 2-10: Start-up Sequence B

6.1.2.1 Test Case 2: GSM Start-up Sequence B

Figure 6-3: GSM Start-up Sequence B (Conducted)

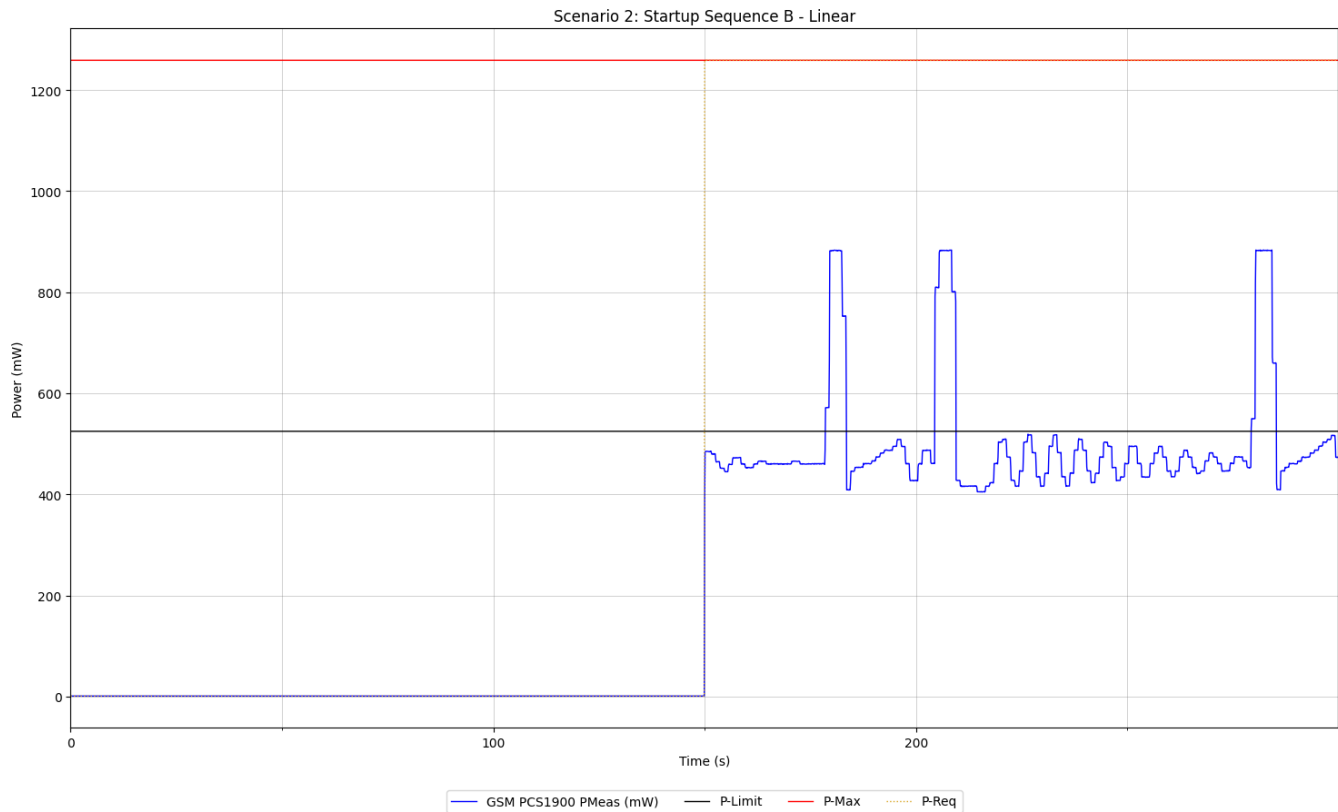
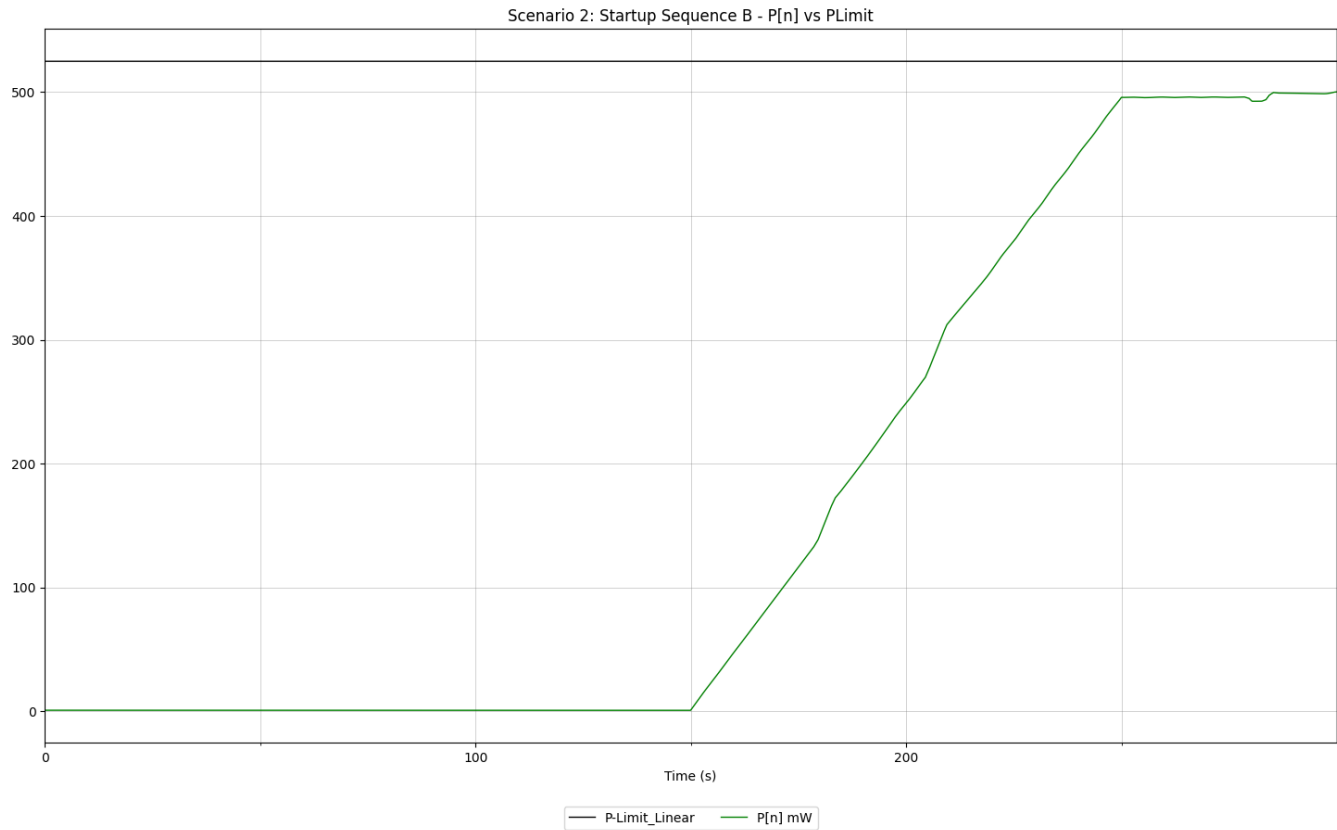


Figure 6-4: GSM P[n] vs. P_{Limit} Sequence B Validation (Conducted)



$P[n]$ was calculated using the 100 second time-averaging duration.

6.1.2.2 Test Case 3: W-CDMA Start-up Sequence B

Figure 6-5: W-CDMA Start-up Sequence B (Conducted)

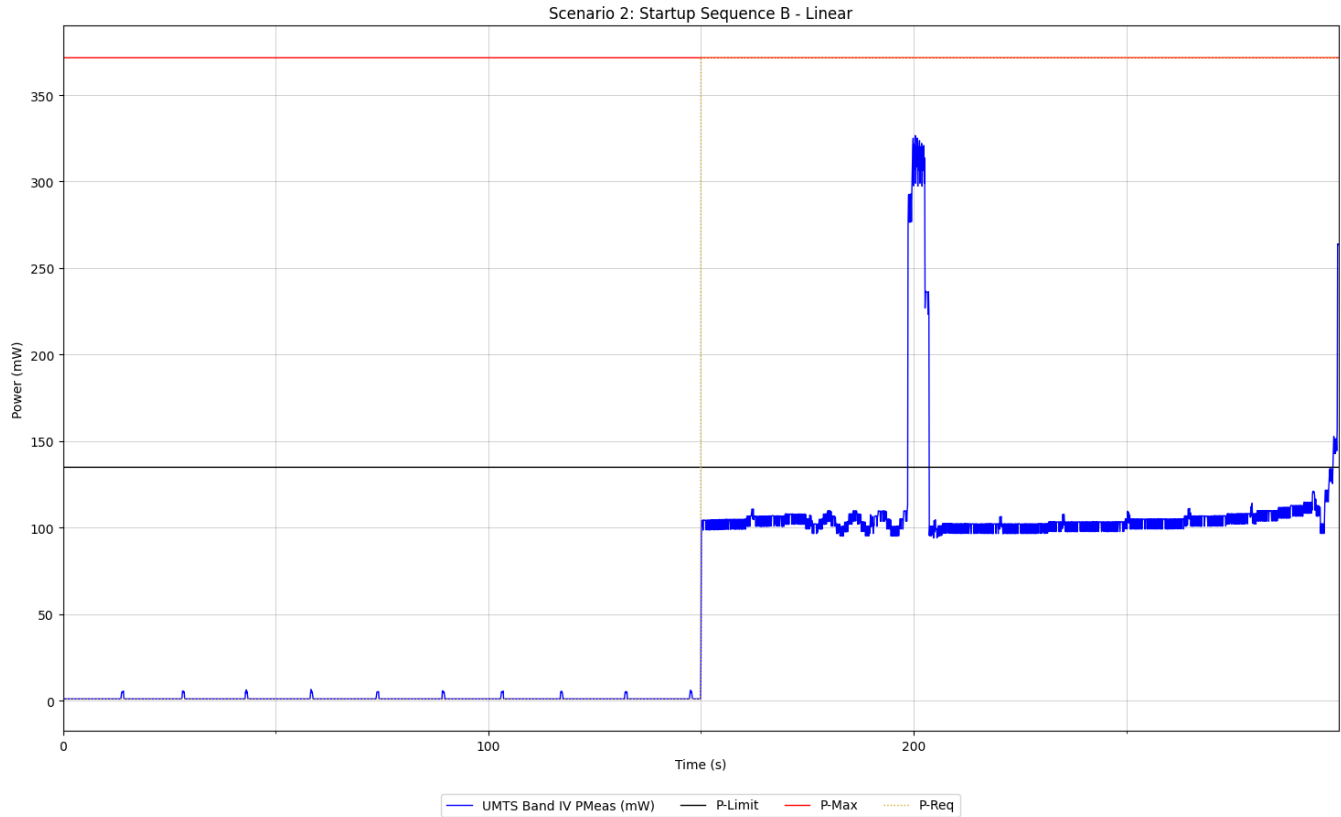
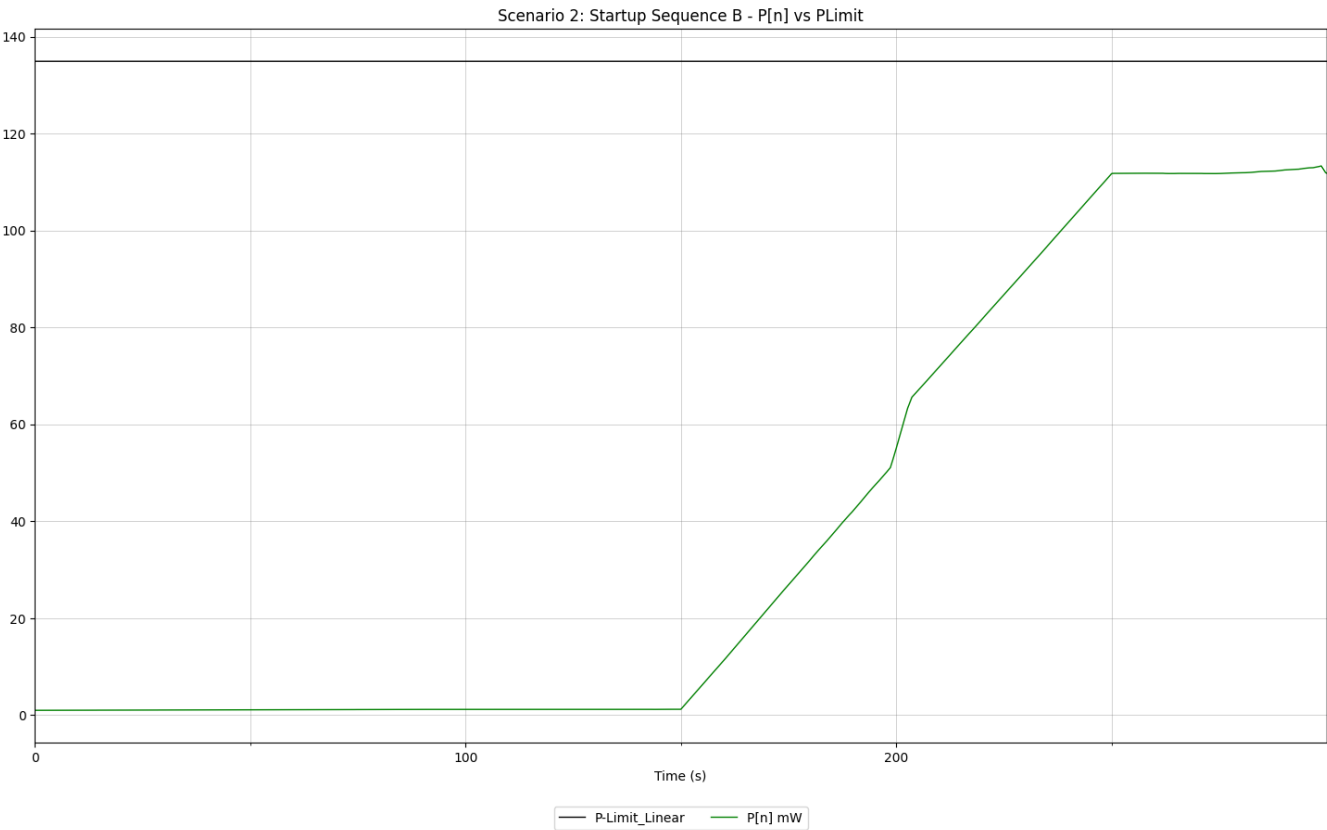


Figure 6-6: W-CDMA P[n] vs. P_{Limit} Sequence B Validation (Conducted)



P[n] was calculated using the 100 second time-averaging duration.

6.1.2.3 Test Case 4: LTE FDD Start-up Sequence B

Figure 6-7: LTE FDD Start-up Sequence B (Conducted)

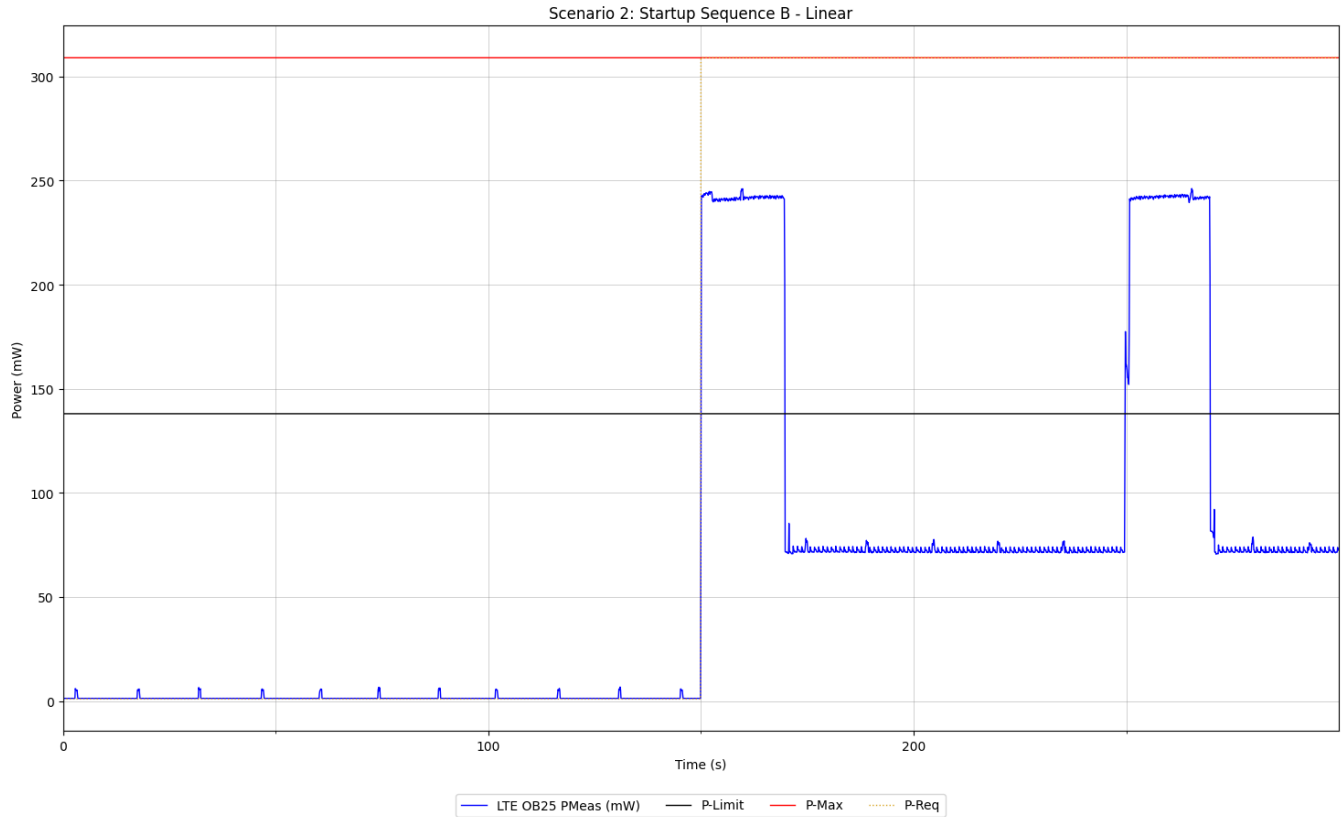
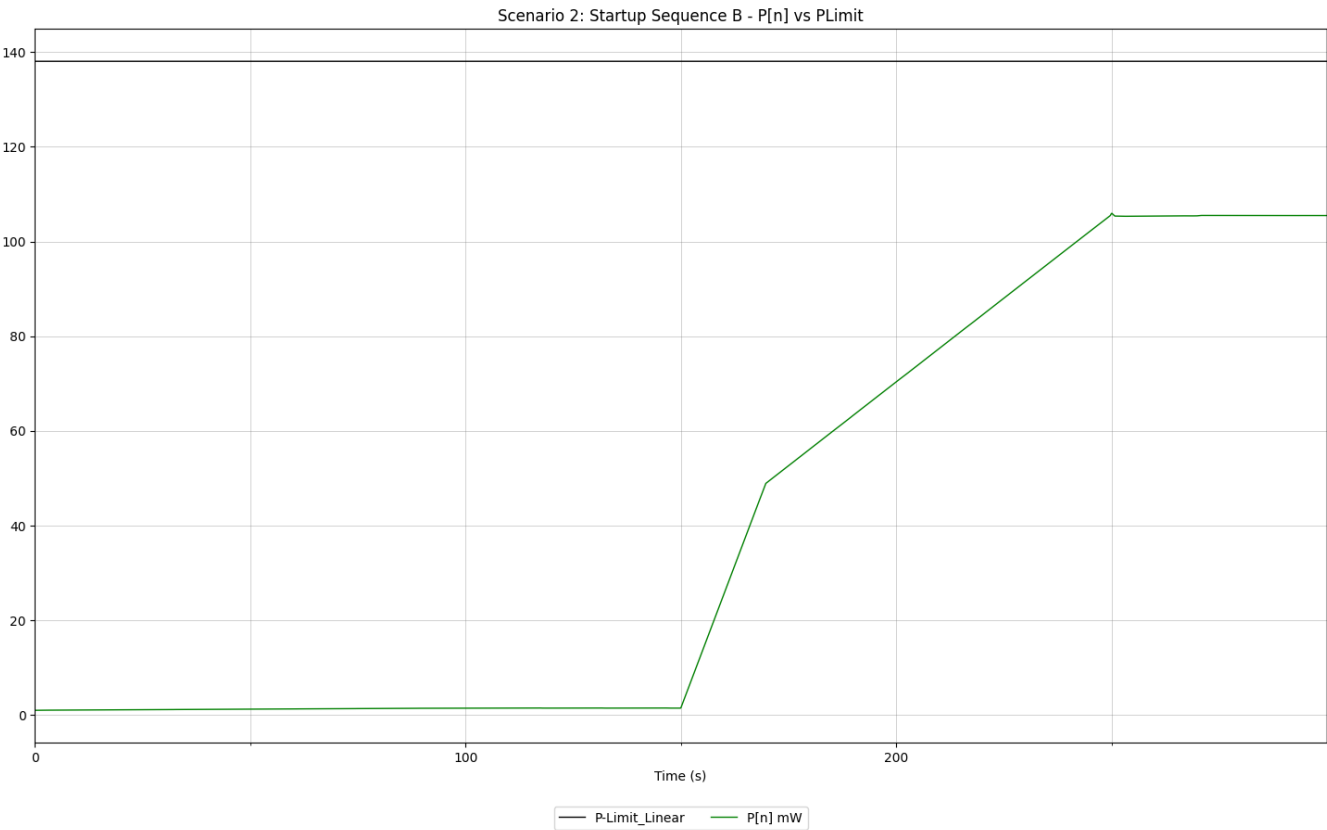


Figure 6-8: LTE FDD P[n] vs. P_{Limit} Sequence B Validation (Conducted)



P[n] was calculated using the 100 second time-averaging duration.

6.1.2.4 Test Case 5: LTE TDD Start-up Sequence B

Figure 6-9: LTE TDD Start-up Sequence B (Conducted)

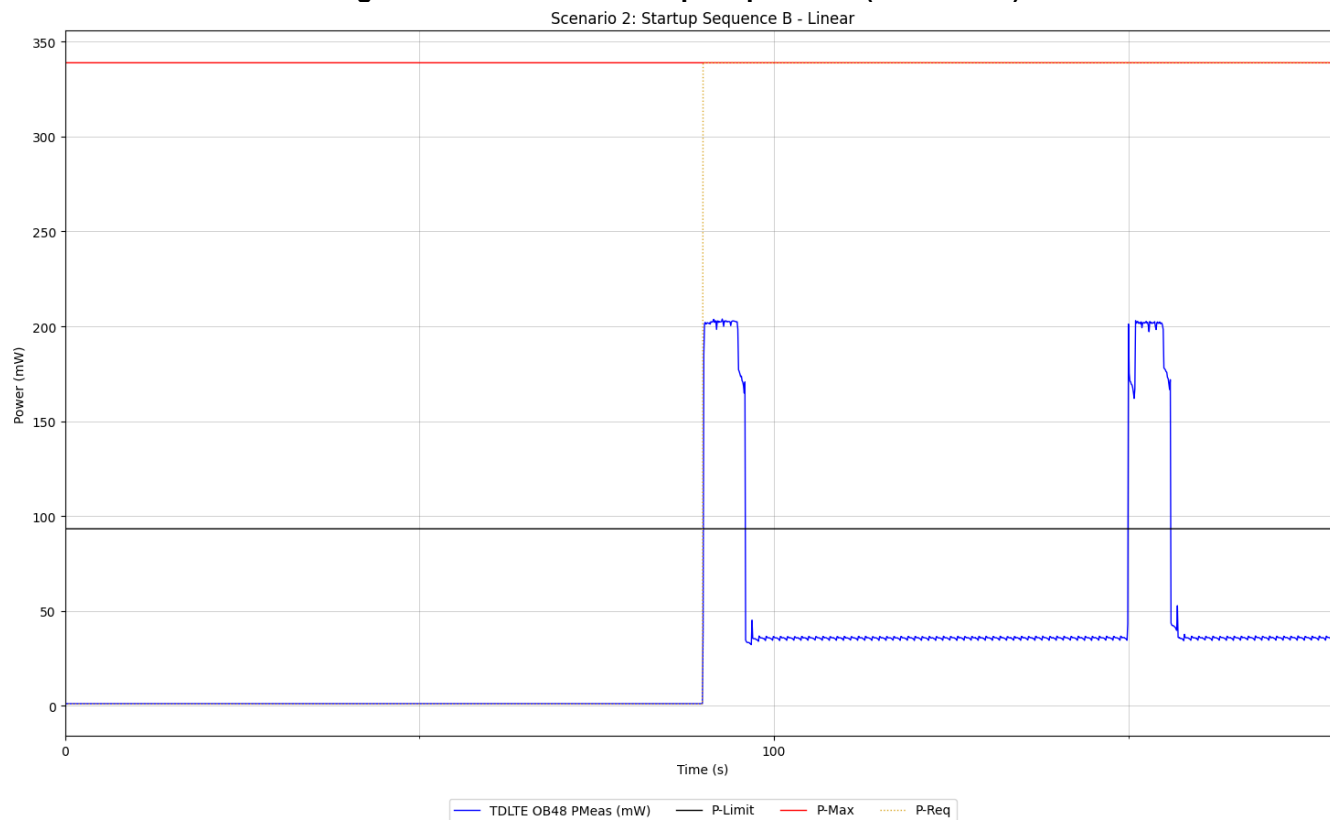
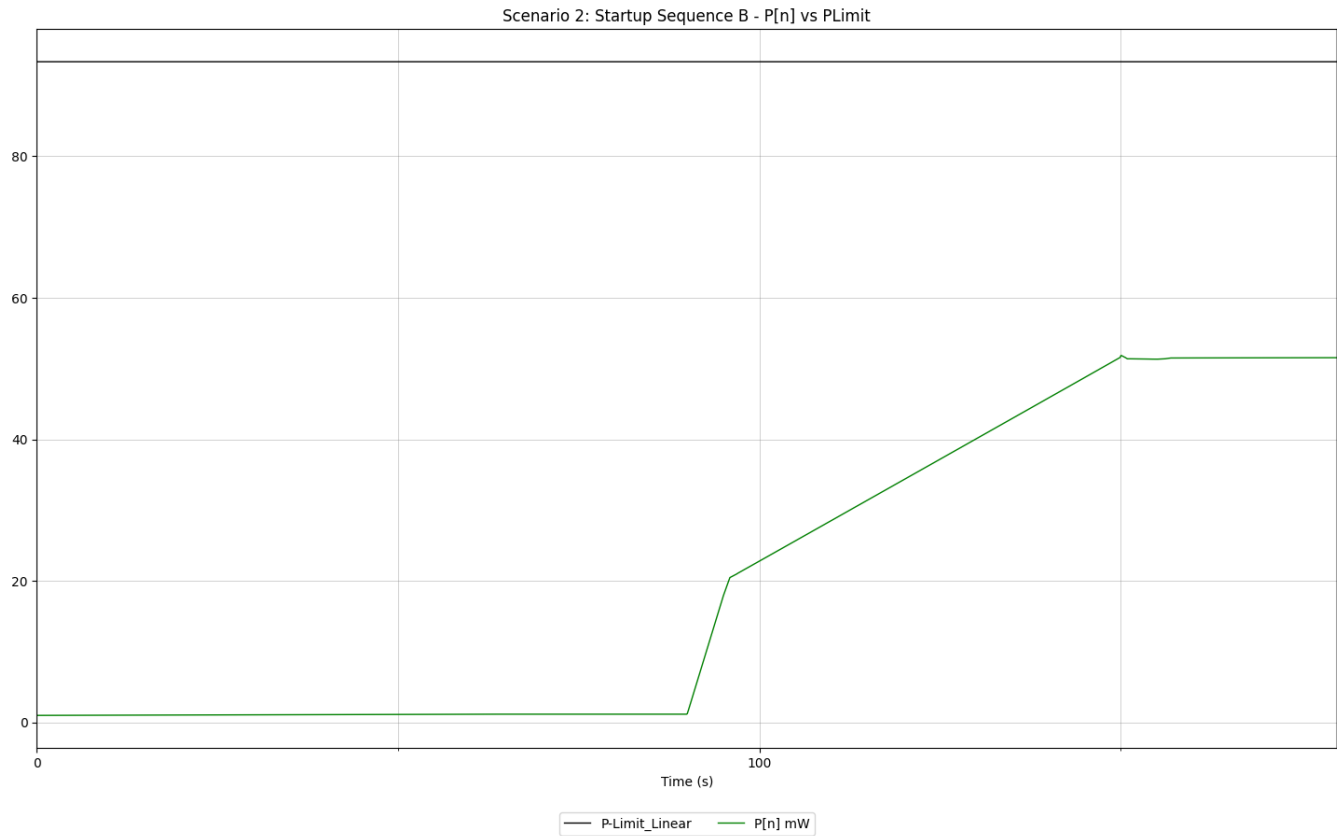


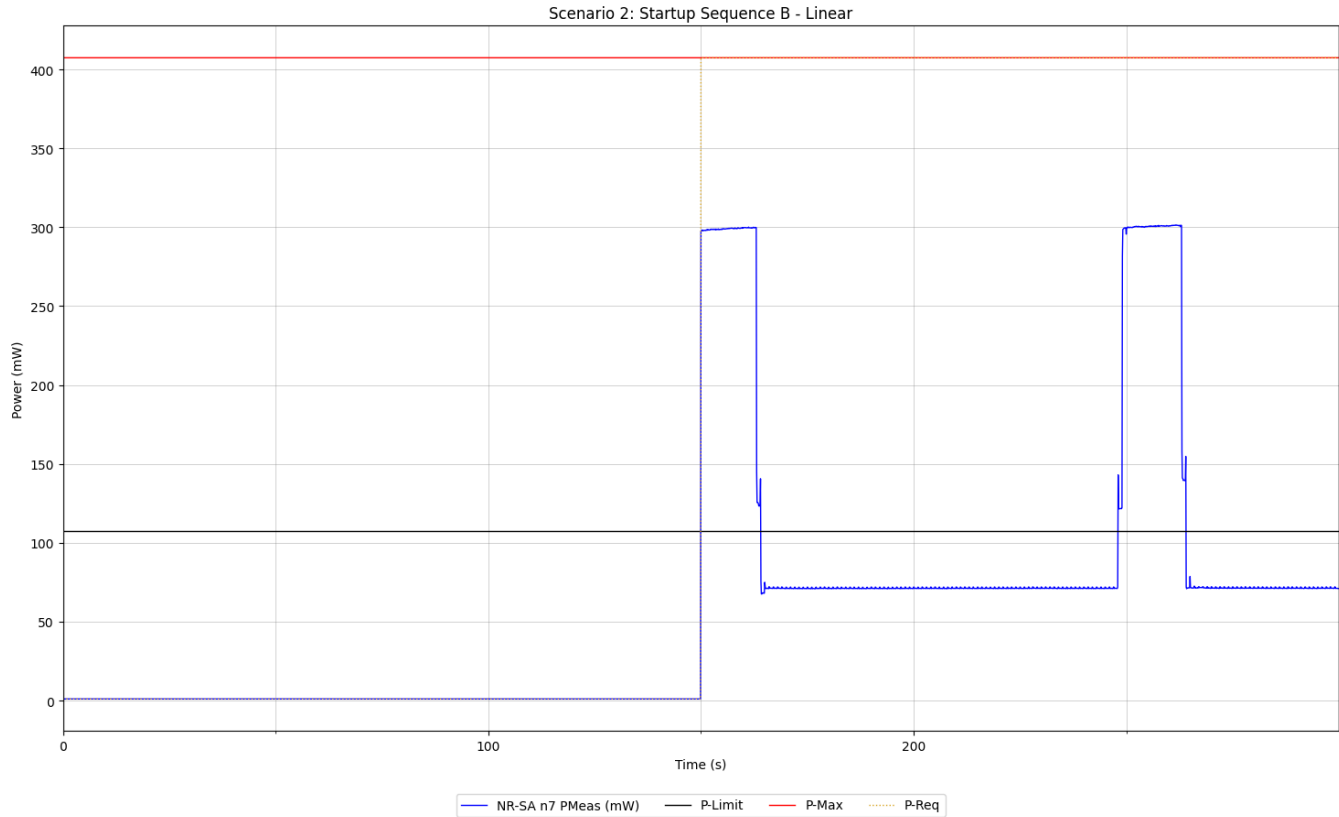
Figure 6-10: LTE TDD P[n] vs. P_{Limit} Sequence B Validation (Conducted)



P[n] was calculated using the 60 second time-averaging duration.

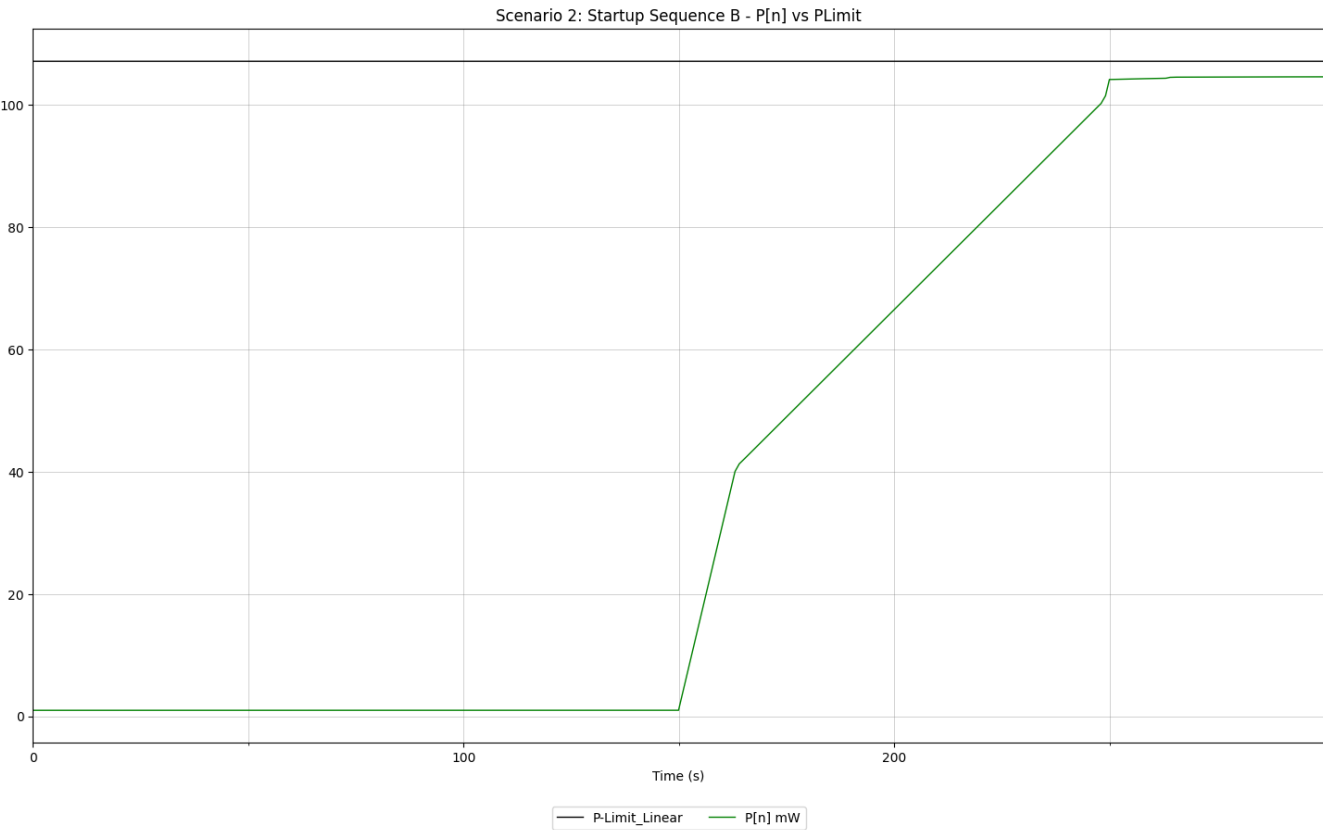
6.1.2.5 Test Case 6: FR-1 FDD Start-up Sequence B²

Figure 6-11: FR-1 FDD Start-up Sequence B (Conducted)



² P_{Max} and P_{Limit} were adjusted to an operating 91% duty cycle.

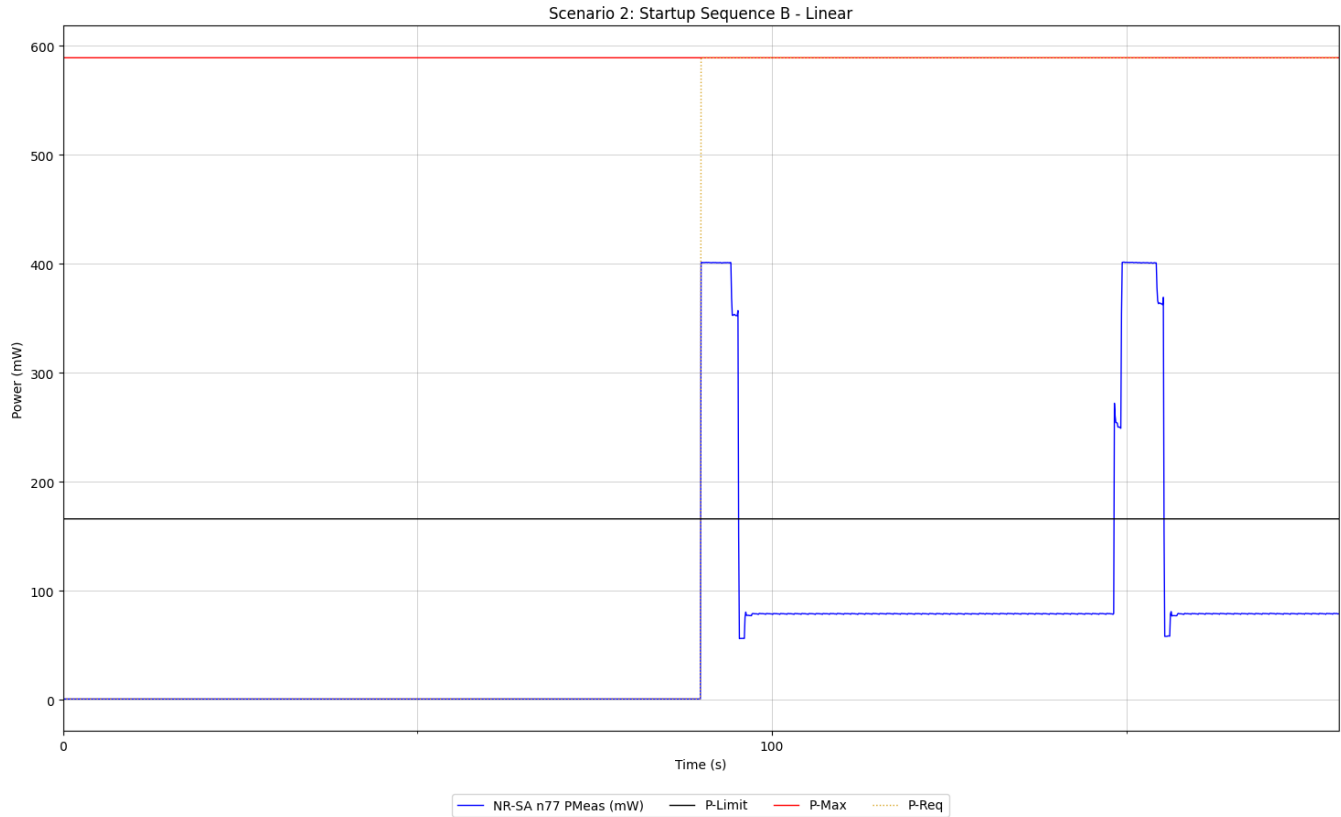
Figure 6-12: FR-1 FDD $P[n]$ vs. P_{Limit} Sequence B Validation (Conducted)



$P[n]$ was calculated using the 100 second time-averaging duration.

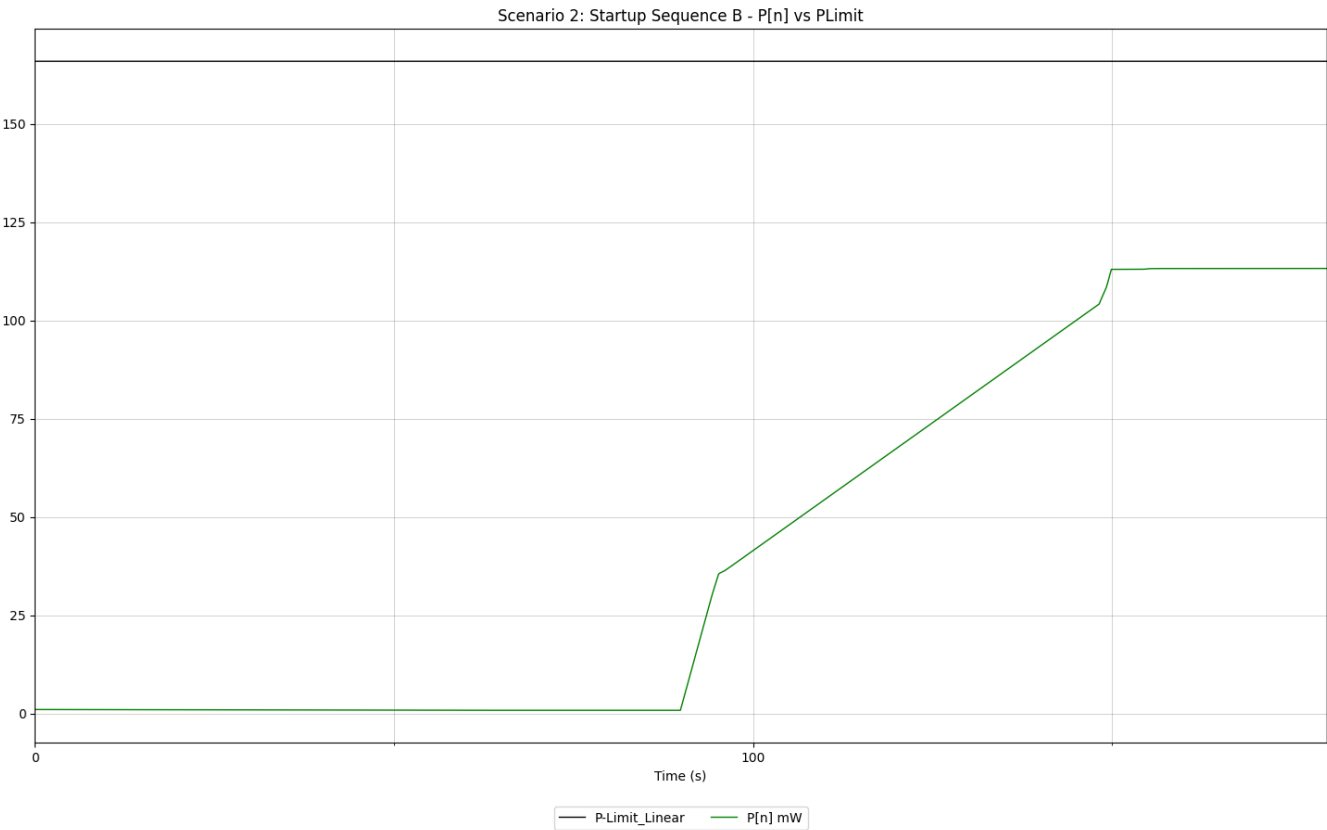
6.1.2.6 Test Case 7: FR-1 TDD Start-up Sequence B³

Figure 6-13: FR-1 TDD Start-up Sequence B (Conducted)



³ P_{Max} and P_{Limit} were adjusted to an operating 67% duty cycle.

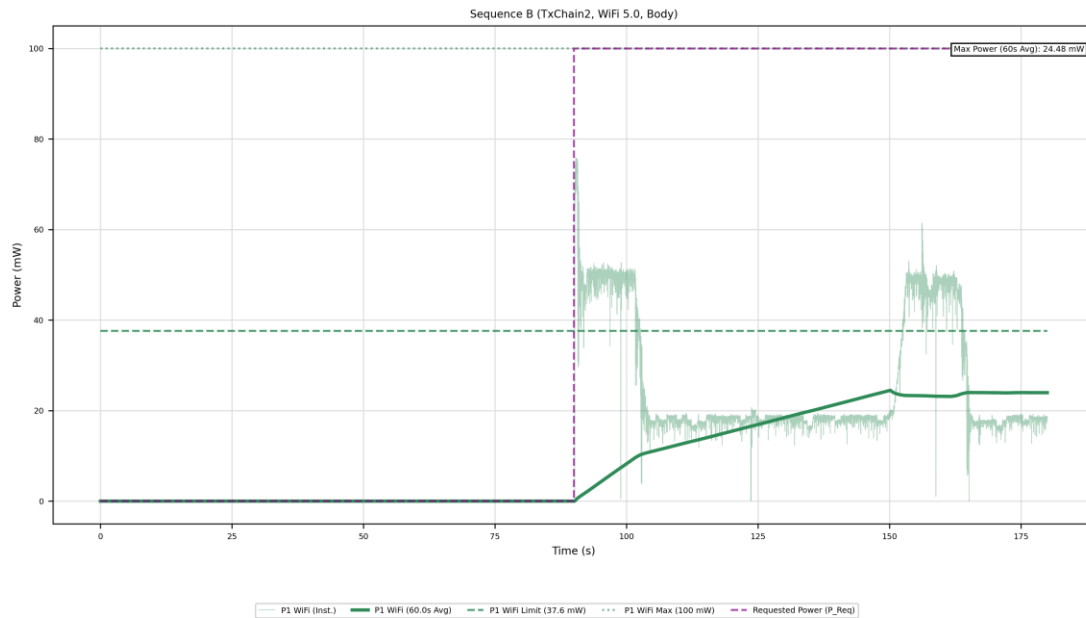
Figure 6-14: FR-1 TDD P[n] vs. P_{Limit} Sequence B Validation (Conducted)



P[n] was calculated using the 60 second time-averaging duration.

6.1.2.7 Test Case 8: Wi-Fi 5 GHz Start-up Sequence B

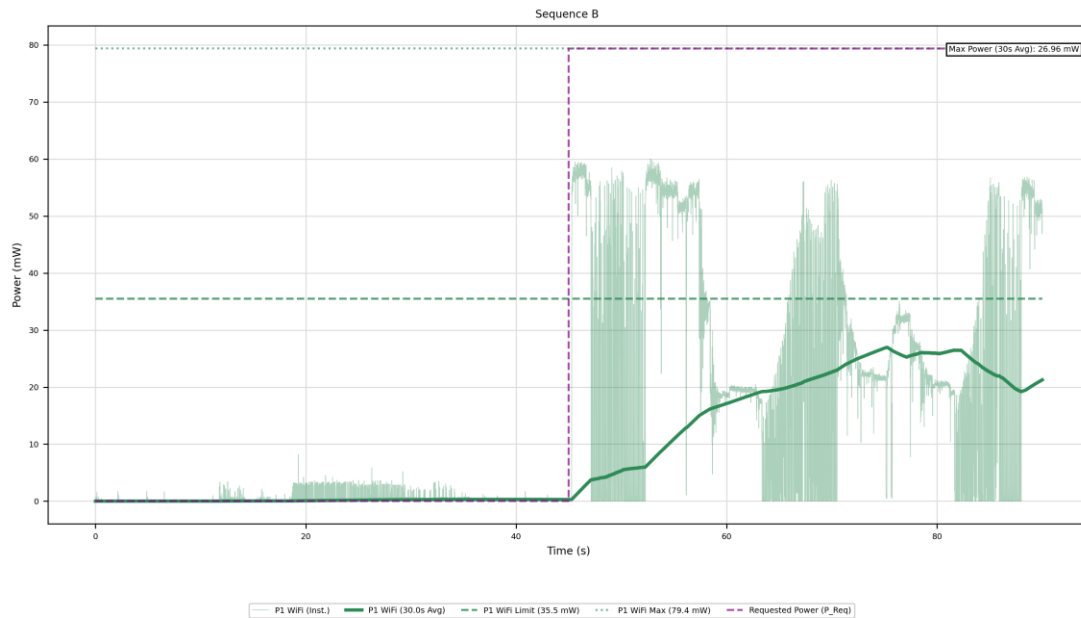
Figure 6-15: Wi-Fi 5 GHz Start-up Sequence B and $P[n]$ vs. P_{Limit} (Conducted)



$P[n]$ was calculated using the 60 second time-averaging duration.

6.1.2.8 Test Case 9: Wi-Fi 6 GHz Start-up Sequence B

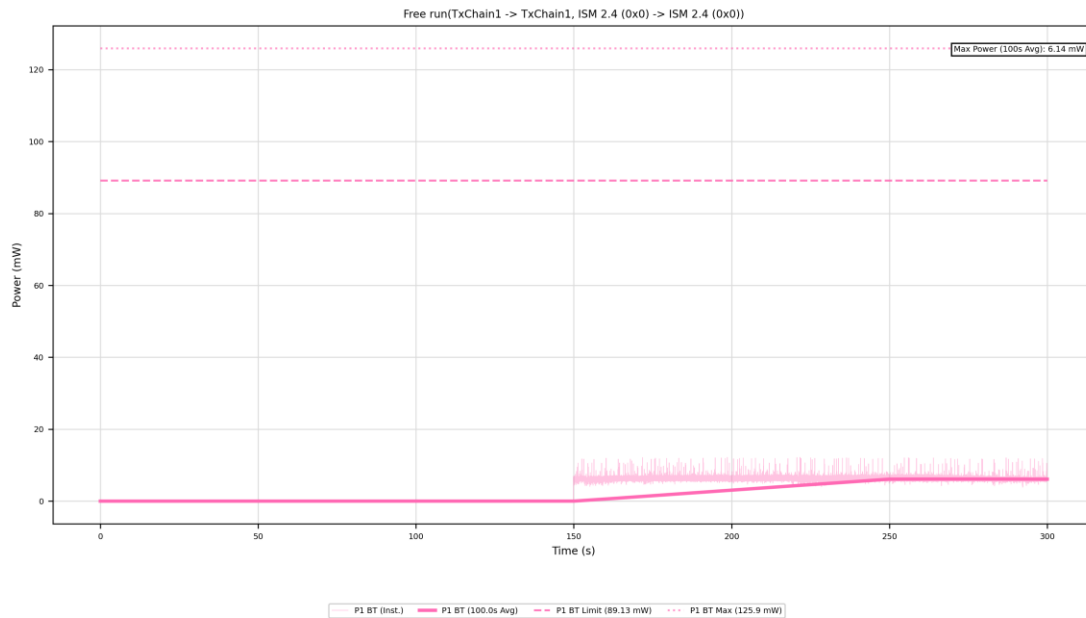
Figure 6-16: Wi-Fi 6 GHz Start-up Sequence B and $P[n]$ vs. P_{Limit} (Conducted)



$P[n]$ was calculated using the 30 second time-averaging duration.

6.1.2.9 Test Case 10: Bluetooth 2.4 GHz Start-up Sequence B

Figure 6-17: Bluetooth 2.4 GHz Start-up Sequence B and P[n] vs. P_{Limit} (Conducted)



P[n] was calculated using the 100 second time-averaging duration.

6.1.3 Pseudo-random Sequence

6.1.3.1 Test Case 11: FR-1 TDD Pseudo-random Sequence

Figure 6-18: FR-1 TDD Pseudo-random Sequence (Conducted)

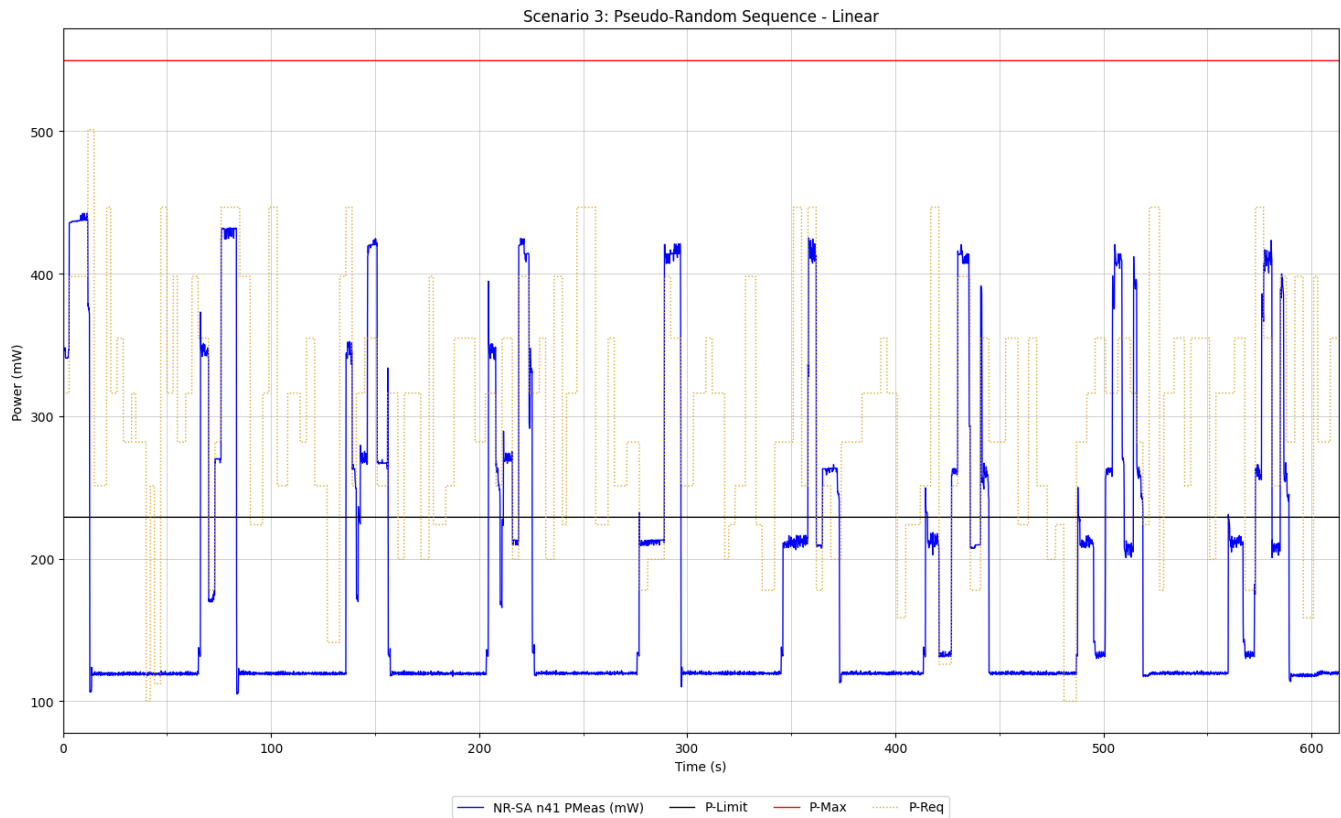
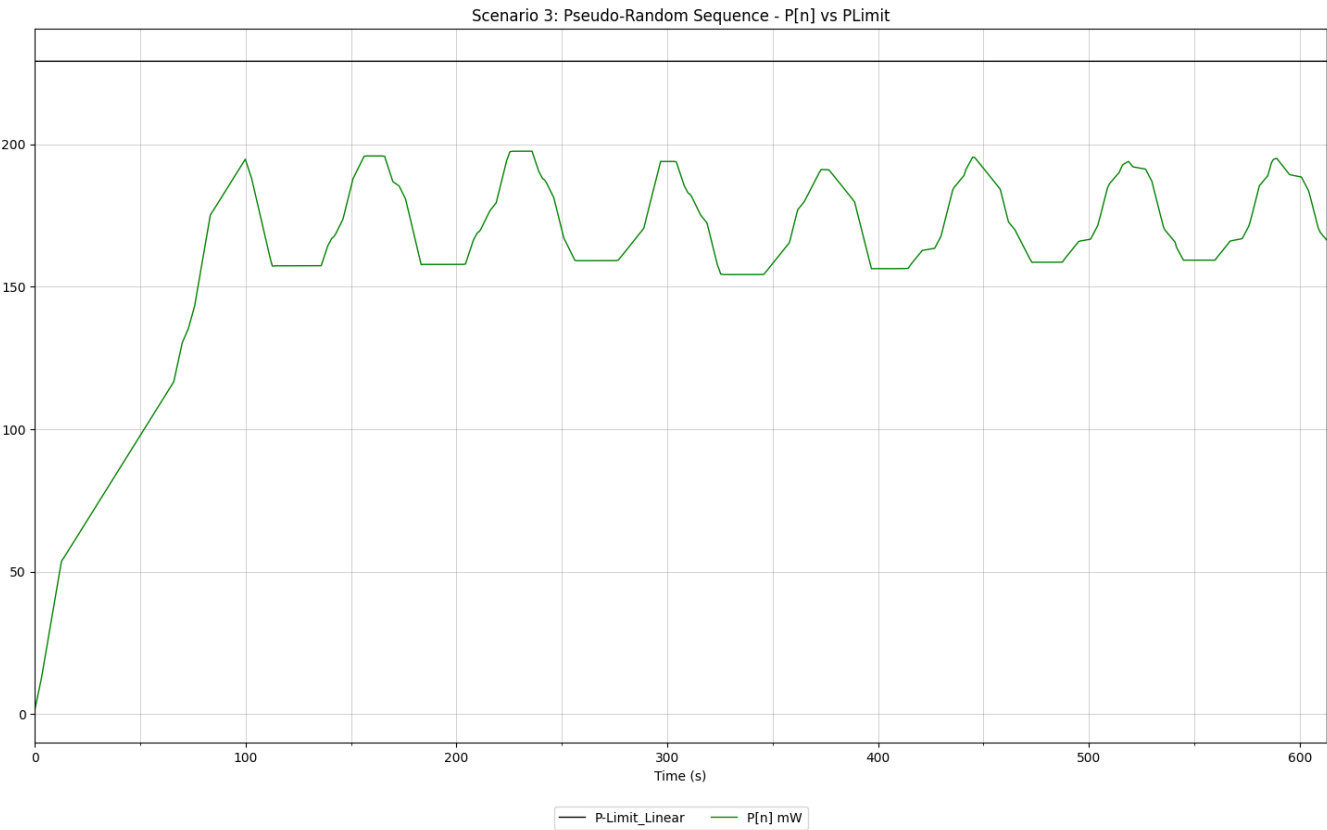


Figure 6-19: FR-1 TDD P[n] vs. P_{Limit} Pseudo-random Sequence Validation (Conducted)



P[n] was calculated using the 100 second time-averaging duration.

Table 6-1: LTE FDD T_{req} vs. P_{req} (Conducted)

Time (s)	T_{Req} (s)	P_{Req} (dBm)	P_{Req} (mW)	Time (s)	T_{Req} (s)	P_{Req} (dBm)	P_{Req} (mW)	Time (s)	T_{Req} (s)	P_{Req} (dBm)	P_{Req} (mW)
0	3	25.0	316.2	182	2	23.5	223.9	396	5	25.0	316.2
3	5	26.0	398.1	184	4	24.0	251.2	401	4	22.0	158.5
8	4	26.0	398.1	188	4	25.5	354.8	405	3	23.5	223.9
12	3	27.0	501.2	192	6	25.5	354.8	408	4	23.5	223.9
15	6	24.0	251.2	198	5	24.5	281.8	412	5	24.0	251.2
21	2	26.5	446.7	203	5	25.0	316.2	417	4	26.5	446.7
23	3	25.0	316.2	208	3	24.0	251.2	421	6	21.0	125.9
26	3	25.5	354.8	211	5	25.5	354.8	427	3	24.0	251.2
29	4	24.5	281.8	216	3	23.0	199.5	430	6	26.0	398.1
33	2	25.0	316.2	219	5	26.0	398.1	436	5	22.5	177.8
35	5	24.5	281.8	224	5	25.0	316.2	441	4	25.5	354.8
40	2	20.0	100.0	229	3	25.5	354.8	445	4	24.5	281.8
42	2	24.0	251.2	232	4	23.0	199.5	449	4	24.5	281.8
44	3	20.5	112.2	236	4	26.0	398.1	453	6	25.5	354.8
47	3	26.5	446.7	240	2	23.5	223.9	459	5	23.5	223.9
50	3	25.0	316.2	242	5	25.0	316.2	464	4	25.5	354.8
53	2	26.0	398.1	247	3	26.5	446.7	468	5	24.0	251.2
55	4	24.5	281.8	250	6	26.5	446.7	473	4	23.0	199.5
59	3	25.0	316.2	256	6	23.5	223.9	477	4	23.5	223.9
62	3	26.0	398.1	262	3	25.5	354.8	481	6	20.0	100.0
65	5	25.5	354.8	265	6	24.0	251.2	487	5	24.5	281.8
70	3	22.5	177.8	271	6	24.5	281.8	492	4	25.0	316.2
73	3	24.5	281.8	277	4	22.5	177.8	496	5	25.5	354.8
76	6	26.5	446.7	281	6	23.0	199.5	501	6	25.0	316.2
82	3	26.5	446.7	287	2	23.0	199.5	507	6	25.5	354.8
85	5	26.0	398.1	289	3	26.0	398.1	513	3	25.0	316.2
90	6	23.5	223.9	292	5	25.5	354.8	516	3	24.5	281.8
96	3	25.0	316.2	297	6	24.0	251.2	519	3	23.5	223.9
99	4	26.5	446.7	303	6	25.0	316.2	522	5	26.5	446.7
103	5	24.0	251.2	309	3	25.5	354.8	527	2	22.5	177.8
108	2	25.0	316.2	312	6	25.0	316.2	529	5	25.0	316.2
110	4	25.0	316.2	318	2	23.0	199.5	534	5	25.5	354.8
114	3	24.5	281.8	320	3	23.5	223.9	539	3	24.0	251.2
117	4	25.5	354.8	323	5	24.0	251.2	542	3	25.5	354.8
121	6	24.0	251.2	328	5	26.0	398.1	545	6	25.5	354.8
127	6	21.5	141.3	333	3	23.5	223.9	551	3	23.0	199.5
133	3	26.0	398.1	336	6	22.5	177.8	554	6	25.0	316.2
136	3	26.5	446.7	342	4	24.5	281.8	560	3	25.0	316.2
139	2	24.0	251.2	346	5	24.5	281.8	563	5	25.5	354.8
141	4	25.0	316.2	351	4	26.5	446.7	568	5	22.5	177.8
145	2	25.5	354.8	355	3	24.0	251.2	573	4	26.5	446.7
147	4	25.5	354.8	358	4	26.5	446.7	577	4	25.5	354.8
151	5	24.0	251.2	362	3	22.5	177.8	581	4	24.0	251.2
156	5	25.0	316.2	365	4	24.0	251.2	585	3	26.0	398.1
161	3	23.0	199.5	369	5	23.0	199.5	588	4	24.5	281.8
164	5	25.0	316.2	374	5	24.5	281.8	592	4	26.0	398.1
169	3	25.0	316.2	379	5	24.5	281.8	596	5	22.0	158.5
172	4	23.0	199.5	384	6	25.0	316.2	601	2	26.0	398.1
176	2	26.0	398.1	390	3	25.0	316.2	603	6	24.5	281.8
178	4	23.5	223.9	393	3	25.5	354.8	609	4	25.5	354.8

6.1.4 Test Case 12: Antenna Switching and Change in Operating State

Figure 6-20: Antenna Switching and Change in Operating State Plot (Conducted)

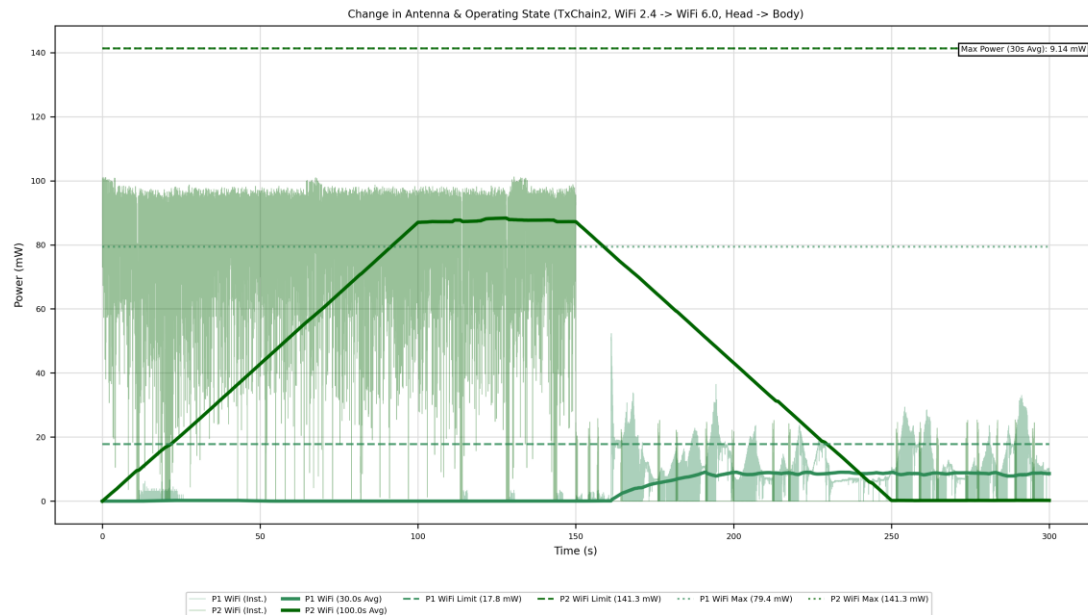
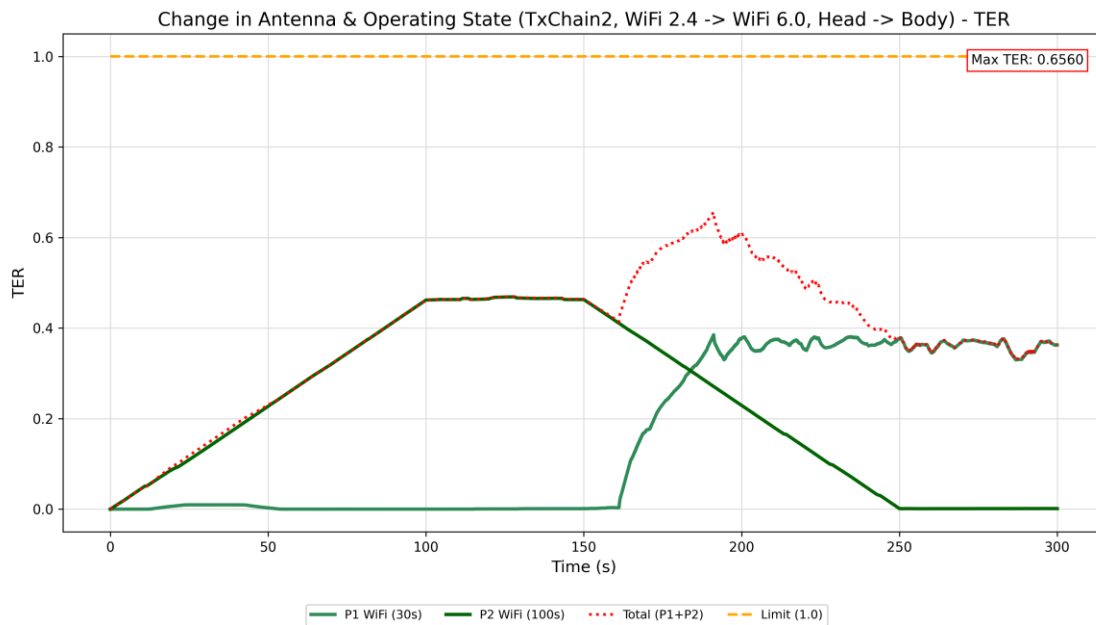


Figure 6-21: Antenna Switching and Change in Operating State Validation (Conducted)



$p[n]$ was calculated using the sum of Wi-Fi 2.4 GHz's 100 second time-averaging duration and Wi-Fi 6 GHz's 30 second time-averaging duration.

6.1.5 Test Case 13: Technology Hand-off ULCA

Figure 6-22: Technology Hand-off ULCA Test Case (Conducted)

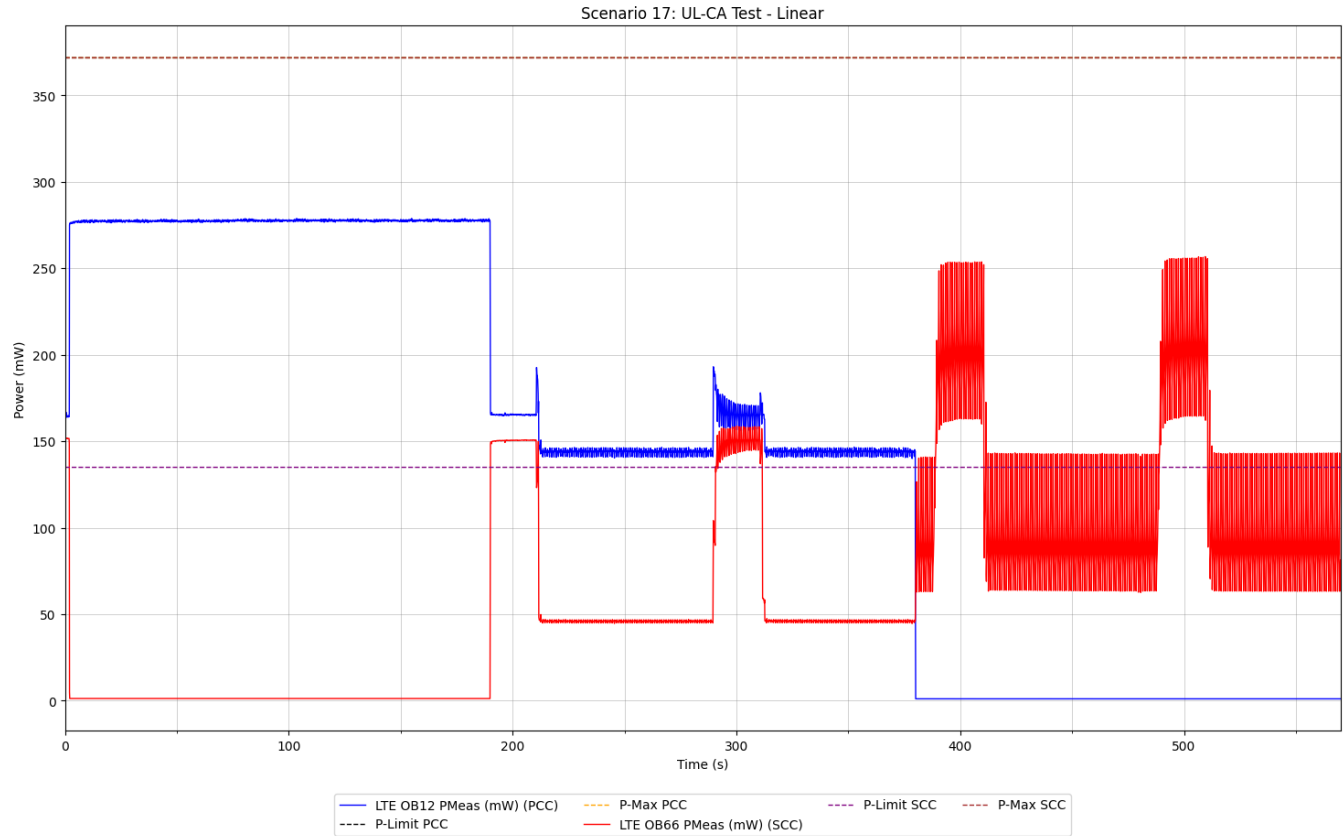
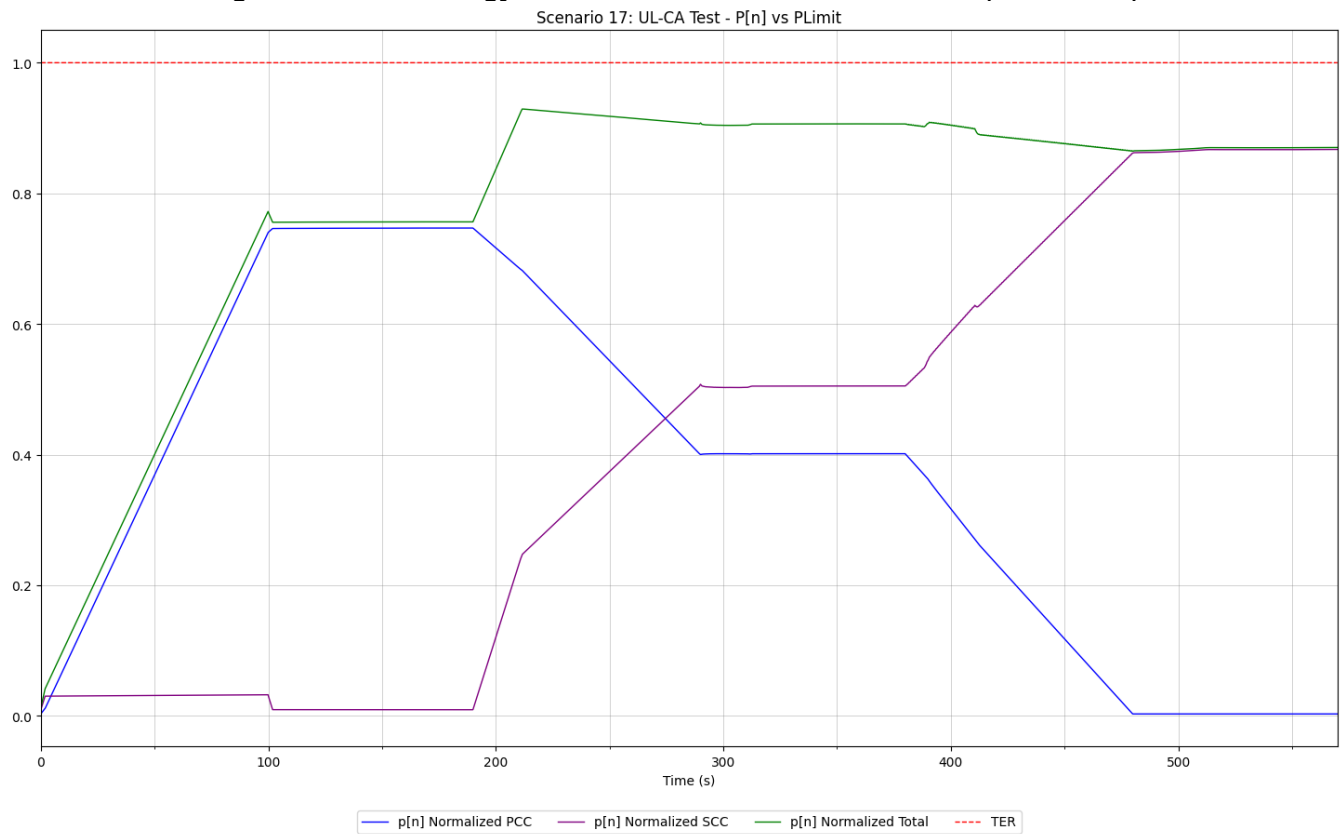


Figure 6-23: Technology Hand-off ULCA Test Case Validation (Conducted)

p[n] was calculated using the sum of LTE PCC's and LTE SCC's 100 second time-averaging duration.

6.1.6 Test Case 14: Technology Hand-off EN-DC

Figure 6-24: Technology Hand-off EN-DC (Conducted)

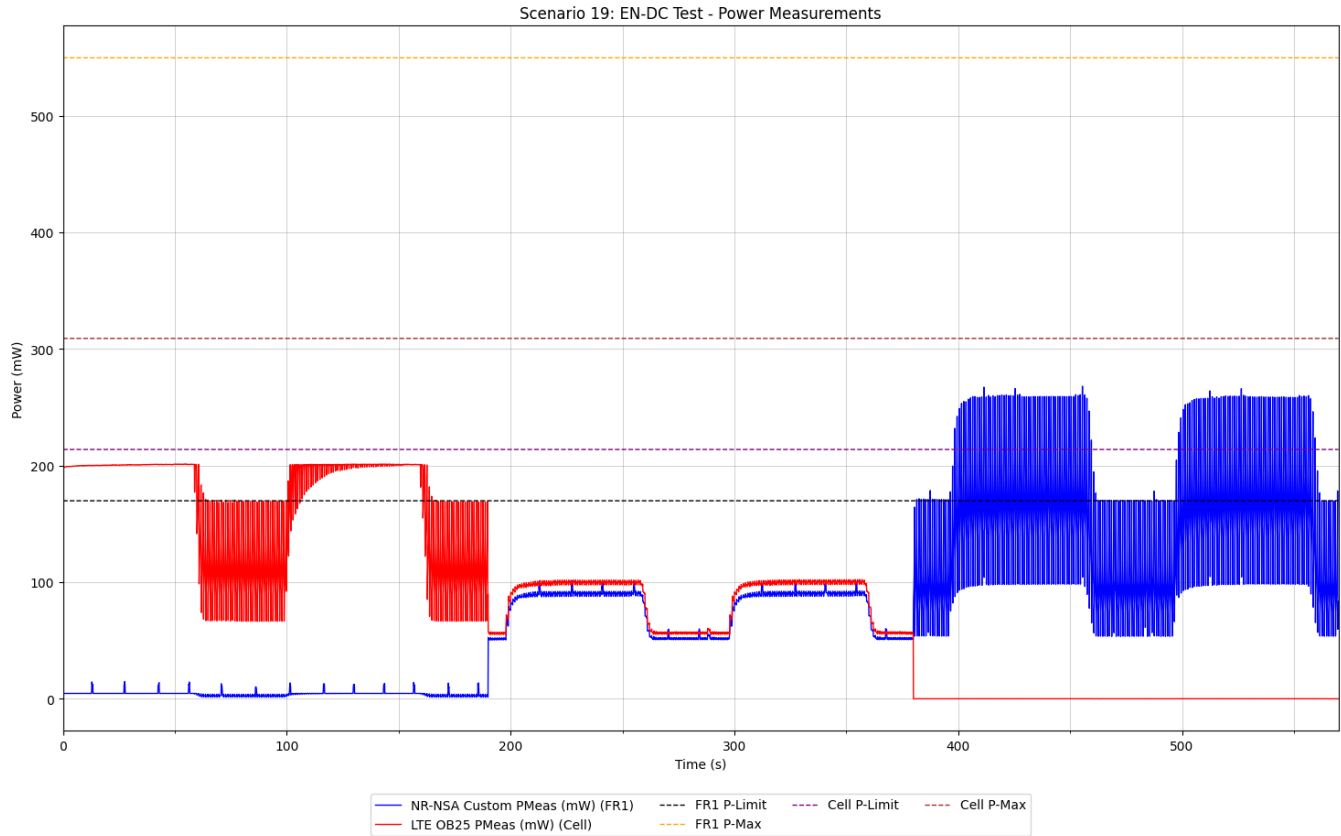
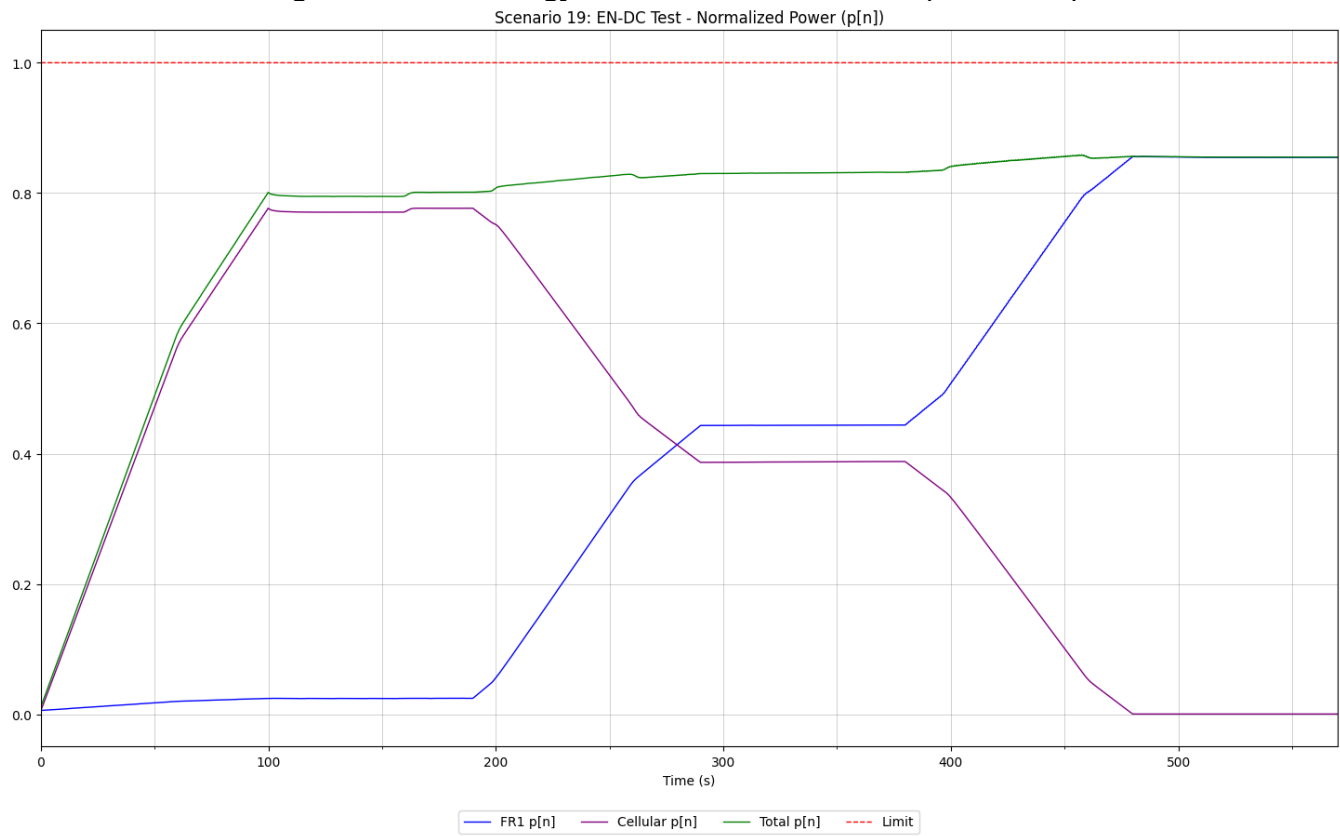


Figure 6-25: Technology Hand-off EN-DC Validation (Conducted)

$p[n]$ was calculated using the sum of LTE's and FR-1's 100 second time-averaging duration.

6.1.7 Test Case 15: Change in Technology

Figure 6-26: Change in Technology Plot (Conducted)

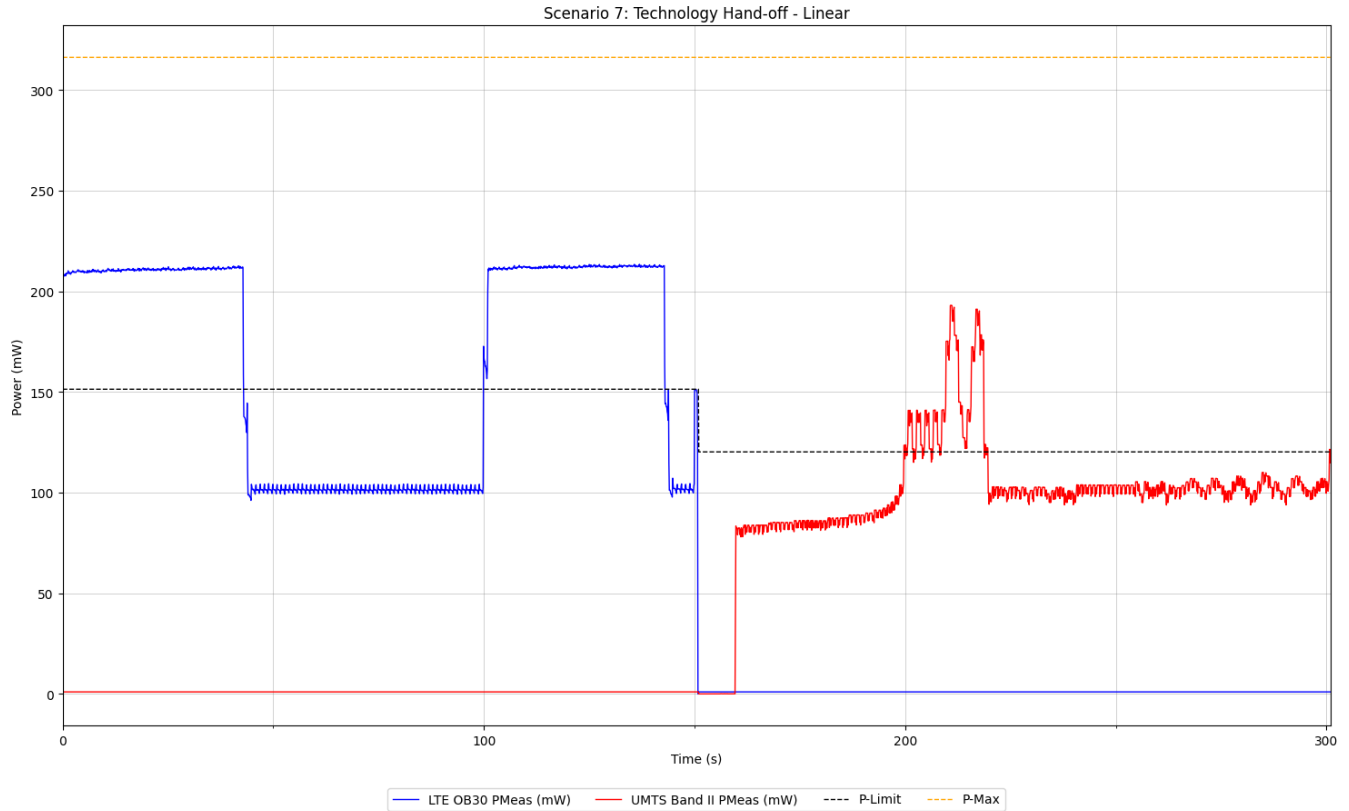
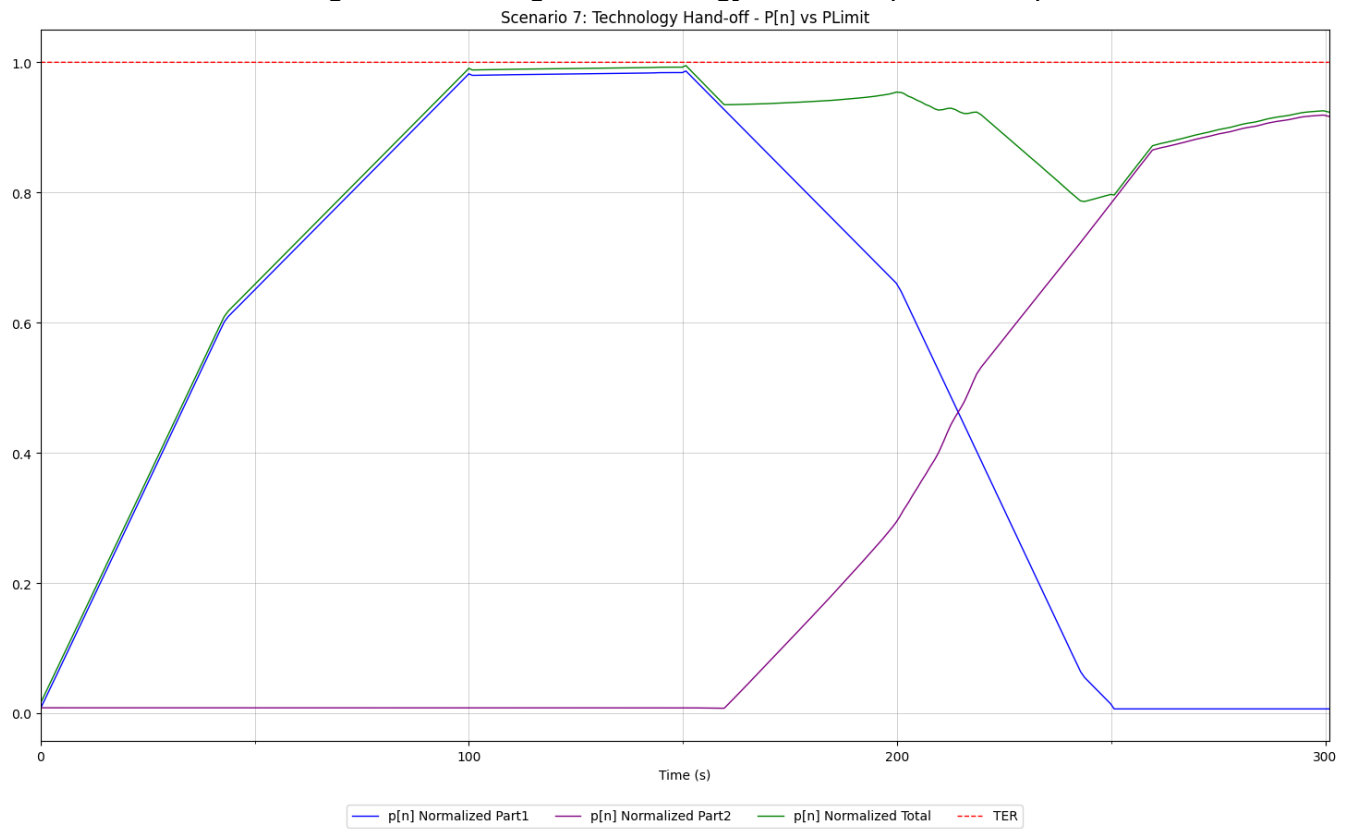


Figure 6-27: Change in Technology Validation (Conducted)

p[n] was calculated using the sum of LTE FDD's and W-CDMA's 100 second time-averaging duration.

6.1.8 Test Case 16: Switching Between Duplexes

Figure 6-28: Switching Between Duplexes Test Case (Conducted)

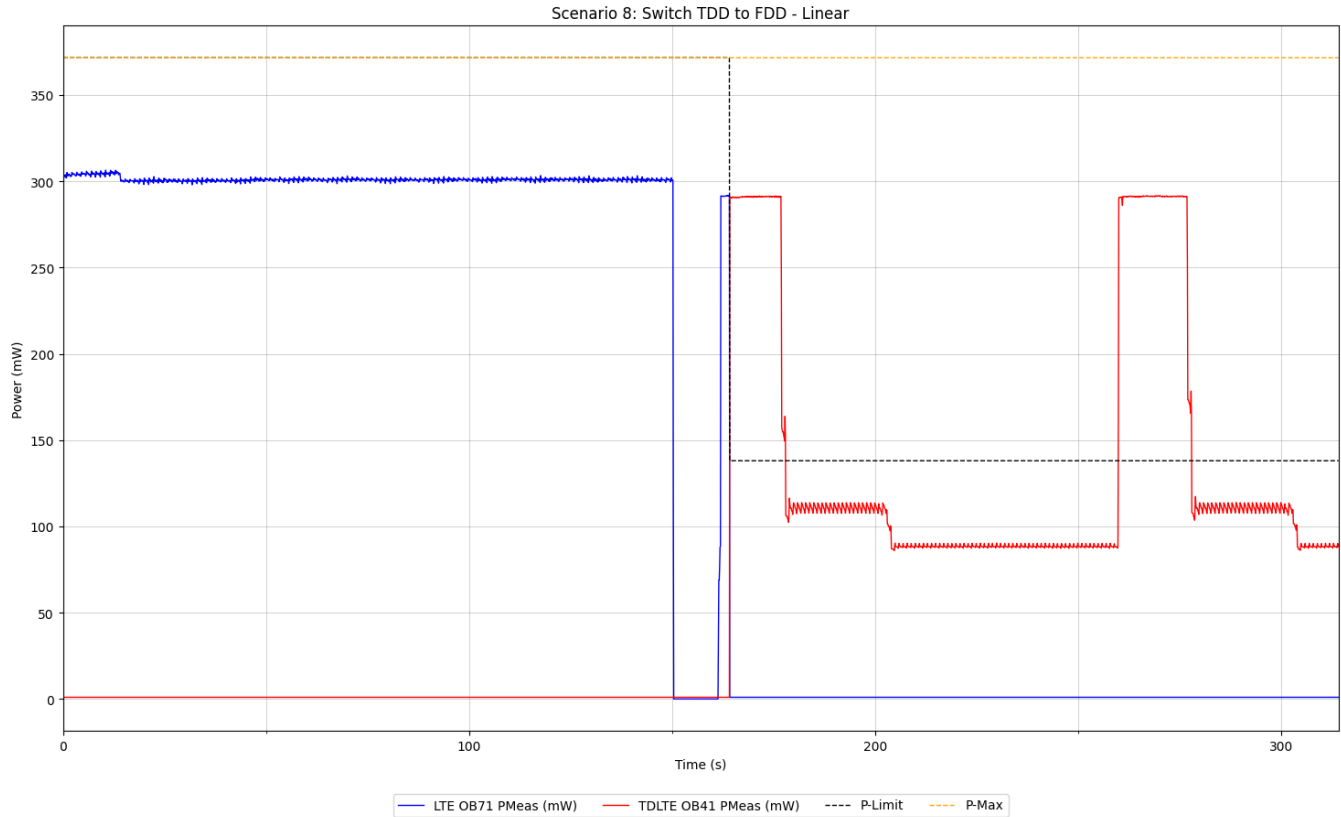
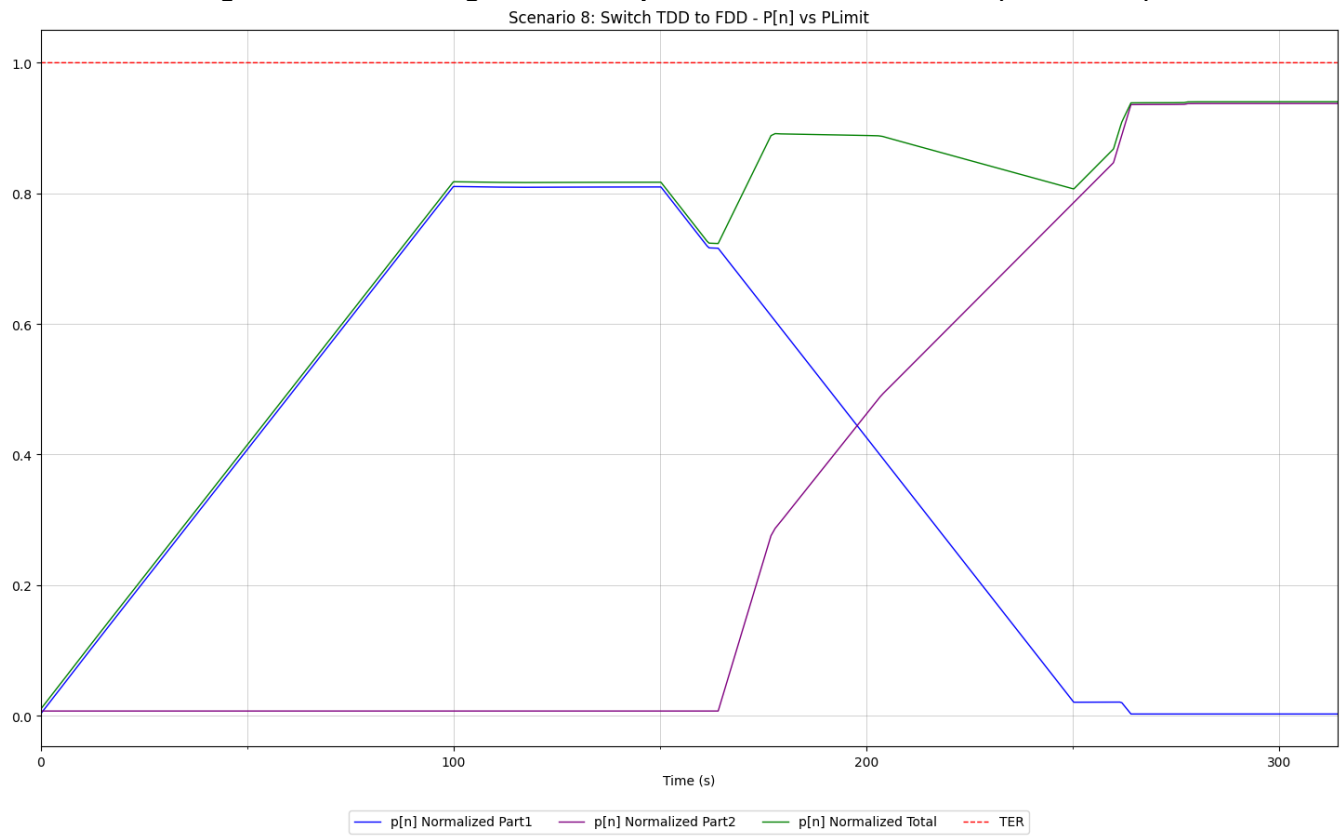


Figure 6-29: Switching Between Duplexes Test Case Validation (Conducted)

p[n] was calculated using the sum of LTE FDD's and LTE TDD's 100 second time-averaging duration.

6.1.9 Test Case 17: Dropped Connection

Figure 6-30: Call Drop Plot (Conducted)

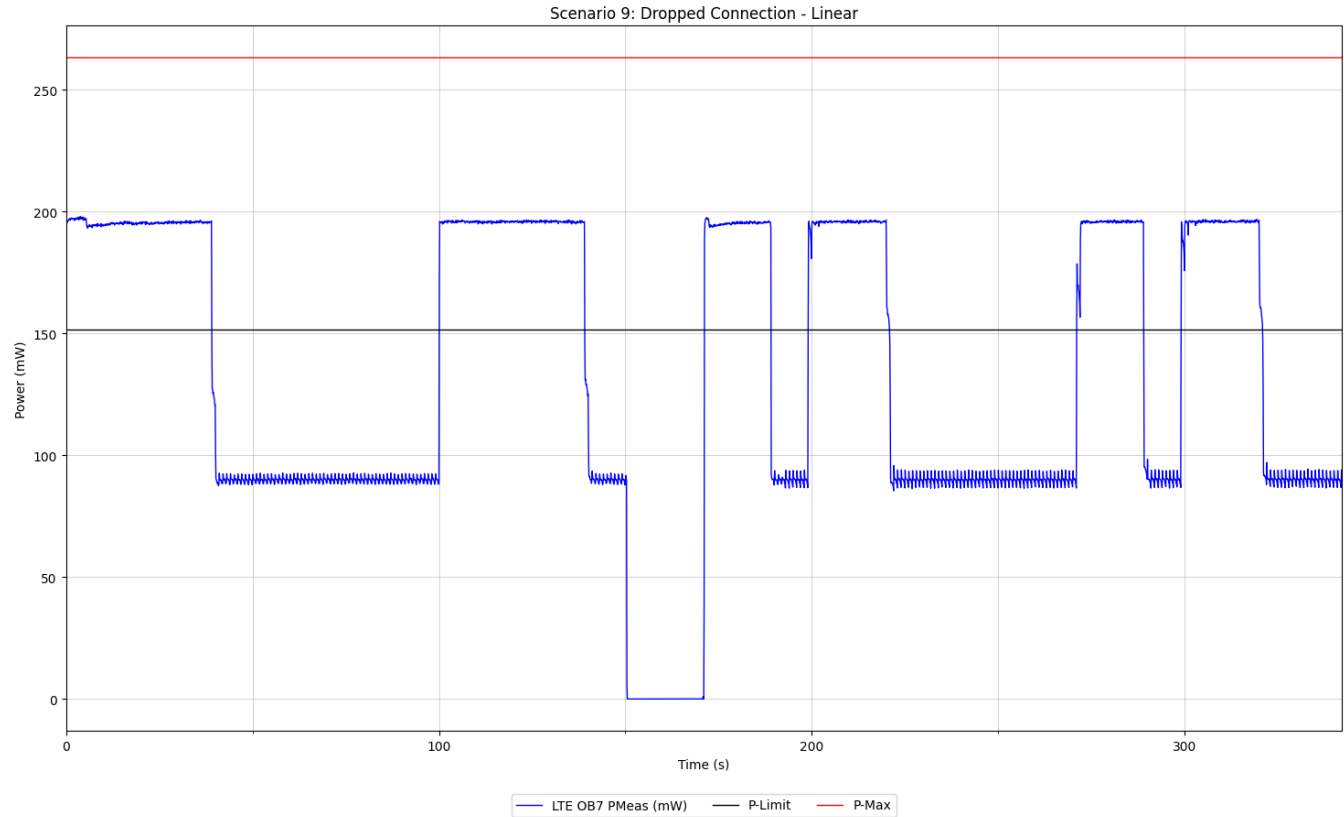
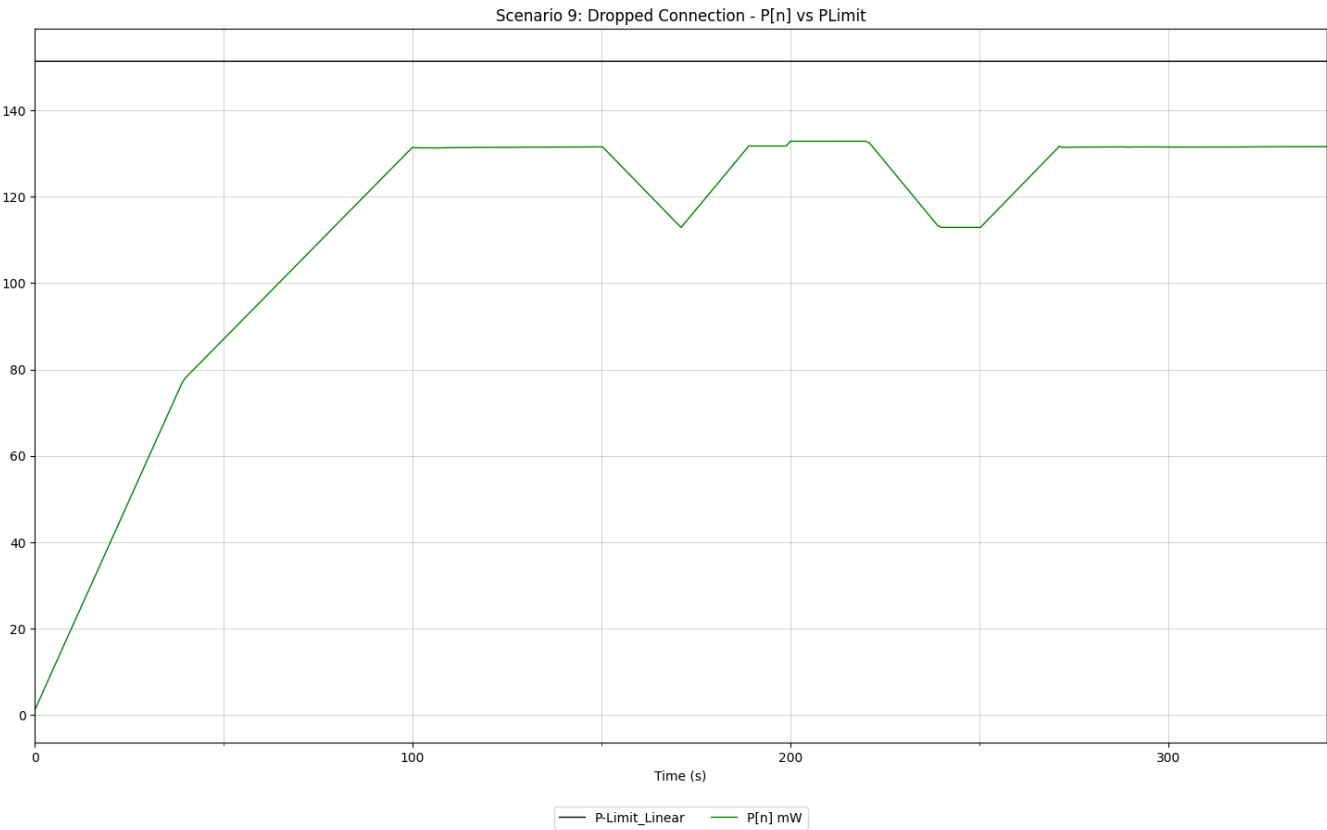


Figure 6-31: Call Drop Validation (Conducted)



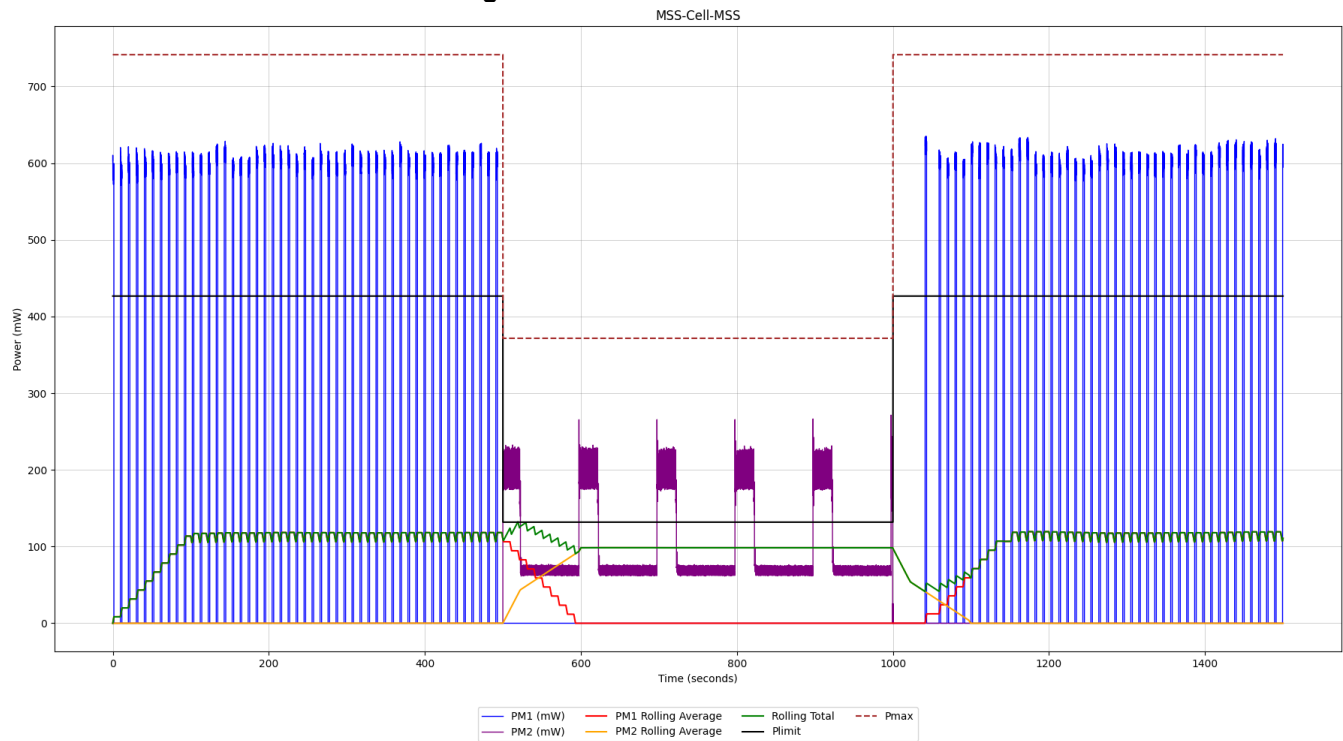
P[n] was calculated using the 100 second time-averaging duration.

7. MSS⁴

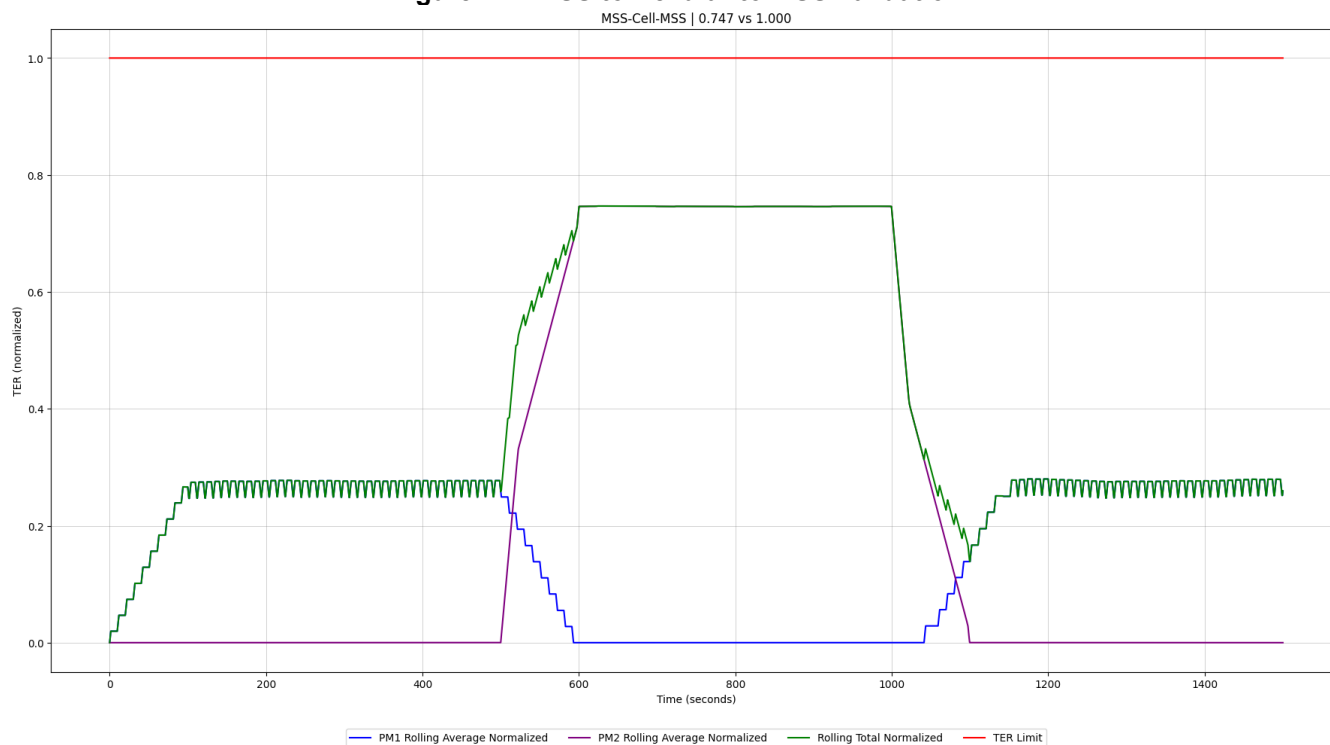
7.1 Conducted

7.1.1 Technology Hand-off MSS to Cellular to MSS

Figure 7-1: MSS to Cellular to MSS



⁴ Data in this section was collected and provided by the OEM; the test lab did not conduct any of these measurements within this section.

Figure 7-2: MSS to Cellular to MSS Validation

p[n] was calculated using the sum of MSS 99.84 second time-averaging duration and WWAN's 100 second time-averaging duration.

8. Dynamic Budget Algorithm Validation

This DUT supports dynamic budget sharing for Cellular, MSS, Wi-Fi, Bluetooth, NB U-NII, and 802.15.4 technologies. The budgeting algorithm dynamically controls the SAR and MPE budgets assigned to radios over time depending on use conditions and power demands.

This dynamic budgeting algorithm ensures simultaneous compliance through management of overall device TER. With this implementation, it is no longer relevant to assess simultaneous SAR, or TER, in a static manner using fixed simultaneous budgets. The validation of this dynamic budgeting algorithm is sufficient to prove simultaneous compliance.

To validate the dynamic budgeting algorithm, the following configurations will be assessed:

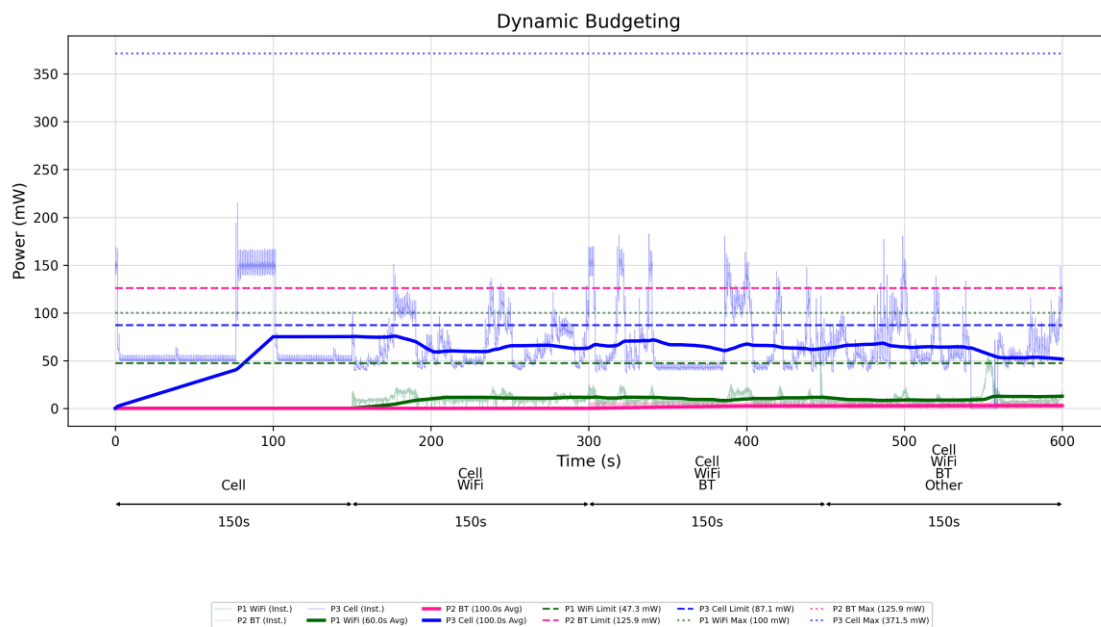
1. Cellular Standalone
2. Cellular + Wi-Fi
3. Cellular + Wi-Fi + Bluetooth
4. Cellular + Wi-Fi + Bluetooth + Static Budget Radios⁵

Table 8-1: Selected Technologies for Dynamic Budget Algorithm Validation

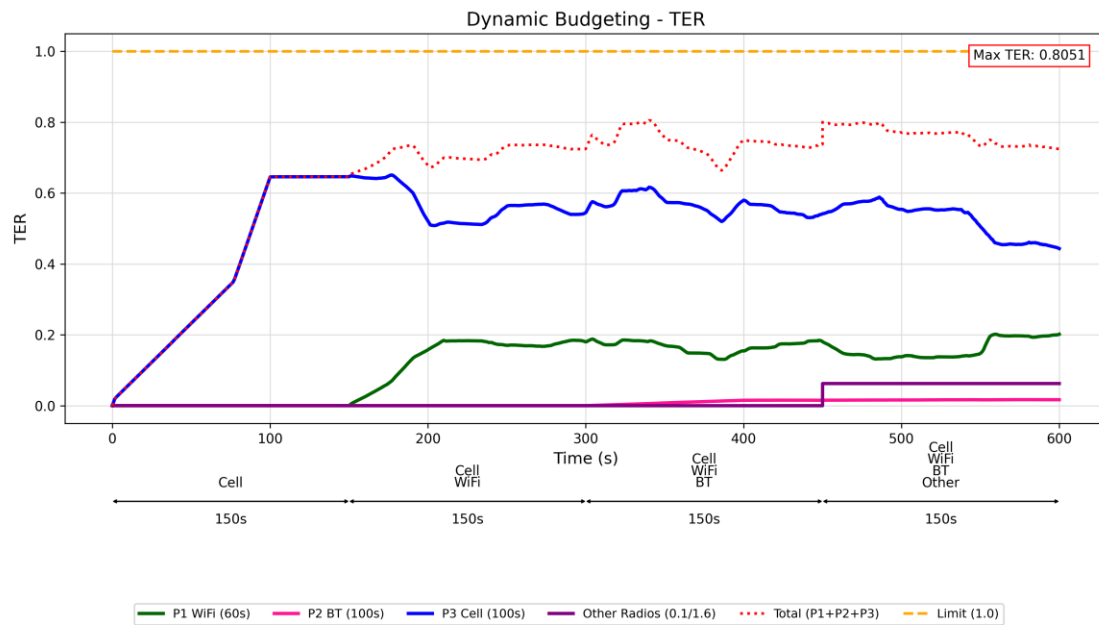
Test Case No.	Test Case	Technology	Band	Modulation	Slot	Power Class	Bandwidth (MHz)	Channel	Port	Transmitter	RF Exposure Condition	P _{Max} (dBm)	P _{Limit} (dBm)	P _{Max} (mW)	P _{Limit} (mW)
20	Dynamic Budgeting	LTE FDD	B30	QPSK	N/A	N/A	10	N/A	A	ANT 14	Body	25.7	19.4	371.5	87.1
		Wi-Fi	U-NII 3	BPSK	N/A	N/A	80	155	N/A	ANT 6	Body	20.0	16.8	100.0	47.3
		Bluetooth	2.4 GHz	LE	N/A	N/A	N/A	N/A	N/A	ANT 4	Body	21.0	21.0	125.9	125.9
		Static Budget Radios													

The test time per sequence will be sufficient to allow steady state to be reached. Static budget radios should capture the maximum transmission budget of any technology that does not have time-averaging or dynamic budgeting support.

Figure 8-1: Cellular + Wi-Fi to Cellular + Wi-Fi + Bluetooth to Cellular + Wi-Fi + Bluetooth + Static Radios



⁵ Static Budget Radios include: WPT, UWB, 802.15.4ab – NB (Narrow Band), and NFC.

Figure 8-2: Cellular + Wi-Fi to Cellular + Wi-Fi + Bluetooth to Cellular + Wi-Fi + Bluetooth + Static Radios Validation

9. Conclusion

Table 9-1: Summary of Results for sub-7.2 GHz Conducted

Test Case No.	Test Case	Technology	Band	Modulation	Slot	Power Class	Bandwidth (MHz)	Channel	Duty Cycle (%)	Duty Cycle (dB)	Port	Transmitter	RF Exposure Condition	P _{Max} (dBm)	P _{Max} Corr. (dBm)	P _{Limit} (dBm)	P _{Limit} Corr. (dBm)	Validation (dBm)	P _{Max} (mW)	P _{Max} Corr. (mW)	P _{Limit} (mW)	P _{Limit} Corr. (mW)	Validation (mW)	
1	Sequence A	LTE FDD	B30	QPSK	N/A	N/A	10	N/A	100.00%	0.00	Port C	ANT 3	Body	26.1	26.1	21.7	21.7	20.8 < 21.7	321.6	321.6	147.9	147.9	115.3 < 147.9	
2		CDMA	900	QPSK	N/A	N/A	10	N/A	25.00%	0.00	Port A	ANT 14	Body	31.0	31.0	27.2	27.2	27.0 < 27.2	1258.9	1258.9	524.8	524.8	500.0 < 524.8	
3		W-CDMA	B4	QPSK	N/A	S	N/A	N/A	100.00%	0.00	Port C	ANT 3	Body	26.7	26.7	23.3	23.3	20.5 < 21.3	371.5	371.5	134.9	134.9	113.2 < 134.9	
4		LTE FDD	B25	QPSK	N/A	N/A	20	N/A	100.00%	0.00	Port D	ANT 4	Body	24.9	24.9	21.4	21.4	20.3 < 21.4	300.0	300.0	138.0	138.0	106.3 < 138.0	
5	Sequence B	LTE TDD	B48	QPSK	N/A	N/A	20	N/A	63.33%	1.98	Port D	ANT 6	Body	25.3	25.3	19.7	19.7	17.2 < 19.7	338.8	338.8	93.3	93.3	52.0 < 93.3	
6		FR-1 FDD	n7	QF-1s m2 BPSK	N/A	N/A	50	N/A	91.00%	0.41	Port A	ANT 14	Body	26.7	26.7	20.3	20.3	20.2 < 20.3	371.5	371.5	97.7	97.7	104.5 < 107.4	
7		FR-1 TDD	277 (Block G)	QF-1s m2 BPSK	N/A	PC3	100	N/A	67.00%	1.74	Port B	ANT 4	Body	26.0	27.7	20.5	22.2	20.6 < 22.2	368.1	594.2	112.2	107.5	113.3 < 167.5	
8		Wi-Fi	U-NII 3	BPSK	N/A	N/A	80	155	N/A	N/A	N/A	ANT 16	Body	20.0	20.0	18.8	18.8	13.9 < 18.8	100.0	100.0	37.6	37.6	24.5 < 37.6	
9		Wi-Fi	U-NII 6	BPSK	N/A	N/A	160	111	N/A	N/A	N/A	ANT 6	Body	19.0	19.0	15.5	15.5	14.3 < 15.5	79.4	79.4	35.5	35.5	26.0 < 35.5	
10		Bluetooth	LE	LE	N/A	N/A	N/A	N/A	N/A	N/A	N/A	ANT 4	Head	21.0	21.0	19.5	19.5	7.9 < 19.5	125.9	125.9	89.1	89.1	6.1 < 89.1	
11		Pseudo-random	FR-1 TDD	n41	QF-1s m2 BPSK	N/A	PC3	100	N/A	67.00%	1.74	Port B	ANT 1	Body	25.7	27.4	21.9	23.6	23.0 < 23.6	371.5	554.5	154.9	231.2	197.7 < 231.2
17		Dropped Connection	LTE FDD	B7	QPSK	N/A	N/A	20	N/A	100.00%	0.00	Port B	ANT 1	Body	26.2	26.2	21.8	21.8	21.2 < 21.8	363.0	363.0	151.4	151.4	132.7 < 151.4
Test Case No.	Test Case	Technology	Band	Modulation	Slot	Power Class	Bandwidth (MHz)	Channel	Duty Cycle (%)	Duty Cycle (dB)	Port	Transmitter	RF Exposure Condition	P _{Max} (dBm)	P _{Max} Corr. (dBm)	P _{Limit} (dBm)	P _{Limit} Corr. (dBm)	Validation						
12	Antenna Switching & Change in Operating State	Wi-Fi	2.4 GHz	DSSS	N/A	N/A	20	6	N/A	N/A	N/A	ANT 3	Head	21.5	21.5	21.5	21.5					0.656 < 1.000		
15	Change in Technology	Wi-Fi	U-NII 7	BPSK	N/A	N/A	160	143	N/A	N/A	N/A	ANT 16	Body	19.0	19.0	12.5	12.5							
		LTE FDD	B30	QPSK	N/A	N/A	10	N/A	100.00%	0.00	Port D	ANT 4	Body	23.8	23.8	21.8	21.8					0.995 < 1.000		
16	Switching Between Duplexes	W-CDMA	B2	QPSK	N/A	N/A	N/A	N/A	100.00%	0.00	Port D	ANT 4	Body	24.9	24.9	20.8	20.8							
		LTE FDD	B71	QPSK	N/A	N/A	10	N/A	100.00%	0.00	Port B	ANT 2	Body	25.7	25.7	25.7	25.7					0.940 < 1.000		
		LTE TDD	B41	QPSK	N/A	PC3	20	N/A	63.33%	1.98	Port A	ANT 14	Body	25.7	25.7	21.4	21.4							
13	Technology Hand-off (ULCA)	LTE FDD	B12	QPSK	N/A	N/A	10	N/A	100.00%	0.00	Port B	ANT 2	Body	25.7	25.7	25.7	25.7					0.929 < 1.000		
		LTE FDD	B66	QPSK	N/A	N/A	20	N/A	100.00%	0.00	Port D	ANT 4	Head	25.7	25.7	21.3	21.3							
		LTE FDD	B25	QPSK	N/A	N/A	20	N/A	100.00%	0.00	Port B	ANT 1	Body	24.9	24.9	23.3	23.3							
14	Technology Hand-off (ENDC)	FR-1 TDD	n41	QF-1s m2 BPSK	N/A	PC3	100	N/A	67.00%	1.74	Port C	ANT 3	Body	25.7	27.4	20.6	22.3					0.859 < 1.000		
19	Technology Hand-off (MSS)	MSS	L-Band	N/A	N/A	N/A	N/A	N/A	N/A	N/A	Port D	ANT 4	Body	28.7	28.7	26.3	26.3							
		LTE FDD	B66	QPSK	N/A	N/A	10	N/A	100.00%	0.00	Port A	ANT 14	Body	25.7	25.7	21.2	21.2					0.747 < 1.000		

Table 9-2: Summary of Results for Dynamic Budget Algorithm Validation

Test Case No.	Test Case	Technology	Band	Modulation	Slot	Power Class	Bandwidth (MHz)	Channel	Port	Transmitter	RF Exposure Condition	P _{Max} (dBm)	P _{Limit} (dBm)	P _{Max} (mW)	P _{Limit} (mW)	Validation
20	Dynamic Budgeting	LTE FDD	B30	QPSK	N/A	N/A	10	N/A	A	ANT 14	Body	25.7	19.4	371.5	87.1	0.805 < 1.000
		Wi-Fi	U-NII 3	BPSK	N/A	N/A	80	155	N/A	ANT 6	Body	20.0	16.8	100.0	47.3	
		Bluetooth	2.4 GHz	LE	N/A	N/A	N/A	N/A	N/A	ANT 4	Body	21.0	21.0	125.9	125.9	
		Static Budget Radios														

The data collected herein is sufficient in displaying the validation of the TAS algorithm(s). As demonstrated, the power limiting enforcement is effective and the total normalized time-averaged conducted power does not exceed 1 nor does P[n] exceed P_{Limit}.

Appendices

Appendix A: Test Setup Photos

Appendix B: cSAR3D Calibration Certificate

Appendix C: Dipole Certification Certificates

Appendix D: Fast SAR Measurement System & Test Equipment

D.1. Fast SAR Measurement System

The cSAR3D Fast SAR measurement system used for performing compliance tests consists of the following items:

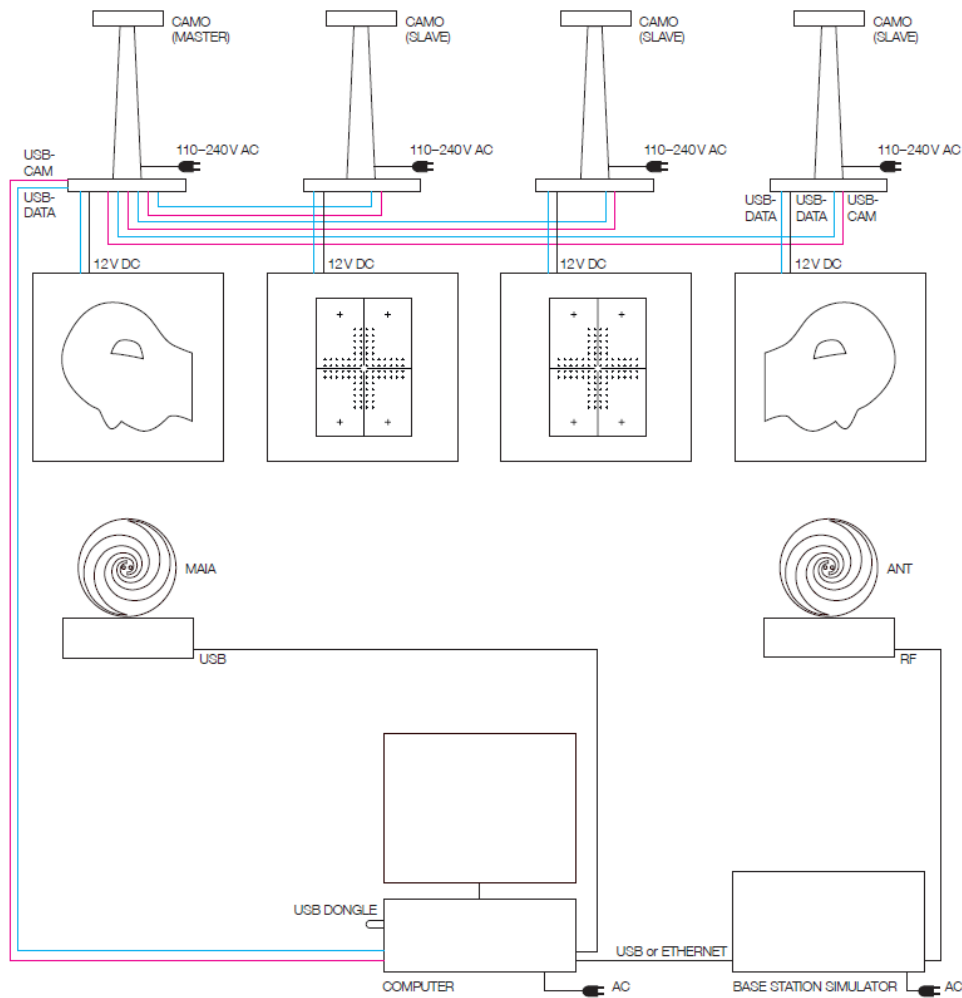


Figure D.1-1: cSAR3D System Block Diagram

- cSAR3D Flat Phantom containing:
 - A Head-Simulating Liquid medium within $\pm 10\%$ of the permittivity and conductivity requirements declared by IEC/IEEE 62209-1528.
 - An array of 3-D sensors embedded within the liquid utilizing mixed-sensor technology.
- “CAMO” arm-mounted positioning cameras and USB hubs.
- A test PC utilizing the cSAR3D Application Version 5.10.6.3469 for measurement control.
- A Base Station Simulator for call processing.
 - A CMW500 Wireless Communications Tester for GSM, W-CDMA, and LTE tests.
 - A UXM 5G Test Platform for 5G NR tests.
- The cSAR3D Left Head, cSAR3D Right Head, and cSAR3D Flat phantom containing Muscle-Simulating Liquid are also pictured in Figure D.1-1: cSAR3D System Block Diagram as a complete cSAR3D Fast SAR System but were not used for measurements in this test report.

D.2. Test Equipment

The measuring equipment used to perform the tests documented in this report has been calibrated in accordance with the manufacturers' recommendations and is traceable to recognized national standards.

System Check

Name of Equipment	Manufacturer	Type/Model	Serial No.	Cal. Due Date
POWERSOURCE1	SPEAG	SE UMS 160 CC	4394	1/18/2027

Lab Equipment

Name of Equipment	Manufacturer	Type/Model	Serial No.	Cal. Due Date
Fast SAR Measurement Platform - Flat	SPEAG	cSARDV2-FHSL	5014	10/28/2026
Fast SAR Measurement Platform - Flat	SPEAG	cSARDV2-FHSL	5279	3/10/2027
System Validation Dipole	SPEAG	D835V2	4d117	11/5/2026
System Validation Dipole	SPEAG	D1950V3	1136	4/14/2027
System Validation Dipole	SPEAG	D2450V2	706	1/9/2027
System Validation Dipole	SPEAG	D5GHz	1138	2/13/2027

Other

Name of Equipment	Manufacturer	Type/Model	Serial No.	Cal. Due Date
Wideband Radio Communication Tester	R&S	CMW500	135384-PJ	3/31/2027
Wideband Radio Communication Tester	R&S	CMW500	171873-PW	2/11/2027
Wideband Radio Communication Tester	R&S	CMW500	137873-WG	3/31/2027
Radio Communication Tester	R&S	CMX500	103011	2/28/2027
Wideband Radio Communication Tester	Keysight	E7515B UXM 5G	MY58460103	1/31/2027
Wideband Radio Communication Tester	Keysight	E7515B UXM 5G	MY58291246	2/28/2027
Power Sensor	R&S	NRP8S	115089-GV	2/28/2027
Power Sensor	R&S	NRP8S	115090-HV	2/28/2027
Power Sensor	R&S	NRP8S	115095-QK	2/28/2027
Power Sensor	R&S	NRP18S	104134-bP	2/28/2027
Power Sensor	R&S	NRP18S	104136-JZ	2/28/2027
Power Sensor	R&S	NRP8S	115091-ZA	2/28/2027
Power Sensor	R&S	NRP8S	115093-GT	2/28/2027
Power Sensor	R&S	NRP8S	110667-vC	2/28/2027
Power Sensor	R&S	NRP8S	109115-nc	2/28/2027
Power Sensor	R&S	NRP8S	115094-Yy	2/28/2027
Power Sensor	R&S	NRP8S	115088-rq	2/28/2027
Power Sensor	R&S	NRP8S	115092-m	2/28/2027

Appendix E: Fast SAR System Verification

A system verification was performed weekly before measurements were made as defined in *cSAR3D Application Note: System Check or Verification, April 2024* per IEC 62209-3 standards.

cSAR3D Verification Test - Flat Phantom **Version date:** May 6, 2024

This spreadsheet is to be used for the System Verification Test described in the Application Note "System Check or Verification" that is part of the cSAR3D System Handbook

Please follow the Application Note "System Check or Verification" and fill the data in the yellow cells.

Date of Tests	19-Jul-2026
Software version	5.10.6.3469
Laboratory	OTA A
Operator	Pwint San
Ambient Temp.	22

- Notes**
- Location (Orientation) Antenna location and orientation as described in the cSAR3D System Handbook
- Reported uncertainty (k = 2) The expanded measurement uncertainty as reported by the cSAR3D software. In the cSAR3D Handbook, this is $\Delta SAR_{meas} = \sqrt{(MM^2 + MN^2 + MD^2 + ME^2)}$
- Total uncertainty (k = 2) The expanded uncertainty that combines the reported uncertainty with the uncertainty for system validation antenna. In the cSAR3D Handbook, this is $\Delta SAR_{syscheck} = \sqrt{(\Delta SAR_{meas}^2 + MV^2)} = \sqrt{(MM^2 + MN^2 + MD^2 + ME^2 + MV^2)}$
- Numerical target The absolute target values as tabulated in IEC 62209-3, known as SAR_1g,cal and SAR_10g,cal
- SAR from cSAR3D Certificate The cSAR3D measured SAR values reported by the SPEAG calibration lab, located in the cSAR3D calibration certificate. These are known in IEC 62209-3 as SAR_1g,sys and SAR_10g,sys
- Tolerance The allowable repeatability tolerance, which is stated as 10% in IEC 62209-3 for the same setup. For different positions and orientations, this is -20% to +10% as explained in the Application Note.

cSAR3D Type		Flat HSL										Verification Against Absolute Targets								Verification Against Measured Reference Values								
Serial Number		5279																										
Dipole Type	Dipole Serial Number	frequency (MHz)	Location (Orientation)	Power (dBm)	Measured SAR		Reported Uncertainty (k = 2)		Measured SAR Normalized to 1 W		Numerical Target		Deviation from Target		Total Uncertainty (k = 2)		Verified Against Numerical Targets?		SAR from cSAR3D Certificate		Deviation from Cal. Cert.		Tolerance		Verified Against Measured Values?			
					1g	10g	1g	10g	1g	10g	1g	10g	1g	10g	1g	10g	(%)	(dB)	1g	10g	1g	10g	1g	10g	-(dB)	+(dB)	1g	10g
D835V2	4d117	835	A0 (0°)	17	0.437	0.273	1.01	1.01	8.72	5.45	9.56	6.22	-0.4	-0.6	30.7%	1.2	Yes	Yes	8.71	5.46	0.0	0.0	-1.0	0.4	Yes	Yes		
D1950V3	1136	1950	A0 (0°)	17	1.982	0.993	1.01	1.01	39.55	19.80	40.5	20.9	-0.1	-0.2	30.7%	1.2	Yes	Yes	39.50	19.50	0.0	0.1	-1.0	0.4	Yes	Yes		
D2450V2	706	2450	A0 (0°)	17	2.523	1.147	1.13	1.13	50.34	22.89	51.4	23.8	-0.1	-0.2	33.8%	1.3	Yes	Yes	51.80	23.10	-0.1	0.0	-1.0	0.4	Yes	Yes		
D5GHzV2	1138	5200	A0 (0°)	17	3.916	1.120	1.13	1.13	78.13	22.35	75.8	21.4	0.1	0.2	33.8%	1.3	Yes	Yes	86.40	24.00	-0.4	-0.3	-1.0	0.4	Yes	Yes		
D5GHzV2	1138	5800	A0 (0°)	17	4.182	1.156	1.13	1.13	83.44	23.07	78.0	21.9	0.3	0.2	33.8%	1.3	Yes	Yes	86.50	23.70	-0.2	-0.1	-1.0	0.4	Yes	Yes		

Appendix F: Measurement Uncertainty

F.1. Fast SAR

The cSAR3D Measurement Platform performs on-the-fly calculation of the uncertainty of each measurement from the measured SAR distribution and other parameters. The calculated uncertainties for each Fast SAR measurement performed is reported with the Fast SAR Test results.

The uncertainty calculations are performed in accordance with IEC 62209-3 and the draft IEC Technical Report for 5G NR FR-1.

The combined expanded uncertainties for *psSAR1g* and *psSAR10g* are calculated as follows:

$$U_{1g} = k \cdot u_{c,1g}$$

$$U_{10g} = k \cdot u_{c,10g}$$

A coverage factor of $k = 2$ is used, corresponding to a 95% confidence interval. The combined standard uncertainties, $u_{c,1g}$ [dB] and $u_{c,10g}$ [dB], having log normal distributions, are calculated as follows:

$$u_{c,1g} = \sqrt{\sum_{i=1}^N (a_{i,1g} \cdot c_i / q_i)^2} = \sqrt{\sum_{i=1}^N u_{i,1g}^2}$$

$$u_{c,10g} = \sqrt{\sum_{i=1}^N (a_{i,10g} \cdot c_i / q_i)^2} = \sqrt{\sum_{i=1}^N u_{i,10g}^2}$$

where

- $a_{i,1g}$ and $a_{i,10g}$ are the uncertainty values of the i^{th} uncertainty component having a probability density function PDF_i . These are determined from the measured SAR distribution, other measurement parameters, phantom type, and system characteristics
- q_i is the divisor corresponding to the probability density function PDF_i .
- c_i is the sensitivity coefficient associated with the i^{th} uncertainty component. It describes how the total uncertainty varies with small changes in the uncertainty component.
- $u_{i,1g} = a_{i,1g} \cdot c_i / q_i$ is standard uncertainty of the i^{th} uncertainty component. It is normalized so that it has a lognormal distribution.

F.1.1 Fast SAR Combined Expanded Uncertainty

The general combined expanded uncertainty declared by the Fast SAR system manufacturer is shown below in both percentages and decibel offsets.

Phantom Type	Distance (mm)	psSAR	$2 \cdot u_c (\pm\%)$				$2 \cdot u_c (\pm\text{dB})$			
			0.69 - 1 GHz	1 - 3 GHz	3 - 5 GHz	5 - 6 GHz	0.69 - 1 GHz	1 - 3 GHz	3 - 5 GHz	5 - 6 GHz
Flat	< 5	1g / 10g	27.5	27.5	29.0	29.0	1.5	1.5	1.6	1.6
Flat	≥ 5	1g / 10g	26.4	26.4	28.5	28.5	1.5	1.5	1.6	1.6
Head	≥ 0	1g / 10g	35.8	35.8	36.9	36.9	1.8	1.8	1.8	1.8

Note(s):

*: Measurement uncertainty declarations for Fast SAR systems are referenced from *Schmid & Partner Engineering AG, cSAR3D V5.10 Manual, October 2025*.

F.2. Conducted

Where relevant, the following measurement uncertainty levels have been estimated for tests performed on the apparatus:

PARAMETER	U _{Lab}
RF Power Measurement Direct Method Using Power Meter	1.300 dB Peak 0.450 dB Avg

Uncertainty figures are valid to a confidence level of 95%.

END OF REPORT