

## **Voltage and Current into Final Devices AMR85**

### 824-849 MHz PA

2 final MMIC devices in parallel,  $V = +5$  VDC,  $I = 800$  mA for each device

### 869-894 MHz PA

2 final MMIC devices in parallel,  $V = +5$  VDC,  $I = 800$  mA for each device