

Andrew Corporation

Model: **MR853D**

Power Back-Off Description

After the final amplifiers, in both the uplink and downlink paths, a power detection circuit measures the output power and produces an analog voltage representative of the output power. The power detect voltage is compared to reference voltage through a comparator circuit to adjust the gain of the device. When the measured voltage reaches the maximum limit, the gain of the device is reduced to limit the output power. This feature is the automatic level control (ALC) and keeps intermodulation and spurious emissions below the limits of -13 dBm.