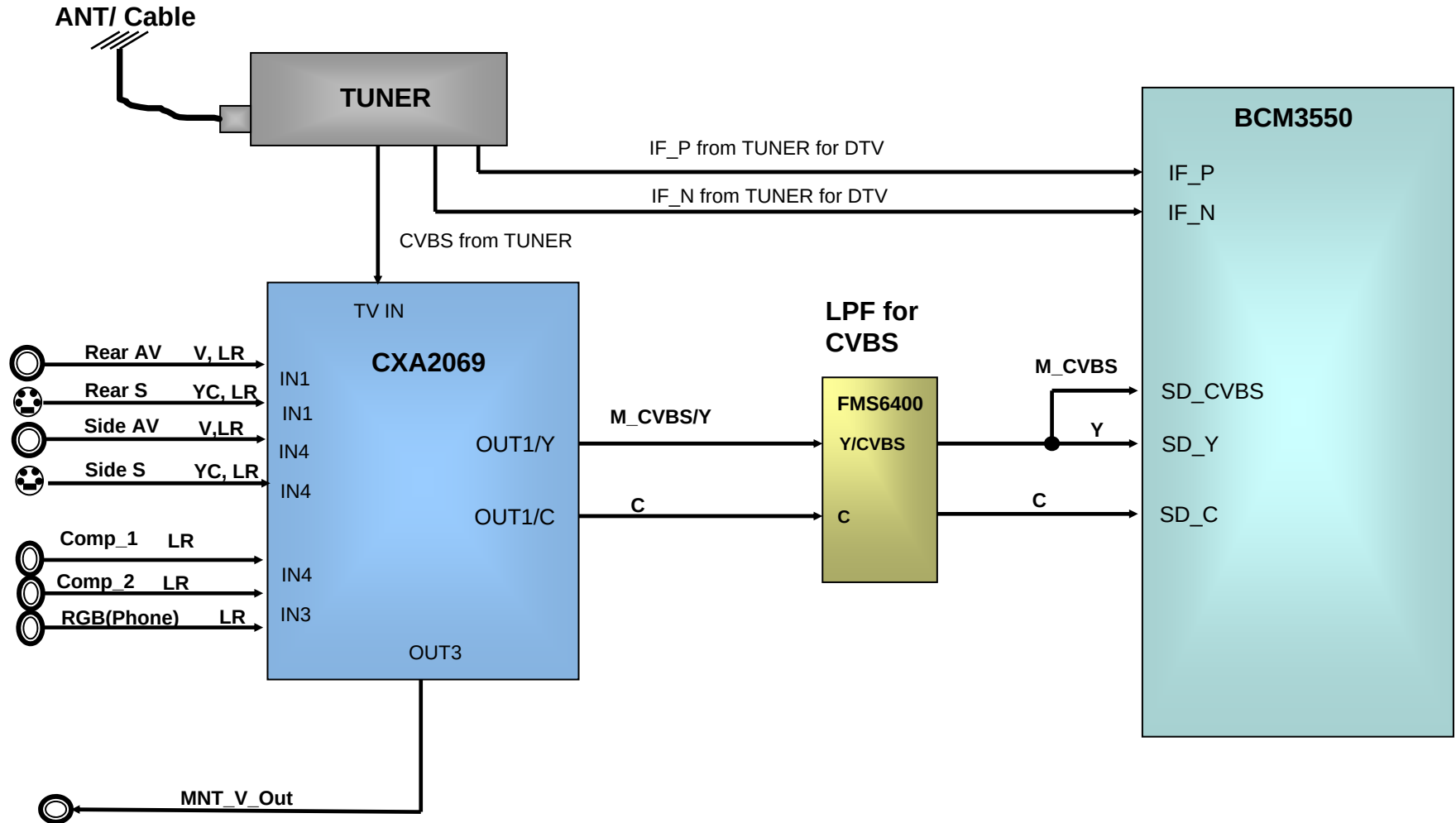


APPENDIX D
: BLOCK DIAGRAM

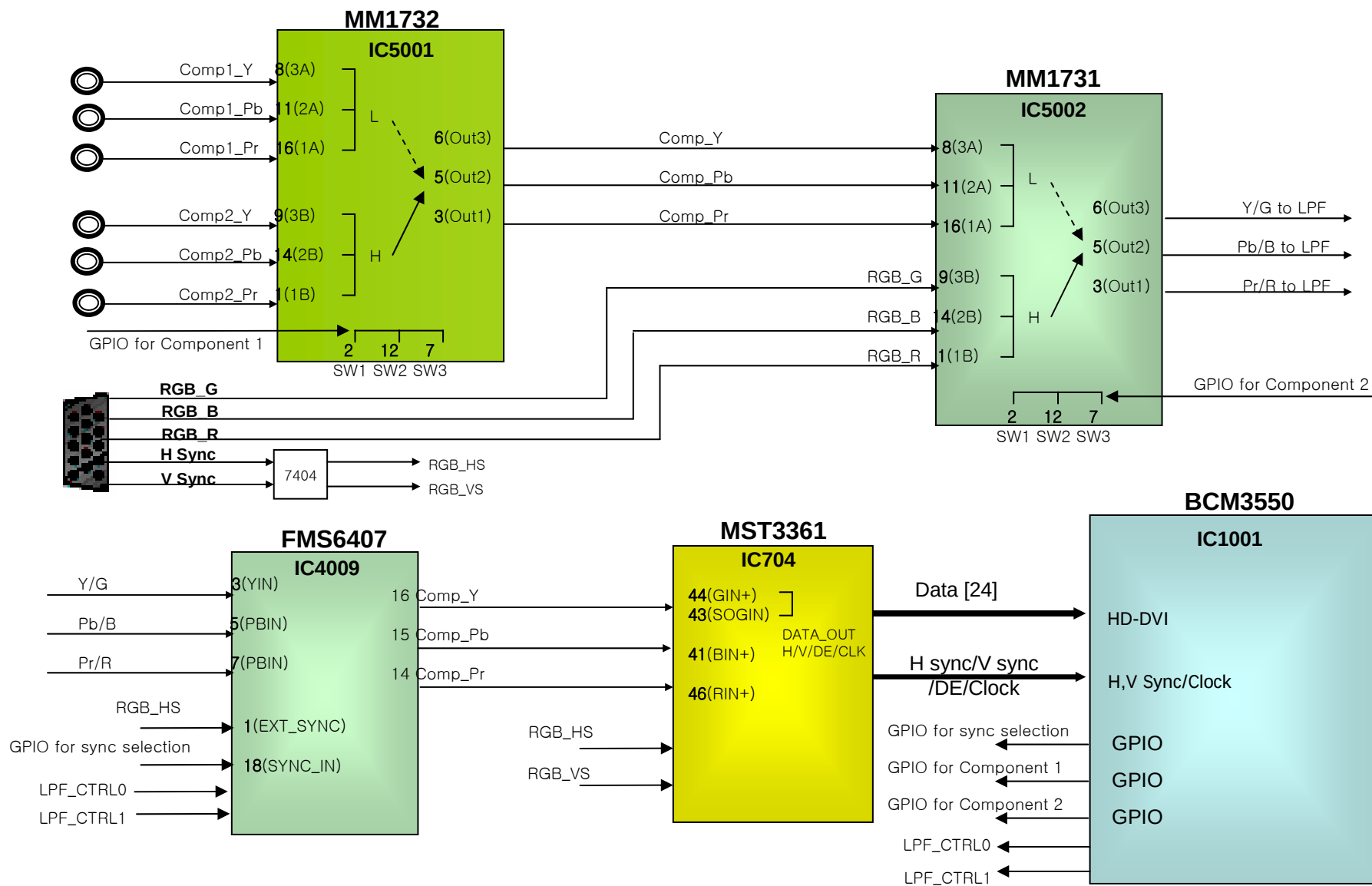
The diagram illustrates the system architecture of the SBC-1000. It features a central BCM3560 processor connected to various components:

- Input/Output and Tuning:** Includes an ATSC/NTSC SC Tuner, CXA2069 (AV S/W), and an MST3361 (HDMI & Component RGB-PC). These handle video and audio inputs from sources like Rear AV, Side AV, MNT_V_Out, and Comp_1/2. They interface with the BCM3560 via SIF for NTSC, CVBS, Y.C, and I2S Audio Input.
- Storage and Memory:** The system uses SDRAM (256Mbit x2) connected via a 64-Bit I/F, and Flash Memory (16MByte). It also includes an X-tal (54MHz) and an EEP ROM.
- Control and Interface:** A Reset IC and a Sub Micom (MTV416) manage the system. The BCM3560 also interfaces with RS-232C, USB for upgrade S/W, and an IIS (Inter-Integrated Sound) block.
- Audio and Video Output:** The BCM3560 outputs via SPDIF_OUT to an SPDIF Out, and through R/L Output to a D-class AMP. It also connects to an AUDIO DAC for MNT_Out LR.
- Display and Sync:** The MST3361 handles H/V Sync and R/G/B signals, outputting Data [24], Hsync/Vsync/DE/Clk, and SPDIF to the BCM3560's DVI Input.

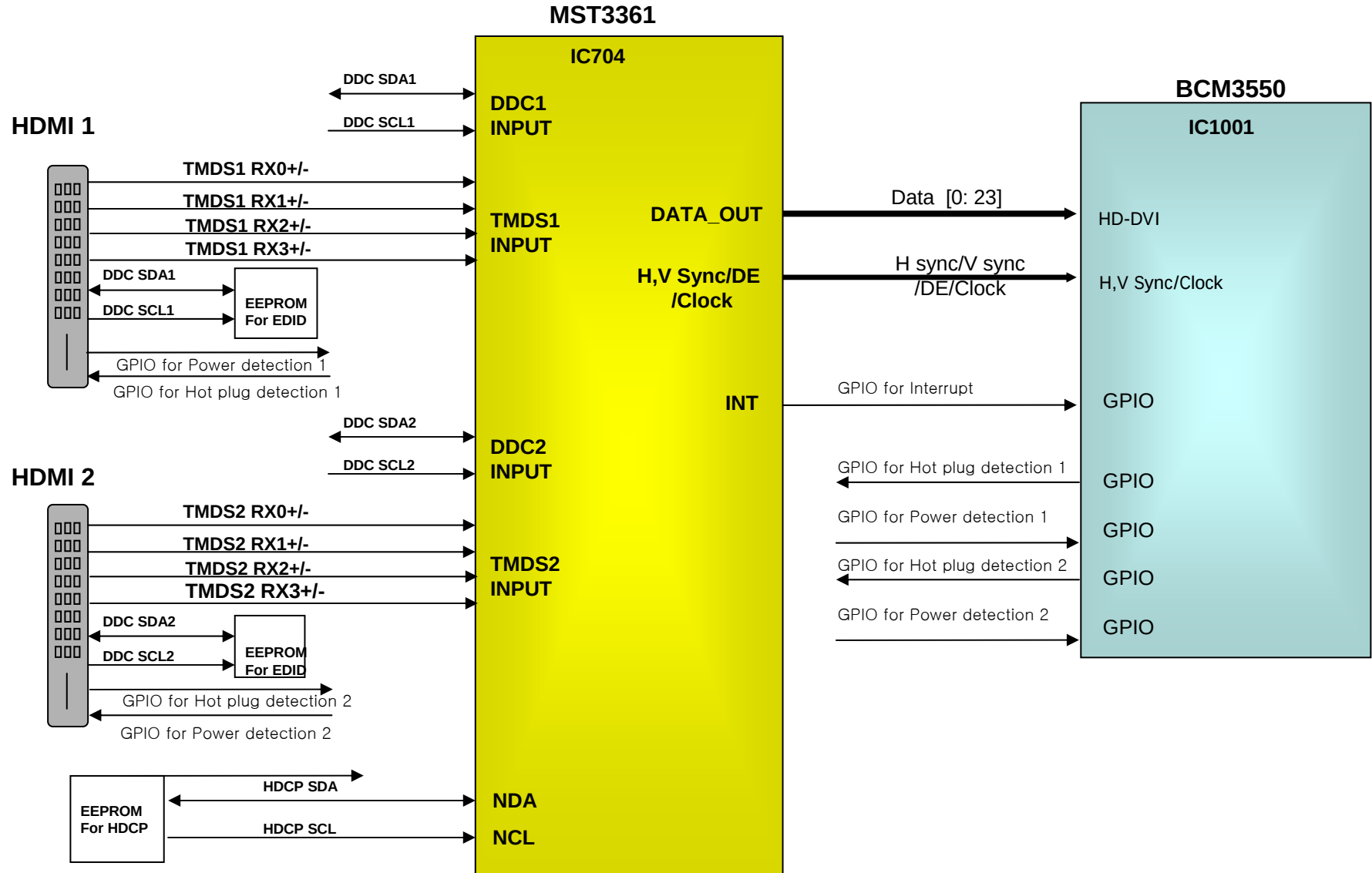
Video signal path for Composite / S-video / IF



Video signal path for Component



Video signal path for HDMI



Audio signal path for all

