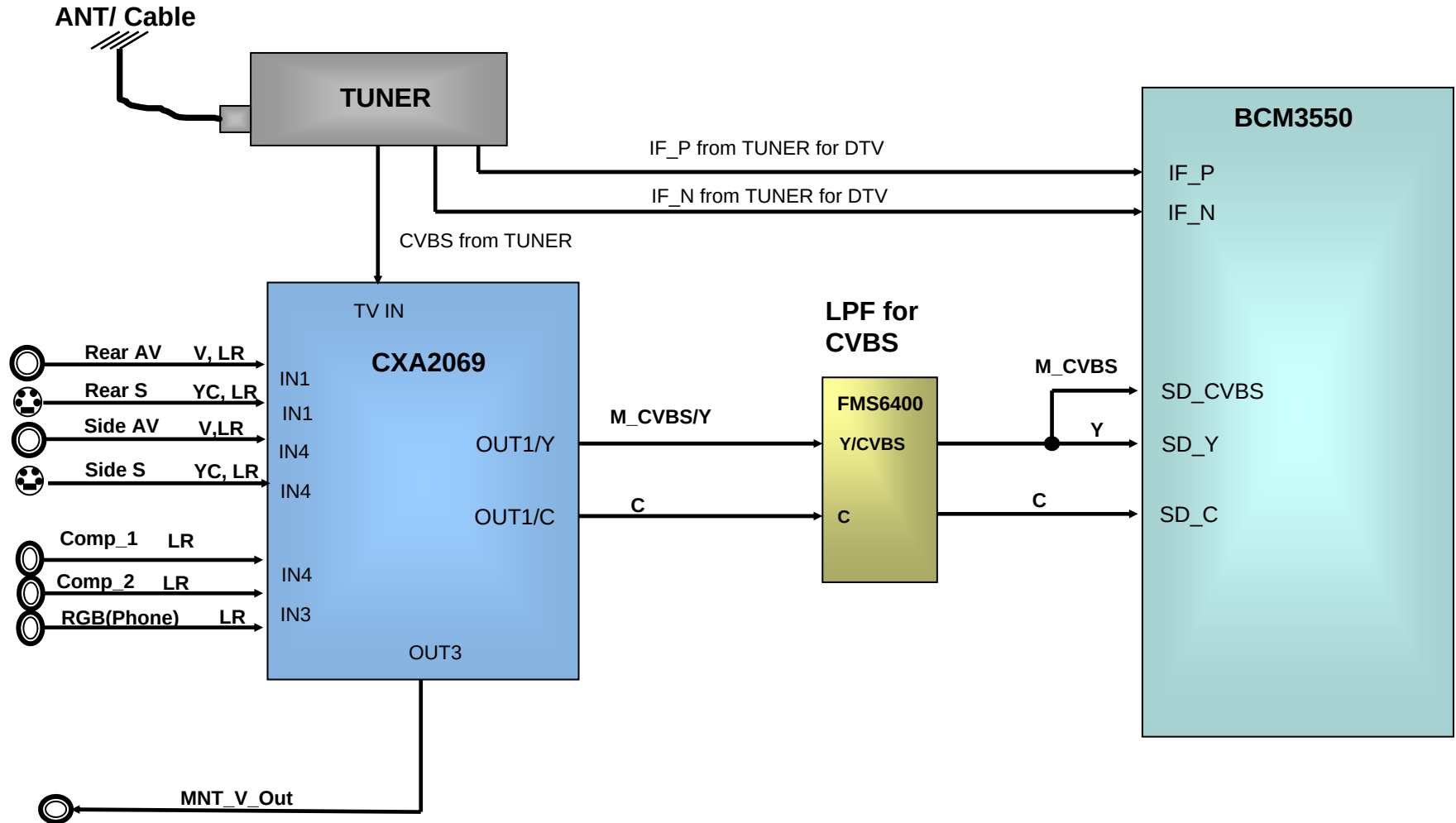


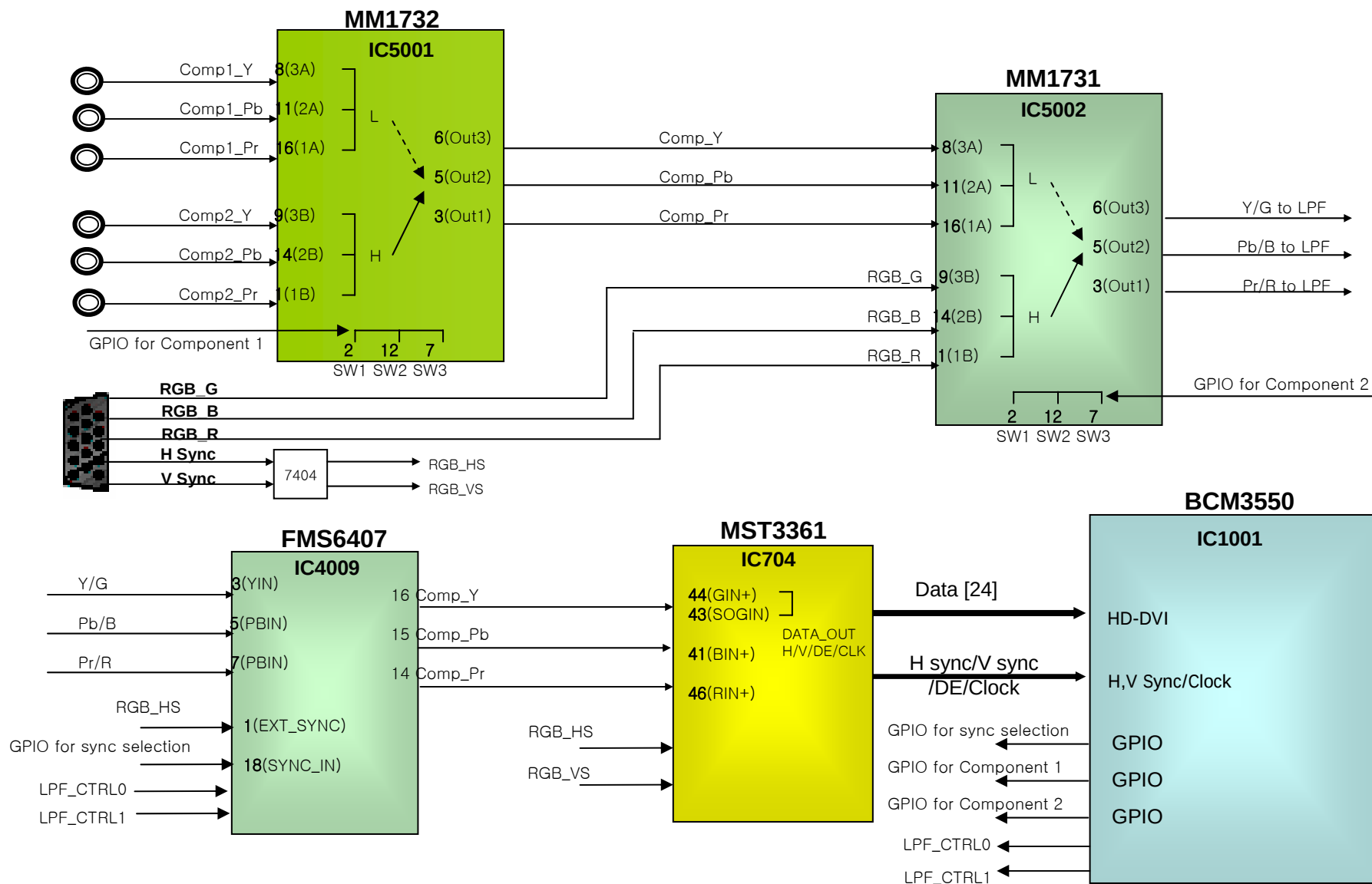
APPENDIX D
: BLOCK DIAGRAM

The diagram illustrates the system architecture of the SBC-1000. At the top left, the **ANT. Cable** connects to the **ATSC/NTSC SC Tuner** (green box), which is part of the **Tuner부** (purple box). The tuner outputs **SIF for NTSC** and **CVBS** signals. The **CVBS** signal is also received from the **CXA2069(AV S/W)** (green box). The CXA2069 has multiple inputs: **Rear AV V, LR**, **Rear S YC, LR**, **Side AV V, LR**, **Side S YC, LR**, **MNT_V_Out**, **Comp_1 LR**, **Comp_2 LR**, and **RGB(Phone) LR**. Its outputs are **IN1**, **IN5**, **OUT1**, **OUT3**, and **IN4**. The **OUT1** output provides **CVBS** and **Y,C** signals to the **BCM3560** (light blue box). The **BCM3560** is connected to **SDRAM 256Mbit x2** via a **64-Bit I/F** and to an **X-tal 54MHz** oscillator. It also receives **SIF for NTSC** and **IF P/N for DTV** signals. The **BCM3560** has several internal blocks: **VSB/QAM/NTSC/SIF**, **VDEC_CVBS/S_Y**, **I2S Audio Input**, **DVI Input**, and **HDMI/DVI**. It is connected to an **RS-232C** port and a **USB for upgrade S/W** port. The **BCM3560** outputs **Digital Output** to an **LVDS Trans** (light blue box), which is connected to a 12-bit display. It also outputs **SPDIF_OUT** to an **SPDIF Out** port and **R/L Output** to a **D-class AMP** (light blue box), which is connected to a speaker. The **BCM3560** is also connected to **Flash Memory 16MByte** and a **Sub Micom (MTV416)** via **IIC**. The **Sub Micom** is connected to an **EER ROM** and an **AUDIO DAC**, which outputs **MNT_Out LR** to a speaker. The **BCM3560** also has an **IIS** block. The **MST3361** (light blue box) is connected to the **BCM3560** via **Data [24]**, **Hsync/Vsync/DE/Clk**, and **SPDIF** signals. It also receives **H/V Sync** and **R/G/B** signals, which are processed by **2X1 S/W** blocks. The **MST3361** is connected to an **HDMI & Component RGB-PC** (green box) via **TMDS** and **DDoS** signals. It also has an **HDCP** block. The **BCM3560** has an **Reset IC** connected to its **Reset** pin. The **BCM3560** also has an **Audio ADC** block connected to its **I2S** input, which receives **Audio R/L** signals from the **CXA2069**.

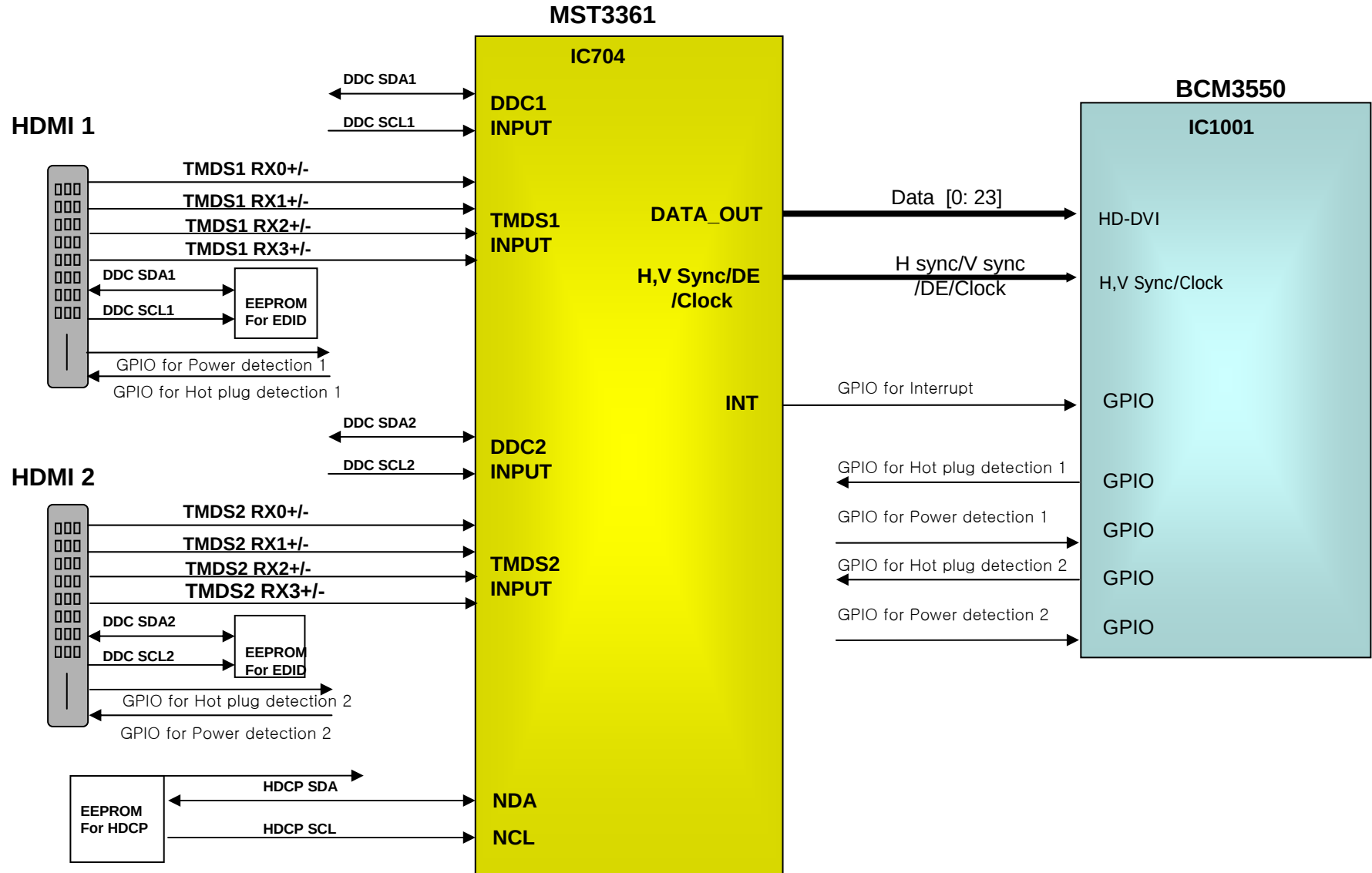
Video signal path for Composite / S-video / IF



Video signal path for Component



Video signal path for HDMI



Audio signal path for all

